

ECE496Y Project Proposal Grading Form - Draft A

Project ID	2011017	Supervisor(s):	Jason Anderson	Section:7
Project Title:	Virtual FPGA fabrics: Implementation of a virtual FPGA architecture			Administrator: Gillett

Administrator's Evaluation

Required Sections for Draft A

	Excellent	Good	Satisfactory	Marginal	Poor	Missing/ Unacceptable
Background and Motivation: A clear introduction and description of the design problem and its context including the state of the art, and existing work and technology. A clear motivation for the work.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Project Goal and Requirements: Clear project goal and requirements, links to the original design problem. Problems (circle all that apply): Unverifiable/unmeasurable requirements, unnecessary implementation details	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Validation and Acceptance Tests: Clear description of tests, explanations of how tests demonstrate that the project goal and requirements have been achieved.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Possible Solutions and Design Alternatives: Clear description of possible approaches and discussion of design trade-offs.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Feasibility Assessment: Clear description of required skills and resources and key risks.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Optional Sections (if included)

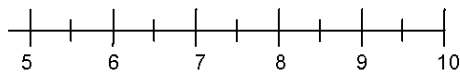
System-level overview: A clear description of the top level design.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Module-level descriptions: A clear description of each key component of the design.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Assessment of Proposed Design: A clear description of how the solution addresses the technical problem. A clear discussion of the strengths, limitations, and trade-offs of the design.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Work breakdown structure (circle specific problems) persons not assigned to tasks, tasks not numbered	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Gantt chart (circle specific problems) missing task numbers or descriptions, missing persons assigned to each task	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Financial plan	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Overall Quality of Document

Presentation (circle specific problem areas) Grammar, spelling, clarity, style, other (specify):	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Content (circle specific problem areas) Organization, logical coherence, substance, references, other (specify):	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Other Problems: Report late, other (specify):

Approximate grade out of 10 (based on required sections)



Administrator's
Signature:

Note to Supervisors:

This draft is not graded. Comments and approximate grade are provided only to give feedback. Actual grading done for the final draft only.

Administrator's Comments (also see comments in report)

**The Edward S. Rogers Sr. Department of
Electrical and Computer Engineering
University of Toronto**

**ECE496Y Design Project Course
Group Project Proposal (Draft A)**

Title: Virtual FPGA fabrics: Implementation of a virtual FPGA architecture

Project I.D.#:	2011017	
Team members: (Select one member to be the main contact. Mark with ‘*’)	Name: Neil Isaac *	Email: n.isaac@utoronto.ca
	Keyi Shi	keyi.shi@utoronto.ca
Supervisor:	Jason Anderson	
Section #:	7	
Course Administrator:	Gillett	
Date:	September 20, 2011	