

ECE496Y Final Report Individual Evaluation Form (one sheet per student)

Student	Project ID: 2011017	Project Title: Virtual FPGA Fabrics
	Student Name: Neil Isaac	Supervisor: Jason Anderson
	Section: 7	Administrator: Ross Gillett

Administrator's Evaluation		Excellent	Good	Adequate	Marginal	Poor/unclear	Comments (also see comments in report)
Provide a rating for each section. Circle to indicate problem areas.							
Document							
Introduction: clear background, motivation, goals, requirements							
Final Design: system diagram, system and module level descriptions, assessment of strengths and weaknesses							
Testing and Verification: adequate documentation, discussion of results, comparison of results to requirements							
Summary and Conclusions: summary of accomplishments and challenges, success in achieving project goals							
Presentation: clear writing, grammar, organization, use of tables, diagrams, figures							
Project							
Final outcome: success in achieving stated project goals, technical complexity							
Individual effort and contributions							
Administrator's grade (/10):		Section average (/10):		Administrator's signature:			
General Comments:							

Note: Supervisor final evaluations are done online. See Supervisor > Final Evaluation on the course menu at <http://int.ece.utoronto.ca/ece496.xxyy/index.html>, where xxyy are the years (ex 0809 or 0910)