

ECE496Y Design Review Meeting Evaluation Form

Student	Project Title: Virtual FPGA fabrics: Implementation of a virtual FPGA architecture				
	Project ID: 2011017		Supervisor: Jason Anderson		
	Section #: 7		Administrator: Ross Gillett		
	Estimate # contact hours per month with supervisor: 4		Suggest optimal # hours per month: 4		

Administrator's Evaluation		Excellent	Good	Adequate	Marginal	Poor/Unclear	Comments to Group
Group Evaluation							
Project Description (background, goals, requirements, acceptance tests)							
Technical Design (alternatives, trade-offs, system overview, testing)							
Work Plan (scheduling, risks, resources)							
December Review Target Received <input type="checkbox"/>							
Additional deliverables for December Review:							
Individual Evaluations (roles and contributions)							Comments to Individuals
Student 1:							
Mark: /10							
Student 2:							
Mark: /10							
Student 3:							
Mark: /10							
Student 4:							
Mark: /10							
Section average (/10):		<input type="checkbox"/> Ethics review recommended					Administrator's Signature:

Note to Supervisors:

Your evaluation was done online. There is no need to return anything else to the administrator.
Please return this report to your students.