

ECE496Y Project Proposal Document Evaluation Form

Student	Project Title: Virtual FPGA fabrics: Implementation of a virtual FPGA architecture				
	Project ID: 2011017		Supervisor: Jason Anderson		
	Section #: 7		Administrator: Ross Gillett		
	Estimate # contact hours per month with supervisor: 4		Suggest optimal # hours per month: 4		

Administrator's Project Proposal Evaluation		Excellent	Good	Adequate	Marginal	Poor	Comments
(Provide a rating for each section)							
Project Description							
Background and Motivation (design problem, past work, references)							
Project Goal and Requirements (Verifiable? Link to original problem?)							
Validation and Acceptance Tests (Link to goal and requirements?)							
Technical Design							
Possible Solutions and Design Alternatives (key trade-offs)							
System-level overview (system block diagram)							
Module-level descriptions (inputs/outputs, functional description)							
Assessment of Proposed Design (strengths, limitations, trade-offs)							
Work Plan							
Work breakdown structure (tasks verifiable? Complete, clear and fair division of work)							
Gantt chart (task described and numbered, logical scheduling)							
Financial plan (Justification for funding? Contingency plan?)							
Feasibility Assessment (skill & resources, risk mitigation plan)							
Overall							
Presentation (grammar, spelling, writing style, figures)							
Content (Exec. summary, organization, logical coherence, substance)							
Other problems (Late, student-supervisor agreement unsigned, supervisor contact time too low)							
<input type="checkbox"/> Ethics review (if gathering personal information or testing on human or animal subjects)							
Administrator's grade only (/10) (*Supervisor's grade on <u>next</u> page)		Document section average (/10)					Administrator's signature:
Administrator's comments (also see comments in report)							