Universidad de las Américas Puebla

Escuela de ingeniería

DEPARTAMENTO DE COMPUTACIÓN, ELECTRÓNICA Y MECATRÓNICA

Lab report #1

Course: Digital design LRT2022-sección

Equipo: 2

183339 Juan Pablo Lopez Moreno LMT

185406 Amy Marianee Ramírez Sánchez LMT

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1 Abstract

2 Introduction

A fundamental part of working with circuits is to have knowledge about the tools that will be used during the design of these components.

During the course of Digital Design, it will be elemental to have a knowledge of the inner workings of certain electronics, especially to find a way to showcase the values resulting from logical operations. Considering the previous statement, during the first and second day of lab work, the team will focus on understanding and employing simple circuits that will help to understand the basic concepts of digital electronics. The main tools that will be used during this lab are the multimeter and the power supply, showcasing their usage through circuits developed both in a breadboard and in Multisim.

3 Objectives

The objectives of this lab are:

- Learn to use the Multimeter and the power supply.
- Implement a circuit of digital input and output in a breadboard and Multisim.
- Verify the proper functioning of the digital circuits by using the Multimeter.

A multimeter is defined as an instrument that measures different electrical quantities such as current, voltage, and resistance, and can be either analogue or digital. It is used for tasks like fault finding and assessing the condition of electrical components, with digital meters offering greater accuracy and ease of reading. [1]

A power supply is defined as the interface between an external power source, which may be noisy and variable, and the clear-cut requirements of internal circuitry in electronic products. It typically takes power from a conventional AC mains supply, though other options like low-voltage DC or specific aircraft supplies may also be used. [2]

- 4 Methodology
- 5 Results
- 6 Analysis
- 7 Conclusions

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[?] x 0
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• Item 1

- Item 2
- Item 3
- Item 4

7.1 Subsección

[?]

7.1.1 Sub-subsección

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Código 1: Código desde archivo

— Simple AND gate design
library IEEE;
use IEEE.std_logic_1164.all;

entity and_gate is
port(
    a in std_logic;
    b in std_logic;
    q out std_logic);
end and_gate;

architecture rtl of and_gate is
begin
    q = a and b;
end rtl;
```

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A Códigos completos

References

[1] ScienceDirect. (2008) Multimeter. [En línea]. Disponible: https://www.sciencedirect.com/topics/engineering/multimeter

[2] —. (2017) Power supply. [En línea]. Disponible: https://www.sciencedirect.com/topics/engineering/power-supply

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