ELECTRICAL ENGINEERING LAB TECHNICIAN

Summary

Over 10 years experience in technical support in high speed networking semiconductor, lab network administration, problem resolution, documentation and lab management. Self-driven team player with strengths in communication and developing interpersonal ralationships. Possess excellent planning, organization, time management and decision-making skills. Strategically coordinate and collaborate with cross-functional departments and individual to maximize performance in facilitating goals and attaining operational excellence. Highlights

- Ability to test, collect data, diagnose and trouble shooting skills: system, test boards, eval boards and lab networking.
- Knowledge of 1G/10G Ethernet, Framers, Routers, Switches, ATM, PON, PHY, SONET, Digital Home.
- Lab management: in charge of systems in lab and procurement of tools needed in lab, including maintain and organize lab networking and test bench setup.
- Fimiliar with Windows and Linux OS, MS Office, LabView and others.
- Proficient in multimeters, programmable DC power supplies, frequency counters, oscilloscopes, function and signal generators, logic analyzers, digital serial analyzer, network traffic generator/performance analyzer and other specialized test equipment: USTC and Thermonic.
- Proficient in schematic capture and PCB layout tools such as PADS, Cadence, ORCAD, Mentor Graphic, View Logic

Experience

Company Name August 2007 to October 2014 Electrical Engineering Lab Technician City, State

- Developed and implemented test and troubleshooting procedures for HSIO, Digital Home, Access and Transport products.
- Supported products design verification, characterization, and validation.
- Successfully managed lab: lab network, PCs and test stations installation.
- · Evaluation boards testing, troubleshooting, reworking, components purchasing, and assembly vendors interfacing.
- ISO 9000 equipment calibration, laboratory procedures and ESD control administration.
- Created schematic symbols, captured schematics.
- Reviewed schematic, and gave recommendations.
- Understood schematic and assembly drawings, Successfully built, assembled, tested, and debugged test fixtures, evaluation and reference boards.
- Provided on-going support for modification and de-bugging of design hardware and software products.
- Performed PCB rework down to 0402 and 0201 components.

Company Name January 2002 to June 2007 Electrical Engineering Lab Technician City, State

- Developed and implemented and troubleshooting procedures procedures for TCP Offload Engines, SONET Framer Products.
- Supported products design verification, characterization, and validation.
- Successfully managed lab: lab network, PCs and test stations installation.
- · Evaluation boards testing, troubleshooting, reworking, components purchasing, and assembly vendors interfacing.
- ISO 9000 equipment calibration, laboratory procedures and ESD control administration.
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- Understood schematic and assembly drawings, Successfully built, assembled, tested, and debugged test fixtures, evaluation and reference boards.
- Provided on-going support for modification and de-bugging of design hardware and software products.
- Performed PCB rework down to 0402 and 0201 components.

Company Name April 1995 to November 2001 Electrical Engineering Technician and PCB Layout Designer City, State

- Developed and implemented and troubleshooting procedures procedures for T1/E1 Framers, Routers, Switches, Ethernet, SONET, ATM Processing Products.
- Supported products design verification, characterization, and validation.
- Successfully managed lab: lab network, PCs and test stations installation.
- · Evaluation boards testing, troubleshooting, reworking, components purchasing, and assembly vendors interfacing.
- ISO 9000 equipment calibration, laboratory procedures and ESD control administration.
- Created schematic symbols, captured schematics.
- Reviewed schematic, and gave recommendations.
- Understood schematic and assembly drawings, Successfully built, assembled, tested, and debugged test fixtures, evaluation and reference boards.
- Provided on-going support for modification and de-bugging of design hardware and software products.
- Performed PCB rework down to 0402 and 0201 components.

Education

Solano College Associate Degree: Electronics Engineering Technology State Electronics Engineering Technology
Certificate in Quality Assurance Technology from De Anza College, CA. Certificate in PCB Design from The Copper Connection, CA.
Skills

assembly, ATM, Cadence, calibration, CA, hardware, DC, de-bugging, Ethernet, Graphic, ISO 9000, LabView, layout, Linux OS, Logic, Mentor, Access, MS Office, Windows, multimeters, network, networking, ORCAD, oscilloscopes, power supplies, procurement, purchasing, Quality Assurance, Routers, schematics, signal generators, SONET, Switches, T1, test equipment, trouble shooting, troubleshooting, validation, View