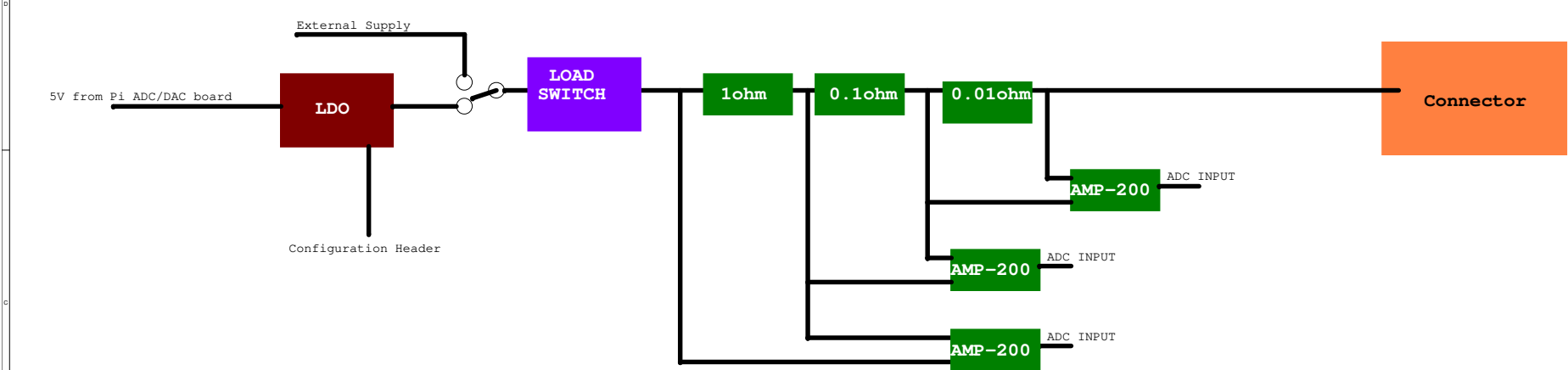
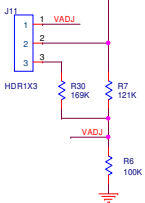
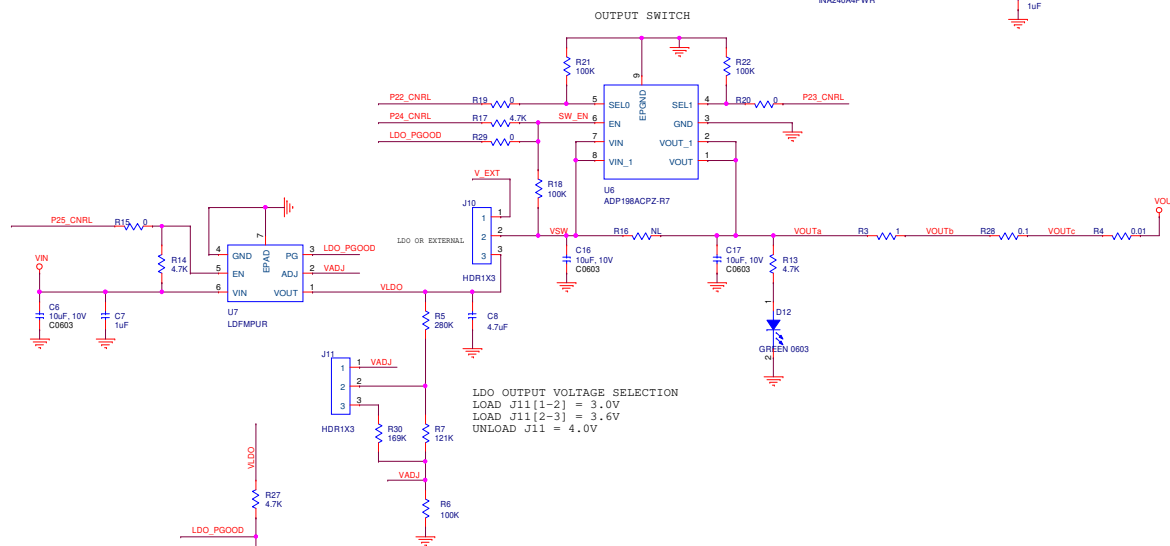
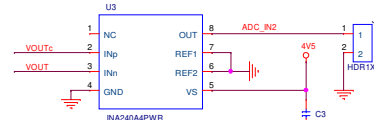
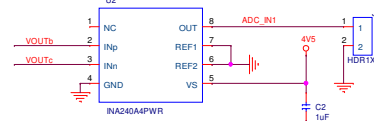
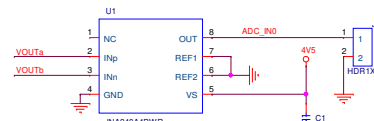
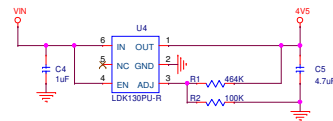
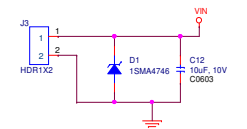
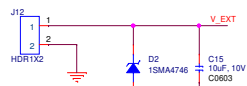
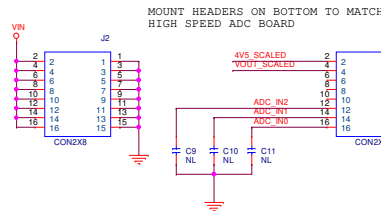
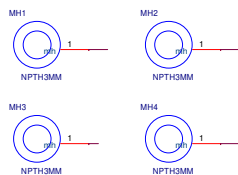


CURRENT MEASURMENT PCB V2

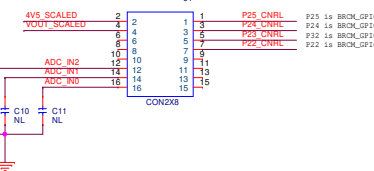




LDO OUTPUT VOLTAGE SELECTION
LOAD J11[1-2] = 3.0V
LOAD J11[2-3] = 3.6V
UNLOAD J11 = 4.0V



MOUNT HEADERS ON BOTTOM TO MATCH
HIGH SPEED ADC BOARD



Current(mA)	Resistor	Voltage	Gain	V-ADC
200	1	0.2	200	40
20	1	0.02	200	4
2	1	0.002	200	0.4
0.2	1	0.0002	200	0.04
0.02	1	0.00002	200	0.004
Current(mA)	Resistor	Voltage	Gain	V-ADC
200	0.1	0.02	200	4
20	0.1	0.002	200	0.4
2	0.1	0.0002	200	0.04
0.2	0.1	0.00002	200	0.004
0.02	0.1	0.000002	200	0.0004
Current(mA)	Resistor	Voltage	Gain	V-ADC
400	0.01	0.004	200	0.8
200	0.01	0.002	200	0.4
20	0.01	0.0002	200	0.04
2	0.01	0.00002	200	0.004
0.2	0.01	0.000002	200	0.0004
0.02	0.01	2E-07	200	0.00004

