1. Description

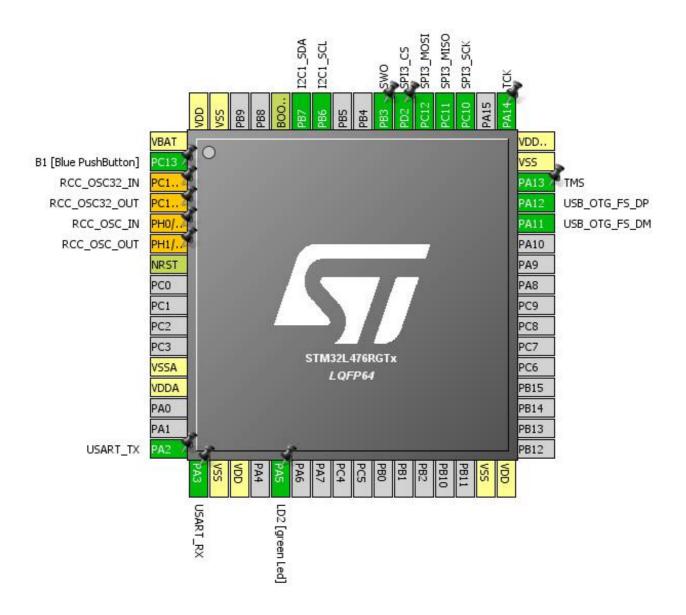
1.1. Project

Project Name	RazorClamDICE
Board Name	NUCLEO-L476RG
Generated with:	STM32CubeMX 4.19.0
Date	01/29/2017

1.2. MCU

MCU Series	STM32L4
MCU Line	STM32L4x6
MCU name	STM32L476RGTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



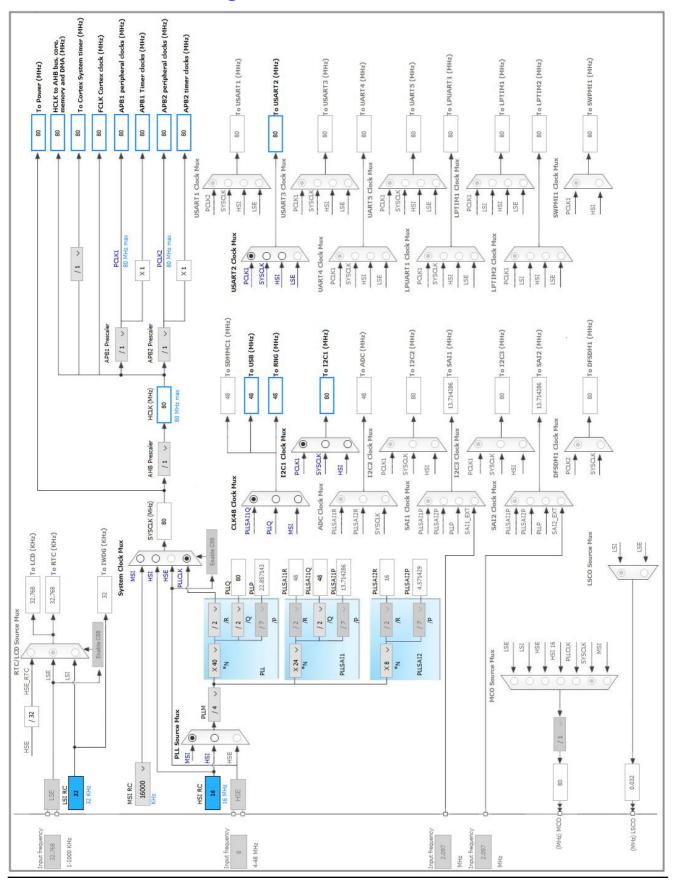
3. Pins Configuration

Pin Number LQFP64	Pin Name (function after	Pin Type	Alternate Function(s)	Label
	reset)			
1	VBAT	Power		
2	PC13	I/O	GPIO_EXTI13	B1 [Blue PushButton]
3	PC14/OSC32_IN *	I/O	RCC_OSC32_IN	
4	PC15/OSC32_OUT *	I/O	RCC_OSC32_OUT	
5	PH0/OSC_IN *	I/O	RCC_OSC_IN	
6	PH1/OSC_OUT *	I/O	RCC_OSC_OUT	
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
16	PA2	I/O	USART2_TX	USART_TX
17	PA3	I/O	USART2_RX	USART_RX
18	VSS	Power		
19	VDD	Power		
21	PA5 **	I/O	GPIO_Output	LD2 [green Led]
31	VSS	Power		
32	VDD	Power		
44	PA11	I/O	USB_OTG_FS_DM	
45	PA12	I/O	USB_OTG_FS_DP	
46	PA13	I/O	SYS_JTMS-SWDIO	TMS
47	VSS	Power		
48	VDDUSB	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	TCK
51	PC10	I/O	SPI3_SCK	
52	PC11	I/O	SPI3_MISO	
53	PC12	I/O	SPI3_MOSI	
54	PD2 **	I/O	GPIO_Output	SPI3_CS
55	PB3	I/O	SYS_JTDO-SWO	SWO
58	PB6	I/O	I2C1_SCL	
59	PB7	I/O	I2C1_SDA	
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

^{**} The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



Page 5

5. IPs and Middleware Configuration

5.1. I2C1

I2C: I2C

5.1.1. Parameter Settings:

Timing configuration:

I2C Speed Mode Fast Mode *

I2C Speed Frequency (KHz)400Rise Time (ns)0Fall Time (ns)0Coefficient of Digital Filter0Analog FilterEnabled

7 manag 1 man

Timing 0x00702991 *

Slave Features:

Clock No Stretch Mode Disabled
General Call Address Detection Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0

5.2. RNG

mode: Activated

5.3. SPI3

Mode: Full-Duplex Master

5.3.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 4 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate)

32 *

Baud Rate 2.5 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled

NSSP Mode Enabled

NSS Signal Type Software

5.4. SYS

Debug: Trace Asynchronous Sw

Timebase Source: TIM1

5.5. **USART2**

Mode: Asynchronous

5.5.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity) *

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

Advanced Features:

Auto Baudrate Disable TX Pin Active Level Inversion Disable **RX Pin Active Level Inversion** Disable Disable Data Inversion TX and RX Pins Swapping Disable Enable Overrun DMA on RX Error Enable MSB First Disable

5.6. USB OTG FS

Mode: Device_Only

5.6.1. Parameter Settings:

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 64 Bytes

Enable internal IP DMA Disabled

Low power Disabled

Link Power Management Disabled

VBUS sensing Disabled *

Signal start of frame Disabled

5.7. USB DEVICE

Class For FS IP: Download Firmware Update Class (DFU)

5.7.1. Parameter Settings:

USBD_DEBUG_LEVEL (USBD Debug Level)

Basic Parameters:

VirtualMode Dfu

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces) 1

USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration) 1

USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors) 512

USBD_SUPPORT_USER_STRING (Enable user string descriptor) Enabled

USBD_SELF_POWERED (Enabled self power) Enabled

USBD_LPM_ENABLED (Link Power Management) 1: Link Power Management supported

Class Parameters:

USBD_DFU_MAX_ITF_NUM (DFU maximum interface numbers)

USBD_DFU_XFER_SIZE 1024

USBD_DFU_MEDIA Interface @Internal Flash

/0x08000000/03*016Ka,01*016Kg,01*06 4Kg,07*128Kg,04*016Kg,01*064Kg,07*1

0: No debug message

1

5.7.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English (United States)

MANUFACTURER_STRING (Manufacturer Identifier) STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier) 57105

PRODUCT_STRING (Product Identifier)

STM32 DownLoad Firmware Update

SERIALNUMBER_STRING (Serial number) 0000000001A
CONFIGURATION_STRING (Configuration Identifier) DFU Config
INTERFACE_STRING (Interface Identifier) DFU Interface

^{*} User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	Pull-up	Very High	
	PB7	I2C1_SDA	Alternate Function Open Drain	Pull-up	Very High *	
SPI3	PC10	SPI3_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PC11	SPI3_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PC12	SPI3_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	TMS
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	TCK
	PB3	SYS_JTDO- SWO	n/a	n/a	n/a	SWO
USART2	PA2	USART2_TX	Alternate Function Push Pull	*	Very High	USART_TX
	PA3	USART2_RX	Alternate Function Push Pull	*	Very High	USART_RX
USB_OTG_ FS	PA11	USB_OTG_FS_ DM	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA12	USB_OTG_FS_ DP	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
Single Mapped	PC14/OSC3 2_IN	RCC_OSC32_IN	n/a	n/a	n/a	
Signals		RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0/OSC_I N	RCC_OSC_IN	n/a	n/a	n/a	
	PH1/OSC_O UT	RCC_OSC_OUT	n/a	n/a	n/a	
GPIO	PC13	GPIO_EXTI13	External Interrupt	Pull-up *	n/a	B1 [Blue PushButton]

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
			Mode with Falling edge trigger detection		•	
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LD2 [green Led]
	PD2	GPIO_Output	Output Push Pull	Pull-up *	Low	SPI3_CS

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true 0		0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
TIM1 update interrupt and TIM16 global interrupt	true	0	0
USB OTG FS global interrupt	true	0	0
PVD/PVM1/PVM2/PVM3/PVM4 interrupts through EXTI lines 16/35/36/37/38	unused		
Flash global interrupt	unused		
RCC global interrupt		unused	
I2C1 event interrupt		unused	
I2C1 error interrupt	unused		
USART2 global interrupt	unused		
EXTI line[15:10] interrupts	unused		
SPI3 global interrupt	unused		
RNG global interrupt	unused		
FPU global interrupt	unused		

^{*} User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32L4
Line	STM32L4x6
MCU	STM32L476RGTx
Datasheet	025976_Rev4

7.2. Parameter Selection

Temperature	25
Vdd	3.0

8. Software Project

8.1. Project Settings

Name	Value
Project Name	RazorClamDICE
Project Folder	C:\git\RazorClamDICE
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_L4 V1.6.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	