

## Exercice 1-1

### Réponse

The screenshot shows an assembler interface with the following components:

- Source Code:** A list of instructions and their addresses. The instructions are: CLO, MOV AL, 4, MOV BL, 8, ADD AL, BL, and END. The addresses are: [00], [01], [04], [07], and [0A].
- RAM Source Code View:** A window showing the machine code in hexadecimal. The first four instructions are: CLO MOV AL 4 MOV BL 8 ADD AL BL, followed by several END instructions.

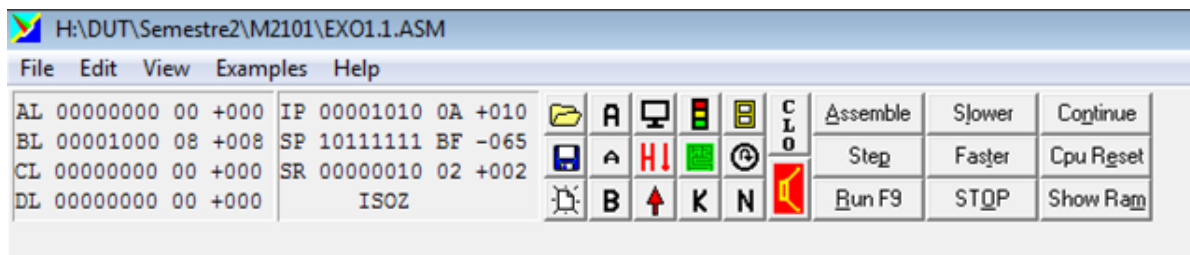
The screenshot shows the H:\DUT\Semestre2\M2101\EXO1.1.ASM file in an assembler. The interface includes a menu bar (File, Edit, View, Examples, Help) and a toolbar with various icons. The main window displays the following instructions and addresses:

Address	Instruction	Address	Instruction
AL 00001100 0C +012	IP 00001010 0A +010		
BL 00001000 08 +008	SP 10111111 BF -065		
CL 00000000 00 +000	SR 00000000 00 +000		
DL 00000000 00 +000	ISOZ		

Nous obtenons les mêmes résultats que lors de notre TD. L'adresse IP a prit la valeur de l'adresse de fin de l'exécution.

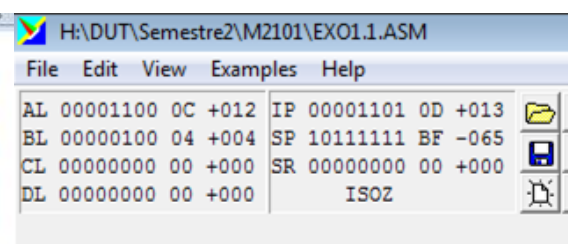
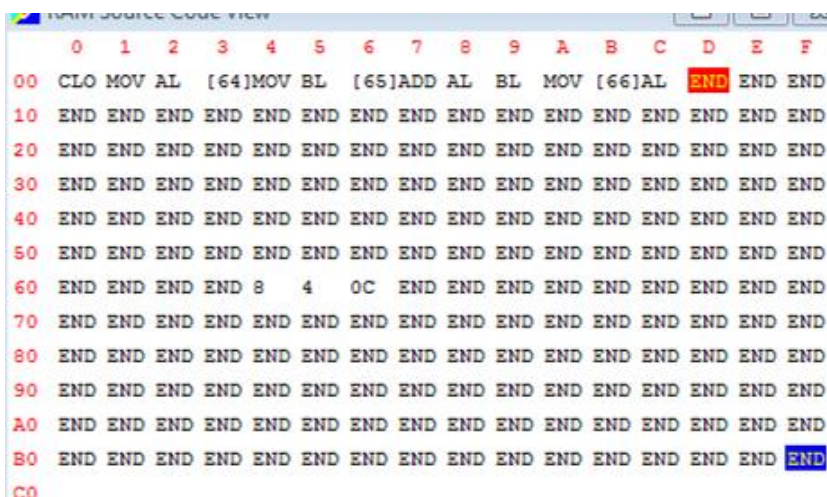
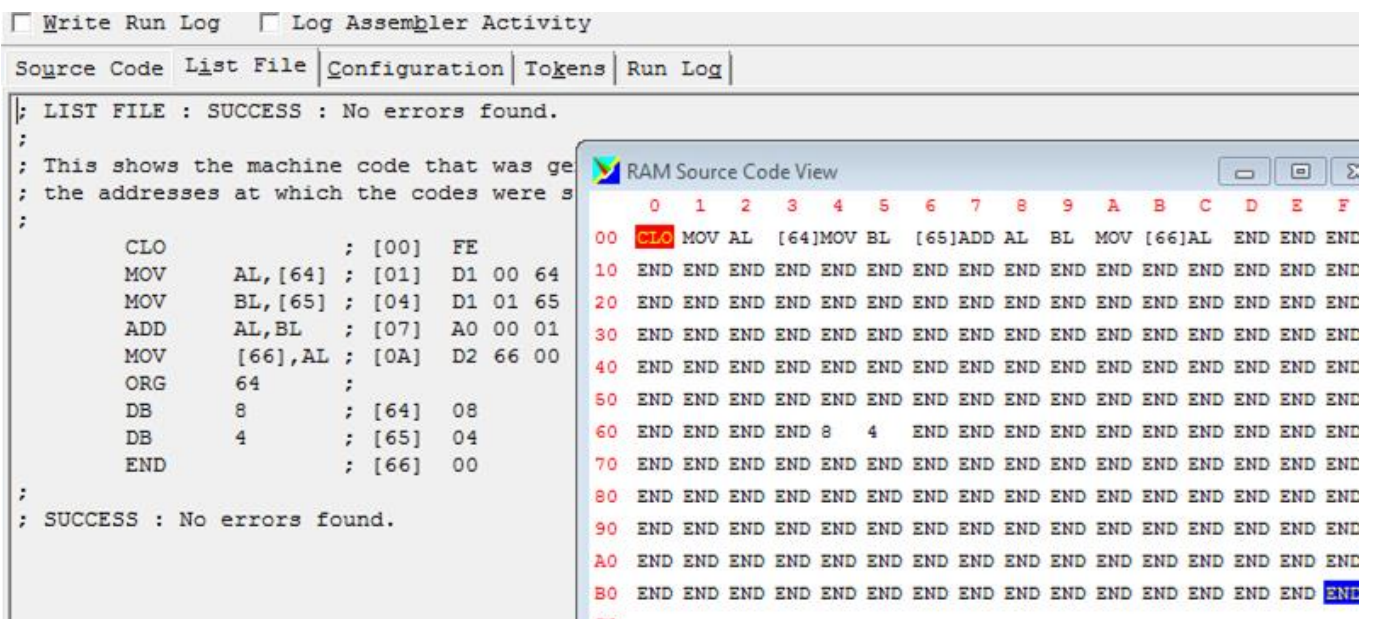
The screenshot shows the assembler interface with the following components:

- Source Code:** A list of instructions and their addresses. The instructions are: CLO, MOV AL, 4, MOV BL, 8, DIV AL, BL, and END. The addresses are: [00], [01], [04], [07], and [0A].
- RAM Source Code View:** A window showing the machine code in hexadecimal. The first four instructions are: CLO MOV AL 4 MOV BL 8 DIV AL BL, followed by several END instructions.



L'exécution fait toujours la même longueur donc l'IP ne prend la même valeur que précédemment.  
La division ne donne pas les réels : 4/8 donne donc 0.

## Exercice 1-2



On remarque que les résultats sont mis en base 16 dans la RAM. Les résultats sont ceux vu en TD.

## Exercise 1-3

Source Code

List File

C

```

CLO
MOV AL, [64]
MOV BL, [65]
CMP AL, BL
JNS SI_AINFB
SI_AINFB:
INC AL
MOV [64], AL
JMP FINSI
SI_ANOTINFB:
INC BL
MOV[65], BL
FINSI:
ORG 64
DB 8
DB 6
END

```

RAM Source Code View

0 1 2 3 4 5 6 7 8 9 A B C D E F

00 CLO MOV AL [64]MOV BL [65]CMP AL BL JNS SI\_AINC AL MOV [64]
10 AL JMP FININC BL MOV [65]BL END END END END END END END
20 END END END END END END END END END END END END END END
30 END END END END END END END END END END END END END END
40 END END END END END END END END END END END END END END
50 END END END END END END END END END END END END END END
60 END END END END 8 6 END END END END END END END END END
70 END END END END END END END END END END END END END END
80 END END END END END END END END END END END END END END
90 END END END END END END END END END END END END END END
A0 END END END END END END END END END END END END END END
B0 END END END END END END END END END END END END END END
C0
D0

RAM Source Code View

0 1 2 3 4 5 6 7 8 9 A B C D E F

00 CLO MOV AL [64]MOV BL [65]CMP AL BL JNS SI\_AINC AL MOV [64]
10 AL JMP FININC BL MOV [65]BL END END END END END END END
20 END END END END END END END END END END END END END END
30 END END END END END END END END END END END END END END
40 END END END END END END END END END END END END END END
50 END END END END END END END END END END END END END END
60 END END END END 8 07 END END END END END END END END END
70 END END END END END END END END END END END END END END
80 END END END END END END END END END END END END END END
90 END END END END END END END END END END END END END END
A0 END END END END END END END END END END END END END END

AL 00001000 08 +008

IP 00011000 18 +024

BL 00000111 07 +007

SP 10111111 BF -065

CL 00000000 00 +000

SR 00000000 00 +000

DL 00000000 00 +000

ISOZ

SR 00000000

## Exercise 1-4