

# Cryptography

## Lab no. 2 – till 26 III 2017

[You can get max 10 points for this list]

RC4 encryption scheme uses two algorithms  $\text{KSA}(N, T)$  which takes a secret key  $K$  as an input, and outputs an array (permutation)  $S$  of size  $N$ . Algorithm  $\text{PRGA}(N)$  outputs pseudo-random bytes from  $S$ .

**Algorithm 1:**  $\text{KSA}_k(N, T) - k[i]$  returns  $i$ th BYTE of the key.  $L$  is the length of the key in bytes.

```
1 for  $i$  from 0 to  $N - 1$  do
2    $S[i] := i$ 
3 end
4  $j := 0$ ;
5 for  $i$  from 0 to  $T$  do
6    $j := (j + S[i] + k[i \bmod L]) \bmod N$ ;
7   swap( $S[i \bmod N], S[j \bmod N]$ );
8 end
```

**Algorithm 2:**  $\text{PRGA}_S(N)$

```
1  $i := 0$ ;
2  $j := 0$ ;
3 while GeneratingOutput do
4    $i := (i + 1) \bmod N$ ;
5    $j := (j + S[i]) \bmod N$ ;
6   swap( $S[i], S[j]$ );
7    $K := S[(S[i] + S[j]) \bmod N]$ ;
8   output  $K$ 
9 end
```

**Algorithm 3:**  $\text{KSA-RS}_k(N, T) - k[i]$  returns  $i$ th BIT of key  $k$ .  $L$  denotes length of the key in bits.

```
1 for  $i$  from 0 to  $N - 1$  do
2    $S[i] := i$ 
3 end
4 for  $r$  from 0 to  $T$  do
5    $Top = \text{array}()$ ;
6    $Bottom = \text{array}()$ ;
7   for  $i$  from 0 to  $N$  do
8     if  $key[rN + i \bmod L] == 0$  then
9        $Top.push(i)$ 
10    else
11       $Bottom.push(i)$ 
12    end
13  end
14  foreach  $Top$  as  $i \Rightarrow v$  do
15     $newS[i] := S[v]$ 
16  end
17  foreach  $Bottom$  as  $i \Rightarrow v$  do
18     $newS[Top.size + i] := S[v]$ 
19  end
20   $S := newS$ ;
21 end
```

Original RC4 =  $\text{RC4}(N, T) = \text{RC4}(256, 256)$  is: RC4-RS( $N, T$ ) is:

1.  $S := \text{KSA}_k(N, N)$

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2.  $outputStream \leftarrow \text{PRGA}_S(N)$

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Function  $\text{RC4-drop}[D]$  drops first  $D$  bytes of PRGA output.

Function  $\text{RC4-SST}$  repeats the loop of KSA (lines 5-8 as long as SST marking is done, see: <https://eprint.iacr.org/2016/1049.pdf> – it is StoppingRuleKLZ from page 15).

**Assignment 1 (10 pts.) – security track** Implement above algorithms and test the quality of generated random bits depending on the parameters:

1.  $\text{RC4}(16, 16)$
2.  $\text{RC4}(16, 16)\text{-drop}[48]$
3.  $\text{RC4}(16, 64)$

Repeat experiments for different key lengths: 8, 16, 24, 32, 40 and 64 bits.

For statistical tests use any of: TestU01, DieHard, Dieharder.

**Assignment 2 (10 pts.) – algorithmic track** Implement above algorithms and test the quality of generated random bits depending on the parameters:

1. RC4(16, 16)
2. RC4-RS(16, 64)
3. RC4-RS(16, 92)
4. RC4-SST(16)

Repeat experiments for different key lengths: 8, 16, 24, 32, 40 and 64 bits.

For statistical tests use any of: TestU01, DieHard, Dieharder.

**Assignment 3 (10 pts.) – algorithmic track** *RandomWalker*( $N, d, l$ ) is defined for the following parameters:

$N$  - number of vertices of the directed (multi)graph  $V = \{0, 1, \dots, N - 1\}$  where  $N = 2^n$ ,

$d$  - the out-degree of each vertex,

$l$  - the number of steps a pseudo-random walk performs between times when it announces where it is.

Let  $S_j$  denote a permutation of  $N$  elements (for  $j = 0, \dots, d - 1$ ). Then the set of (directed-, multi-) edges is defined as:

$$E = \{(i, S_j(i)) : i = 0, \dots, N - 1; j = 0, \dots, d - 1\}$$

The random walk starts at  $v_0 = 0$  and performs  $l$  steps by walking at step  $k$  from a vertex  $v_k$  to the vertex  $v_{k+1} = S_{k \bmod d}(v_k)$ . The output of the generator is a sequence of  $n$ -bit numbers:  $v_l, v_{2l}, v_{3l}, \dots$

For  $n = 4, 6, 8$  ( $N = 16, 64, 256$ ),  $d = n, 2n$  and  $l = n, 2n, 3n$  run statistical tests (TestU01 or Diehard or Dieharder) of the generated output.

Initialize *RandomWalker*( $N, d, l$ ) with  $d$  instances of *RC4 – SST*( $N$ ).

Consider the following extensions:

- at each step the permutation is changed (you may use  $PRGA_{S_j}(N)$  from RC4),
- think about using an additional  $(d+1)$  instance of RC4 which would be used to decide which edge to choose *i.e.*, if the  $k$ th byte of the  $d + 1$ 's instance is equal to  $b_k$  then the walk goes from  $v_k$  to  $v_{k+1} = S_{b_k}(v_k)$ .