SPM Project: BSP

Lorenzo Bellomo, 531423

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1 Introduction

In this report the *Bulk Synchronous pattern* (BSP) will be analysed both from a theoretical viewpoint and from a practical one.
The outline of the report will be the following:

- Section 2 (Parallel Architecture Design)

- Section 3 (Performance Modeling)

- Section 4 (Implementation Structure and Details)

- Section 5 (Experimental Evaluation)

2 Parallel Architecture Design

3 Performance Modeling

4 Implementation Structure and Details

5 Experimental Evaluation