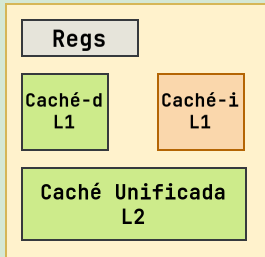
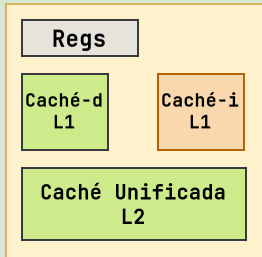


Core 0



...

Core 3



Caché Unificada L3 (Se comparte entre todos los cores)

Memoria Principal