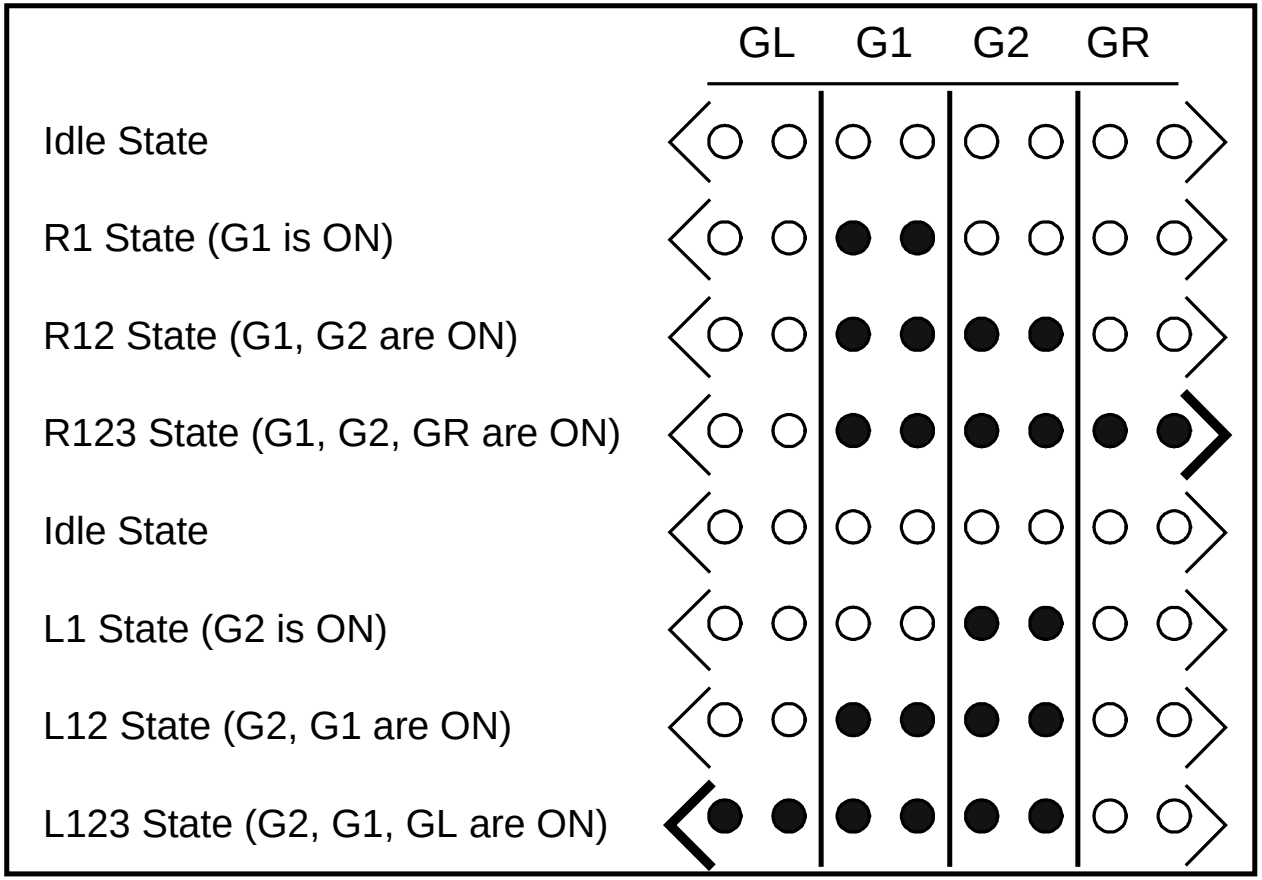
EE201L Detour lab (original)



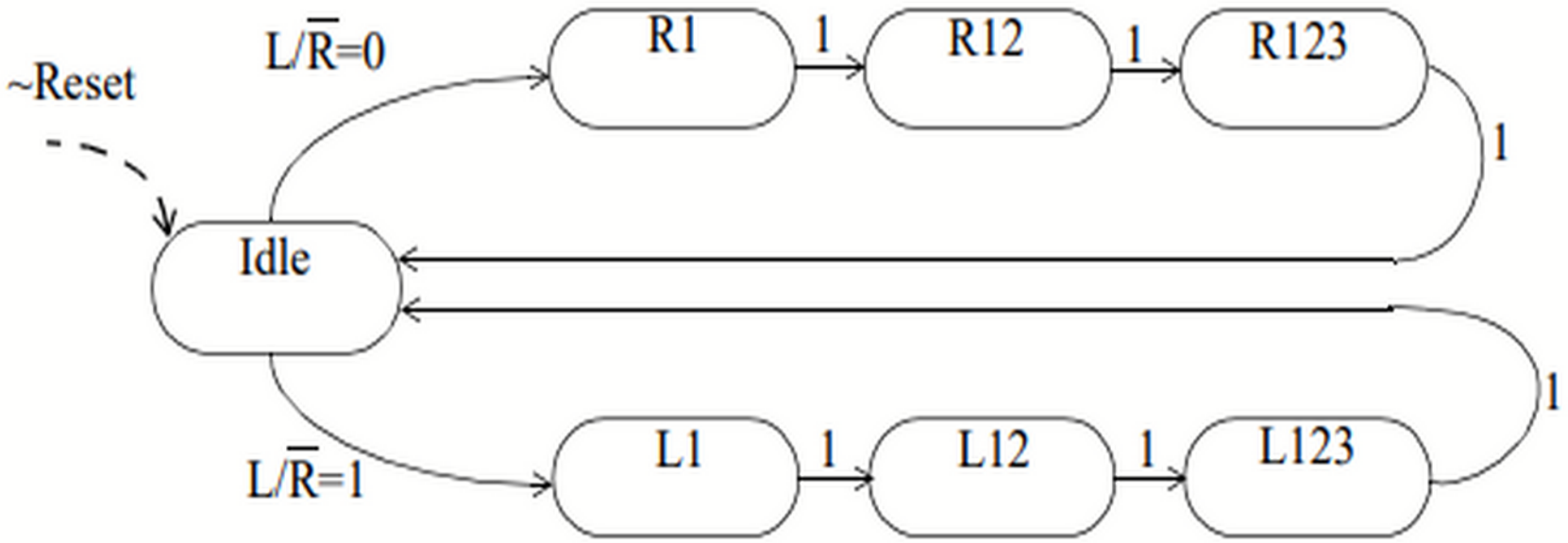
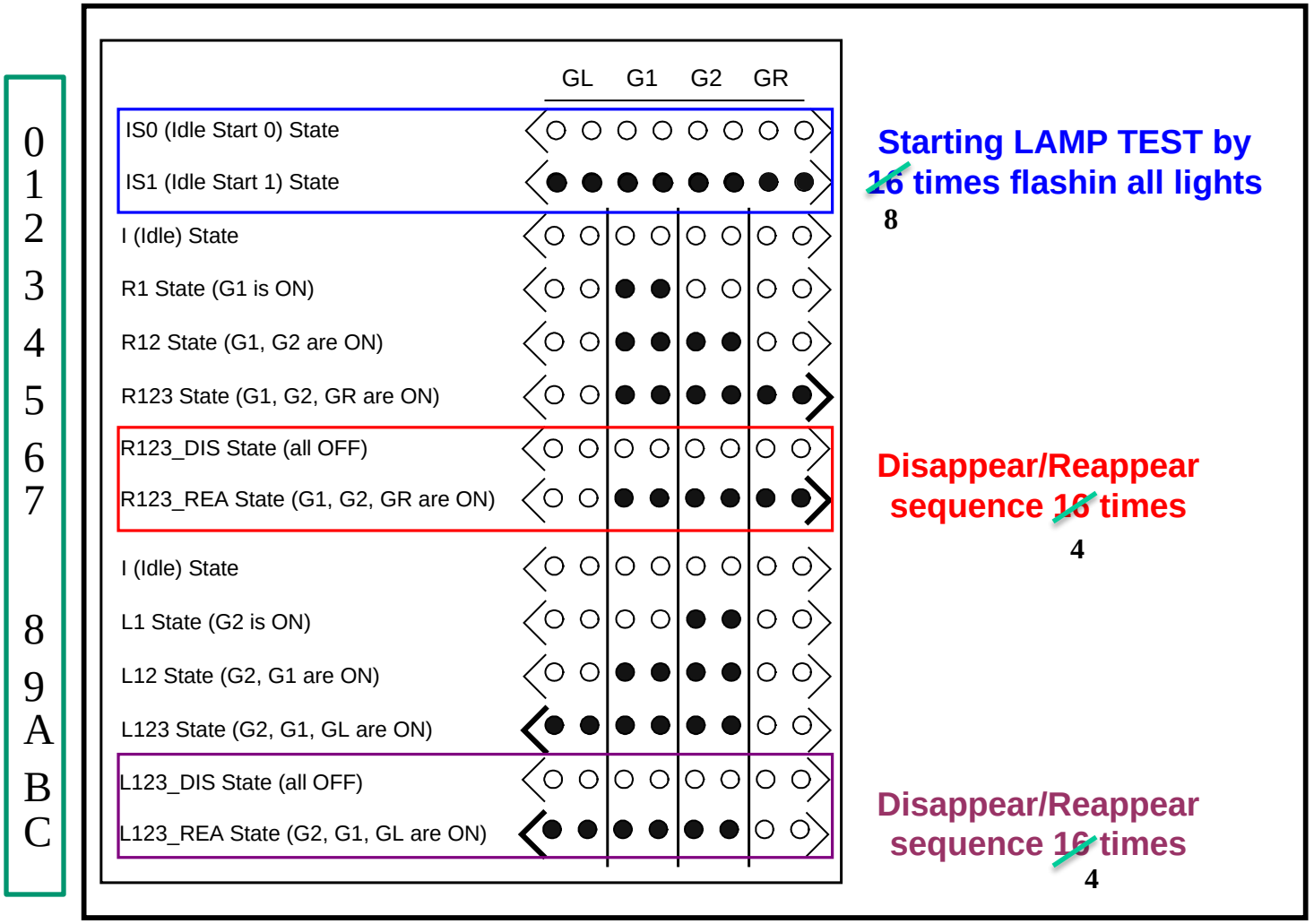
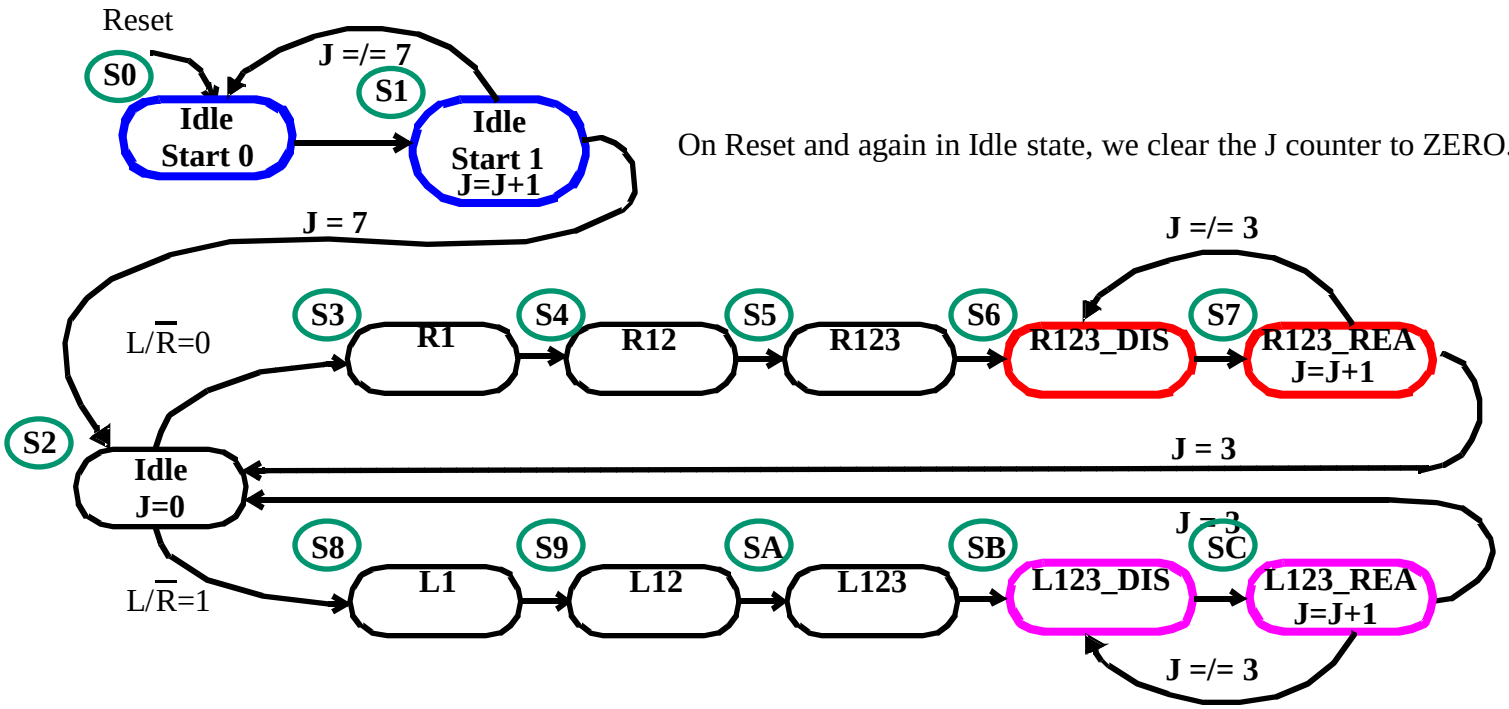


Fig 2: State diagram for the Detour Signal design

EE201L Detour lab (modified)

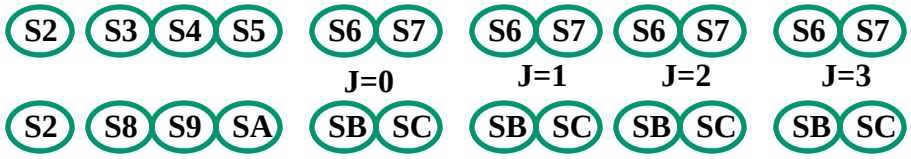


Modified Detour lab state diagram



On Reset and again in Idle state, we clear the J counter to ZERO.

During Reset it remains in **S0** state.

After Reset, it does **S0** **S1** eight times.

Then it does one of the two depending upon $\overline{L/R}$.