Grade Sheet for Lab #1

Fall 2016.

Group Number: _____

Group Member Name: _____JAEHO LEE_____ Student Number: 260663789

Group Member Name: _ANDREW LOWTHER_ Student Number: 260

260 578357

Marks		
2	1.	Schematic diagram for the 6-bit comparator
2	2.	VHDL file for the 6-bit comparator
2	3.	Initial partial simulation results for the 6-bit comparator
2	4.	Complete simulation results for the 6-bit comparator
2	5.	VHDL description for the 16:4 encoder circuit
2	6.	Simulation Testbench VHDL for the 16:4 encoder circuit
2	7.	Simulation results for the 16:4 encoder circuit

TA Signatures

Each part should be demonstrated to one of the TAs who will then give a grade and sign the grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if everything is done correctly. A grade of 1 will be given if there are significant problems, but an attempt was made. A grade of 0 will be given for parts that were not done at all, or for which there is no TA signature.