# Day #3

# Team-2

# TouchLess Elevator KiCad Design

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## Design Rules Checker

☑ Refill all zones before performing DRC   ☐ Test for parity between PCB and schematic
☐ Report all errors for each track

**Violations (3)** | Unconnected Items (0) | Schematic Parity (not run) | Ignored Tests (4)

⌄ **Warning: Silkscreen clipped by solder mask**

　Footprint Text 'ADC_VREF' of U1

⌄ **Warning: Silkscreen overlap**

　Segment on F.Silkscreen

　Footprint Text 'ADC_VREF' of U1

⌄ **Warning: Silkscreen overlap**

　Footprint Text 'ADC_VREF' of U1

　Footprint Text 'GP28' of U1

Click on items to highlight them on the board.

Show: ☐ All    ☑ Errors  **0**    ☑ Warnings  **3**    ☐ Exclusions    Save...

Delete Marker | Delete All Markers | Run DRC | Close