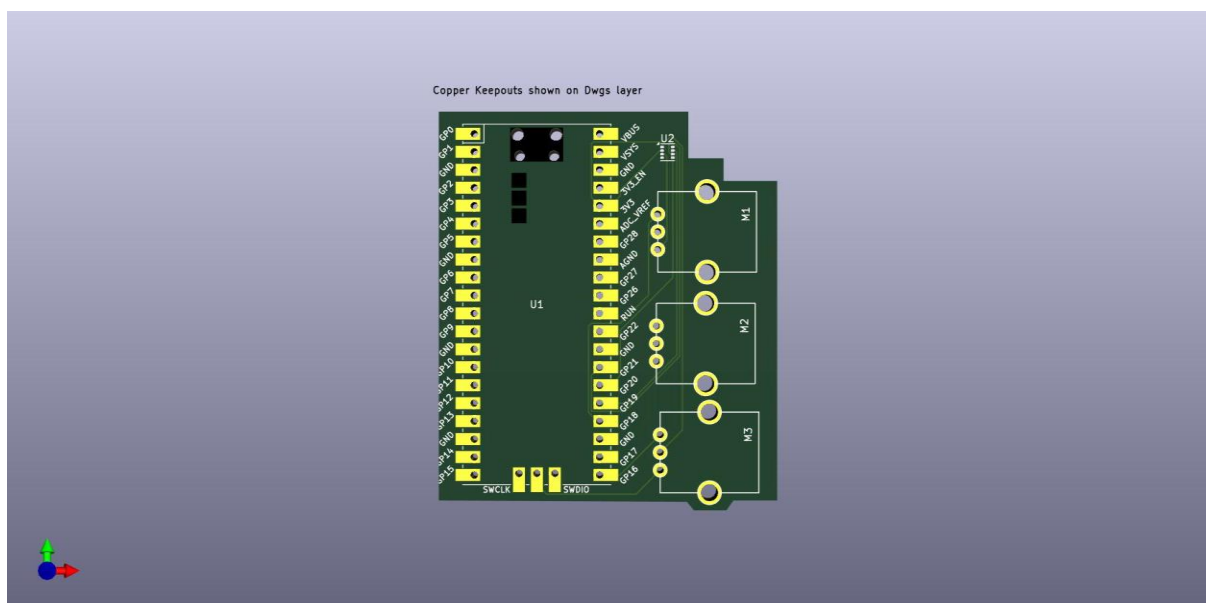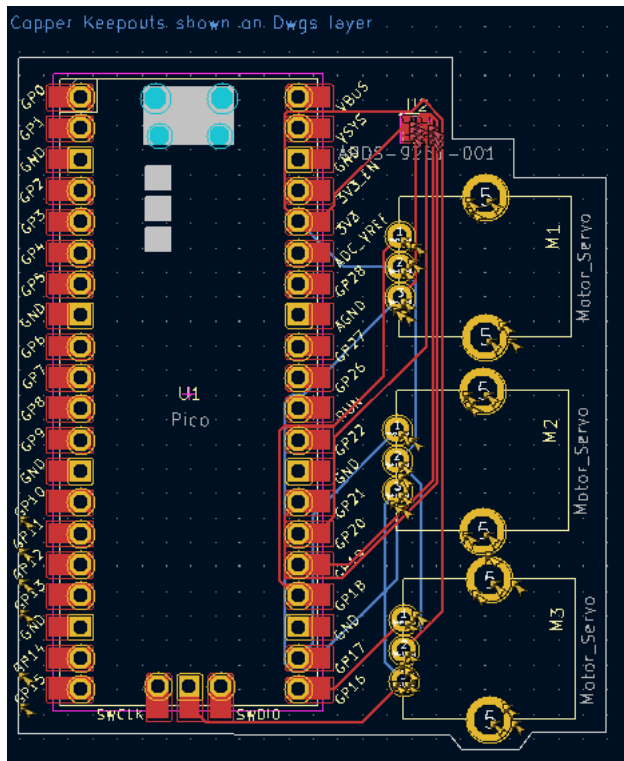# Task-2

Group-2

Ankush Choudhary

Rasberry Pi Pico Conveyor belt using Color Sensor and Servos

Design Rules Checker

☑ Refill all zones before performing DRC   ☐ Test for parity between PCB and schematic
☐ Report all errors for each track

Violations (51) Unconnected Items (0) Schematic Parity (not run) Ignored Tests (4)

> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**
  Pad 1 [unconnected-(U2-NC-Pad1)] of U2 on F.Cu
  Pad 2 [Net-(M1-+)] of U2 on F.Cu
> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**
  Pad 2 [Net-(M1-+)] of U2 on F.Cu
  Pad 3 [Net-(M1--)] of U2 on F.Cu
> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**
  Pad 3 [Net-(M1--)] of U2 on F.Cu
  Pad 4 [unconnected-(U2-NC-Pad4)] of U2 on F.Cu | Click on items to highlight them on the boar
> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**
  Pad 5 [Net-(U1-GPIO20)] of U2 on F.Cu
  Pad 6 [Net-(U1-GPIO21)] of U2 on F.Cu
> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**
  Pad 6 [Net-(U1-GPIO21)] of U2 on F.Cu
  Pad 7 [Net-(U1-GPIO19)] of U2 on F.Cu
> **Error: Clearance violation (netclass 'Default' clearance 0.2000 mm; actual 0.1500 mm)**

Show: ☐ All   ☑ Errors **6**   ☑ Warnings **45**   ☐ Exclusions   Save...

Delete Marker   Delete All Markers   Run DRC   Close