

SystemView by ELANIX

SYSTEM SUMMARY

Date: 11-may-20

File name: C:\Program Files (x86)\SystemView\Digital Comm\ch_03\raised_cosine_pulse.svu

Title:

Created by: Bob Stewart

System Time:0 - 1,066e-2 sec, dT=1,042e-5 sec, Sample Rate=9,6e+4 Hz, Samples=1024, Loops=1

<u>Token</u>	<u>Attribute</u>	<u>Type</u>	<u>Parameters</u>
0	Sink	SystemView	Input from t9 Output Port 0
1	Operator	Linear Sys	Raised Cosine (Parametric) FIR, Symbol Rate = 25,e-3 Rel Hz, Decimate By 1, Quant Bits = None, Roll-Off Factor = 1, Taps = 301, Init Cndtn = 0, DSP Mode Disabled, FPGA Aware = True
2	Sink	SystemView	Input from t3 Output Port 0
3	Operator	Smpl Delay	Delay = 150 samples, Attribute = Passive, Initial Condition = 0 v, Fill Last Register, Output 0 = Delay t2 , Output 1 = Delay - dT
4	MetaSystem	Custom_1	--
5 (m4)	Source	PN Seq	Amp = 1 v, Offset = 0 v, Rate = 2,4e+3 Hz, Levels = 2, Phase = 0 deg
6 (m4)	Source	Pulse Train	Amp = 1 v, Freq = 2,4e+3 Hz, PulseW = 10,4167e-6 sec, Offset = 0 v, Phase = 0 deg
7 (m4)	Multiplier	- -	- -
8 (m4)	Meta I/O	Meta Out	--
9	Operator	Gain	Gain = 32 dB, Gain Units = dB Power