(a) Worst Row Ordering

A→B→C→D	+	D_1	C_1	B_1	A_1	=	**D_1**	**C_1**	**B_1**	**A_1**
B→C→D		D_2	C_2	B_2	A_2		**D_2**	**C_2**	**B_2**	A_2
C→D		D_3	C_3	B_3	A_3		**D_3**	**C_3**	B_3	A_3
D		D_4	C_4	B_4	A_4		**D_4**	C_4	B_4	A_4

(b) Best Row Ordering

A→B→C→D	+	A_1	B_1	C_1	D_1	=	**A_1→B_1→C_1→D_1**			
B→C→D		A_2	B_2	C_2	D_2		A_2	B_2	C_2	D_2
C→D		A_3	B_3	C_3	D_3		A_3	B_3	C_3	D_3
D		A_4	B_4	C_4	D_4		A_4	B_4	C_4	D_4

Sequence List		Worst-Case Bit Set					Required Bits			
A→B→C→D	+	A_1	B_1	C_1	D_1	=	**A_1→B_1→C_1→D_1**			
D→C→B→A		A_2	B_2	C_2	D_2		A_2	B_2	**C_2**	D_2
		A_3	B_3	C_3	D_3		A_3	**B_3**	C_3	D_3
		A_4	B_4	C_4	D_4		**A_4**	B_4	C_4	D_4