Luca Dalmasso

Embedded Systems Engineer · ASIC Design & Verification

Cork, Ireland

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Work Experience

Qualcomm *Cork, Ireland*

GPU Design Verification Engineer *February 2023 - Present*

- Working remotely with the GPU Power DV Team based in San Diego (USA)
- Maintaining and improving existing test cases and verification components into new projects
- Presenting verification strategies to validate the latest features in the design
- Debugging design issues and providing detailed documentation and reports
- Collaborating with cross-functional teams to get closure on all assigned tasks and achieve the verification milestones
- Hands-on experience with verification of multi-million gates ASICs
- **Technical Skills:** SystemVerilog (IEEE 1800), Universal Verification Methodology (UVM), Digital IC Verification.

Sistematica S.p.A *Città di Castello (PG), IT*

Programmer Analyst *April 2020 - July 2020*

- Development of a tool, written in Python, used to interface a Raspberry Pi with the Electronic Control Unit of an electric vehicle prototype to monitor and inject traffic in the CAN Network
- **Technical Skills:** Testing.

I.S.C.S srls *Turin, IT*

Software Programmer - Internship *June 2019 - Sept 2019*

- Development, testing and release of an iOS and Android application for wearable devices
- I mainly contributed to the backend code for the acquisition and processing of biometric data
- **Technical Skills:** Software Engineering.

Education

Politecnico di Torino *Turin, IT*

M.Sc. degree in Computer Engineering *September 2020 - October 2022*

- Design of complex digital systems using hardware description languages
- Study of the synthesis and optimisation techniques to improve the product performances
- Analysis of the correctness and reliability of the final product
- **Technical Skills:** Digital IC Design, Application Specific Integrated Circuits (ASIC), Design For Testability (DFT)

Politecnico di Torino *Turin, IT*

B.Sc. degree in Computer Engineering *September 2017 - March 2020*

- **Technical Skills:** C, Computer Science, Electronics

Projects

MC2101: A RISC-V-based Microcontroller for Security Assessment and Training *Politecnico di Torino, IT*

Master's degree thesis *2022*

- RTL design of the bus infrastructure, memory and peripherals of a RISC-V microcontroller
- Software design of system libraries and test programs to evaluate the system on FPGA.

Design of a LBIST circuit for testing the RI5CY processor *Politecnico di Torino, IT*

Testing and Fault Tolerance course project *2022*

- Design of a synthesizable Logic build-in self-test (LBIST) for a RISC-V processor for power-on testing.
- Adoption of Test-per-Scan methodology optimised for area, timing and complexity reduction for testing big sequential circuits.

Design of a 4-Stage pipelined RISC processor *Politecnico di Torino, IT*

Microelectronic Systems course project *2021*

- Implemented complex arithmetic blocks from Pentium 4 and UltraSPARC T2 processors, Hazard Detection features and Clock Gating technique.

Languages

English Professional proficiency

Italian Native proficiency

Open to relocate.