

Luca Dalmasso

COMPUTER ENGINEER · ASIC DESIGN & VERIFICATION

Cork, Ireland

+353 871809965 | luca.dalmasso.97@outlook.it | github.com/Luca-Dalmasso | linkedin.com/in/luca-dalmasso-997b70255

Work Experience

Qualcomm

Cork, Ireland

GPU Design Verification Engineer

February 2023 - Present

- Worked remotely with the GPU Power DV Team based in San Diego (USA)
- Maintained and improved existing test cases and verification IPs into new projects
- Presented verification strategies to validate the latest features in the design
- Identified and documented RTL & UPF issues
- Collaborated with cross-functional teams located in different geographical locations to get closure of the verification milestones
- Hands-on experience with verification of complex multi-million gates logic partitioned in multiple power domains
- **Technical Skills:** SystemVerilog (IEEE 1800), Universal Verification Methodology (UVM), Digital IC Verification.

Sistematica S.p.A

Città di Castello (PG), IT

Programmer Analyst

April 2020 - July 2020

- Development of a tool, written in Python, used to interface a Raspberry Pi with the Electronic Control Unit of an electric vehicle prototype to monitor and inject traffic in the CAN Network
- **Technical Skills:** Testing.

I.S.C.S srls

Turin, IT

Software Programmer - Internship

June 2019 - Sept 2019

- Development, testing and release of an iOS and Android application for wearable devices
- I mainly contributed to the backend code for the acquisition and processing of biometric data
- **Technical Skills:** Software Engineering.

Education

Politecnico di Torino

Turin, IT

M.Sc. degree in Computer Engineering

September 2020 - October 2022

- Design of complex digital systems using hardware description languages
- Study of the synthesis and optimisation techniques to improve the product performances
- Analysis of the correctness and reliability of the final product
- **Technical Skills:** Digital IC Design, Application Specific Integrated Circuits (ASIC), Design For Testability (DFT)

Politecnico di Torino

Turin, IT

B.Sc. degree in Computer Engineering

September 2017 - March 2020

- Solid background on maths, physics, computer science & electronics
- In-depth aspects of information processing systems and computer architecture
- **Technical Skills:** C, Computer Science, Electronics

Projects

MC2101: A RISC-V-based Microcontroller for Security Assessment and Training

Politecnico di Torino, IT

M.Sc Thesis - MC2101

2022

- RTL design of the bus infrastructure, memory and peripherals of a RISC-V microcontroller
- Coded the firmware and the test suite to synthesise and validate the design on FPGA

Design of a LBIST circuit for stuck-at fault testing

Politecnico di Torino, IT

RISC-V Core

2022

- RTL design of a DFT LBIST wrapper for a RISC-V core
- Adoption of Test-per-Scan methodology optimised for area, timing and complexity reduction for testing big sequential circuits

RTL design & synthesis of a 4-stage pipelined RISC DLX processor

Politecnico di Torino, IT

DLX

2021

- Implemented complex arithmetic blocks from Pentium 4 and UltraSPARC T2 processors, Hazard Detection features and Clock Gating technique

Languages

English Professional proficiency

Italian Native proficiency