

REVISION HISTORY

DATE:	DESCRIPTION	REVISION
July. 2012	Created	1.0
April. 2013	Remove test jumper JP2, JP5. Change 2-pin header JP6 to 3-pin header. Remove JP14 and related JP15, JP16 and JP17.	1.1

Page 1: Revision History

Page 2: KSZ8795 Switch

Page 3: Transformer & RJ45 Ports

Page 4: Port 5 SW5-GMII/RGMII/MII/RMII Interfaces

Page 5: Power & Reset

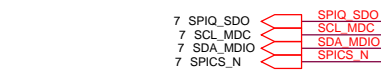
Page 6: USB Interface

KSZ8795 Socket & Evaluation Board

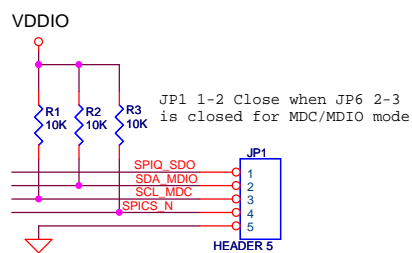


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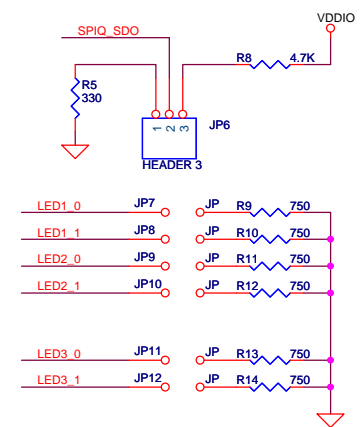
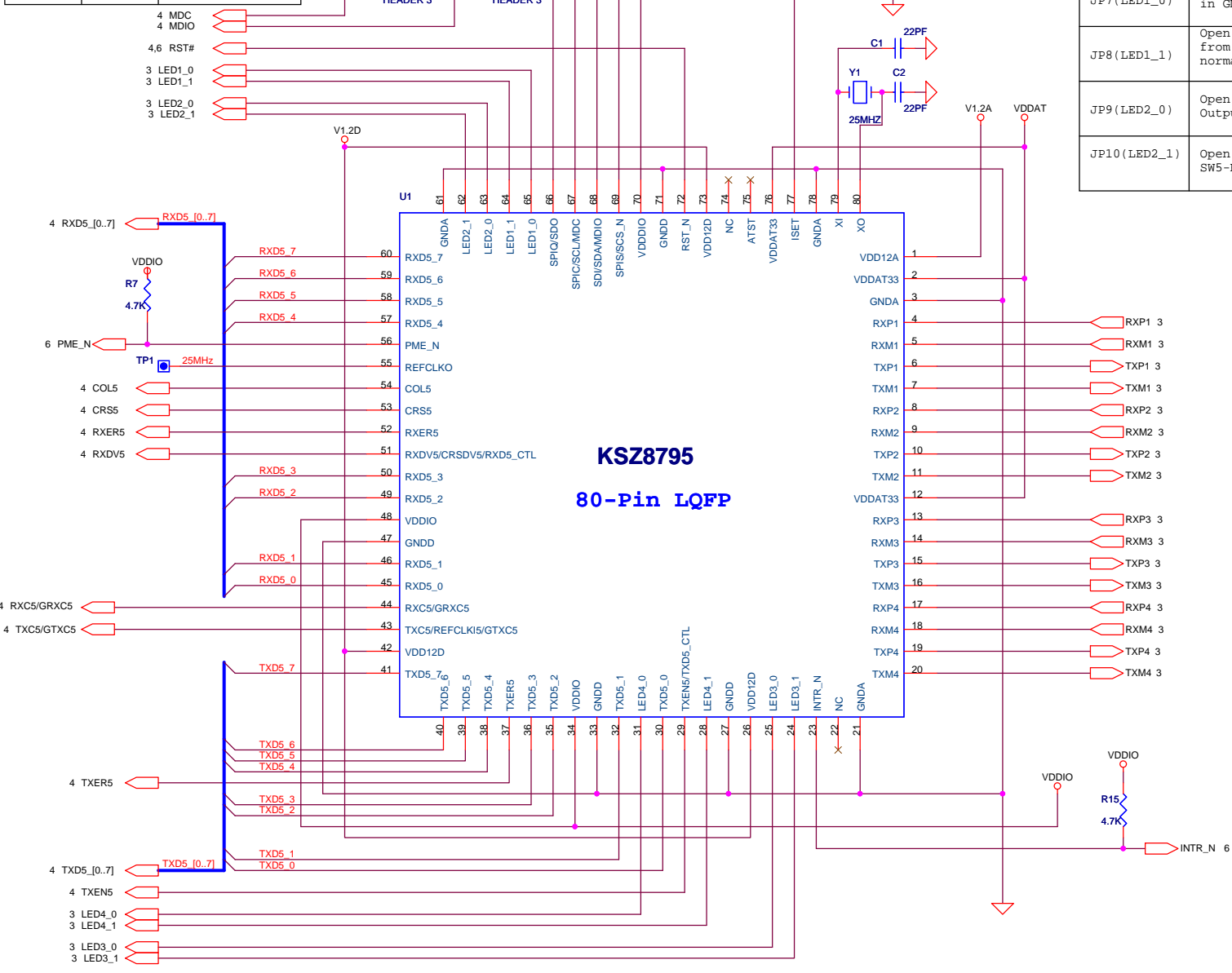
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KSZ8795 EVAL BOARD		
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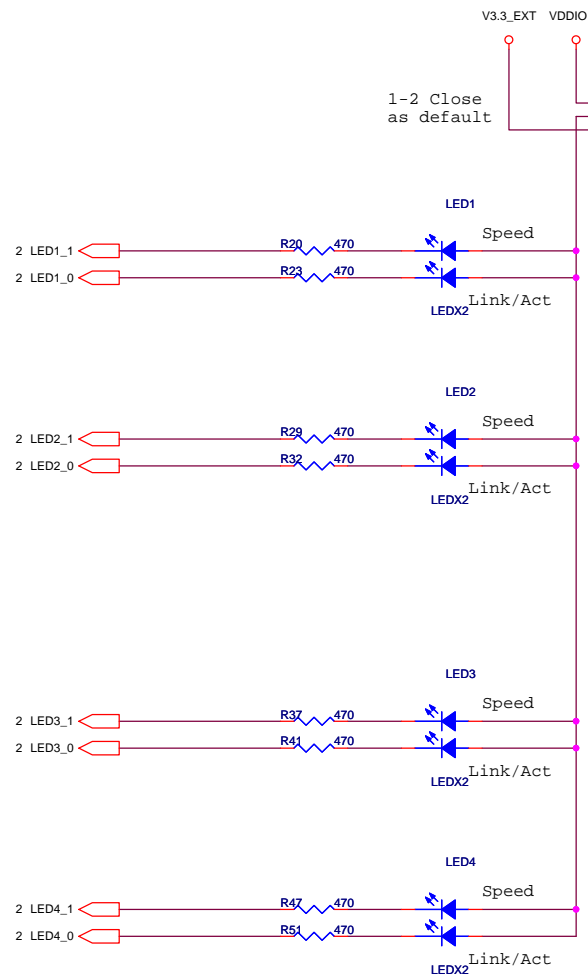
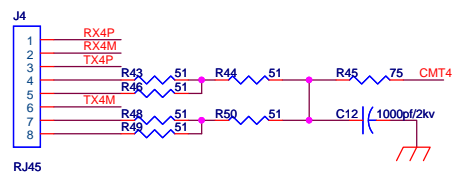
JP3	JP4	Description
1-2 Close	1-2 Close	8795 SPI or MIIM (Default)
2-3 Close	2-3 Close	External MIIM



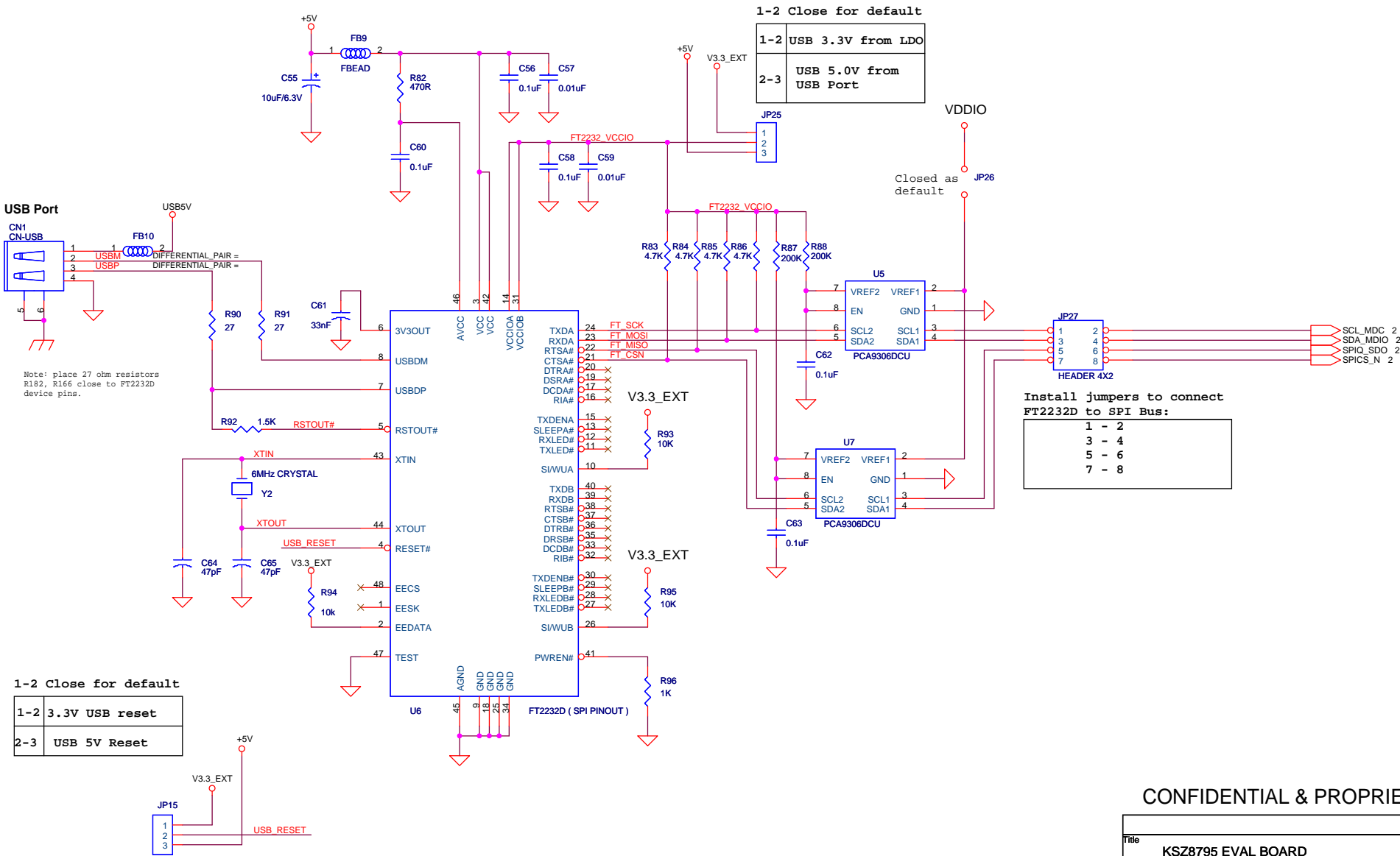
Jumpers	Open (Default)	Description
JP6(SPIQ_SDO)	1-2 Close: SPI Slave Mode	2-3 Close: MDC/MDIO Mode and JP1 1-2 is Closed too
JP7(LED1_0)	Open: Gbps mode on port 5 in GMII/RGMII mode	Close: 10/100Mbps on port 5 in GMII/RGMII mode
JP8(LED1_1)	Open: Device clock source from OSC 25MHz in RMII normal mode	Close: Device clock source from SW5-RMII TXC5 in RMII normal mode
JP9(LED2_0)	Open: REFCLKO pin 25MHz Output Enable	Close: REFCLKO pin 25 MHz output is disabled
JP10(LED2_1)	Open: SW5-MII in MAC mode, SW5-RMII in Clock mode	Close: SW5-MII in PHY mode, SW5-RMII in Normal mode



JP12	JP11	SW5-GMAC Mode
Close	Close	MII Mode
Close	Open	RMII
Open	Close	GMII
Open	Open	RGMII (Default)



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	USB to SPI/MDIO Interface	1.1
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