QR Decomposition on ARM920T

By Lucas Antonsen and Ty Ellison

Outline

Introduction to QR Decomposition

Design Challenges

Work Plan

Optimizations

Assembly

Numerical Results

Conclusion

What is QR Decomposition?

Classical Gram-Schmidt Orthogonalization

$$A = [e_1|e_2|\cdots|e_n] egin{bmatrix} v_1\cdot e_1 & v_2\cdot e_1 & \cdots & v_n\cdot e_1 \ 0 & v_2\cdot e_2 & \cdots & v_n\cdot e_2 \ dots & dots & \ddots & dots \ 0 & 0 & \cdots & v_n\cdot e_n \end{bmatrix} = QR$$

Classical Gram-Schmidt Orthogonalization

$$\begin{aligned} \mathbf{u}_1 &= \mathbf{v}_1, & \mathbf{e}_1 &= \frac{\mathbf{u}_1}{\|\mathbf{u}_1\|} \\ \mathbf{u}_2 &= \mathbf{v}_2 - \operatorname{proj}_{\mathbf{u}_1}(\mathbf{v}_2), & \mathbf{e}_2 &= \frac{\mathbf{u}_2}{\|\mathbf{u}_2\|} \\ \mathbf{u}_3 &= \mathbf{v}_3 - \operatorname{proj}_{\mathbf{u}_1}(\mathbf{v}_3) - \operatorname{proj}_{\mathbf{u}_2}(\mathbf{v}_3), & \mathbf{e}_3 &= \frac{\mathbf{u}_3}{\|\mathbf{u}_3\|} \\ \mathbf{u}_4 &= \mathbf{v}_4 - \operatorname{proj}_{\mathbf{u}_1}(\mathbf{v}_4) - \operatorname{proj}_{\mathbf{u}_2}(\mathbf{v}_4) - \operatorname{proj}_{\mathbf{u}_3}(\mathbf{v}_4), & \mathbf{e}_4 &= \frac{\mathbf{u}_4}{\|\mathbf{u}_4\|} \\ &\vdots & \vdots & \vdots & \\ \mathbf{u}_k &= \mathbf{v}_k - \sum_{j=1}^{k-1} \operatorname{proj}_{\mathbf{u}_j}(\mathbf{v}_k), & \mathbf{e}_k &= \frac{\mathbf{u}_k}{\|\mathbf{u}_k\|}. \end{aligned}$$

Modified Gram-Schmidt Orthogonalization

$$\mathbf{u}_k = \mathbf{v}_k - \mathrm{proj}_{\mathbf{u}_1}(\mathbf{v}_k) - \mathrm{proj}_{\mathbf{u}_2}(\mathbf{v}_k) - \cdots - \mathrm{proj}_{\mathbf{u}_{k-1}}(\mathbf{v}_k),$$

$$egin{aligned} \mathbf{u}_k^{(1)} &= \mathbf{v}_k - \operatorname{proj}_{\mathbf{u}_1}(\mathbf{v}_k), \ \mathbf{u}_k^{(2)} &= \mathbf{u}_k^{(1)} - \operatorname{proj}_{\mathbf{u}_2}\left(\mathbf{u}_k^{(1)}
ight), \ dots & dots \ \mathbf{u}_k^{(k-2)} &= \mathbf{u}_k^{(k-3)} - \operatorname{proj}_{\mathbf{u}_{k-2}}\left(\mathbf{u}_k^{(k-3)}
ight), \ \mathbf{u}_k^{(k-1)} &= \mathbf{u}_k^{(k-2)} - \operatorname{proj}_{\mathbf{u}_{k-1}}\left(\mathbf{u}_k^{(k-2)}
ight), \ \mathbf{e}_k &= rac{\mathbf{u}_k^{(k-1)}}{\left\|\mathbf{u}_k^{(k-1)}
ight\|} \end{aligned}$$

CGSO vs. MGSO

Classical Gram-Schmidt orthogonalization

```
Let A_j, j=1,...,n be linearly independent vectors.

for j=1,2,...,n

y=A_j

for i=1,2,...,j-1

r_{ij}=q_i^TA_j

y=y-r_{ij}q_i

end

r_{jj}=||y||_2

q_j=y/r_{jj}

end
```

Modified Gram-Schmidt orthogonalization

Let A_j , j = 1, ..., n be linearly independent vectors.

```
for j = 1, 2, ..., n

y = A_j

for i = 1, 2, ..., j - 1

r_{ij} = q_i^T y

y = y - r_{ij}q_i

end

r_{jj} = ||y||_2

q_j = y/r_{jj}

end
```

Images reproduced from the third edition of "Numerical Analysis," by Timothy Sauer.

Stability GSO vs MGSO

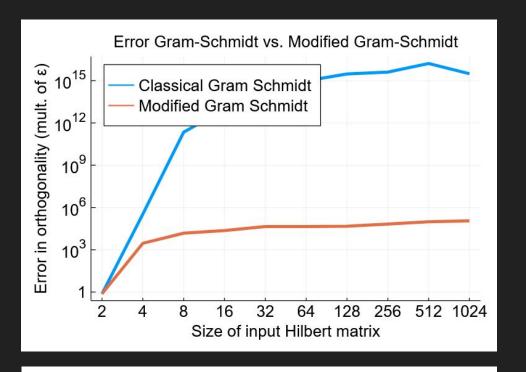


Figure reproduced from <u>Gram-Schmidt vs. Modified</u> <u>Gram-Schmidt | Laurent Hoeltgen</u>

Properties of QR Decomposition

$$Q^TQ = I$$

$$R = \left[egin{array}{ccccc} 1 & 2 & 3 & 4 \ 0 & 5 & 6 & 7 \ 0 & 0 & 8 & 9 \ 0 & 0 & 0 & 10 \end{array}
ight]$$

What is QR Decomposition used for?

- Linear least squares
- Basis for QR Eigenvalue algorithm
- Absolute value of a determinant of a square matrix
- Rank of matrix A

Design Challenge

- Limitations of sequential processors
- Efficient vector and matrix operations
- Fractional arithmetic without a dedicated floating point unit
- Mathematical routines without library support

Code Optimization Plan

- Loop unrolling
- Cache-oblivious matrix transpose
- Macros
- Operator strength reduction
- Inline functions
- Fixed Point Arithmetic
- Reduce branch and load/store instructions
- Dedicated square root instruction

Base Code

- 24 functions
- 20 / 24 functions are 1 or more for loops
- Most functions perform some kind of simple vector operation e.g. element wise addition and subtraction, dot product, square root, and absolute value
- More complex functions require the use of multiple vector routines such as the Euclidean Norm which performs a dot product and square root

Base Code

```
void QR(SIZE_T rows, SIZE_T cols, DATA_T At[cols][rows], NUM_T Q[rows][rows], NUM_T R[rows][cols]) {
    assert(rows >= cols);
    NUM_T y[rows], q[rows];
    NUM_T y_norm;
    SIZE_T i, j;
    for (j=0; j<cols; ++j) {
        vec_copy(rows, At[j], y);
        for (i=0; i<j; ++i) {
            numt_copy_col(rows, cols, i, Q, q);
            R[i][j] = vec_dot(rows, q, y);
            vec_mulc(rows, q, R[i][j]);
            vec_sub(rows, q, y);
        y_norm = l2_norm(rows, y);
        R[j][j] = y_norm;
        vec_divc(rows, y, y_norm);
        numt_set_col(rows, rows, j, y, Q);
```

Base Code

```
NUM_T sqr_rt(NUM_T x, NUM_T eps, NUM_T tol, size_t max_iter) {
    assert((int)x >= 0);
    NUM_T x0 = (NUM_T) closest_perfect_square(x, MAX_ITER);
    NUM_T xn, err;
    NUM_T f_n, f_prime;
    size_t i;
    for (i=0; i<max_iter; ++i) {</pre>
        f_n = (x0 * x0) - x;
        f_{prime} = 2. * x0;
        if (abs_val(f_prime) < eps) {</pre>
            xn = 0.;
            break;
        xn = x0 - (f_n / f_prime);
        err = abs_val(xn - x0);
        if (err <= tol) {
            break;
        x0 = xn;
    return xn;
```

For Loop Example

```
void numt_copy_col(SIZE_T rows, SIZE_T cols, SIZE_T target_col, NUM_T src[rows][cols], NUM_T dest[rows]) {
        SIZE_T i;
        for (i=0; i<rows; ++i) {
            dest[i] = src[i][target_col];
        }
}</pre>
```

```
NUM T sqr rt(NUM T x, NUM T eps, NUM T tol, size t max iter) {
        assert((int)x >= 0);
        NUM T x0 = (NUM T) closest perfect square(x, MAX ITER);
        NUM T xn, err;
        NUM_T f_n, f_prime;
        size t i;
        for (i=0; i<max_iter; ++i) {</pre>
               f n = (x0 * x0) - x;
                f prime = 2. * x0;
                if (abs val(f prime) < eps) {</pre>
                        xn = 0.;
                        break;
                }
                xn = x0 - (f n / f prime);
                err = abs val(xn - x0);
                if (err <= tol) {
                        break;
                x0 = xn;
        return xn;
```

```
int closest perfect square(DATA_T x, size_t max_iter) {
       int sq = 0, xn = 1;
        size_t i;
       for (i=0; i<max iter; ++i) {
                sq = xn * xn;
               if (sq > (int)x) {
                        break;
                xn += 1;
        }
        return xn;
```

Optimization 1: Loop Unrolling

```
void vec_sub(SIZE_T size, NUM_T v1[size], NUM_T v2[size]) {
        SIZE_T i;

        for (i=0; i<size; ++i) {
            v2[i] -= v1[i];
        }
}</pre>
```

```
//loop unrolling done
void vec_sub(SIZE_T size, NUM_T v1[size], NUM_T v2[size]) {
        SIZE_T i;
        for(i=0; i<size; i+=2){
                v2[i] -= v1[i];
                if(i+1 != size){
                        v2[i+1] -= v1[i+1];
```

```
//loop unrolling done
void numt_set_col(SIZE_T rows, SIZE_T cols, SIZE_T target_col, NUM_T v[rows], NUM_T A[rows][cols]) {
    SIZE_T i;
   for (i=0; i<rows; i+=2) {
       A[i][target_col] = (NUM_T) v[i];
                if(i+1 != rows){
                        A[i+1][target\_col] = (NUM_T) v[i+1];
                }
```

```
//loop unrolling done
void mat_set_col(SIZE_T rows, SIZE_T cols, SIZE_T target_col, DATA_T v[rows], DATA_T A[rows][cols]) {
        SIZE_T i;
        for(i=0; i<rows; i+=2){
                A[i][target_col] = v[i];
                if(i+1 != rows){
                        A[i+1][target_col] = v[i+1];
```

Optimization 2: Cache Oblivious Matrix Transpose

```
void transpose_m(SIZE_T rows, SIZE_T cols, DATA_T A[rows][cols], DATA_T B[cols][rows]) {
        SIZE_T i, j;
        for (i=0; i<cols; ++i) {
            for (j=0; j<rows; ++j) {
                B[i][j] = A[j][i];
            }
        }
}</pre>
```

```
//cache friendly matrix transpose
void transpose_m(SIZE_T rows, SIZE_T cols, DATA_T A[rows][cols], DATA_T B[cols][rows], SIZE_T start_i, SIZE_T end_i, SIZE_T start_j, SIZE_T end_j){
        SIZE T l i = end i - start i;
        SIZE_T l_j = end_j - start_j;
        SIZE_T i, j;
        if (1 i <= 2 && 1 j <= 2) {
               for (i = start_i; i < end_i; i++) {</pre>
                        for (j = start_j; j < end_j; j++) {</pre>
                                B[j][i] = A[i][j];
        } else if (l_i >= l_j) {
                transpose m(rows, cols, A, B, start i, start i + (l i / 2), start j, end j);
                transpose m(rows, cols, A, B, start i + (l i / 2), end i, start j, end j);
        } else {
                transpose_m(rows, cols, A, B, start_i, end_i, start_j, start_j + (1_j / 2));
                transpose m(rows, cols, A, B, start i, end i, start j + (1 j / 2), end j);
```

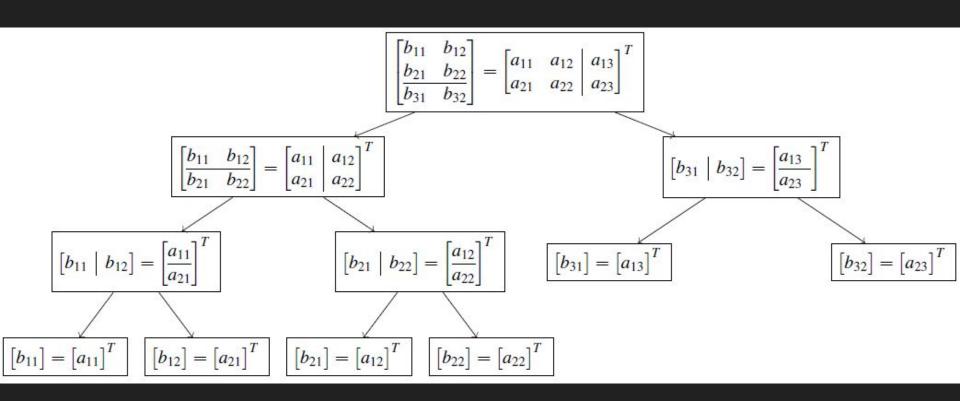
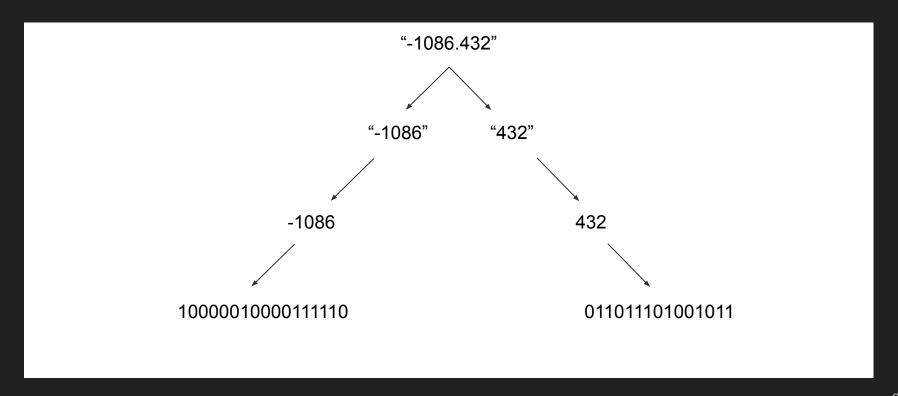


Image from SENG 475 Lecture Slides by Michael Adams

Optimization 3 - Fixed Point Arithmetic



Fixed Point Arithmetic - String to Fixed Width Integer

```
UFX_T str_to_fx(char *s, char *delim, FX_SIZE_T scale) {
    FX_SIZE_T sign = 0;
   FX_T num = 0;
   FX_SIZE_T digits = 0;
   UFX_T threshold = 0;
   char *p;
    char *tok = strtok(s, delim);
   num = (FX_T) strtol(tok, &p, 10);
   if (num < 0) {
       sign = 1;
       num -= 1;
       num = ~num;
   num = ((UFX_T) num << scale);
   tok = strtok(NULL, delim);
    if (tok != NULL) {
        digits = strnlen(tok, FX_MAX_DEC_CHARS);
        threshold = get_threshold(tok, digits);
        num |= fract_dec_to_bin((UFX_T) strtoul(tok, &p, 10), threshold);
   return (sign) ? FX_SIGN | num : num;
```

Fixed Point Arithmetic - Fixed Width Integer to String

```
void bin_fx_to_str(char *s, UFX_T x) {
    FX_SIZE_T max_chars = FX_MAX_BIN_CHARS - 1;
    UFX_T stack = 0;
    FX_SIZE_T i = 0;
    for (; i < FX_SIZE; ++i) {</pre>
        stack <<= 1;
        stack |= (x & 1);
        x >>= 1;
    i = 0;
    if (stack & 1) {
        s[0] = '-';
        i = 1;
        ++max_chars;
    s[FX_WHOLE_BITS + i + 1] = '.';
    for (; i < max_chars; ++i) {</pre>
        if ('.' == s[i]) continue;
        s[i] = '0' + (stack & 1);
        stack >>= 1;
    s[FX_MAX_BIN_CHARS] = '\0';
```

Fixed Point Arithmetic - Decimal to Binary fractions

```
inline UFX_T fract_dec_to_bin(UFX_T x, UFX_T threshold) {
    UFX_T fract_bin = 0;
    FX_SIZE_T i = 0;
    for (; i < FX_FRACT_BITS; ++i) {</pre>
        fract_bin <<= 1;</pre>
        if ((x <<= 1) > threshold) {
            fract_bin |= 1;
            x -= threshold;
    return fract_bin;
```

Assembly Comparison - vec_sub Base Version

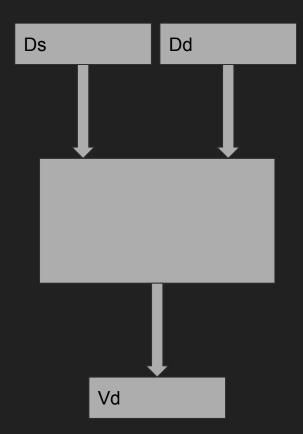
```
vec sub:
       @ Function supports interworking.
       @ args = 0, pretend = 0, frame = 0
       @ frame needed = 0, uses anonymous args = 0
             r0, #0
       cmp
       stmfd sp!, {r4, r5, r6, r7, r8, lr}
           r7, r1
       mov
       beg .L31
       sub r3, r0, #1
            r3, r3, asl #16
       mov
             r3, r3, 1sr #13
       mov
             r5, r2
       mov
             r6, r3, #8
       add
               r4, #0
       mov
```

```
.L30:
       add r1, r7, r4
       ldmia r1, {r2-r3}
       ldmia r5, {r0-r1}
       bl aeabi dsub
            r4, r4, #8
       add
            r4, r6
       CMD
       stmia r5!, {r0-r1}
              .L30
       bne
.L31:
       1dmfd sp!, {r4, r5, r6, r7, r8, 1r}
       bx
              lr
       .size vec sub, .-vec sub
       .align 2
       .global numt vec copy
              numt vec copy, %function
       .type
```

Assembly Comparison - vec_sub Loop Unrolled Version

```
vec_sub:
                                                                           ip, r8, r3
                                                                   add
                                                                          r6, r7, r3
                                                                   add
        @ Function supports interworking.
                                                                          r4, {r0-r1}
                                                                   stmia
        @ args = 0, pretend = 0, frame = 0
                                                                   beg
                                                                           .L34
        @ frame needed = 0, uses anonymous args = 0
                                                                          ip, {r2-r3}
                                                                   ldmia
        stmfd
                sp!, {r4, r5, r6, r7, r8, r9, s1, 1r}
                                                                          r6, {r0-r1}
                                                                   ldmia
                sl, r0, #0
        subs
                                                                   bl
                                                                           aeabi dsub
               r8, r1
        mov
                                                                          r6, {r0-r1}
                                                                   stmia
               r7, r2
        mov
                                                           .L34:
                .L36
        beg
                                                                          r3, r5, #2
                                                                   add
                r5, #0
        mov
                                                                          r3, r3, asl #16
                                                                   mov
.L35:
                                                                          r5, r3, lsr #16
                                                                   mov
               r4, r5, asl #3
        mov
                                                                          sl, r5
                                                                   cmp
              r1, r8, r4
        add
                                                                           .L35
                                                                   bhi
               r4, r7, r4
        add
                                                           .L36:
        ldmia
               r1, \{r2-r3\}
                                                                          sp!, {r4, r5, r6, r7, r8, r9, s1, 1r}
        ldmia
                r4, {r0-r1}
                                                                           1r
                                                                   bx
        bl
                aeabi dsub
                                                                   .size
                                                                          vec sub, .-vec sub
        add
                r2, r5, #1
                                                                   .align 2
                r3, r2, as1 #3
        mov
                                                                   .global numt vec copy
                                                                   .type numt_vec_copy, %function
                r2, s1
        CMD
```

Dedicated square root instruction



Compile and Run Commands

arm-linux-gcc -static -O2 qr.c -o qr.exe ./qr.exe m20x20.txt out.txt 20 20

Numerical Results - Run 100 Times for Average

Base version runtime: 0.739600 seconds

Loop unrolling added: 0.736900 seconds

For loop optimization added: 0.736600 seconds

Cache-oblivious matrix transpose added: 0.736400 seconds

Macros added: 0.736600 seconds

Operator strength reduction added: 0.738400 seconds

Unnecessary loop branches removed: 0.745700 seconds

Matrix transpose run once with 20x20 matrix

Ι	refs:	407,515							I	refs	::	430,594						
I1	misses:	1,013							I1	miss	es:	1,017						
LLi	misses:	1,007							LLi	miss	es:	1,009						
I1	miss rate:	0.25%							I1	miss	rate:	0.24%						
LLi	miss rate:	0.25%							LLi	miss	rate:	0.23%						
D	refs:	141,806	(8	39,773	rd	+	52,033 wi	r)	D	refs	:	156,878	(9	98,684	rd	+	58,194	wr)
D1	misses:	3,318	(2,518	rd	+	800 W	r)	D1	miss	es:	3,324	(2,517	rd	+	807	wr)
LLd	misses:	2,628	(1,995	rd	+	633 w	r)	LLd	miss	es:	2,630	(1,994	rd	+	636	wr)
D1	miss rate:	2.3%	(2.8	%	+	1.5%)	D1	miss	rate:	2.1%	(2.6	%	+	1.49	%)
LLd	miss rate:	1.9%	(2.2	%	+	1.2%)	LLd	miss	rate:	1.7%	(2.0	%	+	1.19	%)
LL r	efs:	4,331	(3,531	rd	+	800 WI	r)	LL	refs:		4,341	(3,534	rd	+	807	wr)
LL m	isses:	3,635	(3,002	rd	+	633 WI	r)	LL	misse	es:	3,639	(3,003	rd	+	636	wr)
LL m	iss rate:	0.7%	(0.6	%	+	1.2%)	LL	miss	rate:	0.6%	(0.69	%	+	1.19	%)
Bran	ches:	79,195	(7	78,038	cond	+	1,157 i	nd)	Bra	nches	::	80,416	(7	79,259	cond	+	1,157	ind)
Misp	redicts:	5,700	(5,603	cond	+	97 i	nd)	Mis	predi	cts:	5,878	(5,781	cond	+	97	ind)

8.4%

7.2%

Mispred rate:

Mispred rate:

8.4%

7.3%

7.3% (

Conclusion

- QR decomposition is challenging to optimize when there are many function calls or loops required
- Cache-oblivious matrix transpose lowers cache misses but does not lower runtime
- -O2 provides significant optimization
- FPA is challenging to implement

Future Work

- MLA instruction to parallelize vector and matrix routines
- Implement more robust rounding schemes for decimal to fixed point conversion to reduce round-off error
- Integrate existing QR decomposition code with custom Fixed Point library
- More robust testing procedures which measure round-off and truncation error, and runtime for matrices of arbitrary size and conditioning
- Research other QR decomposition algorithms