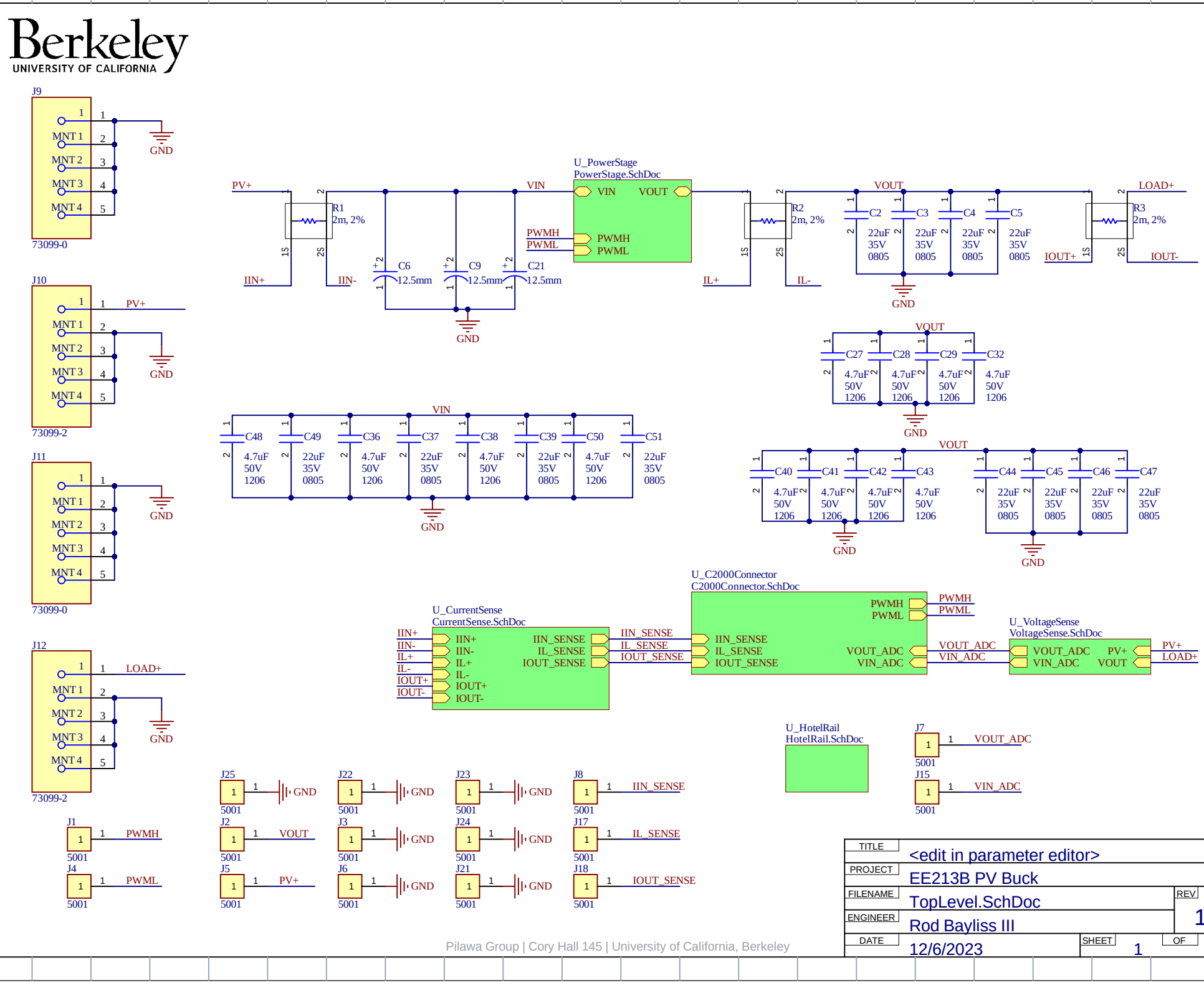

TITLE	<edit in parameter editor>
PROJECT	EE213B PV Buck
FILENAME	TopLevel.SchDoc
ENGINEER	Rod Bayliss III
DATE	12/6/2023
REV	1.0
SHEET	1 OF 6

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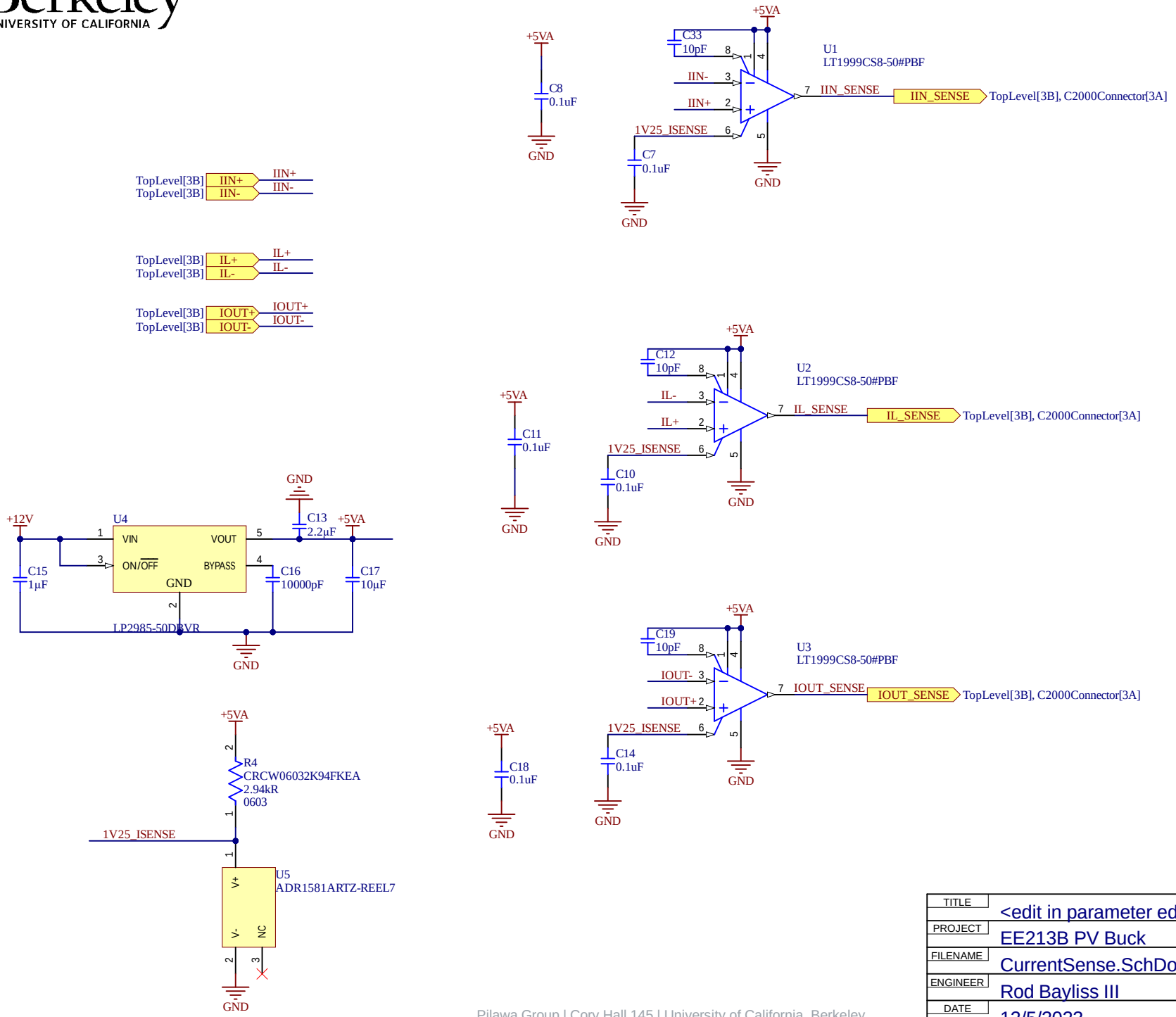
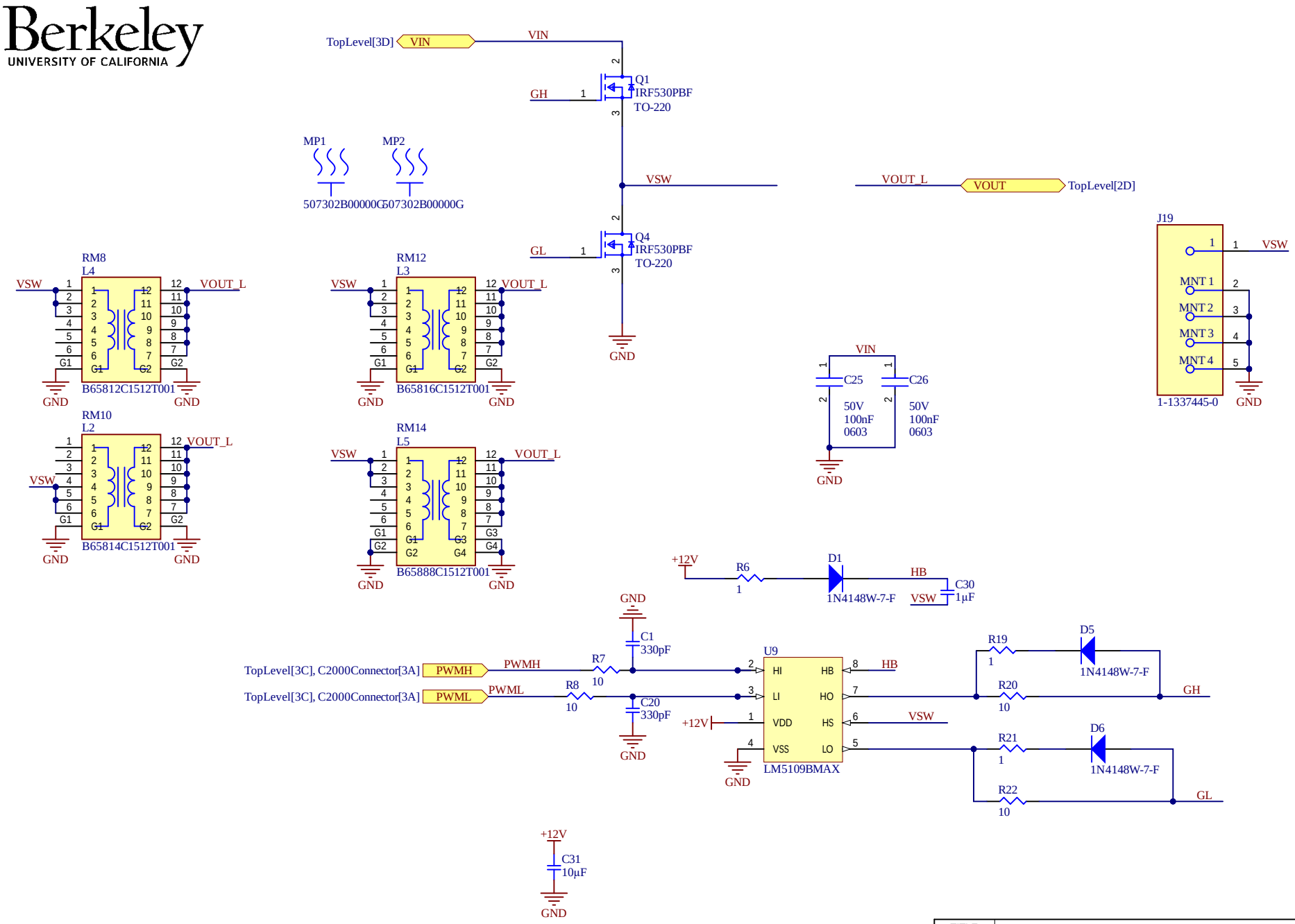


TITLE	<edit in parameter editor>
PROJECT	EE213B PV Buck
FILENAME	CurrentSense.SchDoc
ENGINEER	Rod Bayliss III
DATE	12/5/2023
REV	1.0
SHEET	2
OF	6



TITLE	<edit in parameter editor>
PROJECT	EE213B PV Buck
FILENAME	PowerStage.SchDoc
ENGINEER	Rod Bayliss III
DATE	12/6/2023
REV	1.0
SHEET	3 OF 6

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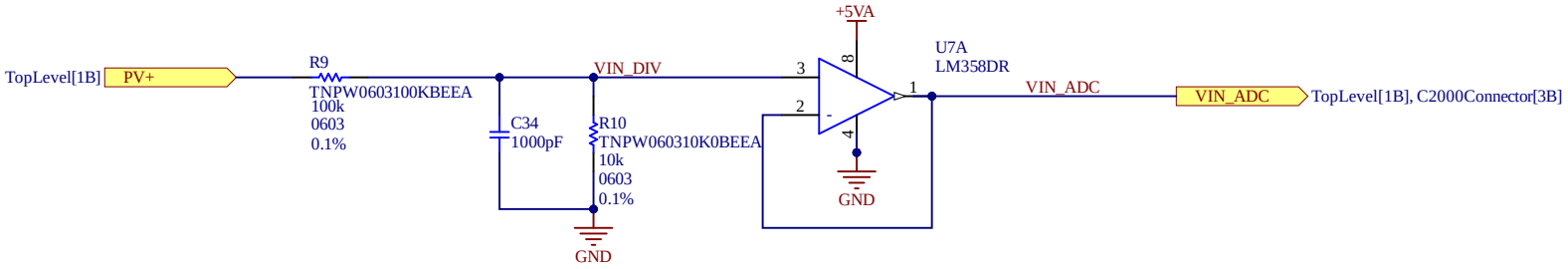

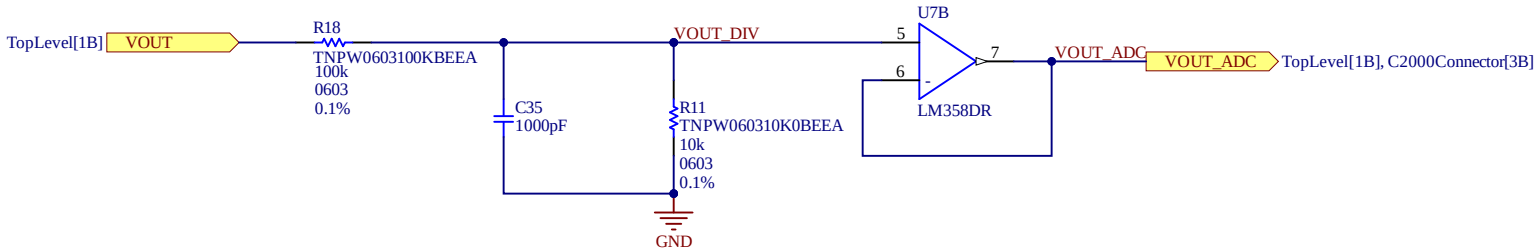

TITLE	<edit in parameter editor>
PROJECT	EE213B PV Buck
FILENAME	VoltageSense.SchDoc
ENGINEER	Rod Bayliss III
DATE	12/5/2023
REV	1.0
SHEET	4
OF	6

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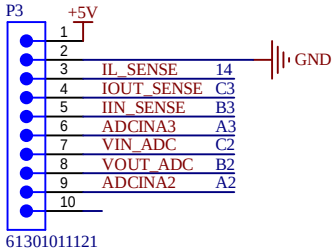
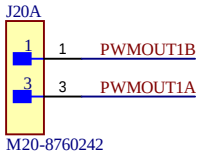
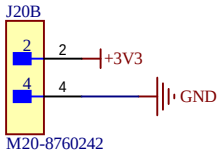
C2000 Pinout Here
https://www.ti.com/lit/ug/sprui77c/sprui77c.pdf?ts=1695459542898&ref_url=https%253A%252F%252Fwww.google.com%252F







TopLevel[2B], VoltageSense[2C] VIN_ADC → VIN_ADC
TopLevel[2B], VoltageSense[2B] VOUT_ADC → VOUT_ADC

TopLevel[2B], CurrentSense[2D] IIN_SENSE → IIN_SENSE
TopLevel[2B], CurrentSense[2C] IL_SENSE → IL_SENSE
TopLevel[2B], CurrentSense[2B] IOUT_SENSE → IOUT_SENSE

TopLevel[2B], PowerStage[3B] PWMH → PWMOUT1A
TopLevel[2B], PowerStage[3B] PWML → PWMOUT1B

TITLE	<edit in parameter editor>		
PROJECT	EE213B PV Buck		
FILENAME	C2000Connector.SchDoc	REV	1.0
ENGINEER	Rod Bayliss III		
DATE	10/26/2023	SHEET 5	OF 6

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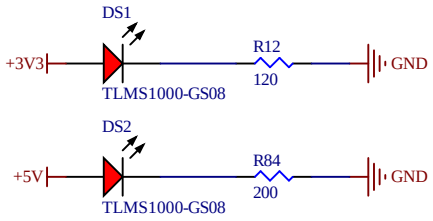

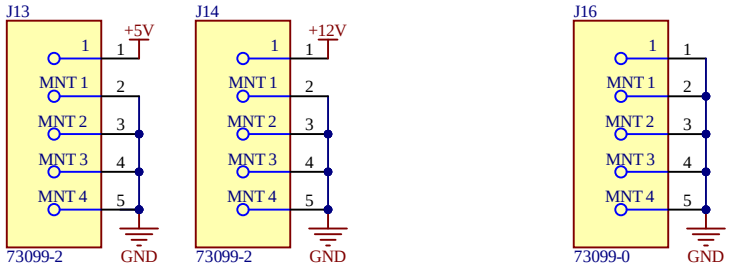

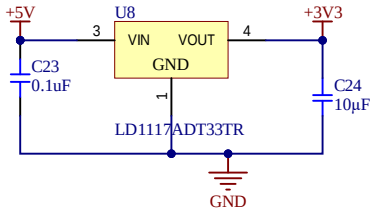

TITLE	<edit in parameter editor>
PROJECT	EE213B PV Buck
FILENAME	HotelRail.SchDoc
ENGINEER	Rod Bayliss III
DATE	10/24/2023
REV	1.0
SHEET	6 OF 6