

C6.2.29 BR

Branch to Register branches unconditionally to an address in a register, with a hint that this is not a subroutine return.

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9				5	4	3	2	1	0
1	1	0	1	0	1	1	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0		Rn			0	0	0	0	0	

op

Integer variant

BR <Xn>

Decode for this encoding

integer n = [UInt](#)(Rn);

Assembler symbols

<Xn> Is the 64-bit name of the general-purpose register holding the address to be branched to, encoded in the "Rn" field.

Operation

bits(64) target = [X](#)[n];

[BranchTo](#)(target, [BranchType_JMP](#));