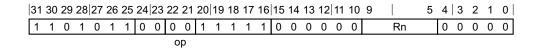
C6.2.29 BR

Branch to Register branches unconditionally to an address in a register, with a hint that this is not a subroutine return.



Integer variant

BR <Xn>

Decode for this encoding

```
integer n = UInt(Rn);
```

Assembler symbols

<Xn>

Is the 64-bit name of the general-purpose register holding the address to be branched to, encoded in the "Rn" field.

Operation

```
bits(64) target = X[n];
BranchTo(target, BranchType_JMP);
```