

Optimizing Matrix Multiplication: A Compiler Implementation of Strassen's Algorithm



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Summary

This project demonstrates that low-level optimisation using hand-written LLVM Intermediate Representation (IR) yields significant performance gains over compiler-optimised native C code. Implementing Strassen's algorithm directly in IR resulted in a 5.15x speedup for 1024x1024 matrices, achieving 3.58 GFLOPS compared to 0.70 GFLOPS for the standard algorithm.

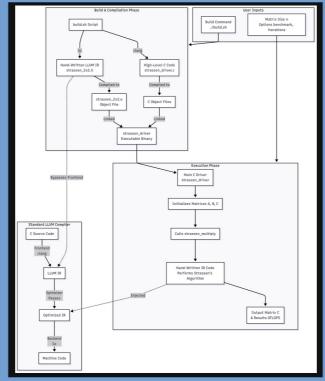
Introduction & The Problem

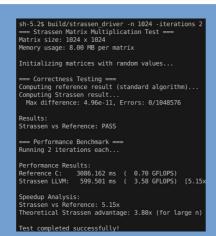
- Industry drive for AI computational efficiency.
- Optimization often neglects fundamental operations.
- Hypothesis: Manual optimization in LLVM IR improves efficiency.
- Case Study: Matrix multiplication.

O(n³) vs. O(n^{2.807})

Algorithmic Advantage: Strassen's algorithm reduces the complexity from $O(n^3)$ to $O(n^{2.807})$ by trading computationally expensive multiplications for a larger number of cheaper additions and subtractions, resulting in significantly faster runtime for large matrices.

Architecture Diagram





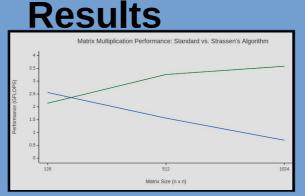


Figure 1.3 Graph Results

The performance benchmark demonstrates a clear crossover point where the low-level LLVM IR implementation of Strassen's algorithm begins to significantly outperform the optimized C baseline.

n=128: The overhead of Strassen's recursion and memory management makes it slightly slower than the highly optimized C baseline.

n=512: The crossover point is reached. Strassen's algorithm more than doubles the performance, achieving a 2.09x speedup (3.26 vs. 1.56 GFLOPS).

n=1024: Strassen's algorithm demonstrates overwhelming dominance, achieving a 5.15x speedup (3.58 vs. 0.70 GFLOPS). The reference implementation's performance collapses as the matrices exceed CPU cache capacity, while Strassen's cache-friendly recursive approach maintains high throughput.

Numerical correctness was validated across all tests with a tolerance of 1e-10, ensuring the speedup comes without sacrificing accuracy.

Figure 1.3 illustrates the growing performance advantage of the Strassen LLVM IR implementation as matrix size increases, highlighting its scalability and efficiency for large-scale computations.

Conclusions & Applications

Conclusion: Successfully proves hardware-oriented optimization in LLVM IR provides substantial performance benefits.

- Applications:
 - Artificial Intelligence: Accelerating training and inference.
 - High-Performance Computing (HPC): Scientific simulations.
 - Compiler Development: Creating optimized libraries.
- Impact: Reduces computational time and energy consumption.

Software & Technologies

- Languages: C, LLVM IR
- Compiler Toolchain: LLVM (clang, llc)
- **Development Tools:** GNU Make, Git, VS Code
- Platform: Linux
- **Key Techniques:** Memory alignment, SIMD optimization, recursive algorithms.

References

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