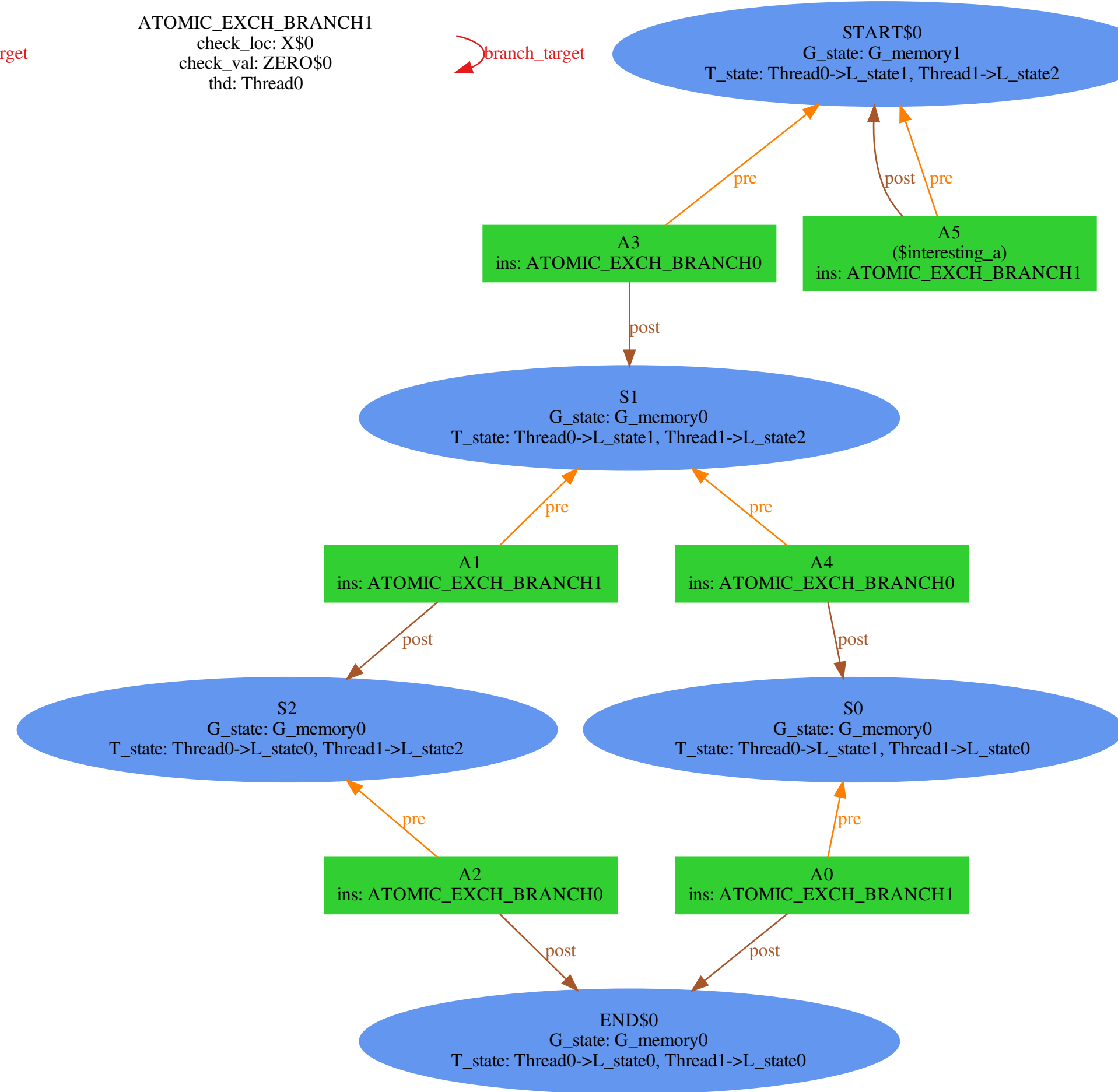


ATOMIC\_EXCH\_BRANCH0  
check\_loc: X\$0  
check\_val: ZERO\$0  
exch\_val: ONE\$0  
thd: Thread1

branch\_target

ATOMIC\_EXCH\_BRANCH1  
check\_loc: X\$0  
check\_val: ZERO\$0  
thd: Thread0

branch\_target



Thread0

Thread1

t\_order

L\_state2  
next\_ins: ATOMIC\_EXCH\_BRANCH0

L\_state1  
next\_ins: ATOMIC\_EXCH\_BRANCH1

L\_state0

G\_memory1  
mem: X\$0->ZERO\$0

G\_memory0  
mem: X\$0->ONE\$0