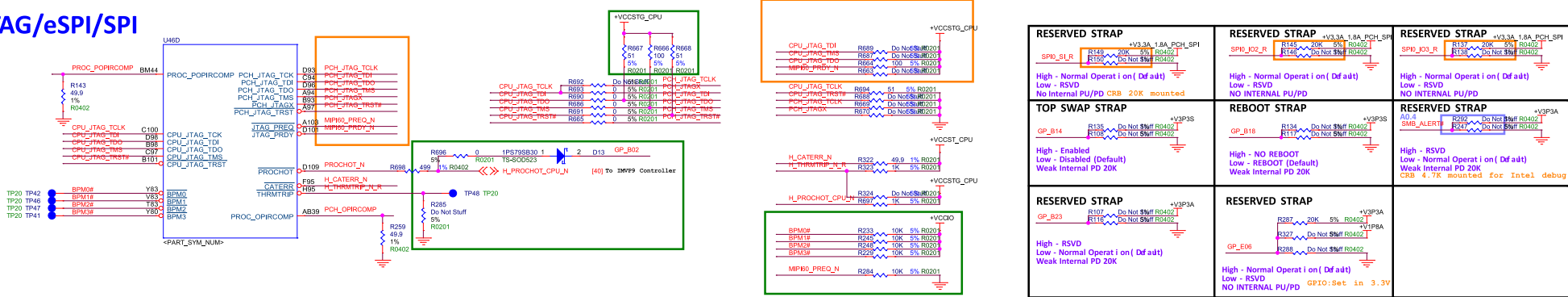
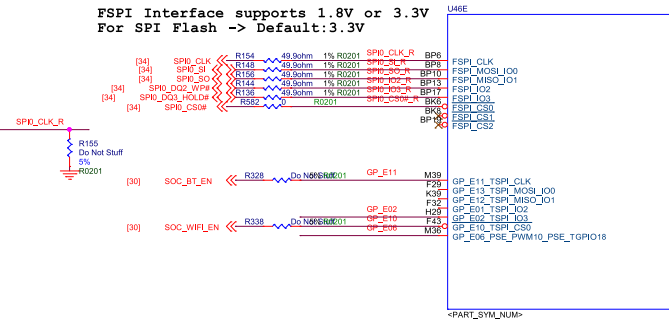


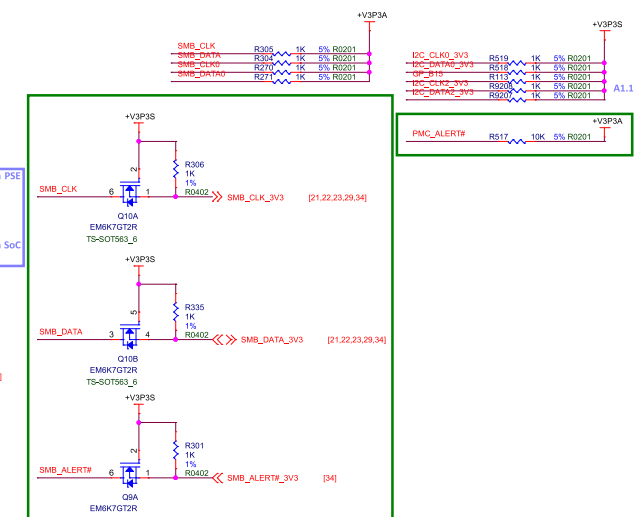
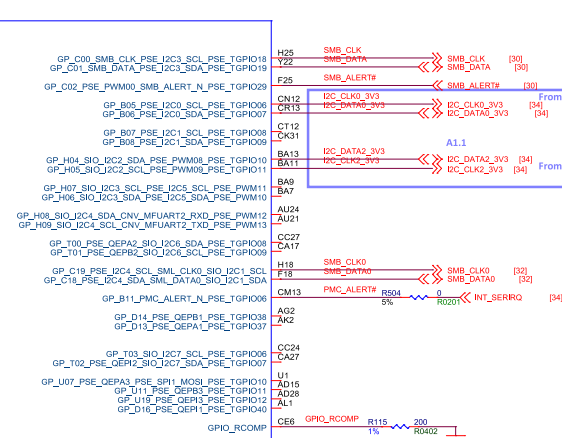
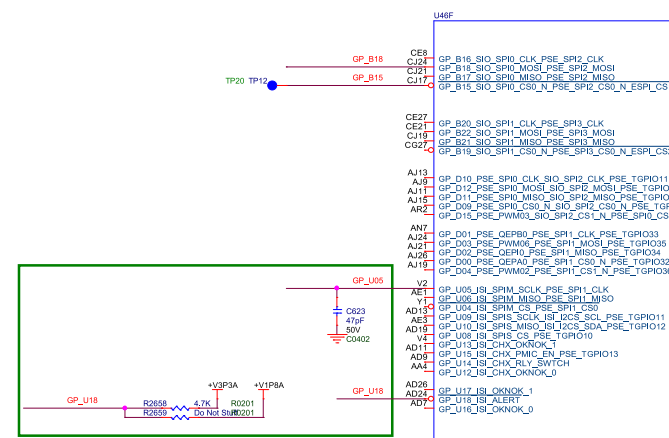
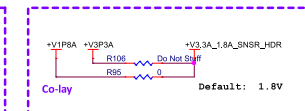
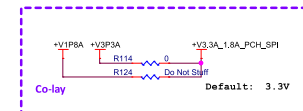
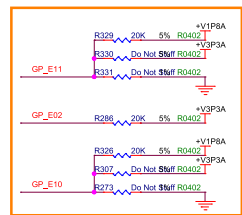
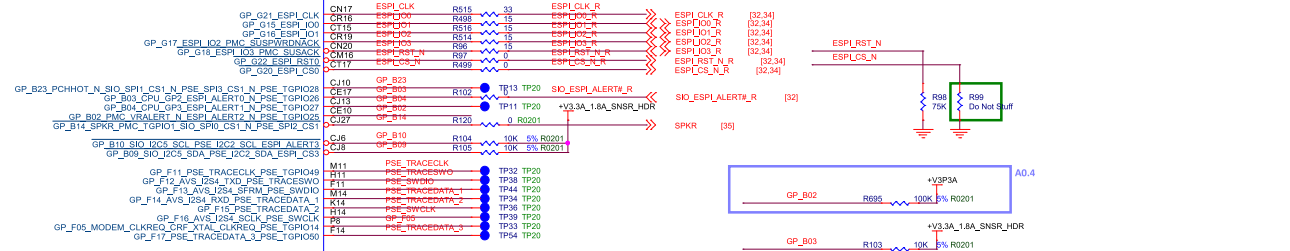
SoC JTAG/eSPI/SPI



FSPI Interface supports 1.8V or 3.3V
For SPI Flash -> Default:3.3V



eSPI Interface supports 1.8V only



CAN BUS

