EVN-KLU Cash Drawer Port Description

Create Date: 2020/4/30
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Part A. Port Address

Cash Drawer	
Read(Input)	Write(Output)
GPP_B17	GPP_B15
	GPP_B16

Part B. Port Address Information

Table1.

Pin	Memory Address	Default value	Bit Status
GPP_B15	0xFDAF0538	Output	High bit: 04000201 Low bit: 04000200
GPP_B16	0xFDAF0540	Output	High bit: 04000201 Low bit: 04000200
GPP_B17	0xFDAF0548	Input	High bit: 84000102 Low bit: 84000100

Part C. Port Address Detail Setting

Below Figures are bits status setting, and we can compare with Bits Status from Table1.

1. Figure1. show GPP_B15, GPP_B16, GPP_B17 **Input/Output** bits status
a. bit 9 set 0 = Enable input buffer
b. bit 9 set 1 = Disable input buffer
c. bit 8 set 0 = Enable output buffer
d. bit 8 set 1 = Disable output buffer

Figure1.

9	1h RW	**GPIO RX Disable (GPIORXDIS):** 0 = Enable the input buffer (active low enable) of the pad. 1 = Disable the input buffer of the pad. Notes: When the input buffer is disabled, the internal pad state is always driven to '0'.
8	1h RW	**GPIO TX Disable (GPIOTXDIS):** 0 = Enable the output buffer (active low enable) of the pad. 1 = Disable the output buffer of the pad; i.e. Hi-Z

2. Figure2. show GPP_B15, GPP_B16, GPP_B17 **High/Low** bits status

a. bit 1 show 0 = current input status is Low
b. bit 1 show 1 = current input status is High
c. bit 0 set 0 = Set ouput status as Low
d. bit 0 set 1 = Set ouput status as High

Figure2.

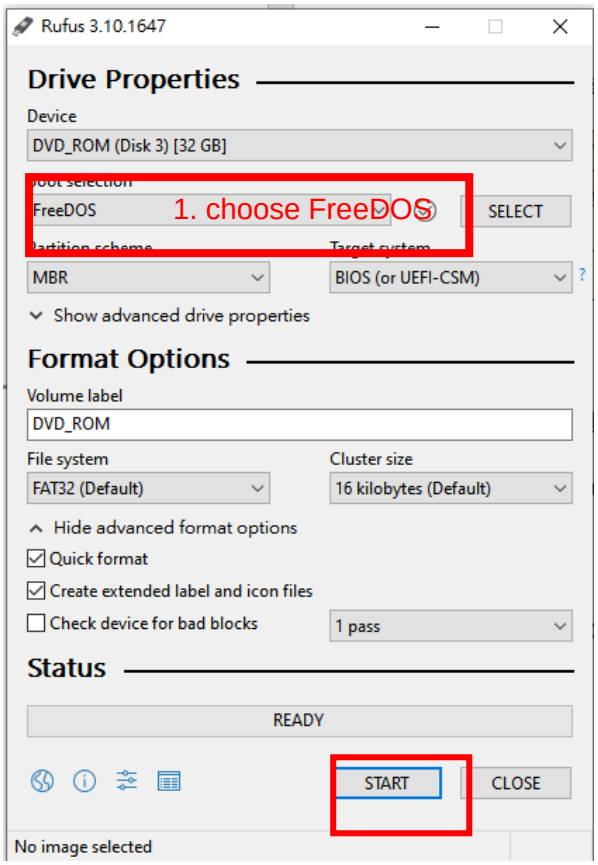
1	0h RO	**GPIO RX State (GPIORXSTATE):** This is the current internal RX pad state after Glitch Filter logic stage and is not affected by PMode and RXINV settings.
0	0h RW	**GPIO TX State (GPIOTXSTATE):** 0 = Drive a level '0' to the TX output pad. 1 = Drive a level '1' to the TX output pad

Part D. Testing Port Address

Step1. Download Rufus-3.10 : https://rufus.ie/

Step2. Run Rufus-3.10.exe

Step3. Choose “FreeDOS” and click start



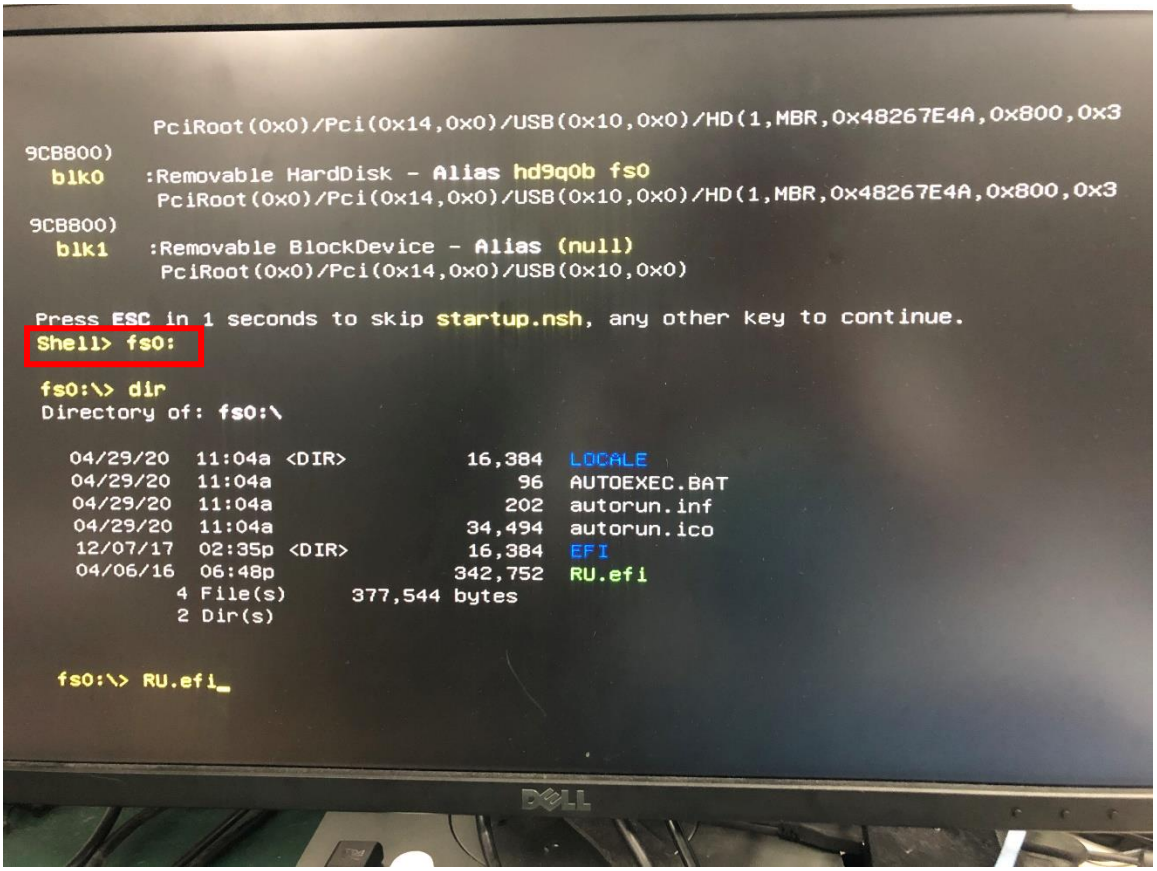
Step4. Copy RU.efi and EFI folder to your USB drive

Step5. Boot from USB drive with UEFI mode and enter following command

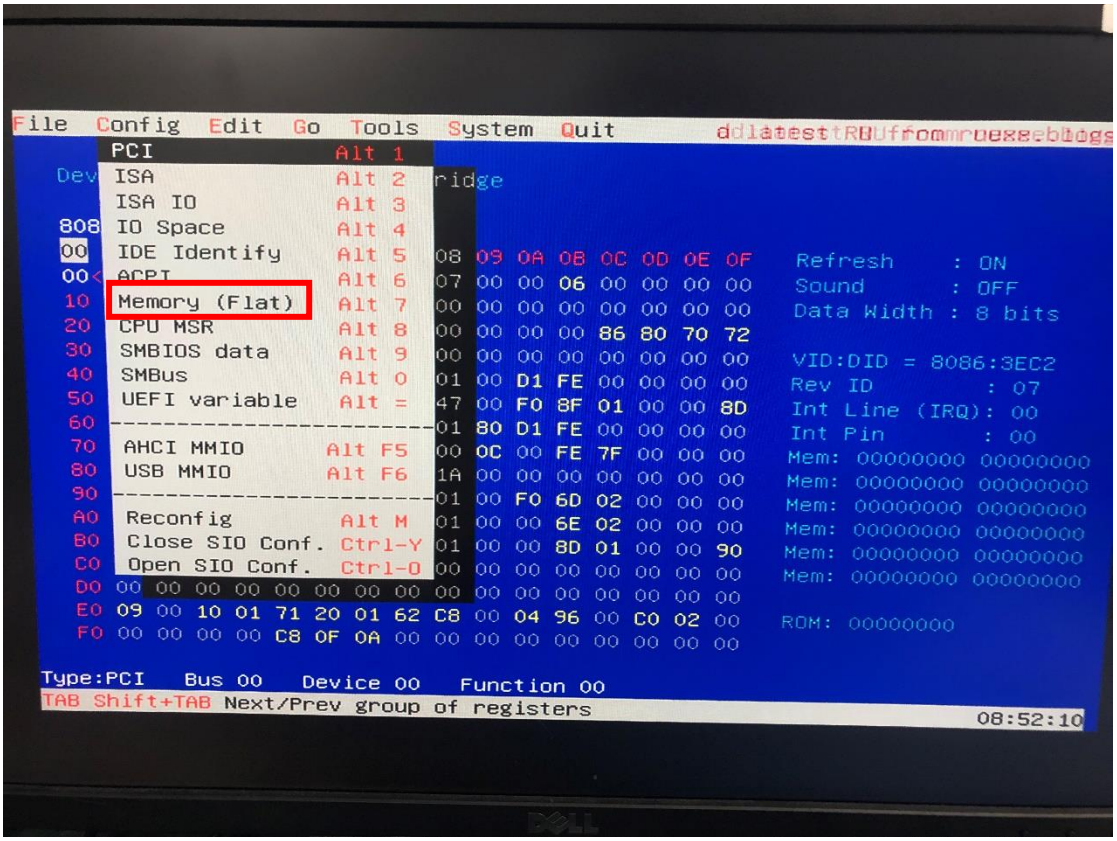
```
fs0:

RU.efi
```

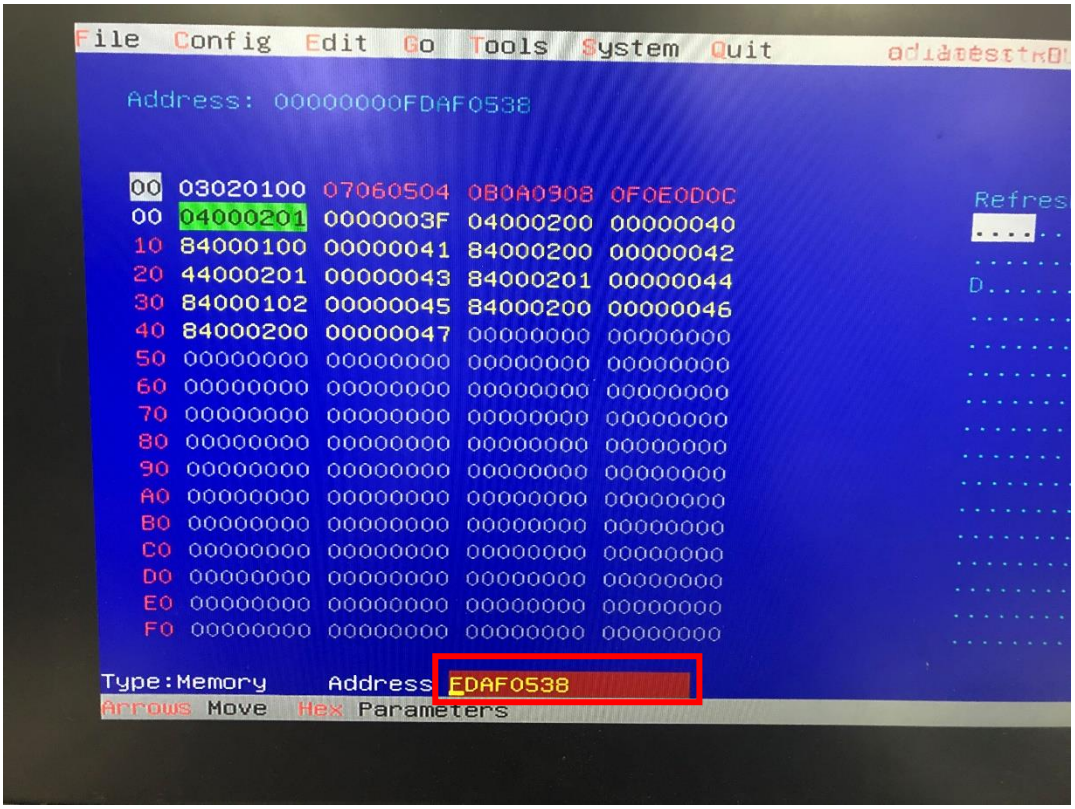
Note. Sometime USB drive will not be fs0: ,if have this situation please try to enter fs1: or fs2: or keep searching next number



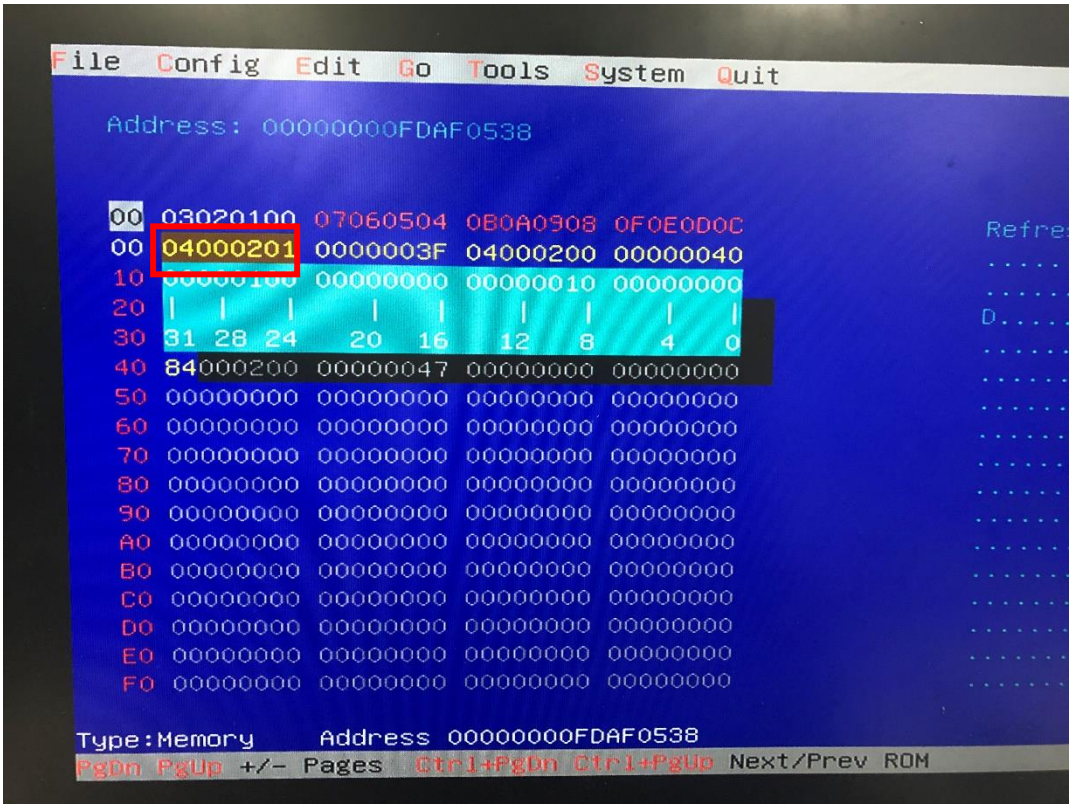
Step6. Enter “Alt+C” and choose “Memory (Flat)”



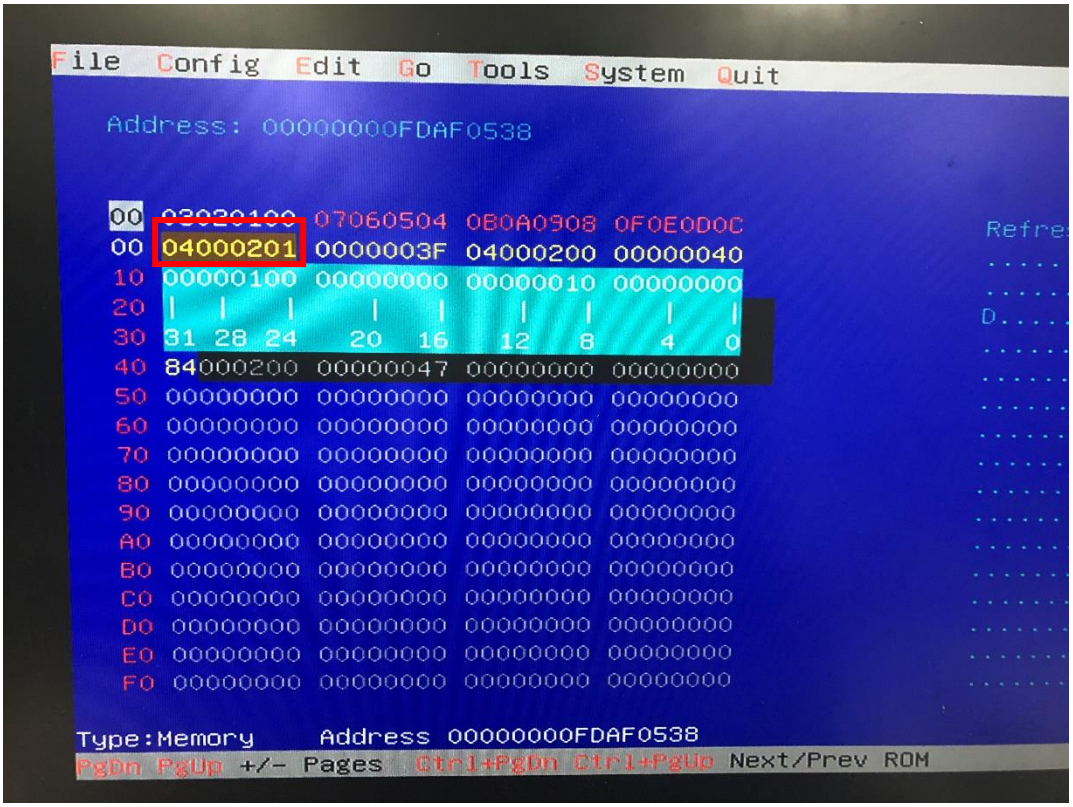
Step7. Enter your GPIO Memory Address (in this example will be GPP_B15 and address will be 0xFDAF0538)



Step8. Click “F7” twice to change your address to 32-bits



Step9. Enter your GPIO bits status
a. In this example bits status default will be Low bit: 84000200
b. We can click 84000201 change to High bit
c. Click “Tab” to confirm our setting



Part E. Intel EDS Document Reference

7th Generation Intel® Processor Family I/O for U/Y Platforms and 8th Generation Intel® Processor Family I/O for U Quad Core Platforms

7th and 8th Generation Intel®Processor Family I/O for U/Y Platforms and 10th Generation Intel® Processor Family I/O for Y PlatformsDatasheet - Volume 2 of 2