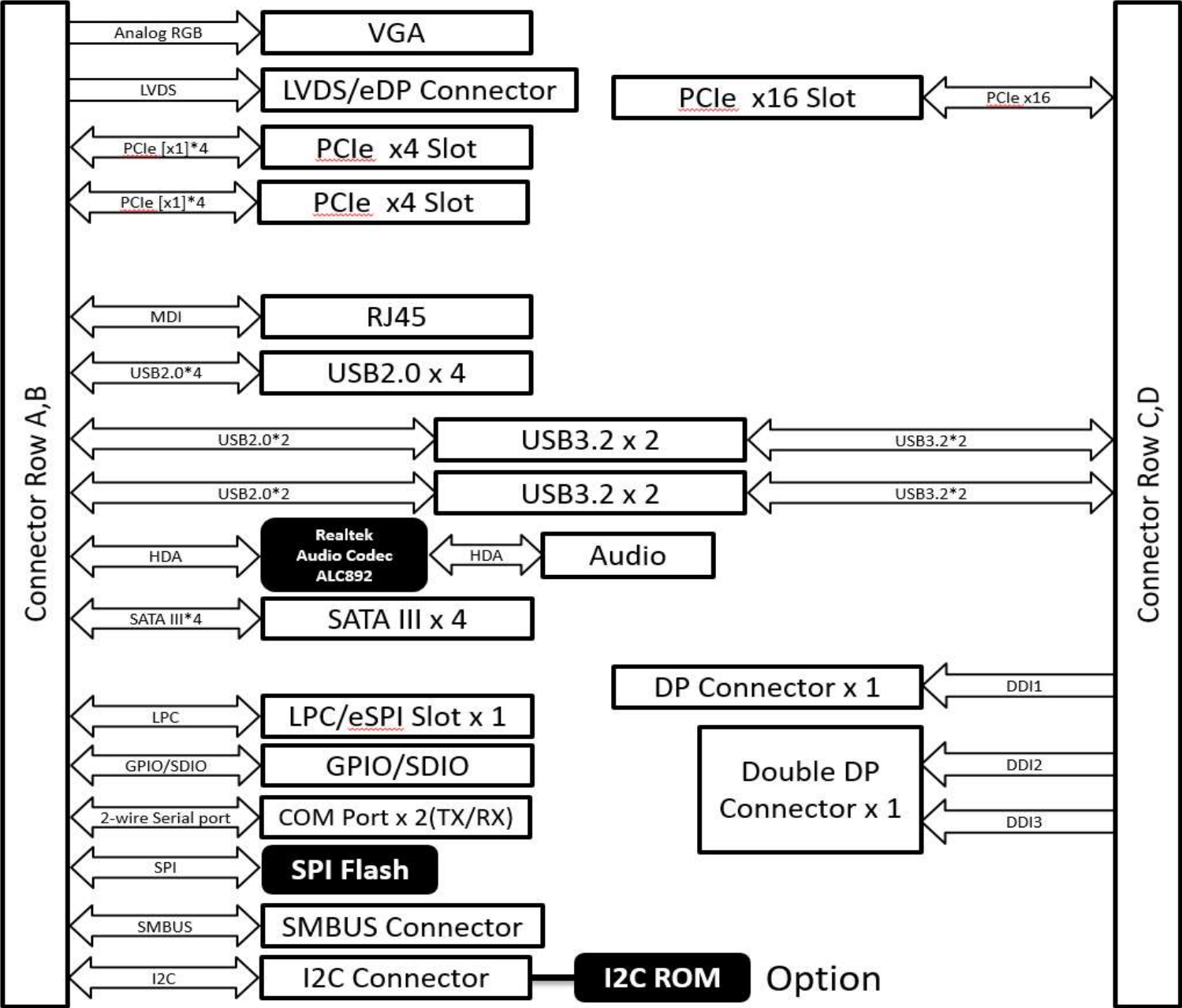
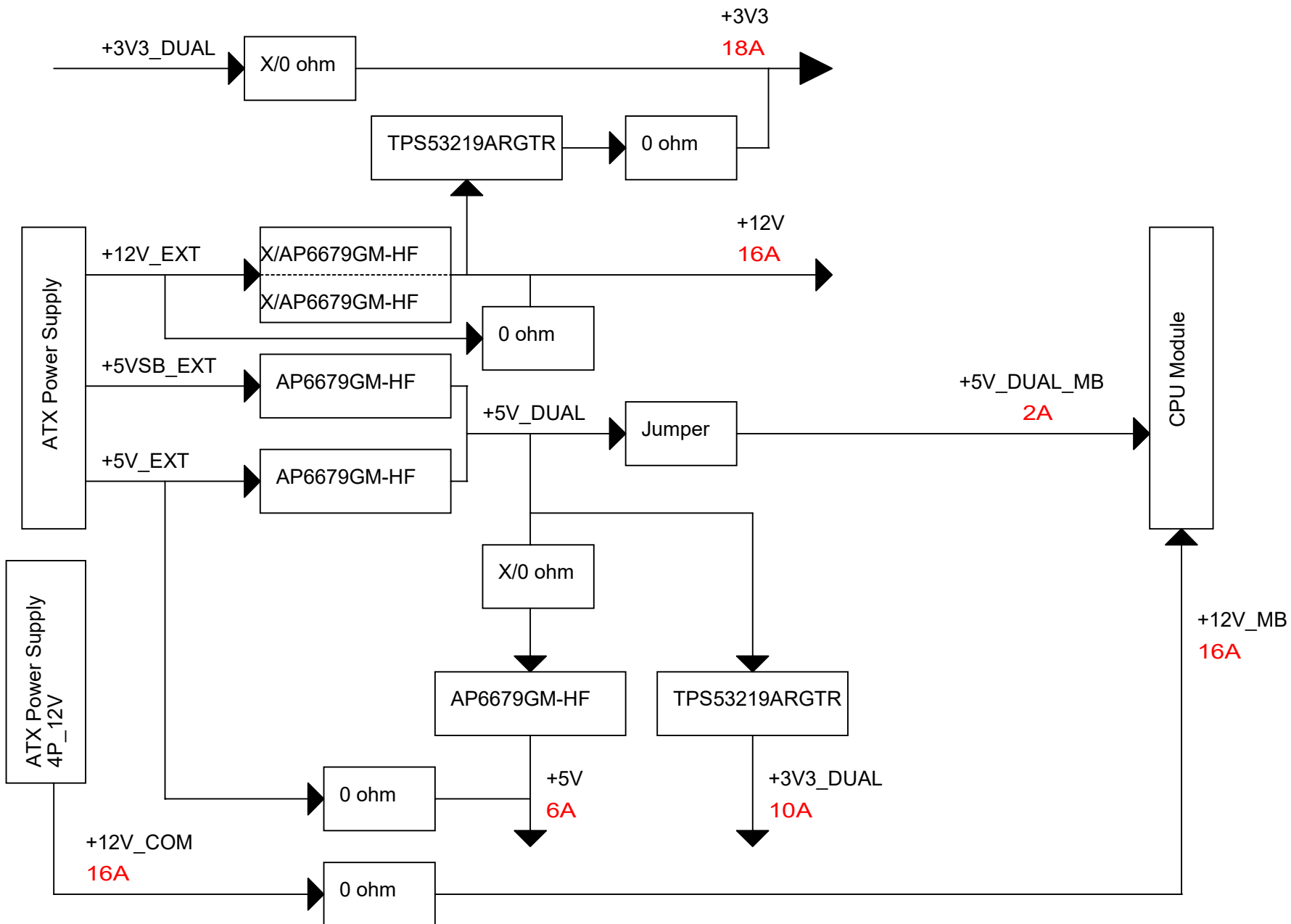


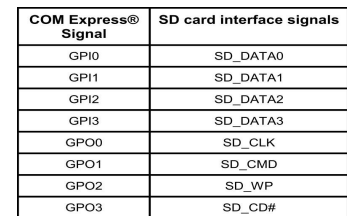
Page	Index
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2	Block Diagram
3	Power Map
4	ROW A/B
5	ROW C/D
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20	DP Port x2
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22	RESET + PWROK
23	SIO Card SLOT
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28	ATX Power + Front Panel
29	+5V_DUAL / +3V3_DUAL
30	+12V / +5V / +3V3
31	eDP+eDP/LVDS Switch
32	History

ECB-920A

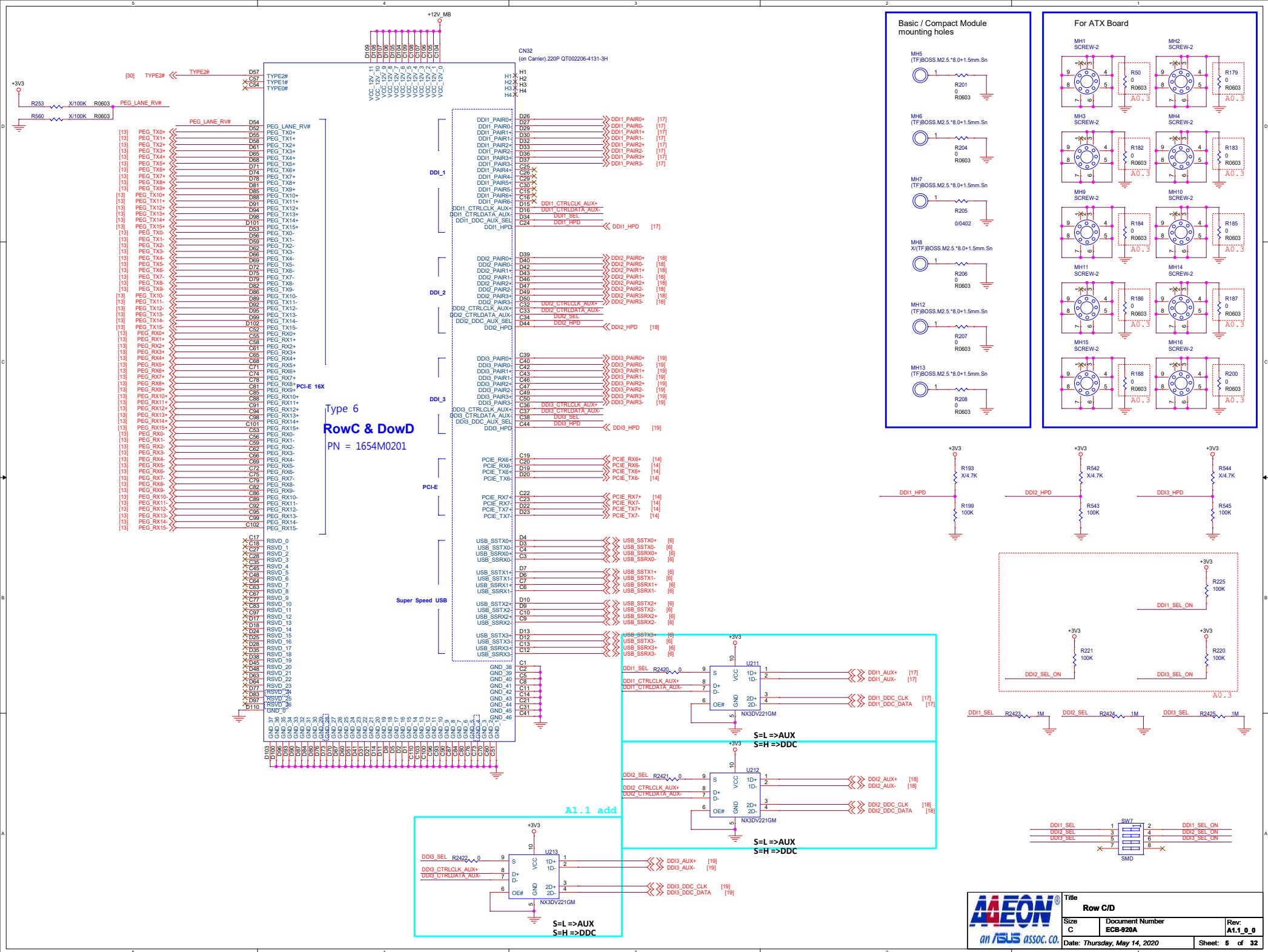




Title Power Map		
Size A	Document Number ECB-920A	Rev: A1.1_0_0
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Power Rail	Module Pin Current Capability (Amps)	Nominal Input (Volts)	Input Range (Volts)	Derated Input (Volts)	Max Input Ripple (mV)	Max Module Input Power (w. derated input) (Watts)	Assumed Conversion Efficiency	Max Load Power (Watts)
VCC_12V	12	12	11.4 - 12.6	11.4	+/- 100	137	85%	116
VCC_5V_SBY	2	5	4.75 - 5.25	4.75	+/- 50	9		
VCC_RTC	0.5	3	2.0 - 3.3		+/- 20			

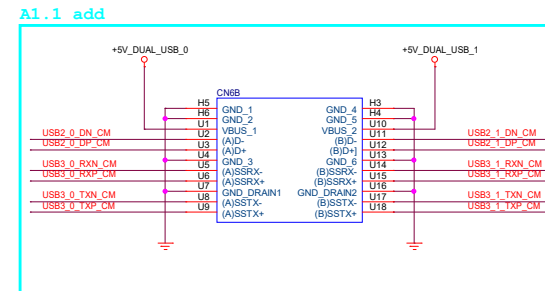


The image displays two circuit diagrams for USB-to-serial converters, each centered around a different IC: U229 (top) and U230 (bottom).

Top Diagram (U229): This circuit uses the U229 IC. It features a USB interface with pins for USB_SSTX0-, USB_SSTX0+, USB_SSRX0-, and USB_SSRX0+. The serial interface includes pins for A_TX-, A_TX+, B_RX-, B_RX+, C_TX-, C_TX+, D_RX-, and D_RX+. Power is supplied via +3V3_DUAL, with decoupling capacitors C124, C99, C122, C1594, and C1593. Signal conditioning is provided by resistors R2543, R2544, C123, C122, R2545, R2546, C126, C125, R2547, R2548, C1555, C1554, R2549, R2550, R2506, R2507, R2508, and R2509. The IC is connected to a microcontroller via pins CTRL_A0, CTRL_A1, CTRL_B0, CTRL_B1, VCC3, and GND1.

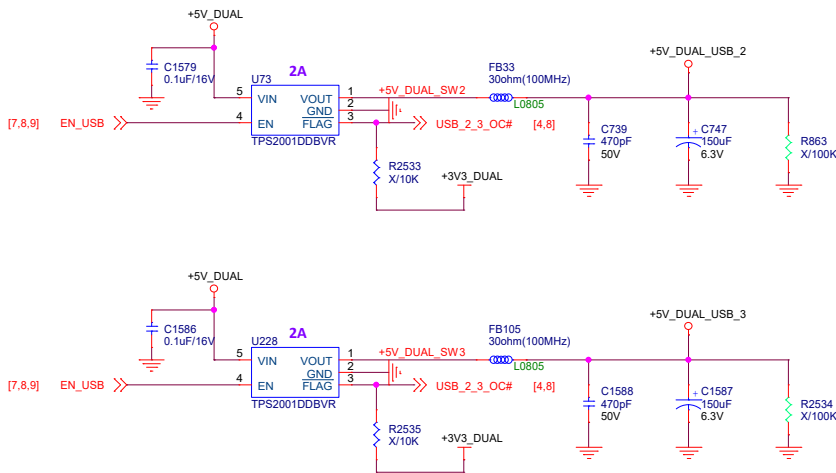
Bottom Diagram (U230): This circuit uses the U230 IC. It features a USB interface with pins for USB_SSTX2-, USB_SSTX2+, USB_SSRX2-, and USB_SSRX2+. The serial interface includes pins for A_TX-, A_TX+, B_RX-, B_RX+, C_TX-, C_TX+, D_RX-, and D_RX+. Power is supplied via +3V3_DUAL, with decoupling capacitors C50, C92, C89, C87, C1608, and C1607. Signal conditioning is provided by resistors R2551, R2552, C91, C90, R2553, R2554, C88, C87, R2555, R2556, C1601, C1602, R2537, R2538, R2512, R2511, R2536, and R2537. The IC is connected to a microcontroller via pins CTRL_A0, CTRL_A1, CTRL_B0, CTRL_B1, VCC3, and GND1.

A1.1 change for USB3.1 GEN2

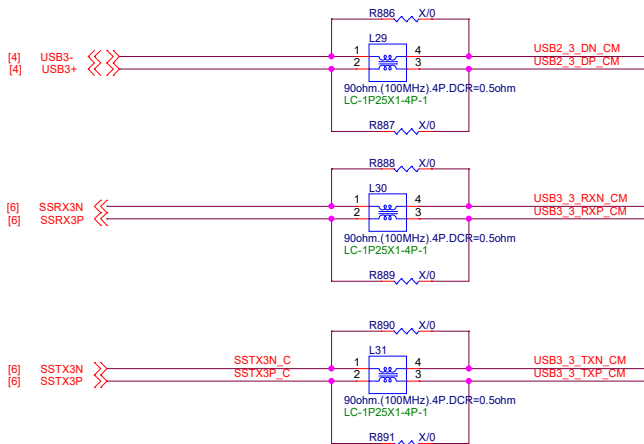
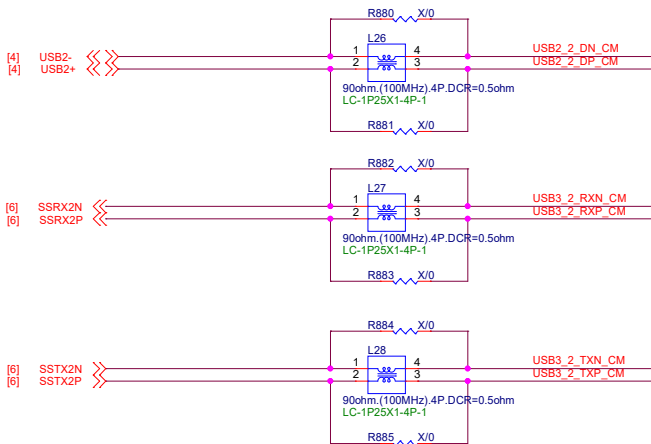
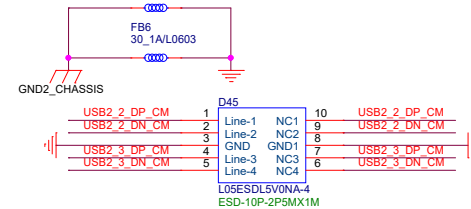
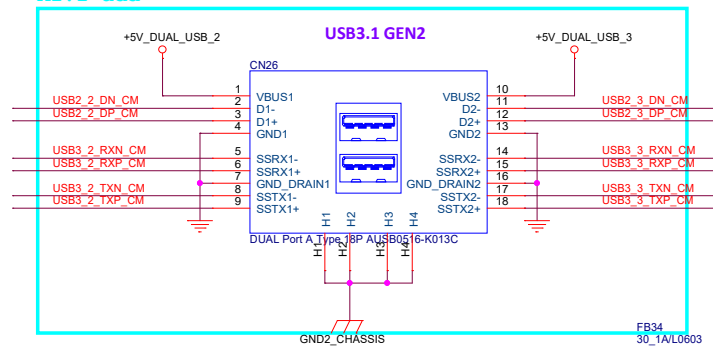


USB 3.1

A1.1 change for USB3.1 GEN2

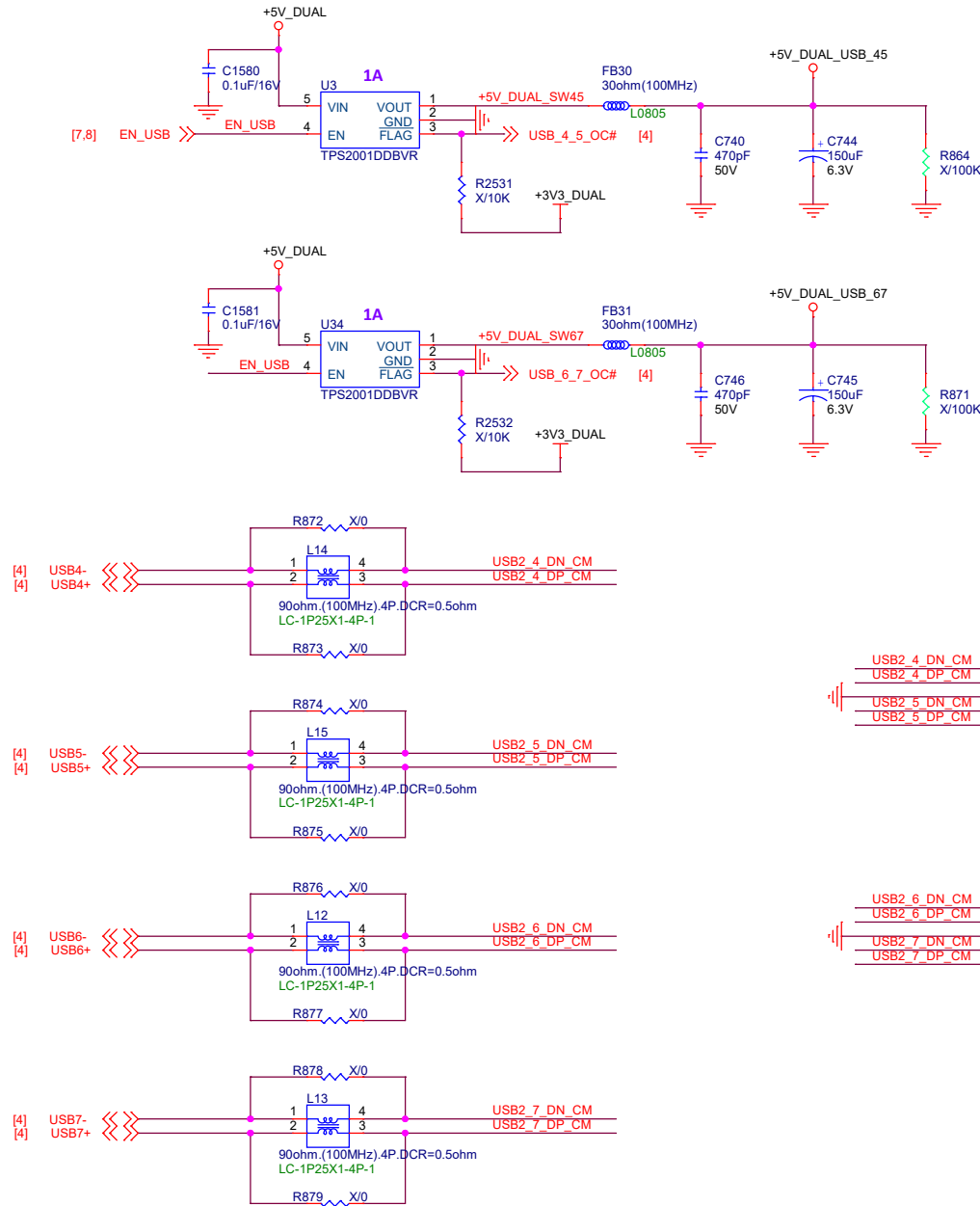


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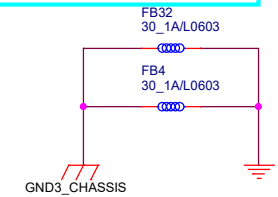
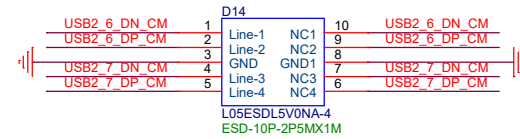
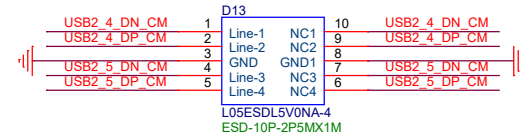
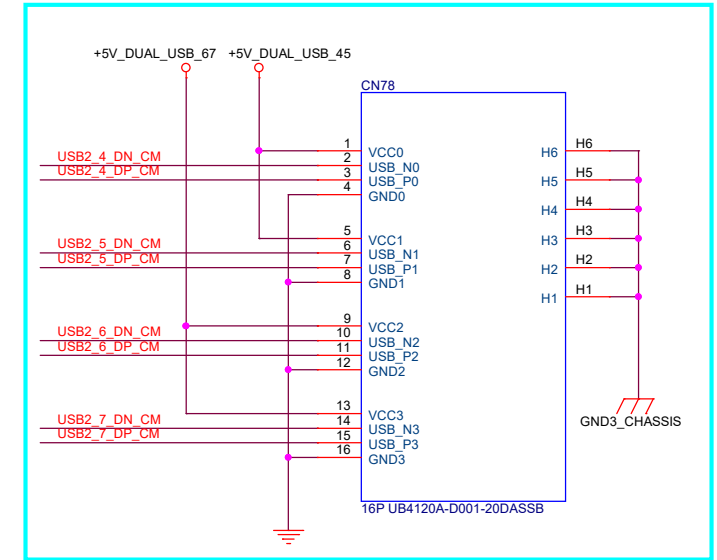


USB 2.0

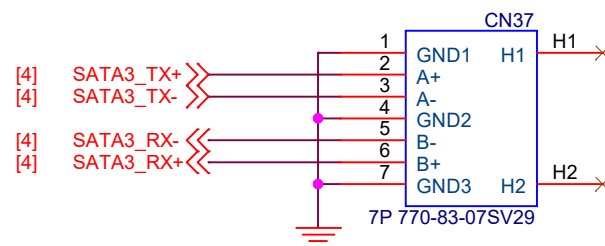
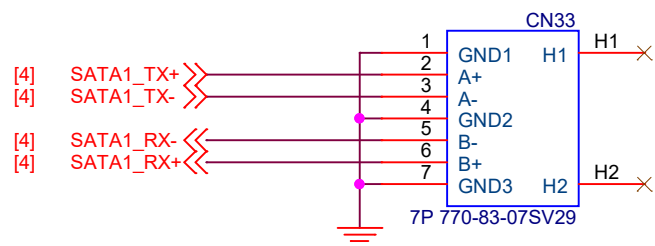
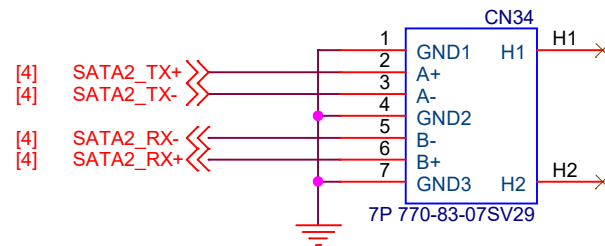
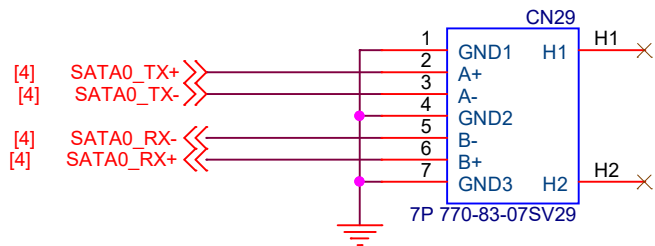
A1.1 change to quad port



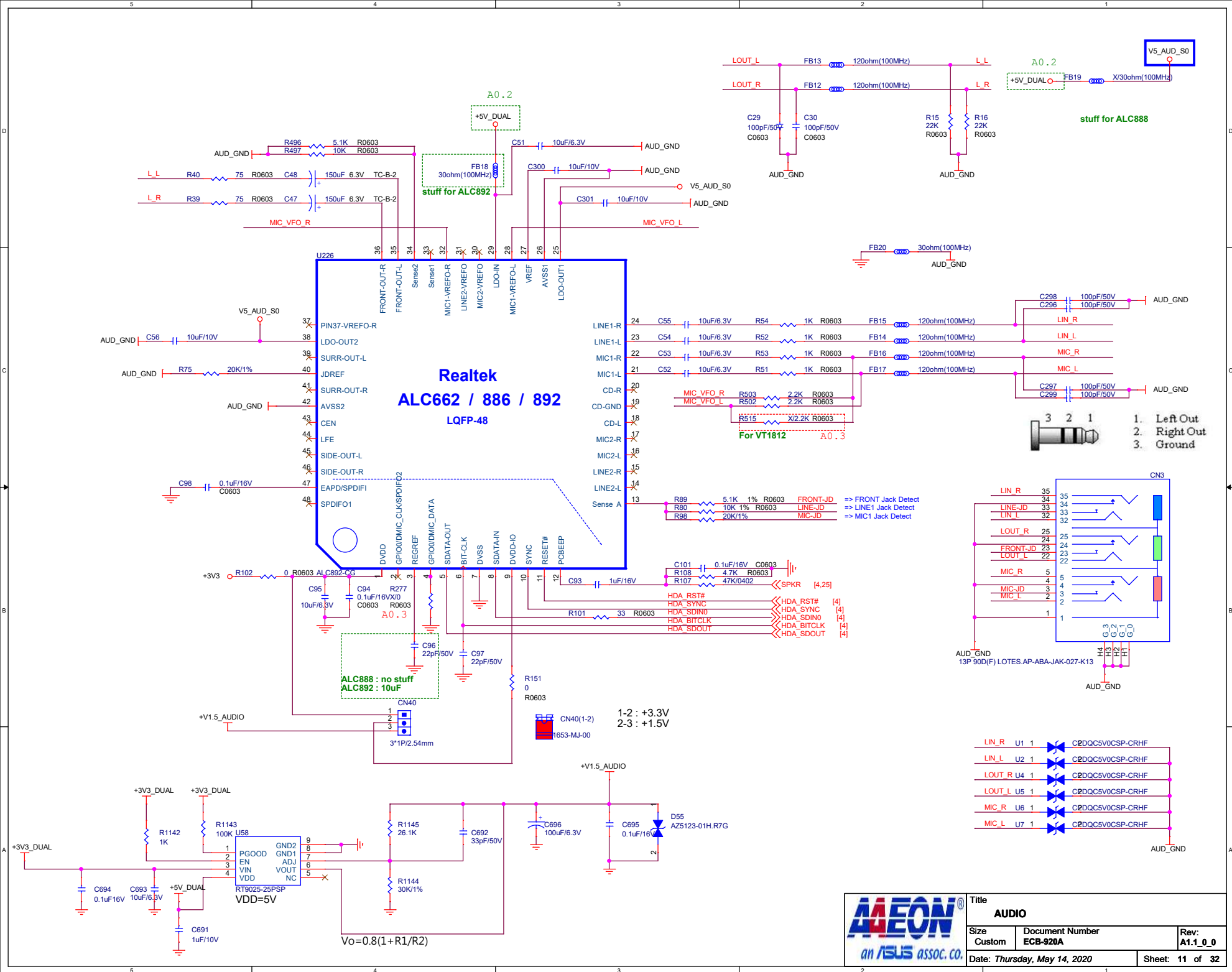
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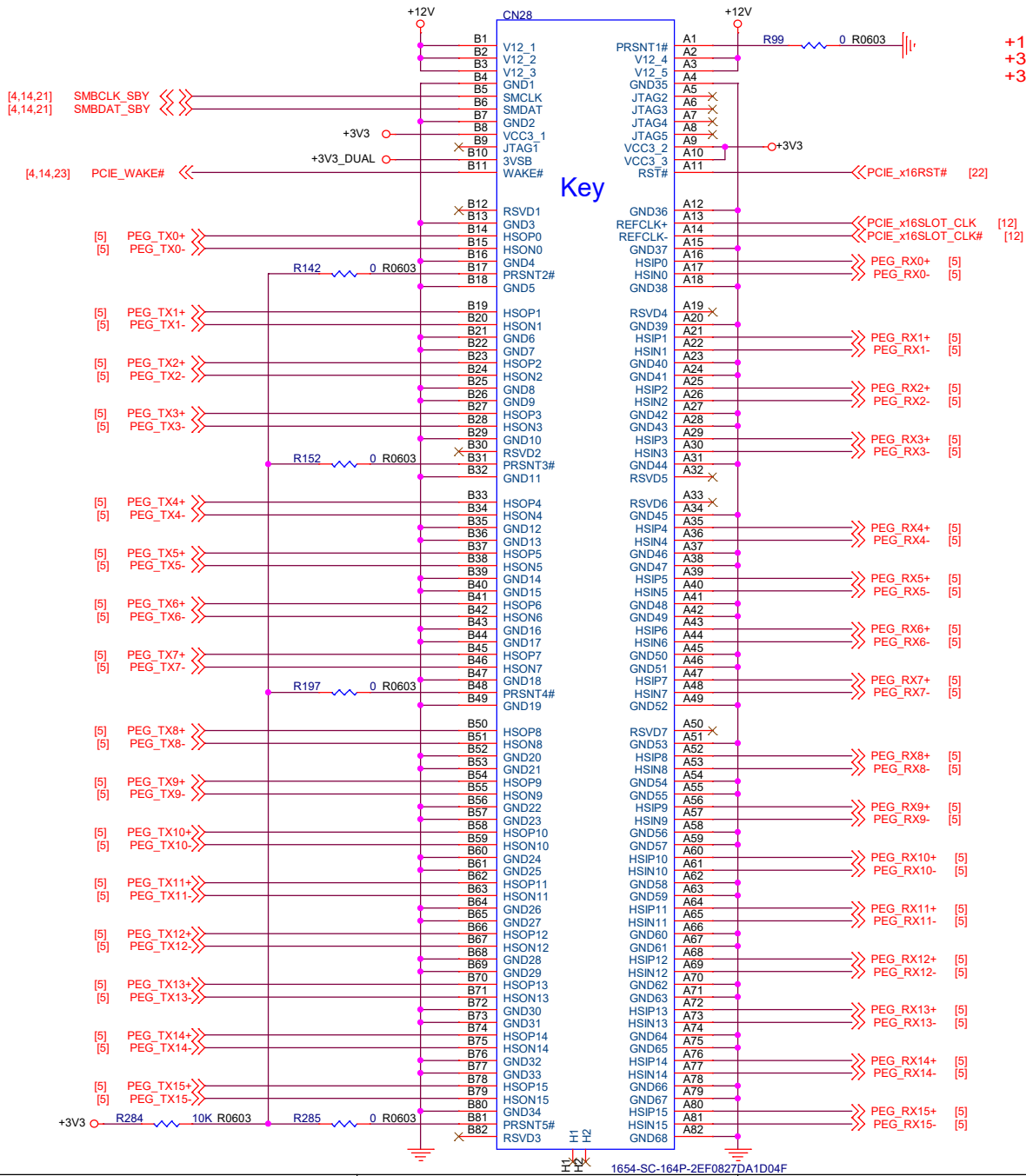


Title USB2.0		
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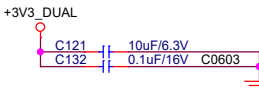
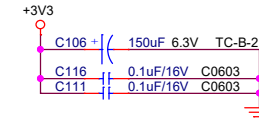
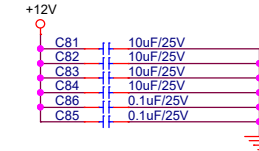


Title SATA		
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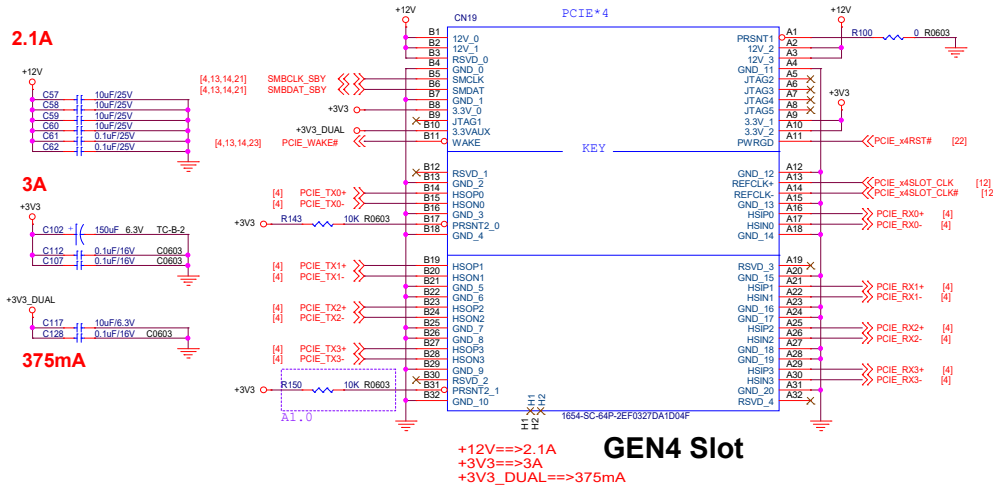


+12V==>5.5A
+3V3==>3A
+3V3_DUAL==>375mA

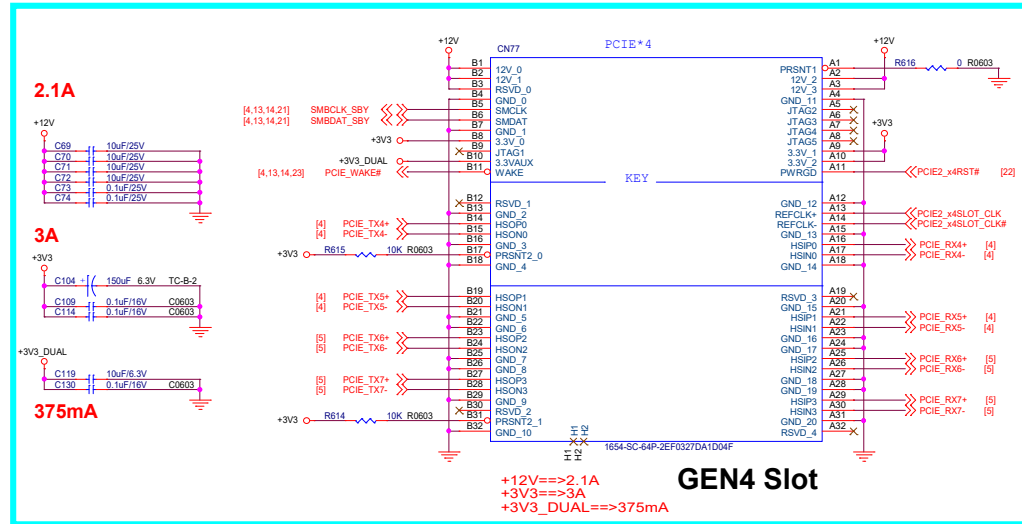


Title PCI Express Graphic		
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PCIe x4 GEN4 Slots



A1.1 add

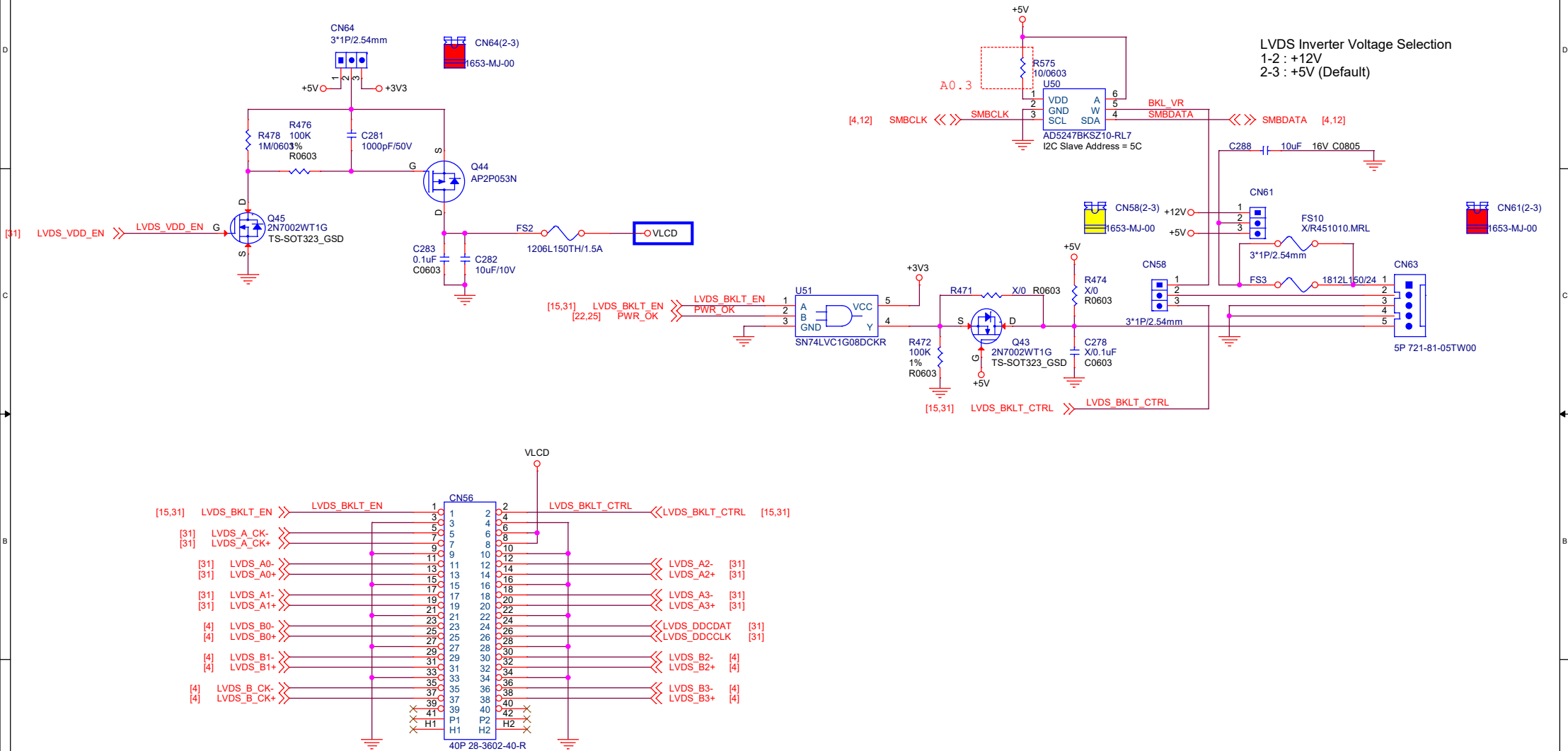


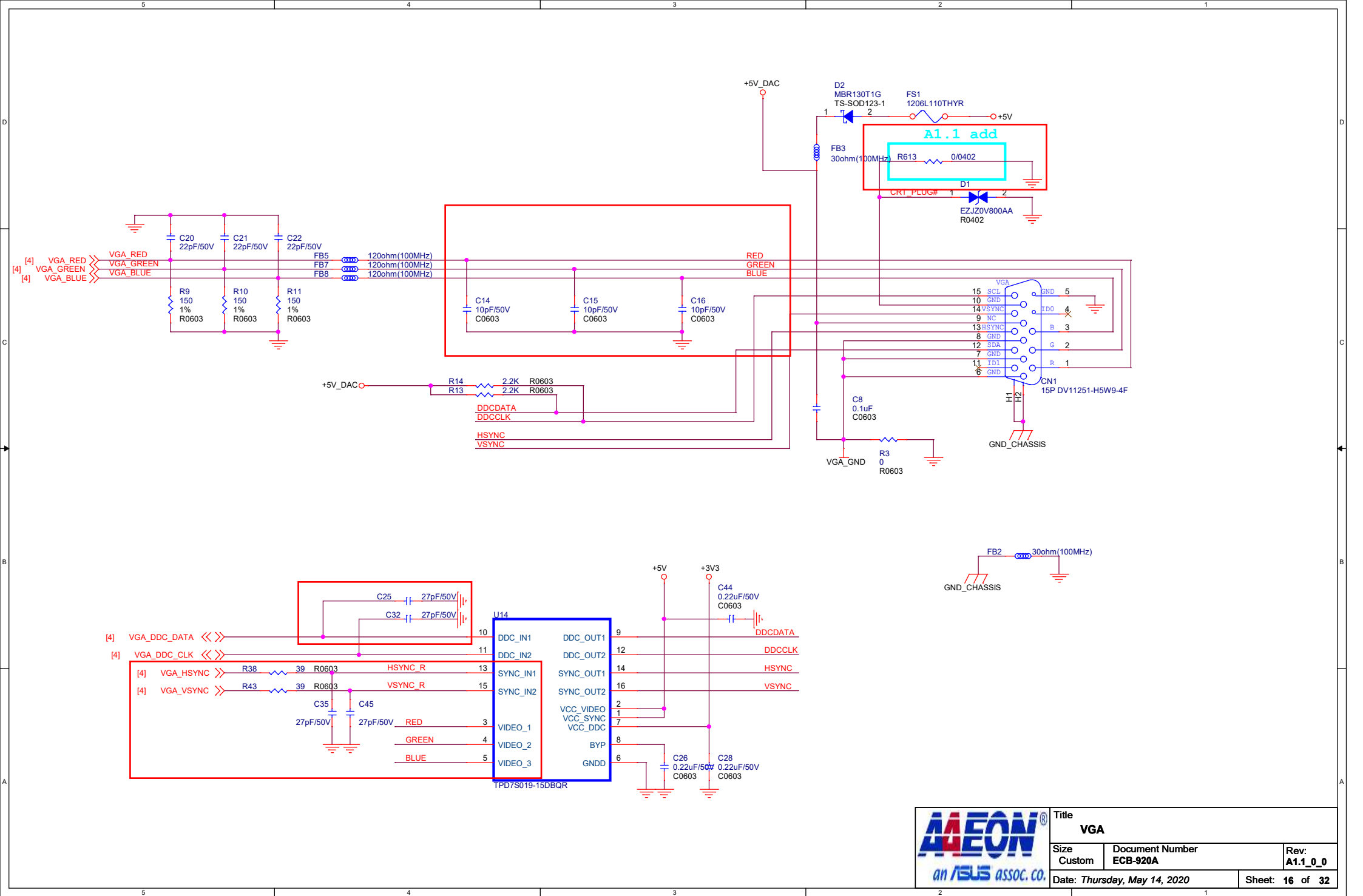
	X1		x4/x8	x16	
Standard height	10 W ¹ (max)	25 W ¹ (max)	25 W (max)	25 W ² (max)	75 W ^{2,4} (max)
Low profile card ³	10 W (max)		25 W (max)	25 W (max)	

Power Rail	10 W Slot	25 W Slot	75 W Slot
+3.3V			
Voltage tolerance	± 9% (max)	± 9% (max)	± 9% (max)
Supply Current	3.0 A (max)	3.0 A (max)	3.0 A (max)
Capacitive Load	1000 µF (max)	1000 µF (max)	1000 µF (max)
+12V			
Voltage tolerance	± 8%	± 8%	± 8%
Supply Current	0.5 A	2.1 A (max)	5.5 A (max)
Capacitive Load	300 µF (max)	1000 µF (max)	2000 µF (max)
+3.3Vaux			
Voltage tolerance	± 9% (max)	± 9% (max)	± 9% (max)
Supply Current			
Wakeup Enabled	375 mA (max)	375 mA (max)	375 mA (max)
Non-wakeup Enabled	20 mA (max)	20 mA (max)	20 mA (max)
Capacitive Load	150 µF (max)	150 µF (max)	150 µF (max)

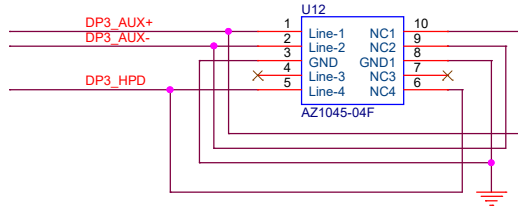
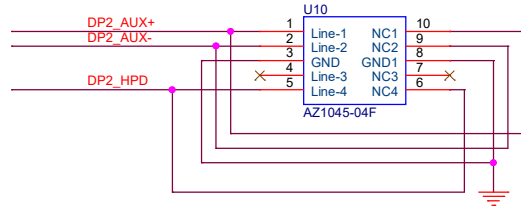
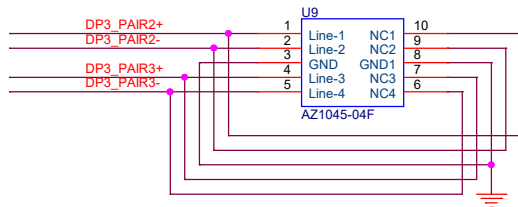
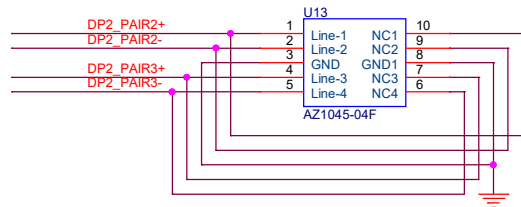
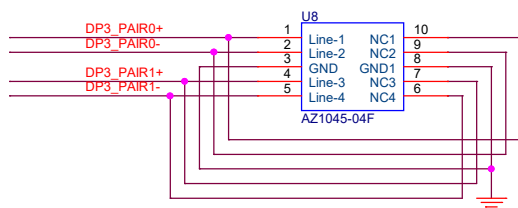
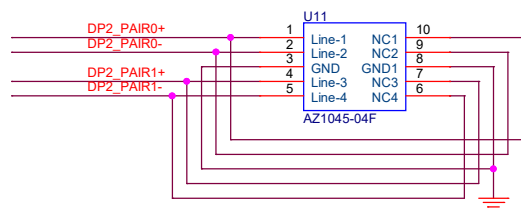
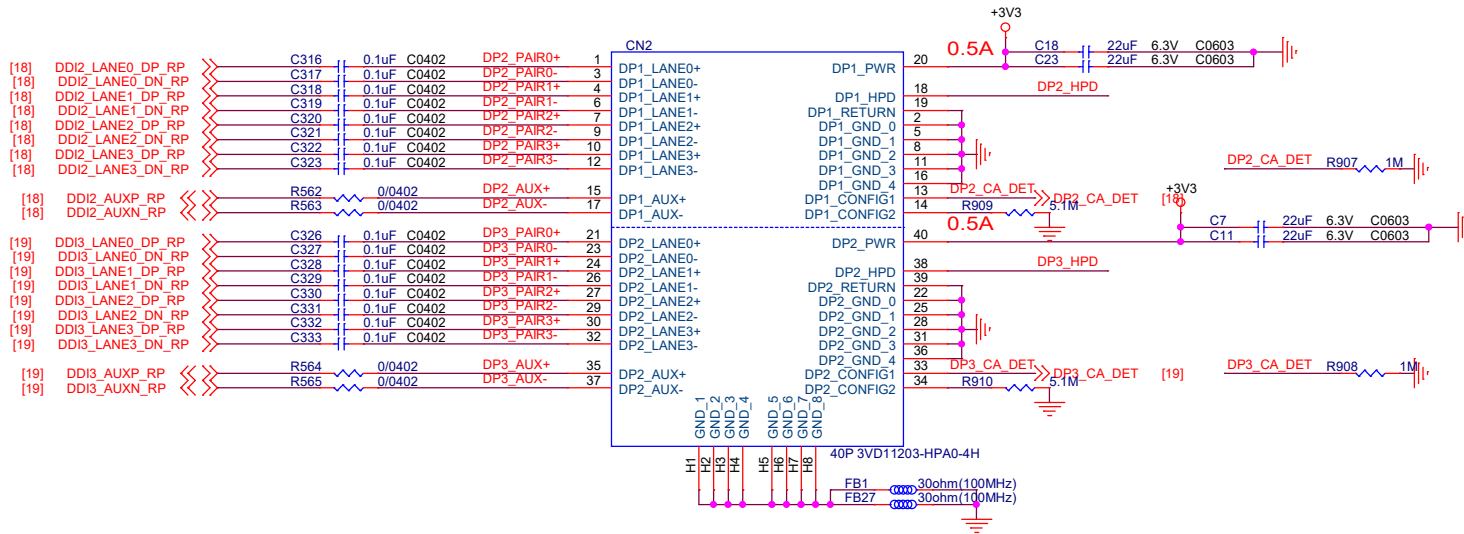
LVDS Operating Voltage Selection
 1-2 : +5V
 2-3 : +3.3V (Default)

LVDS Inverter Voltage Selection
 1-2 : +12V
 2-3 : +5V (Default)

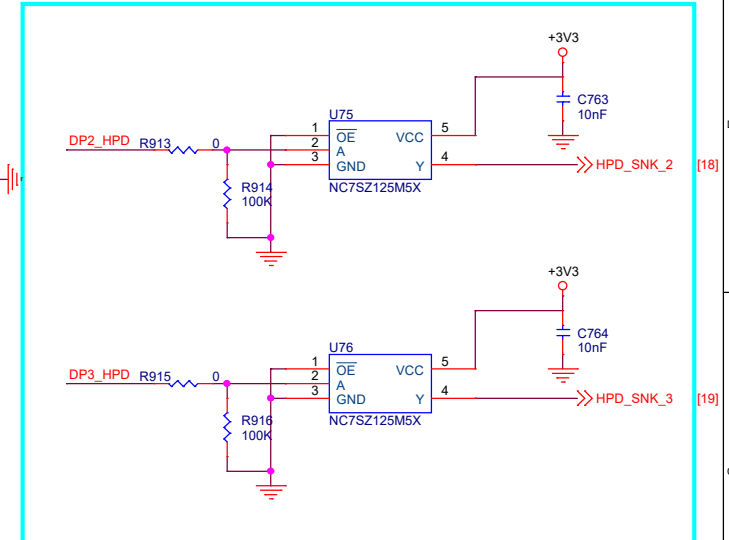




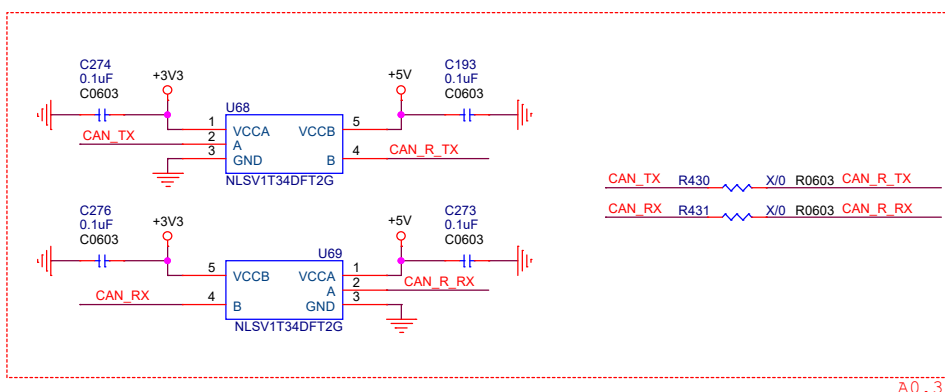
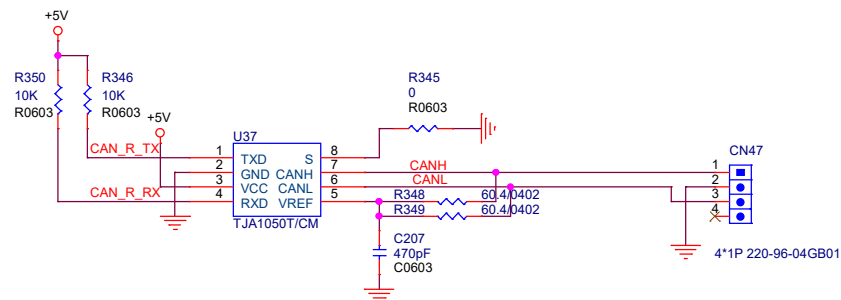
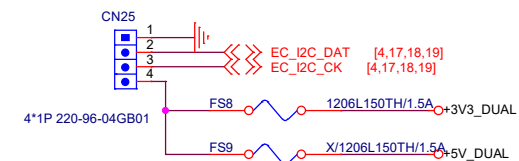
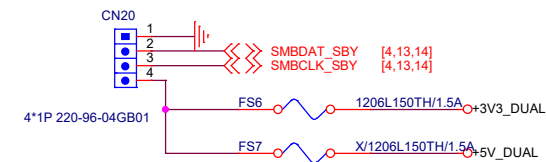
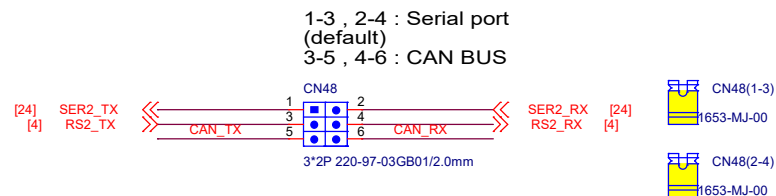
Title VGA		
Size Custom	Document Number ECB-920A	Rev: A1.1_0_0
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A1.1 add



Title DP port x2		
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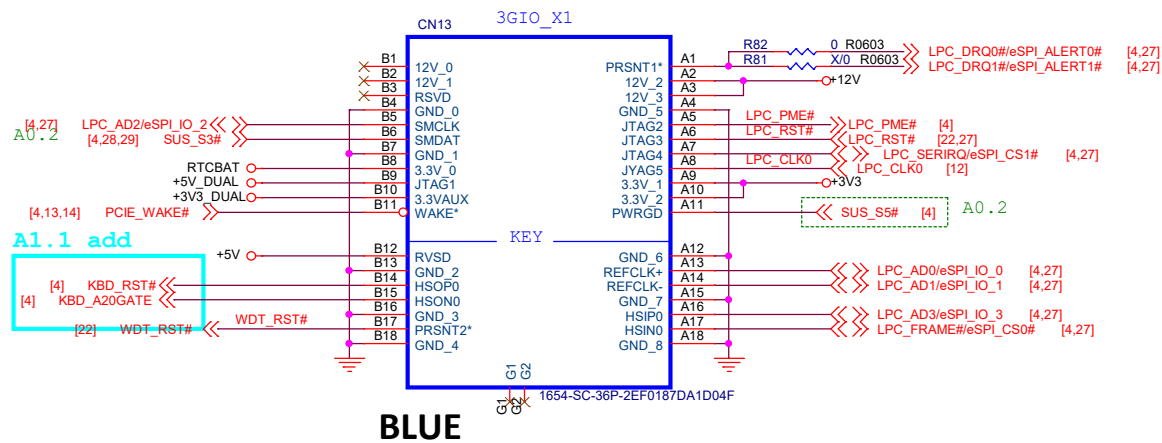


A0.3

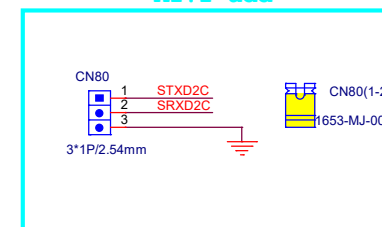
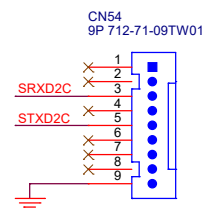
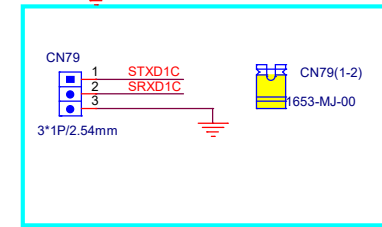
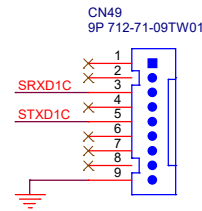
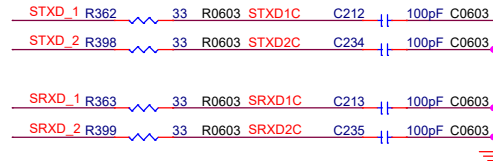
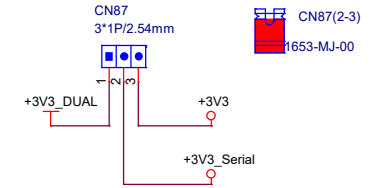
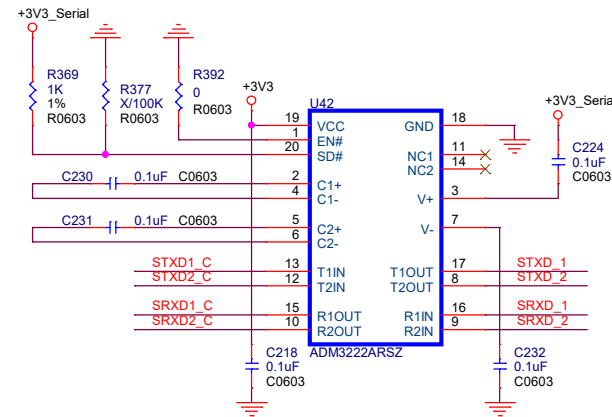
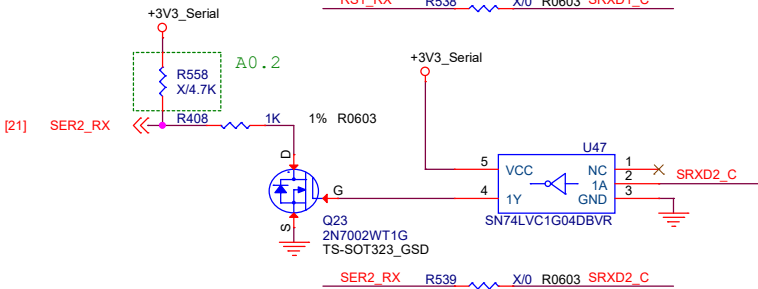
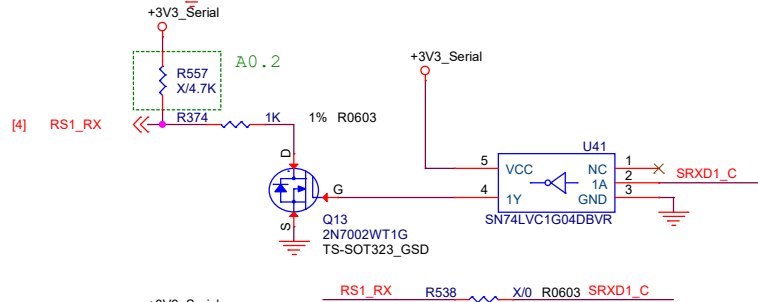
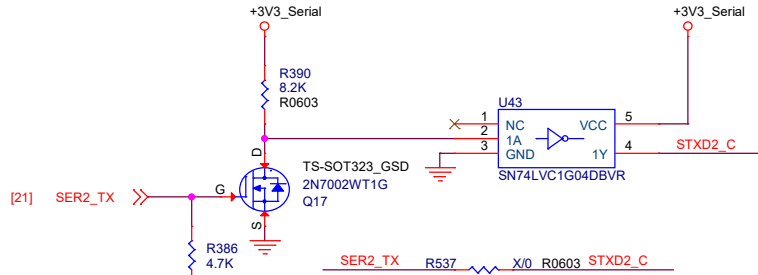
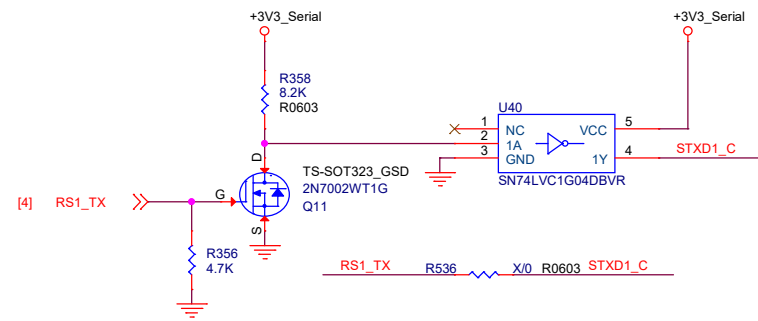


Title CANBUS / SMBUS / I2C		
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SIO Slot



Title		SIO Card Slot	
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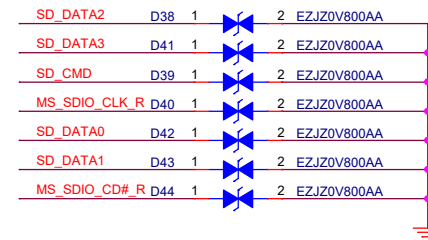
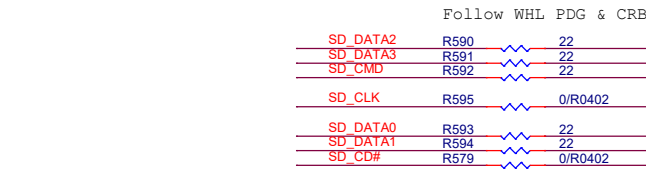
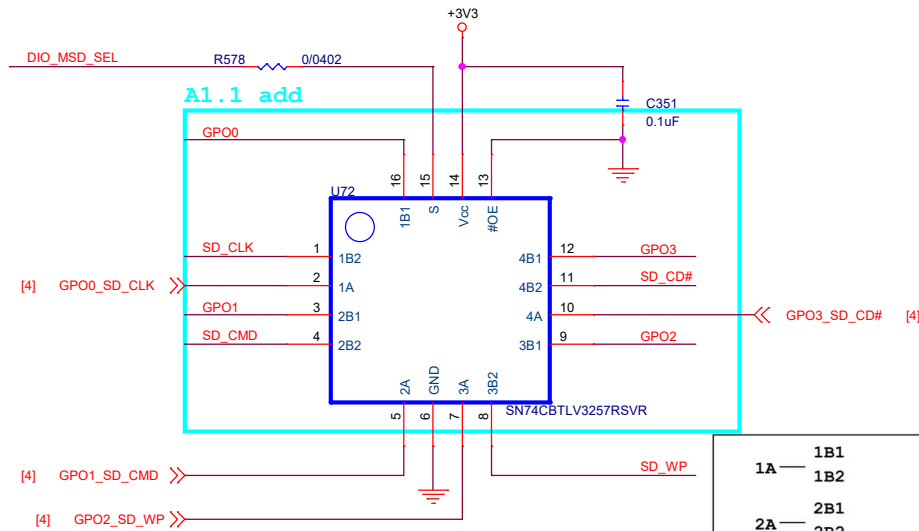
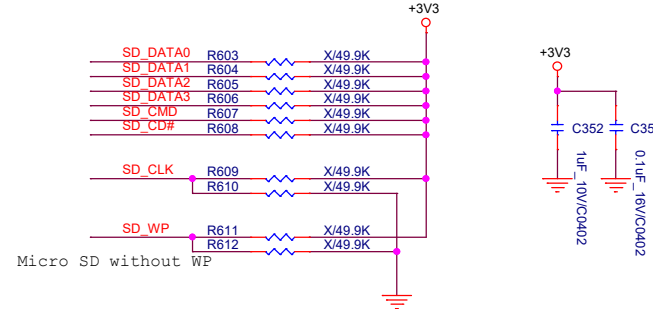
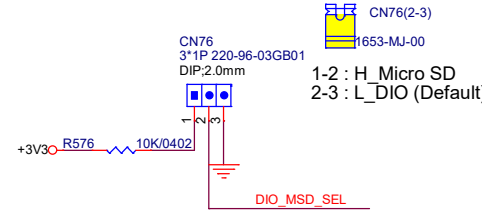
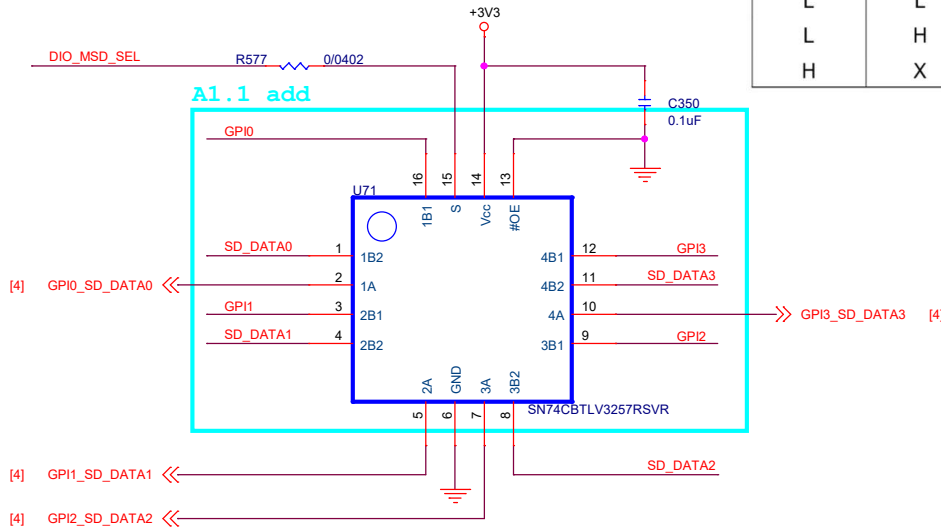
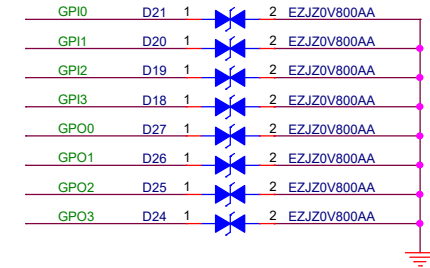
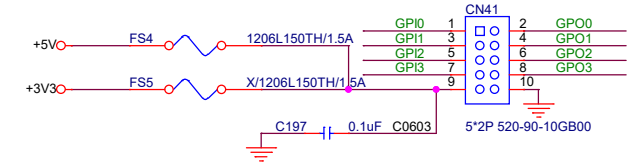


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Size B	Document Number ECB-920A	Rev: A1.1_0_0
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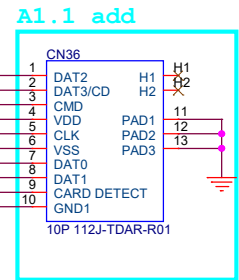
DIO/ Micro SD

INPUTS		FUNCTION
OE	S	
L	L	A port = B1 port
L	H	A port = B2 port
H	X	Disconnect

Digital I/O

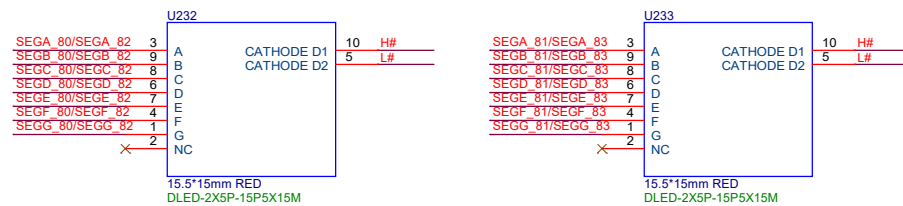


Micro SD

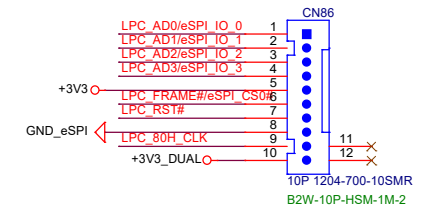


Title		
DIO/ Micro SD		
Size B	Document Number ECB-920A	Rev: A1.1_0_0
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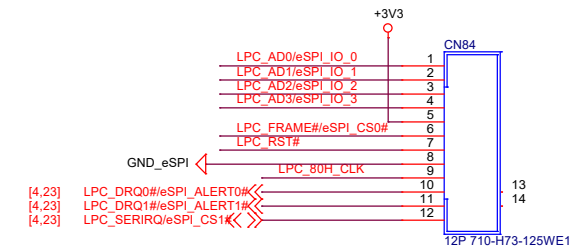
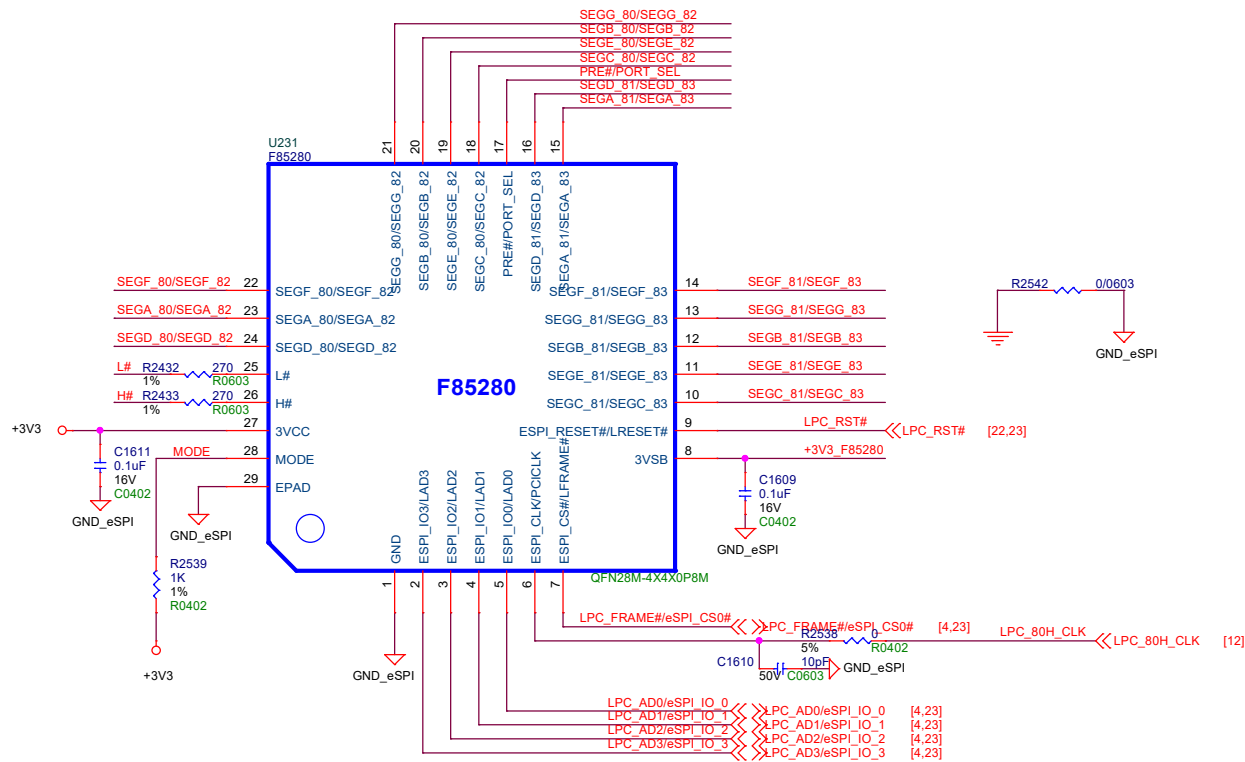
eSPI Connector



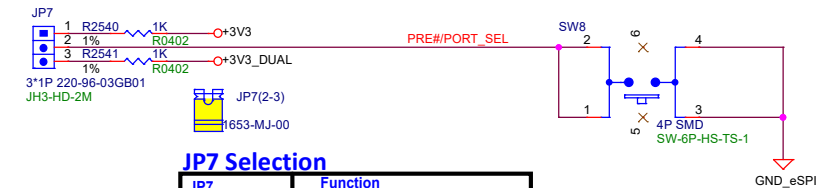
JP6 Selection	
JP6	Function
1-2	LPC
2-3 (Default)	eSPI



LPC Connector



Port Selection

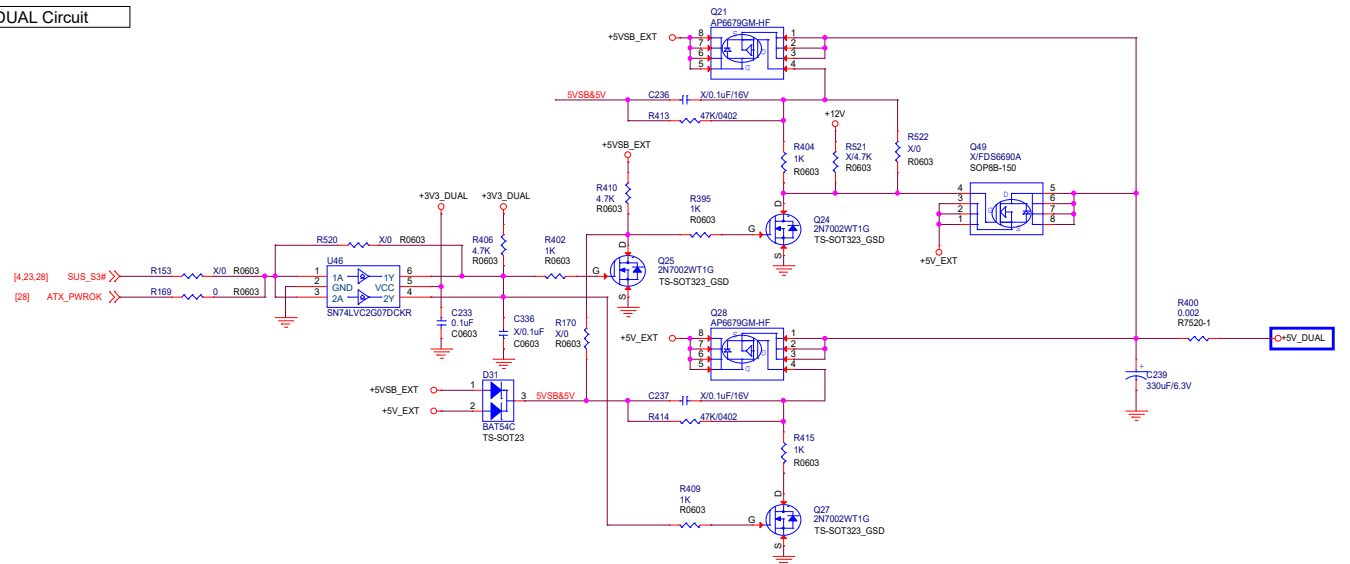


JP7	Function
1-2	82/83 Port
2-3 (Default)	80/81 Port

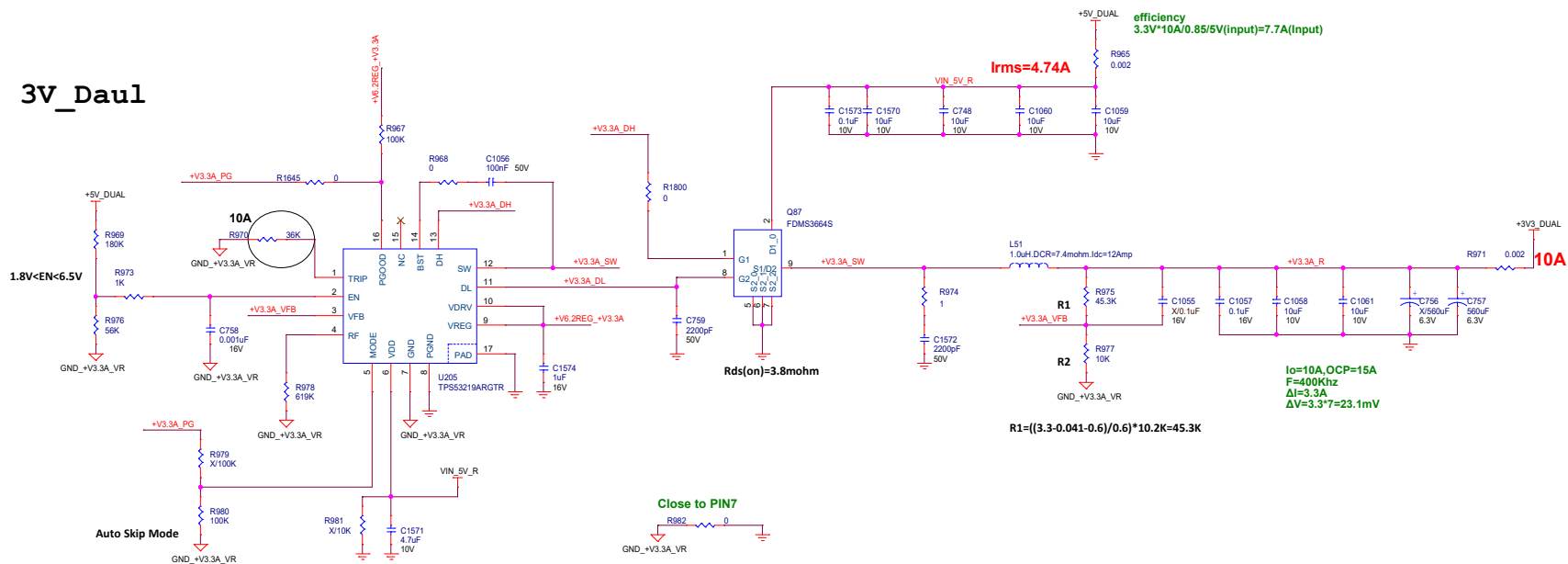


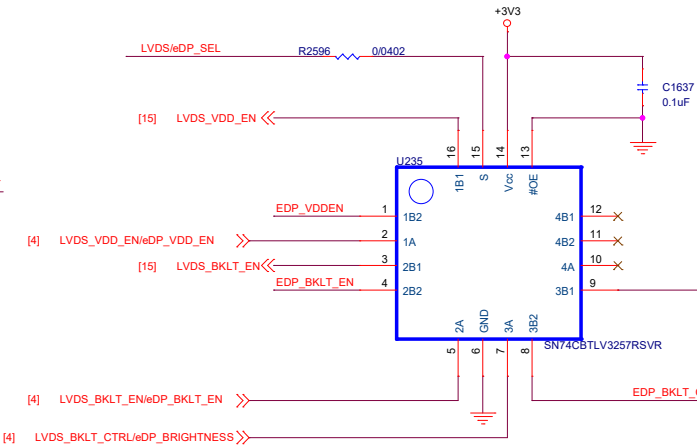
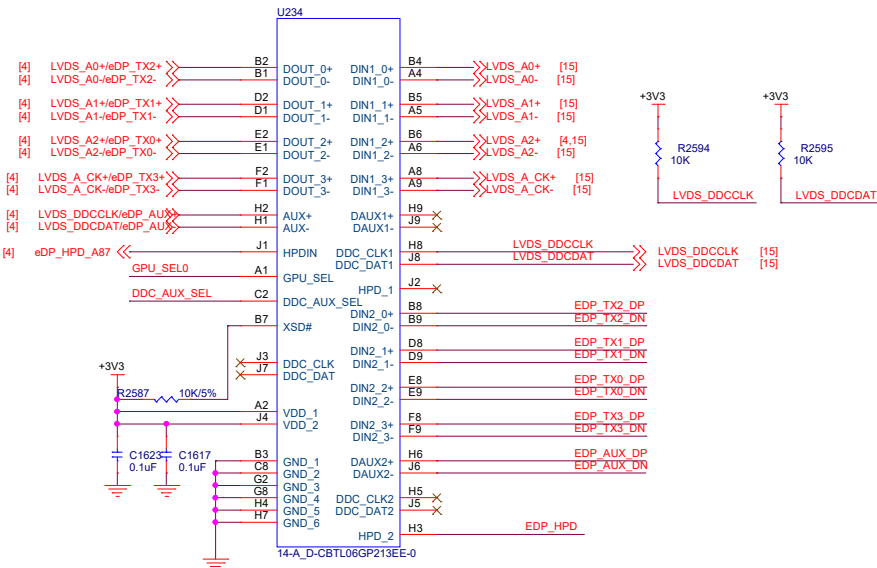
Title		
F85280 LPC+eSPI 80H		
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+5V DUAL Circuit

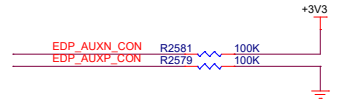
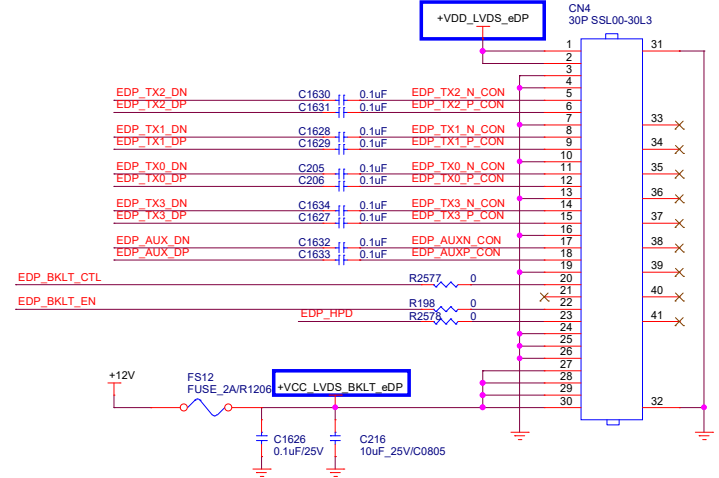
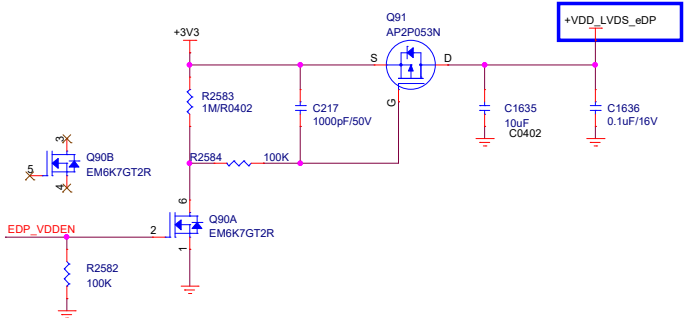
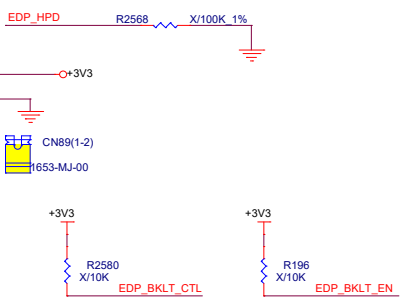
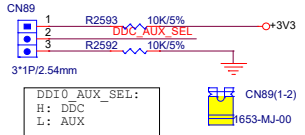
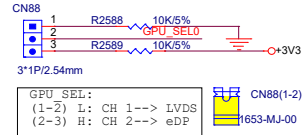
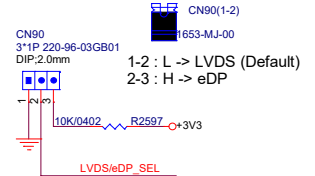



3V_Daul





INPUTS		FUNCTION
OE	S	
L	L	A port = B1 port
L	H	A port = B2 port
H	X	Disconnect





Title

eDP+eDP/LVDS Switch

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Change History

Item	Date	Revision	Description	Page	Design By	Approve By
1	2012/12/03	A0.1	First Release.	1 ~ 28	Ronald Lin	Richard Wu
2	2013/03/25	A0.2	1.Add U59,C312,R559,CN67 for USB 2.0 Switch.	4	Ronald Lin	Richard Wu
			1.Add U60,C314,C313,C315,R317,R316,R312,R310,R517,R331,R519,R516,R224,R223,R330,CN22 for SATA Switch.	7		
			1.Codec Power change from +5V to +5V_DUAL.	8		
			1.Add Q50,R561. 2.Change Q47 to AP2305GN.	10		
			1.Change DP Port AC CAP from 0 ohm to 0.1uF.	14		
			1.Change SIO Slot pin define and add SUS_S3#,SUS_S5 #signal.	17		
			1.ADD R557,R558.	18		
3	2013/07/23	A0.3	1.Chang U25 from IC59DB108BFLF to 9DB833AFLF. 2.Add Q57,R532.	10	Ronald Lin	Richard Wu
			1.Remove C324,C325,C334,C335. 2.Add R562,R563,R564,R565.	14		
			1.Change R44,R220,R221,R225 from 1K to 100K. 2.Add R50,R179,R182,R183,R184,R185,R186,R187,R188,R200,R201,R204,R205,R206,R207,R208	3		
			1.Add Q54,Q55,R460,R245,R456,R248.	20		
			1.Reserve R277,R515.	8		
			1.Change DDI1 connector to DP connector.	13		
			1.Add U66,U69,C193,C273,C274,C276,R430,R431.	15		
			1.Change DDI0 connector to DP connector. 2.Add R210.	24		
			1.Modify smart FAN circuit.	19		
			1.Add R575.	12		
3	2013/10/25	A1.0	1.Change R150 connect to B31 of CN19.	9	Ronald Lin	Richard Wu

Item	Date	Revision	Description	Page	Design By	Approve By
1	2020/02/12	A1.1_0_0	Add U211, U212, U213 AUX/DDC switch for DP retimer	5	JeffChang	
2	2020/02/12	A1.1_0_0	Add U229, U230 USB3.1 GEN2 redriver	6	JeffChang	
3	2020/02/13	A1.1_0_0	change CN6 RJ45USB to USB3.1 GEN2	7	JeffChang	
4	2020/02/13	A1.1_0_0	change CN26 USB port to USB3.1 GEN2	8	JeffChang	
5	2020/02/13	A1.1_0_0	CN78 change to quad port	9	JeffChang	
6	2020/02/13	A1.1_0_0	upgrade U25 Clock Buffer to GEN4 speed	12	JeffChang	
7	2020/02/13	A1.1_0_0	PCIext*4 change to PCIe*4	14	JeffChang	
8	2020/02/13	A1.1_0_0	Add CN85 LVDS_A3+ & eDP HPD jumper	15	JeffChang	
9	2020/02/13	A1.1_0_0	Add R613 for CRT Always On function disable	16	JeffChang	
10	2020/02/13	A1.1_0_0	Add R613 for CRT Always On function disable	17~19	JeffChang	
11	2020/02/13	A1.1_0_0	Add U75, U76 DP HPD buffer	20	JeffChang	
12	2020/02/13	A1.1_0_0	CN13 Add KB_RST# & A20GATE	23	JeffChang	
13	2020/02/13	A1.1_0_0	Add CN79, CN80 for COM Port TX/RX aways short	24	JeffChang	
14	2020/02/13	A1.1_0_0	Debug Code change to LPC/eSPI Interface	27	JeffChang	
15	2020/03/24	A1.1_0_0	Add eDP & eDP/LVDS switch	31	JeffChang	