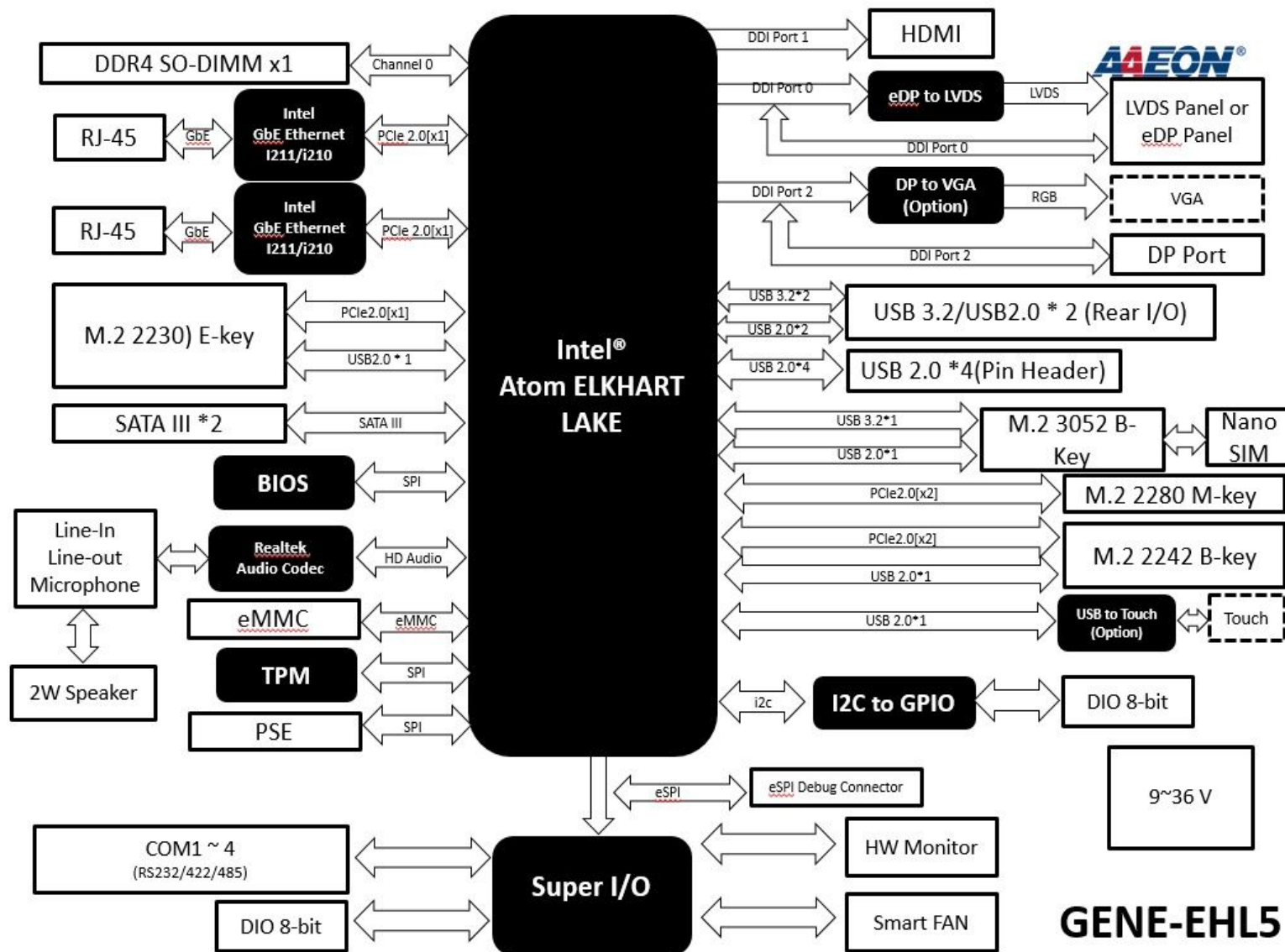


System block diagram

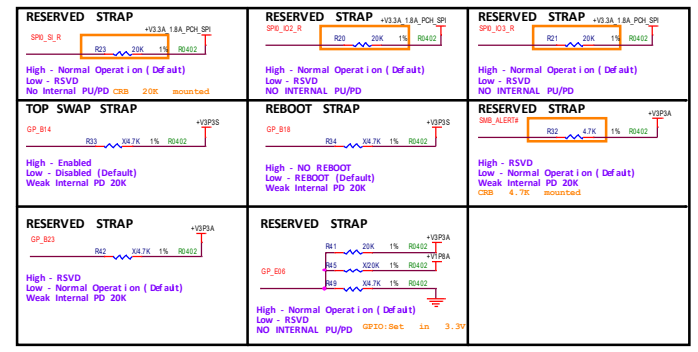
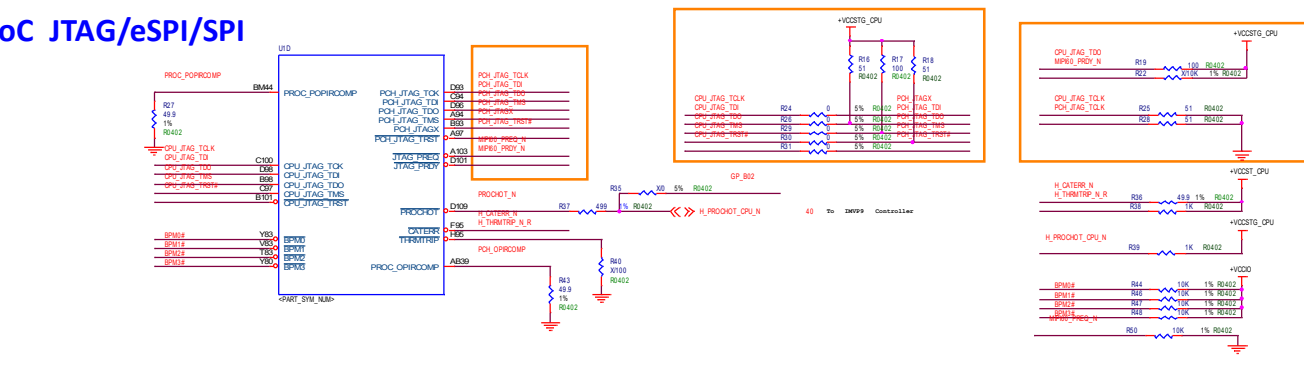


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40	PWR_IMVP9 Controller
41	PWR_+VCCIN
42	PWR_+V12S/ +V5S/ +V3P3S
43	PWR_+VCCSTG/+VCCSFR_OC
44	Power Sequence
45	Revision History

GENE-EHL5

<Variant Name>

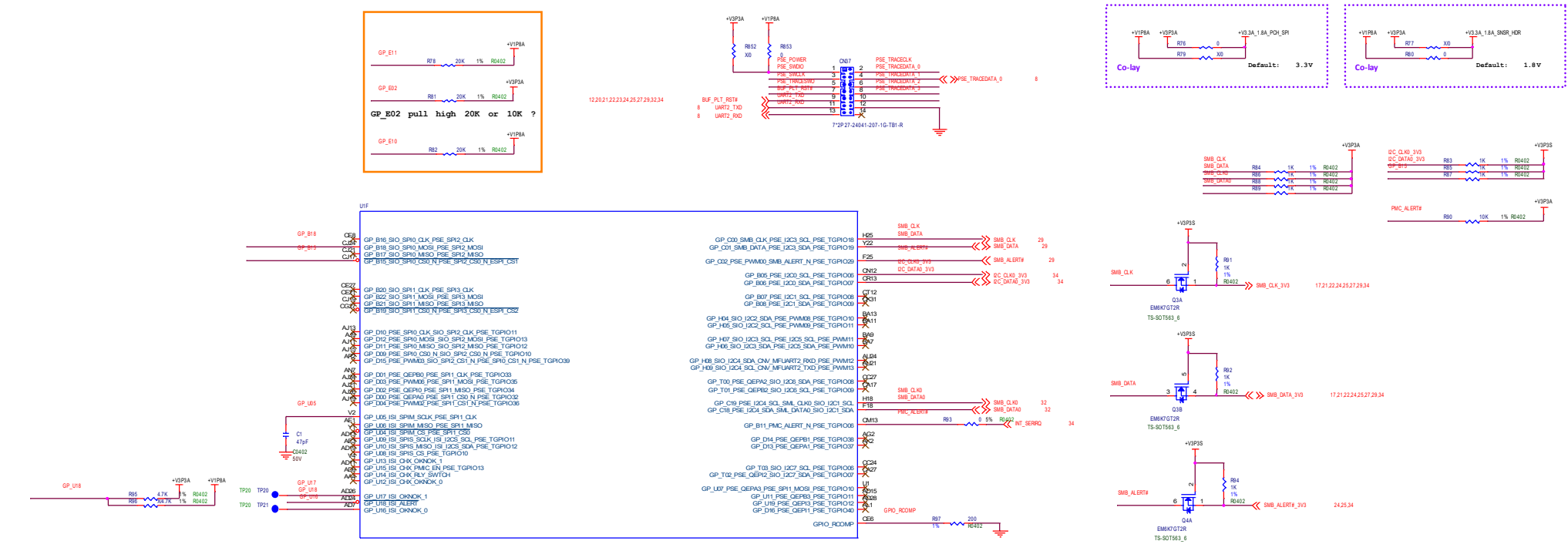
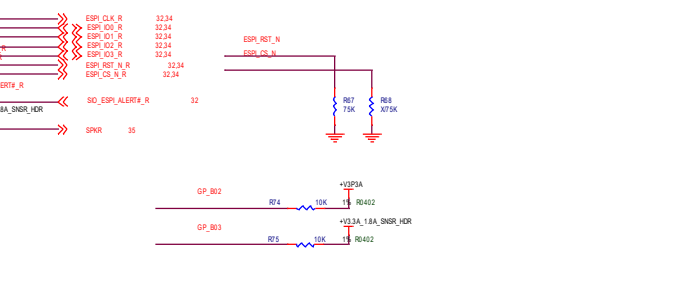
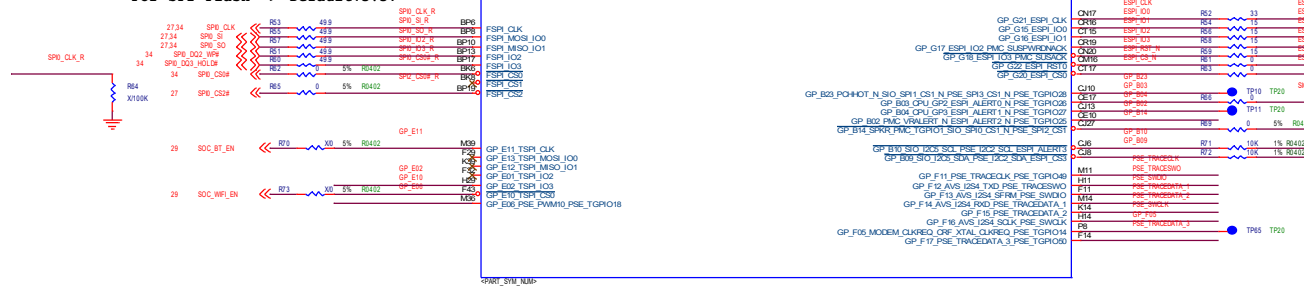
SoC JTAG/eSPI/SPI



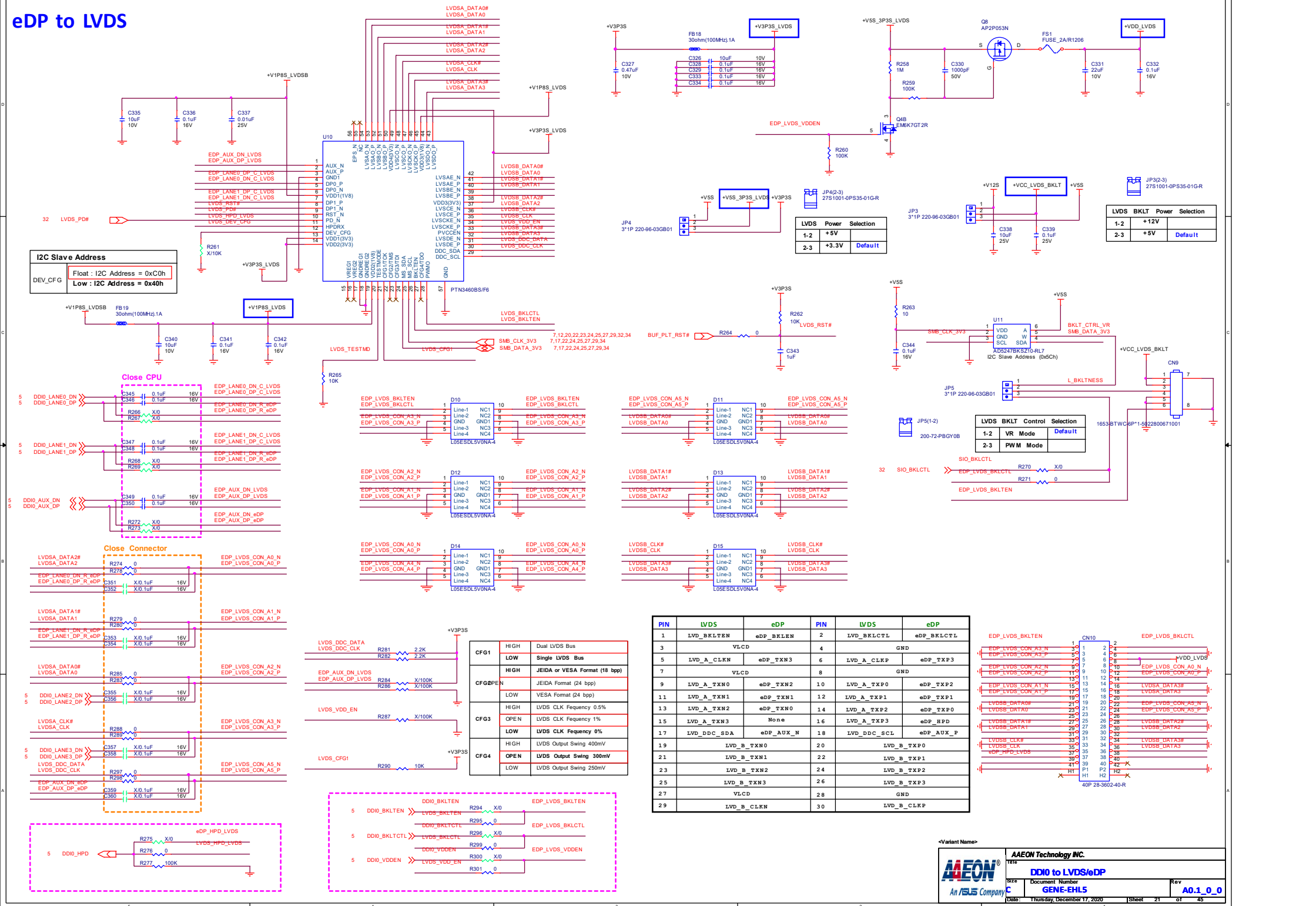
For GP E06:
The pull-up rail will be V1P8A or V3P3A,
depending on what I/O voltage the GPIO is configured to.

FSPI Interface supports 1.8V or 3.3V
For SPI Flash -> Default:3.3V

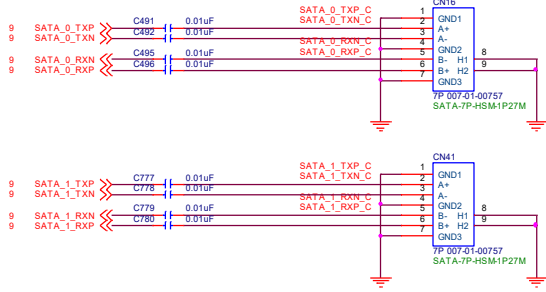
eSPI Interface supports 1.8V only



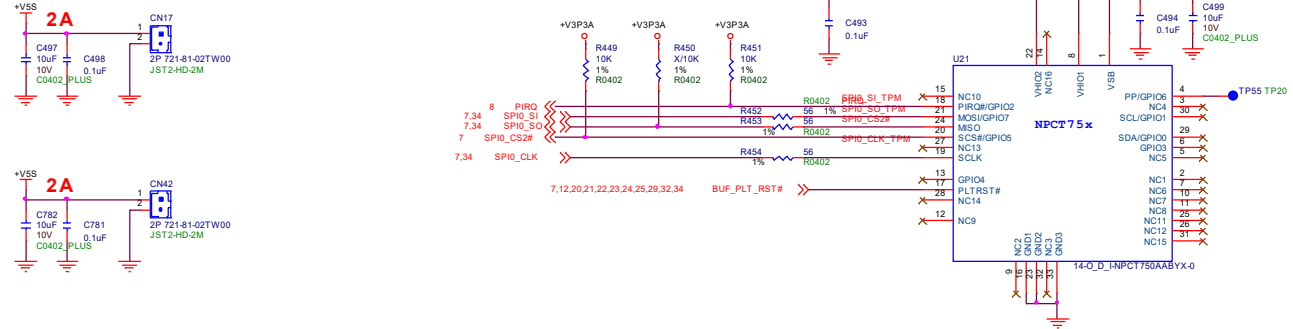
eDP to LVDS



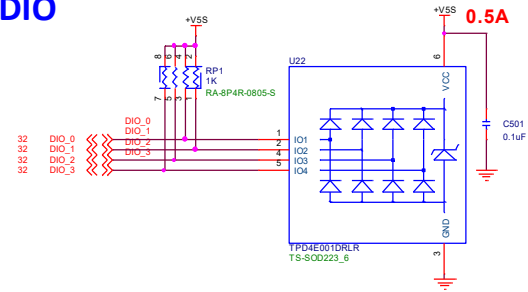
SATA



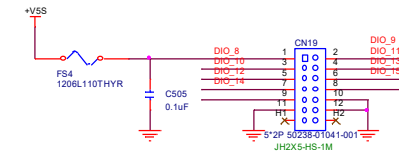
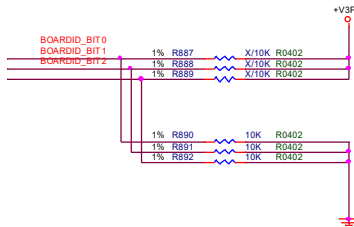
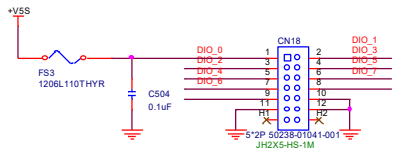
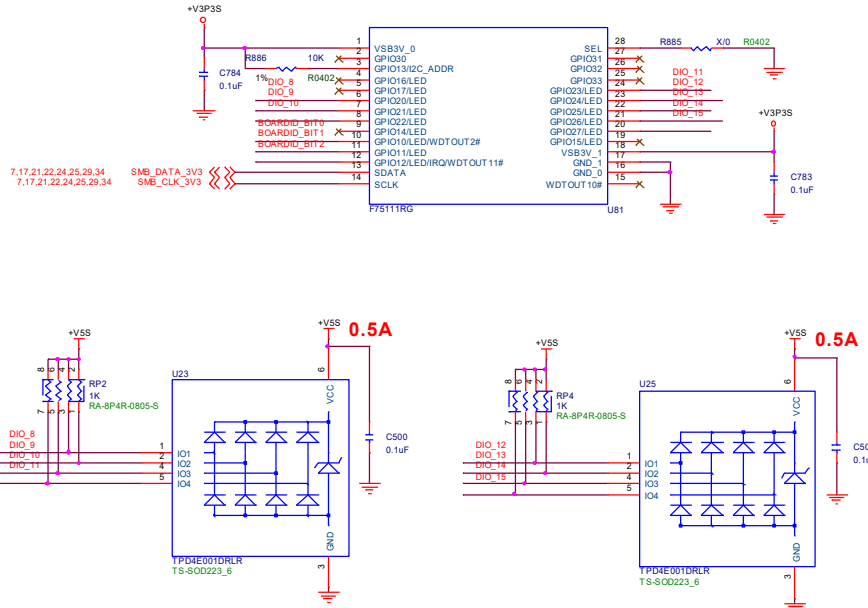
TPM



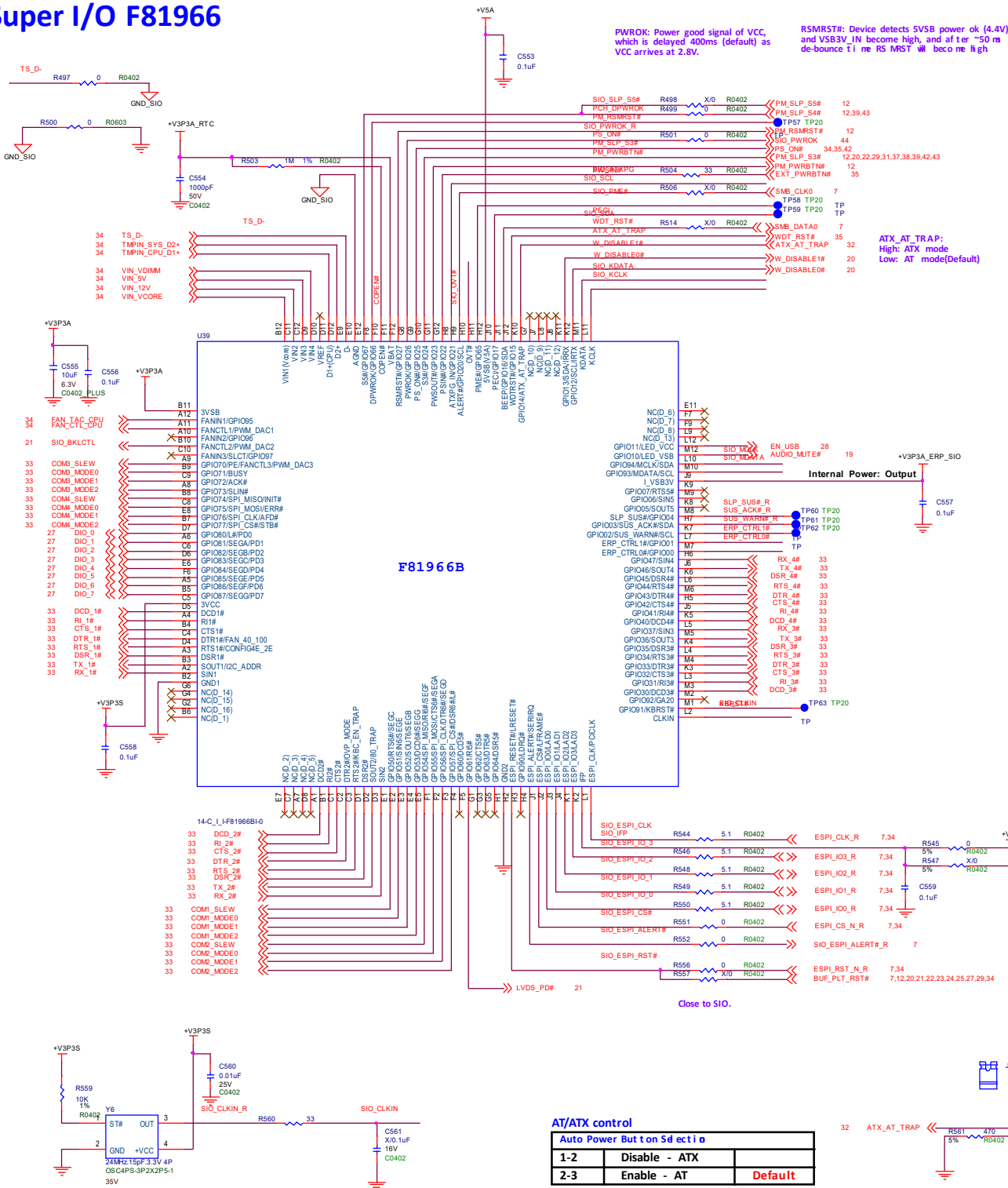
DIO



I2C Address	
GPIO13	Float : I2C Address = 0x9Ch
	High : I2C Address = 0x6Eh



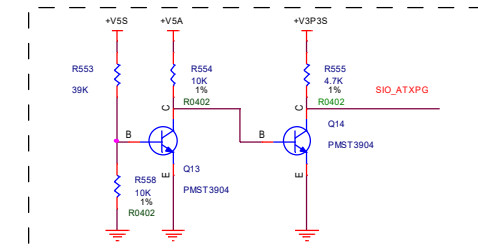
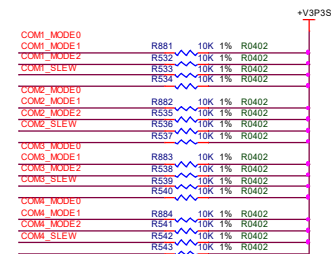
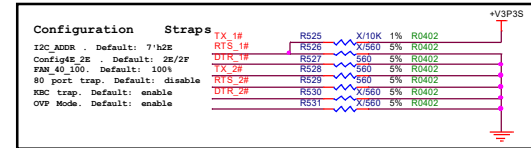
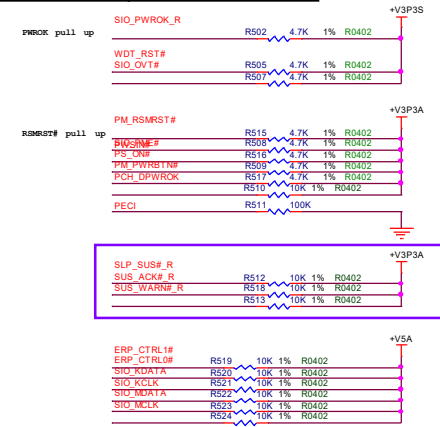
Super I/O F81966



Power On Strapping Options pin

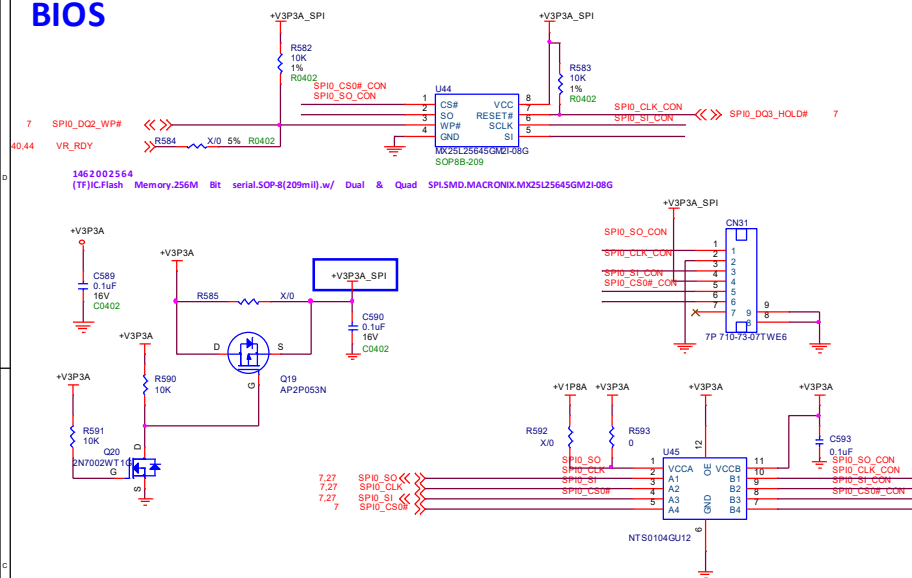
Power Trip

	0(Low)	1(High)
TX_1#	80 PORT ENABLE	80 PORT DISABLE
RTS_1#	CONFIG 2E	CONFIG 4E
DTR_1#	FAN 100%	FAN 40%



<Variant Name>

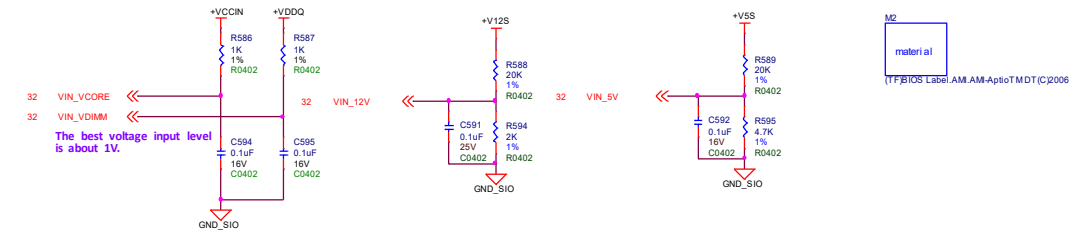
BIOS



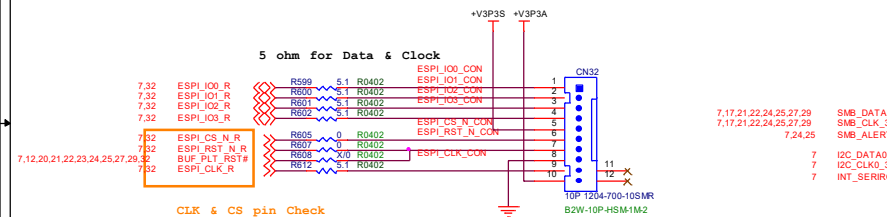
Temperature Monitor(CPU, SYS)



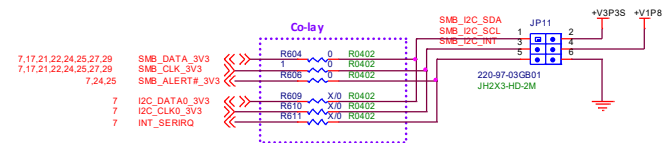
Voltage Monitor



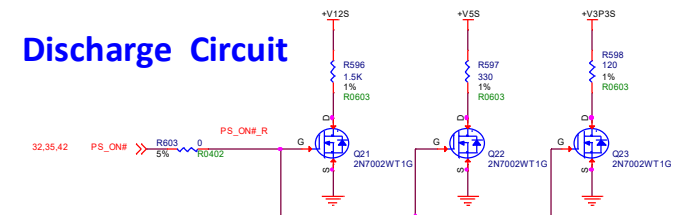
eSPI Connector (Debug Card)



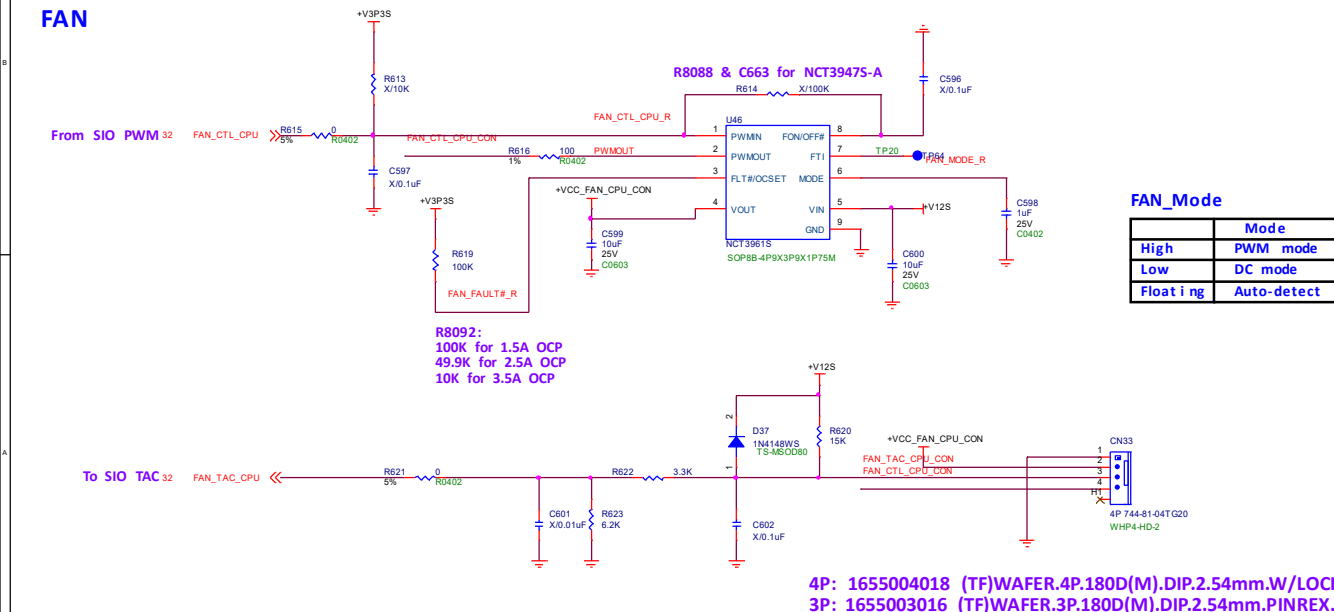
I2C/SMBus header



Discharge Circuit



FAN



4P: 1655004018 (TF)WAFER.4P.180D(M).DIP.2.54mm.W/LOCK.PINREX.744-81-04TG20
3P: 1655003016 (TF)WAFER.3P.180D(M).DIP.2.54mm.PINREX.742-81-03TW00