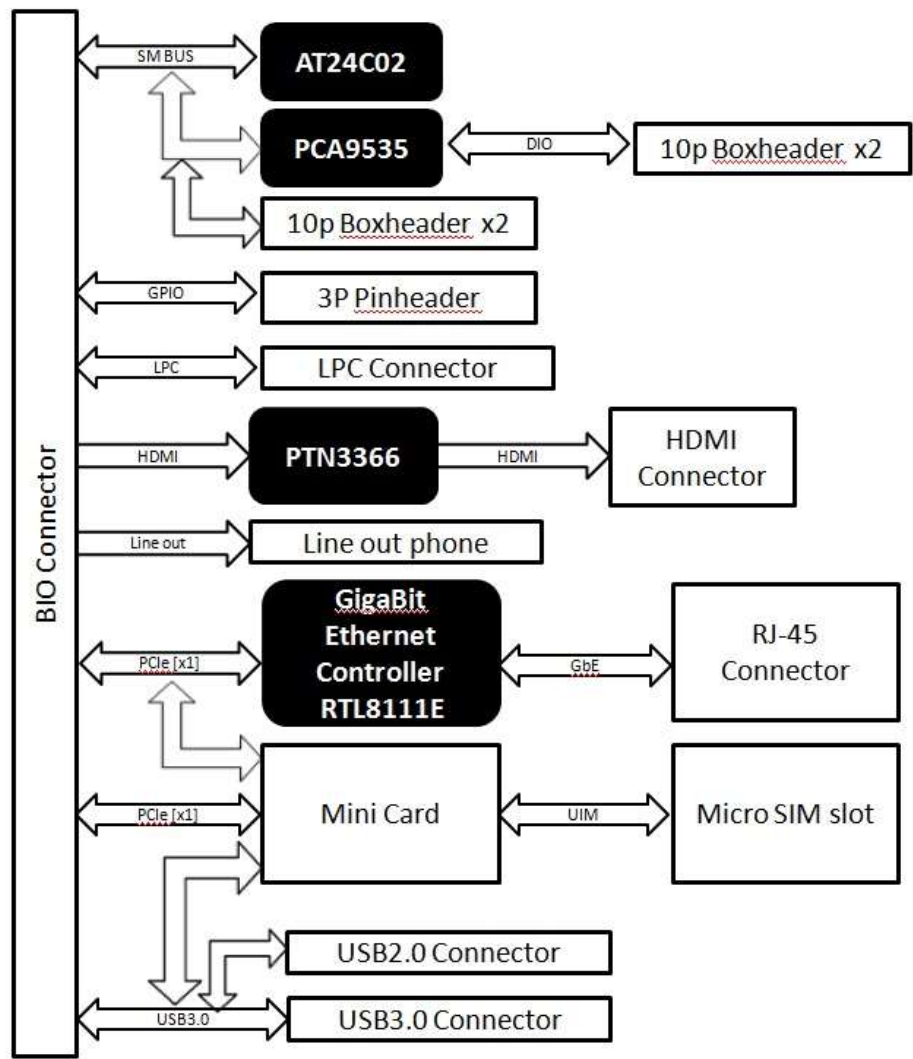


# BIO-ST05-H1L1 Rev.A0.1\_0\_0

## System Block Diagram



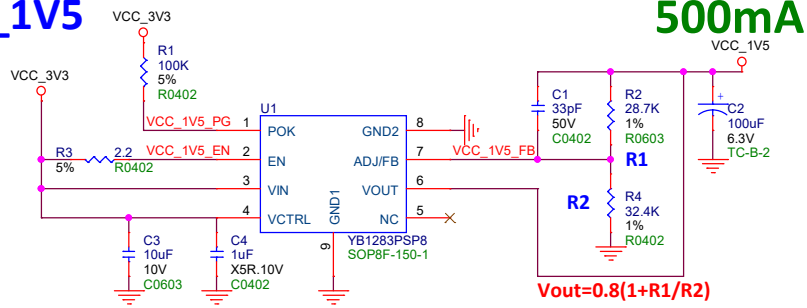
**PCB STACK :**

Impedence: 50ohm +/-10%  
Thickness: 1.6mm +/-10%

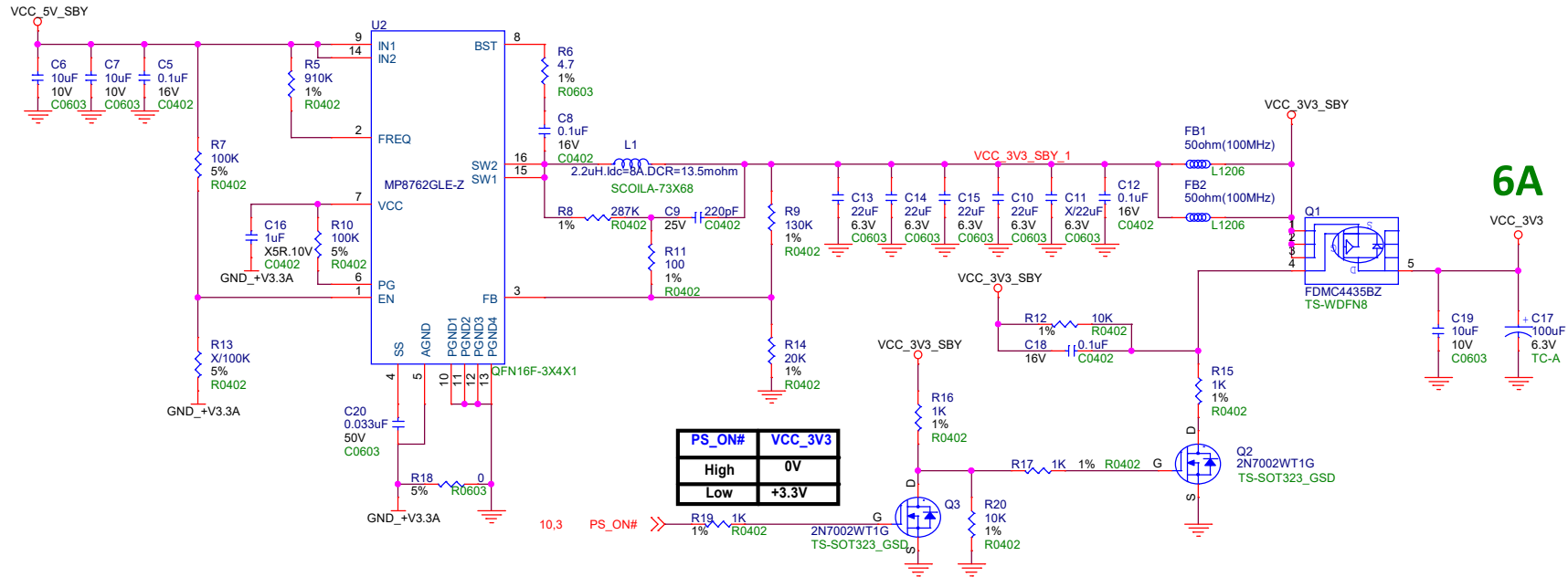
Layer 1 : Component (Top)  
Layer 2 : GND (GND1)  
Layer 3 : POWER (VCC)  
Layer 4 : Solder (Bottom)

Page	Index
1	Cover Sheet
2	Power
3	BIO/ LPC
4	LAN
5	Mini Card
6	Audio
7	USB
8	HDMI
9	SM BUS
10	Power Input
11	Revision History

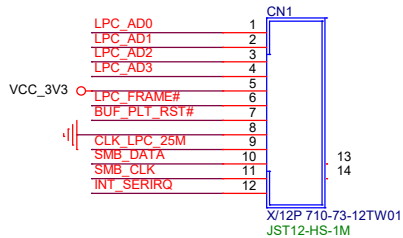
## VCC\_1V5



## VCC\_3V3\_SBY



## Debug Port



## LAN

## Input

## Clock Buffer

## mini-Card

## BIO Connector

## Mini Card

## Mini Card

## LAN

## mini-Card

## mini-Card

## LAN



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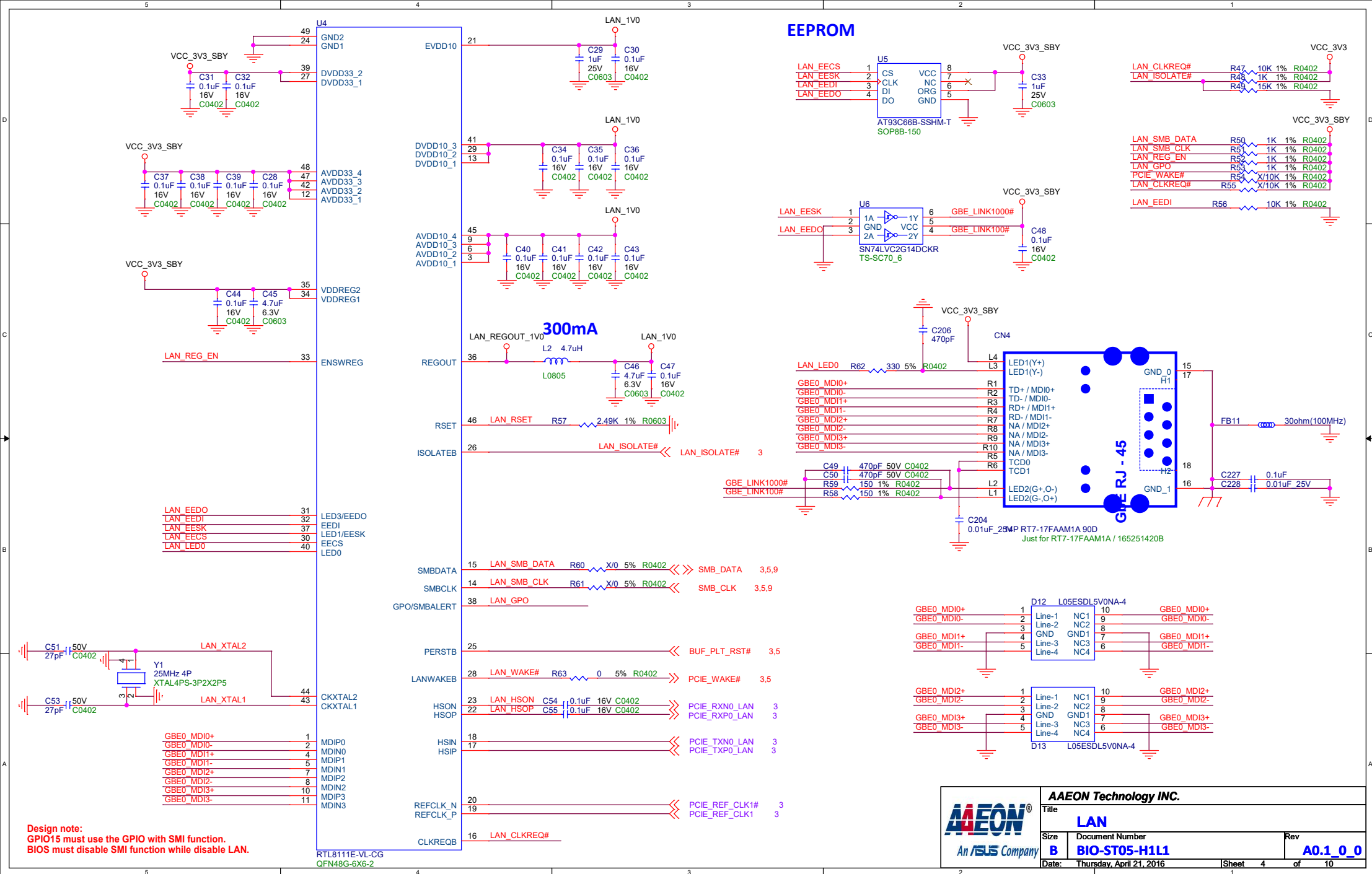
Title  
**BIO/ LPC**

Size  
**B**

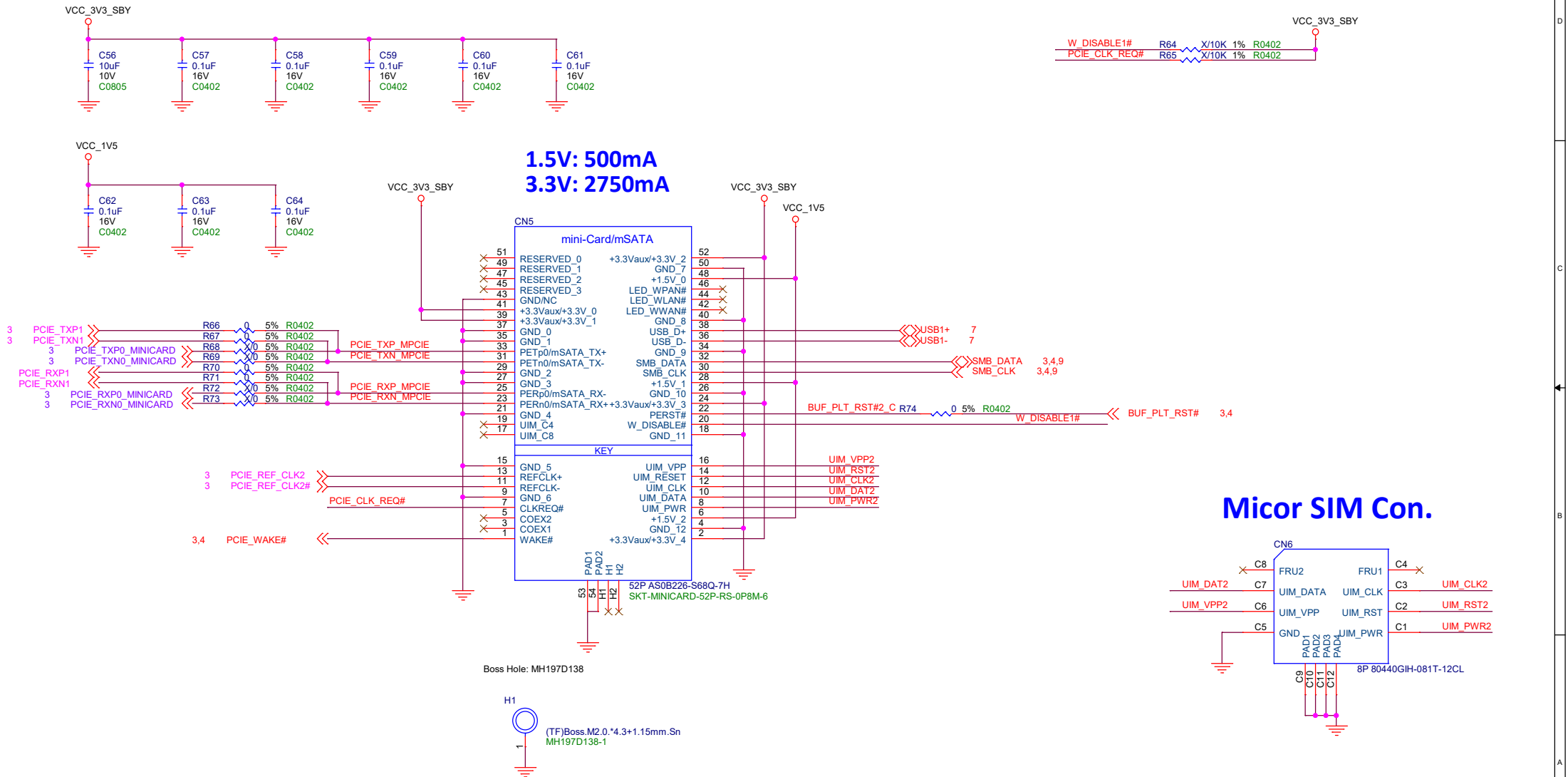
Date: Thursday, April 21, 2016

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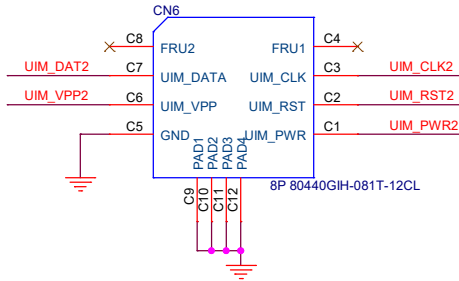
Rev  
**A0.1\_0\_0**



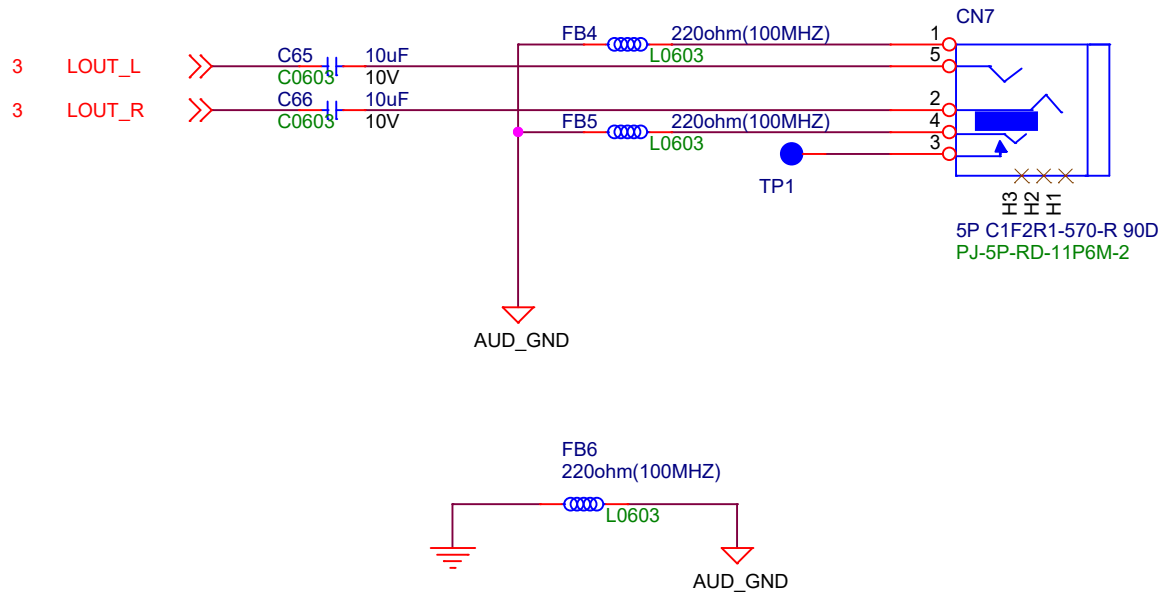
Mini-Card



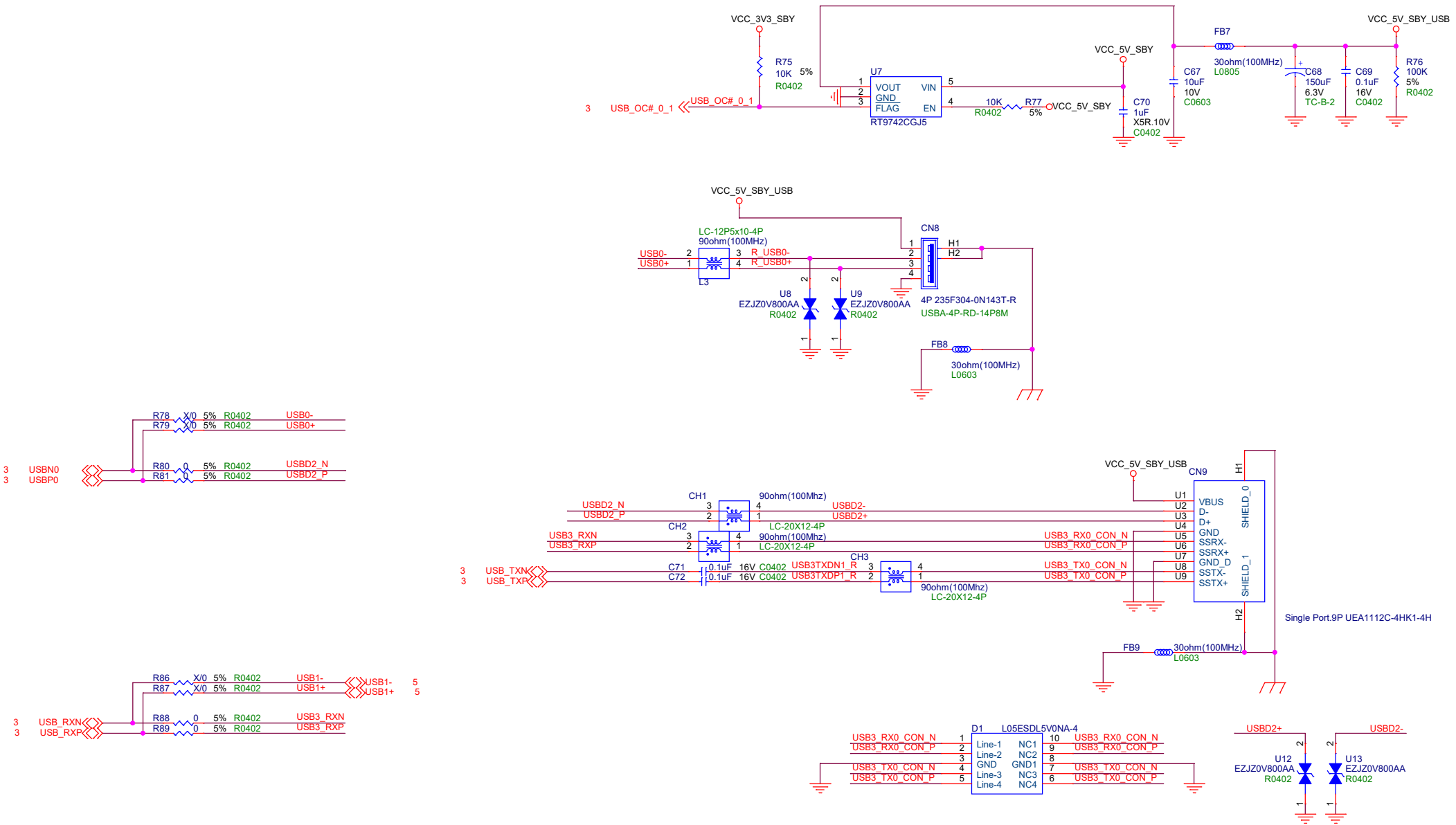
Micor SIM Con.

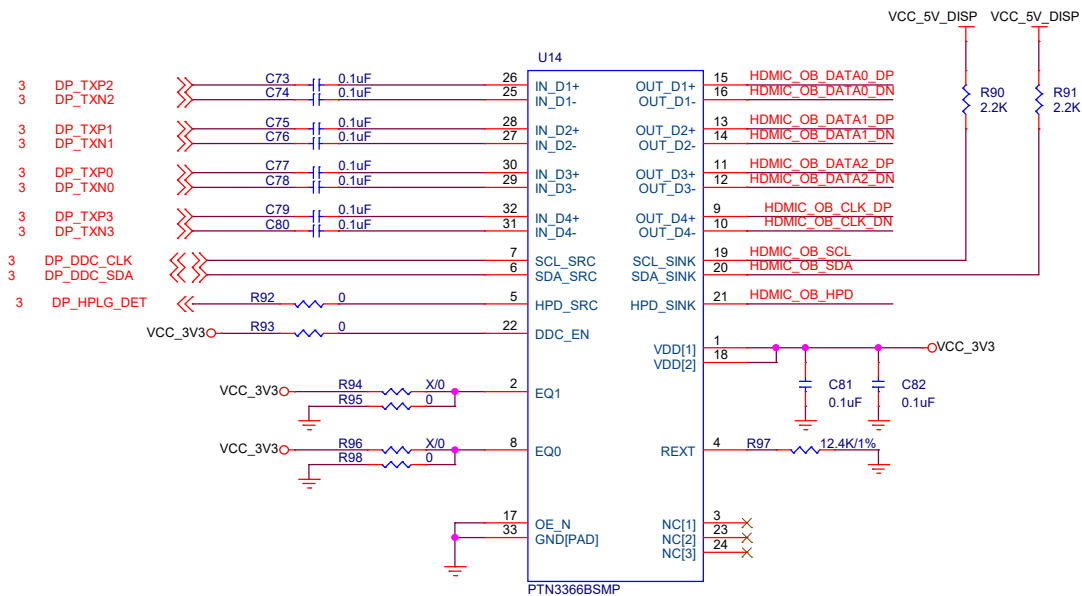


<b>AAEON Technology INC.</b>	
Title	
<b>Mini Card</b>	
Size	Document Number
<b>B</b>	<b>BIO-ST05-H1L1</b>
Date:	Thursday, April 21, 2016
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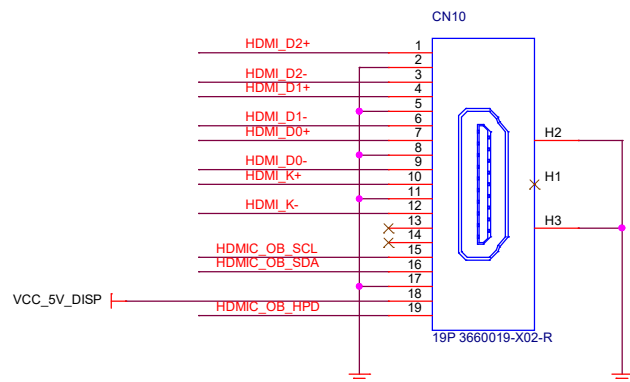
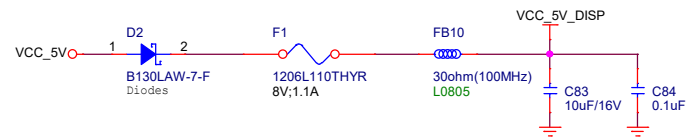
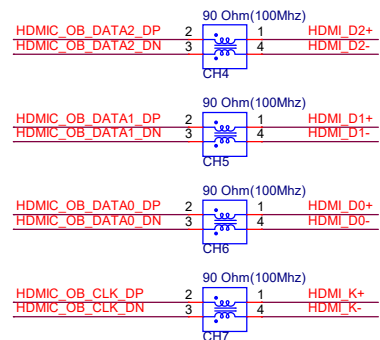
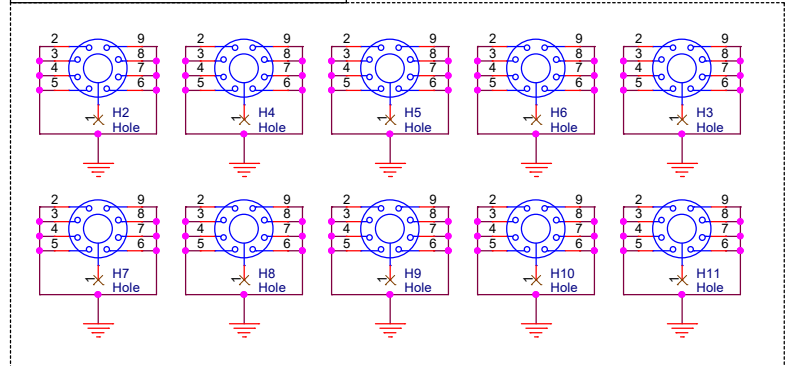


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Title		
Audio		
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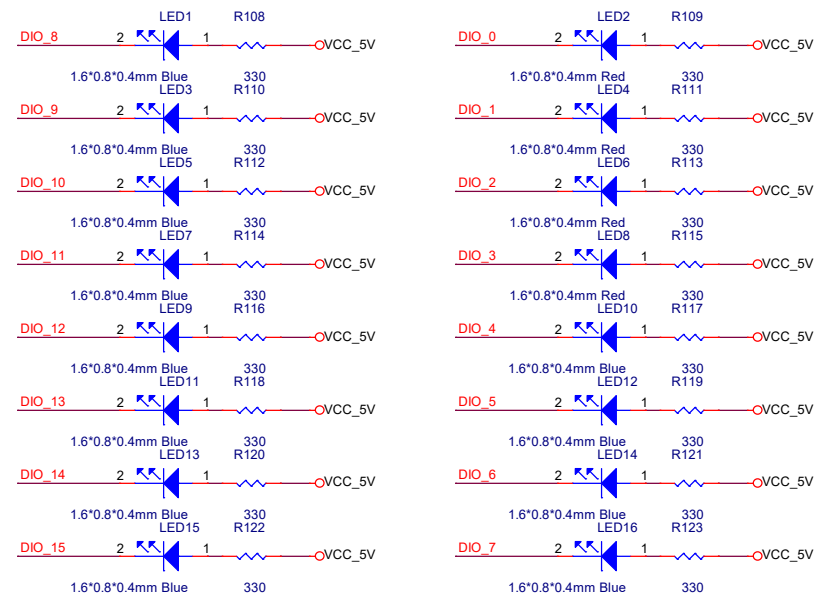
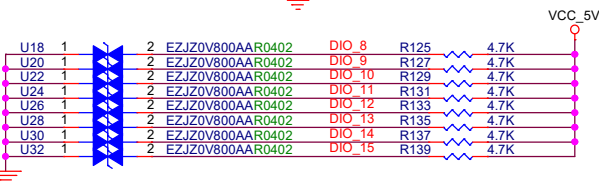
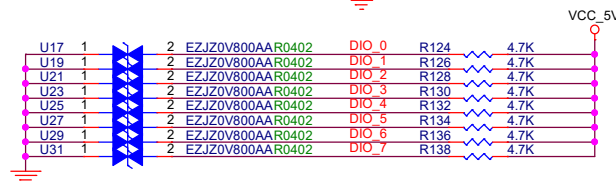
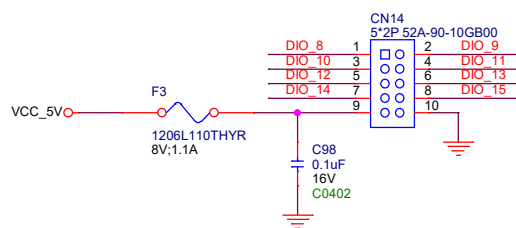
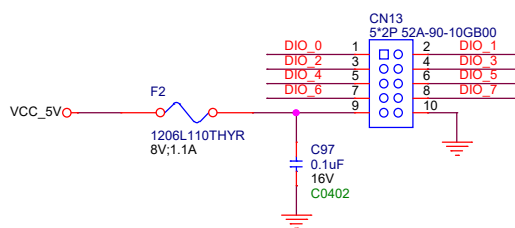
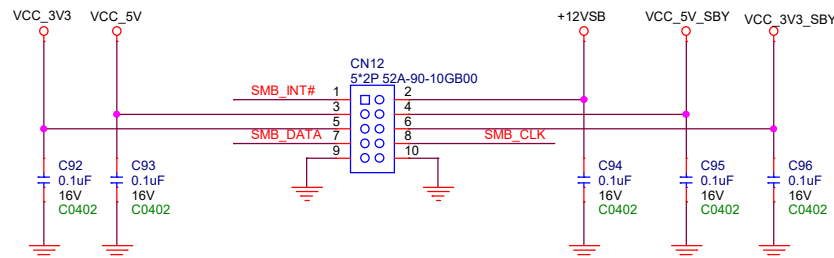
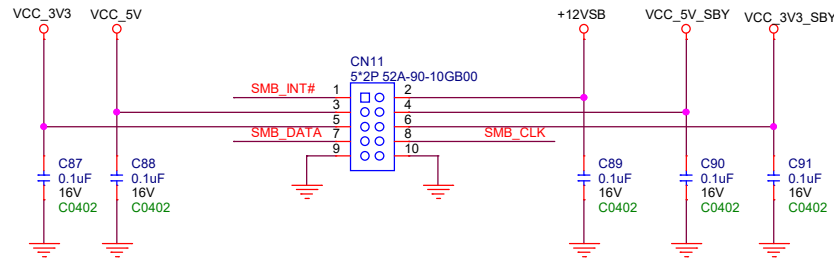
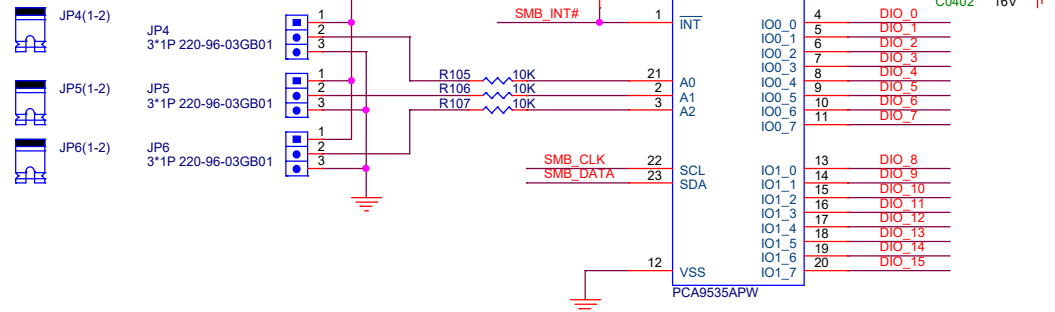
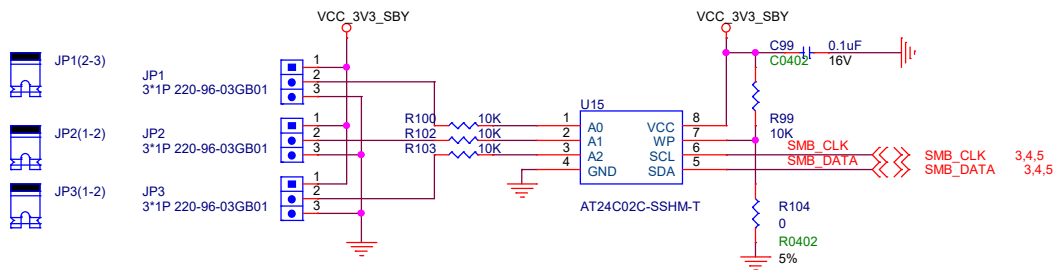



## Mounting Holes / Non-PTH

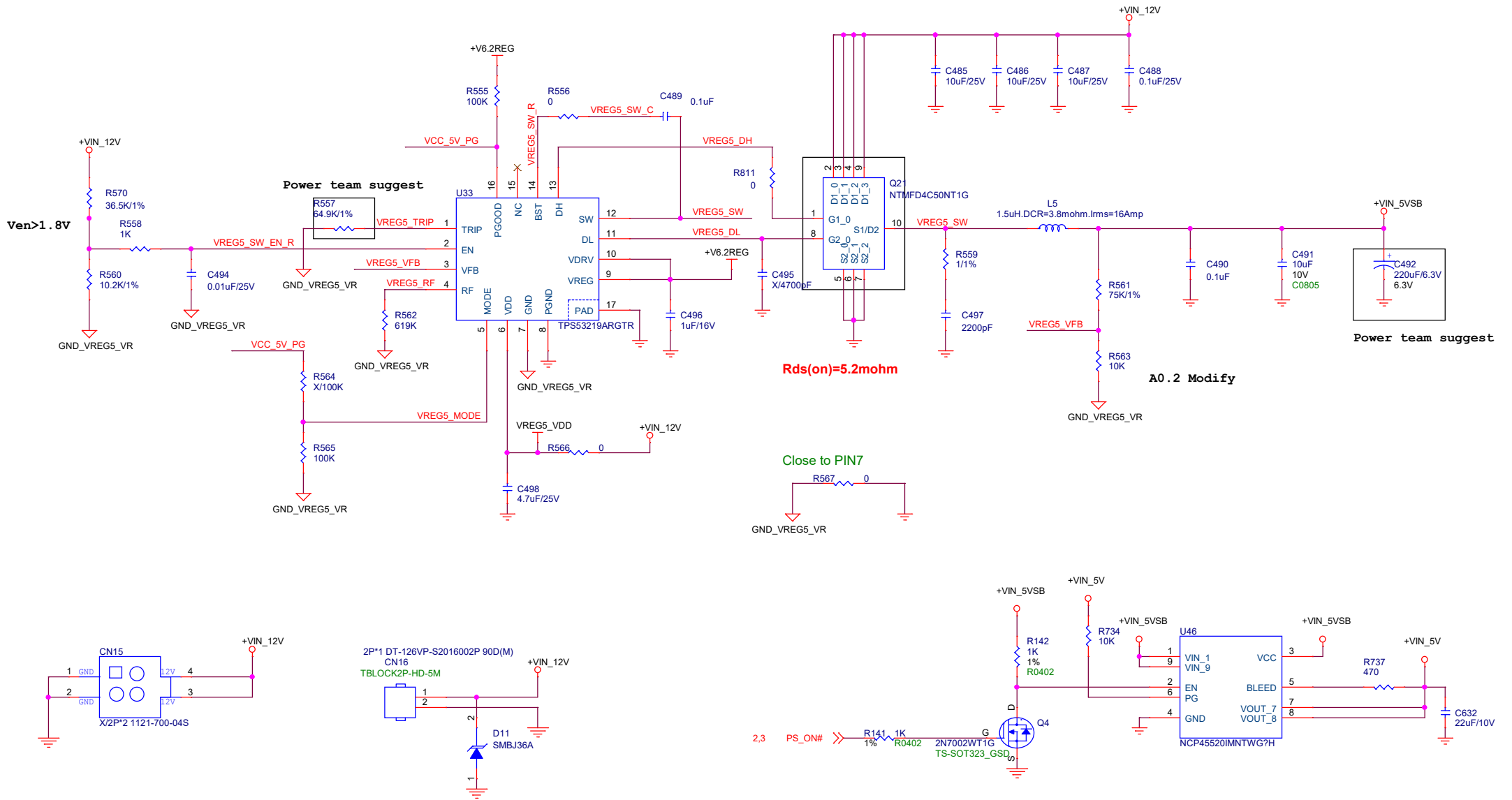


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HISTORY

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