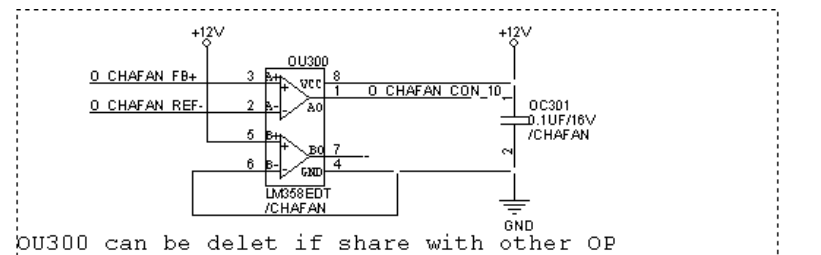
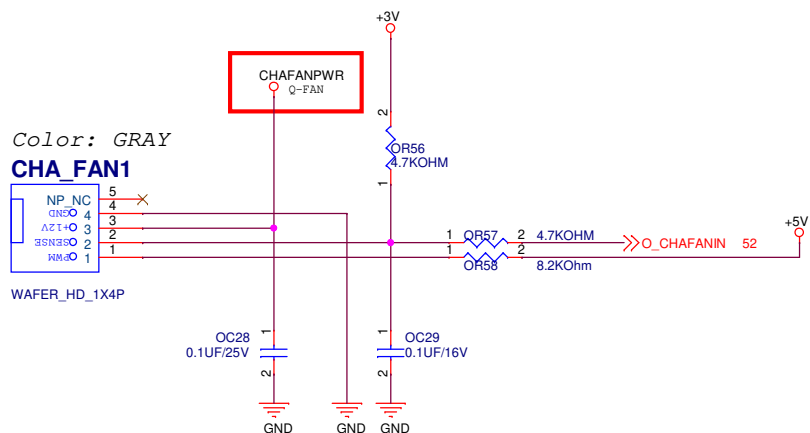
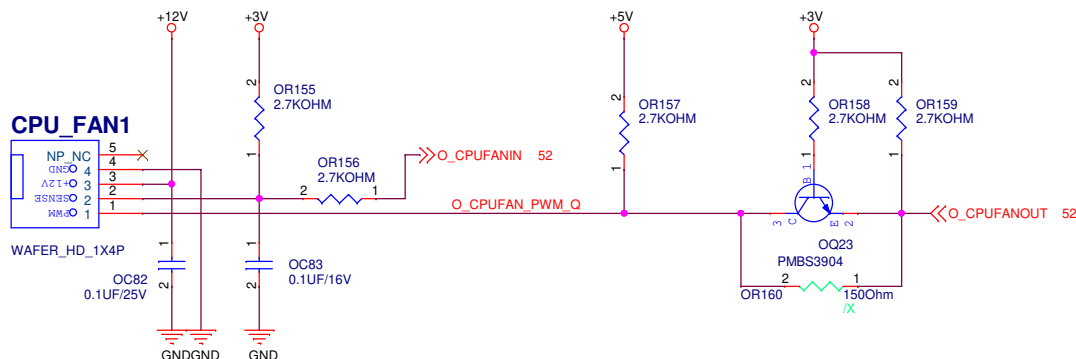


FAN CONTROL

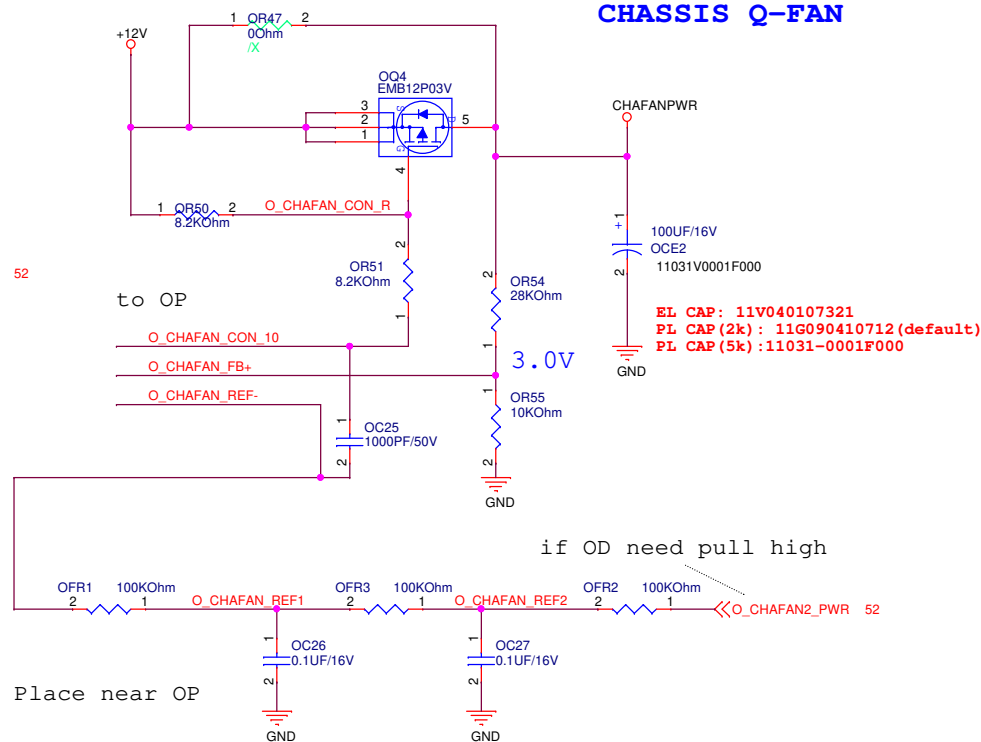
PWM FAN CONTROL (New)

Pin Number	Signal
1	Control
2	Sense
3	+12V
4	GND

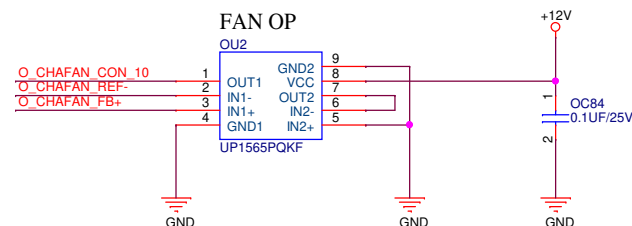


OU300 can be delete if share with other OP

CHASSIS Q-FAN



Place near OP



<Variant Name>

ASUS		Title : CPUFAN_SYSFAN1	
ASUSTEK COMPUTER INC		Engineer: <OrgAddr1>	
Size B	Project Name MIX-H310A2	Rev R1.00	
Date: Wednesday, July 17, 2019		Sheet 58 of 74	