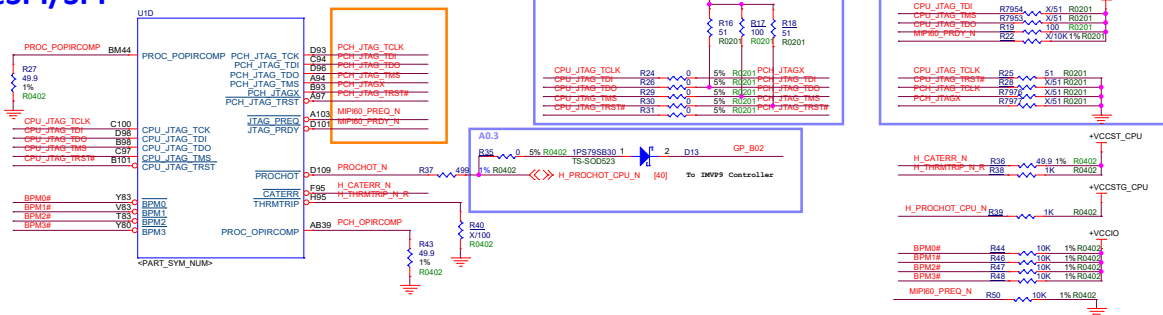
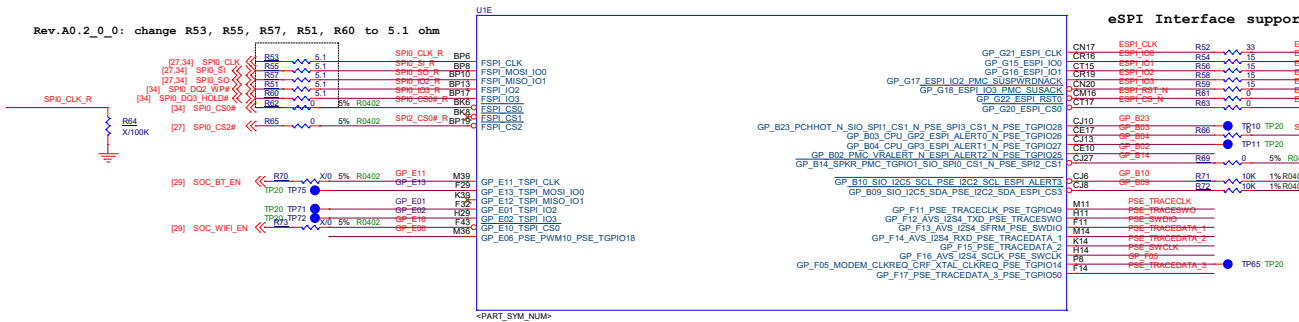


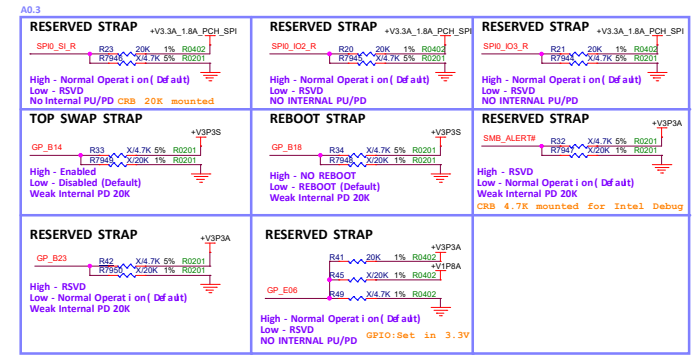
## SoC JTAG/eSPI/SPI



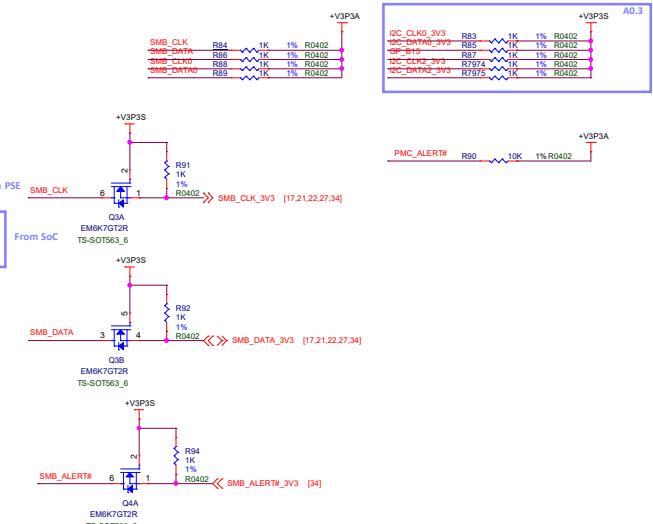
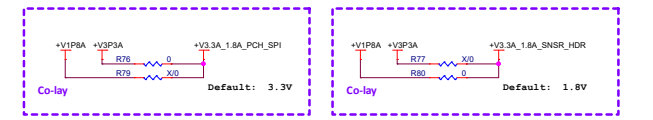
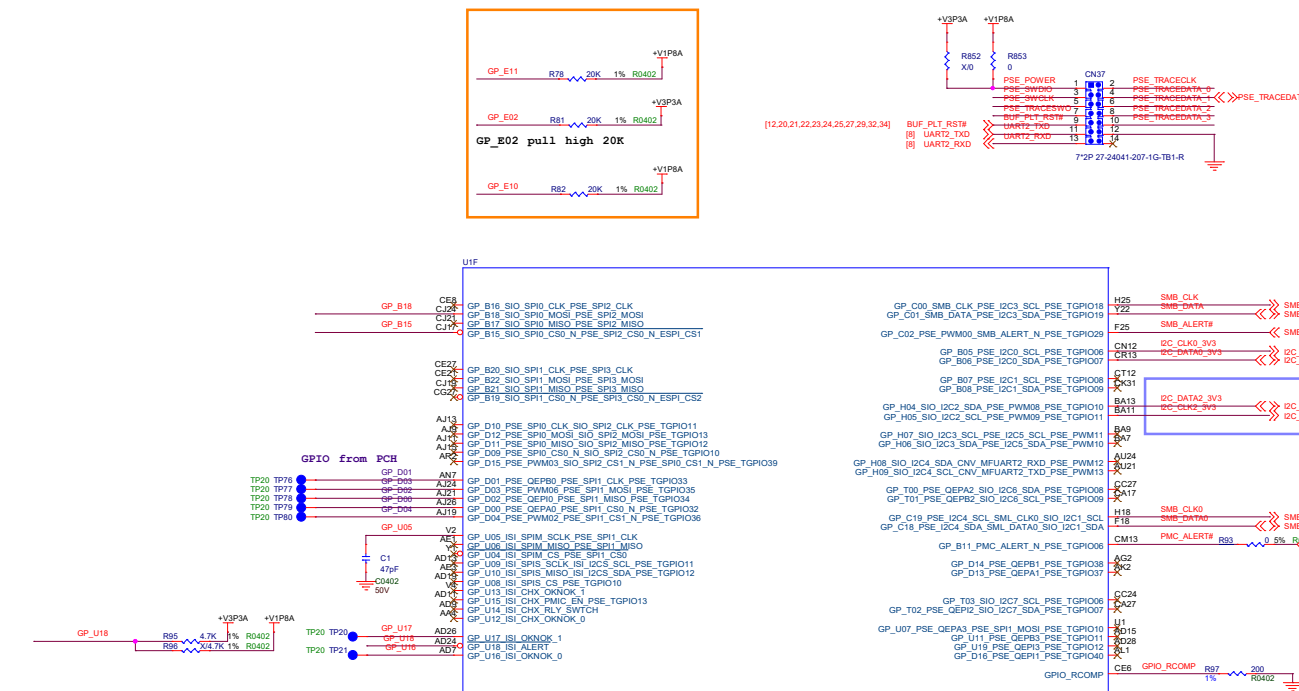
FSPI Interface supports 1.8V or 3.3V  
For SPI Flash -> Default:3.3V



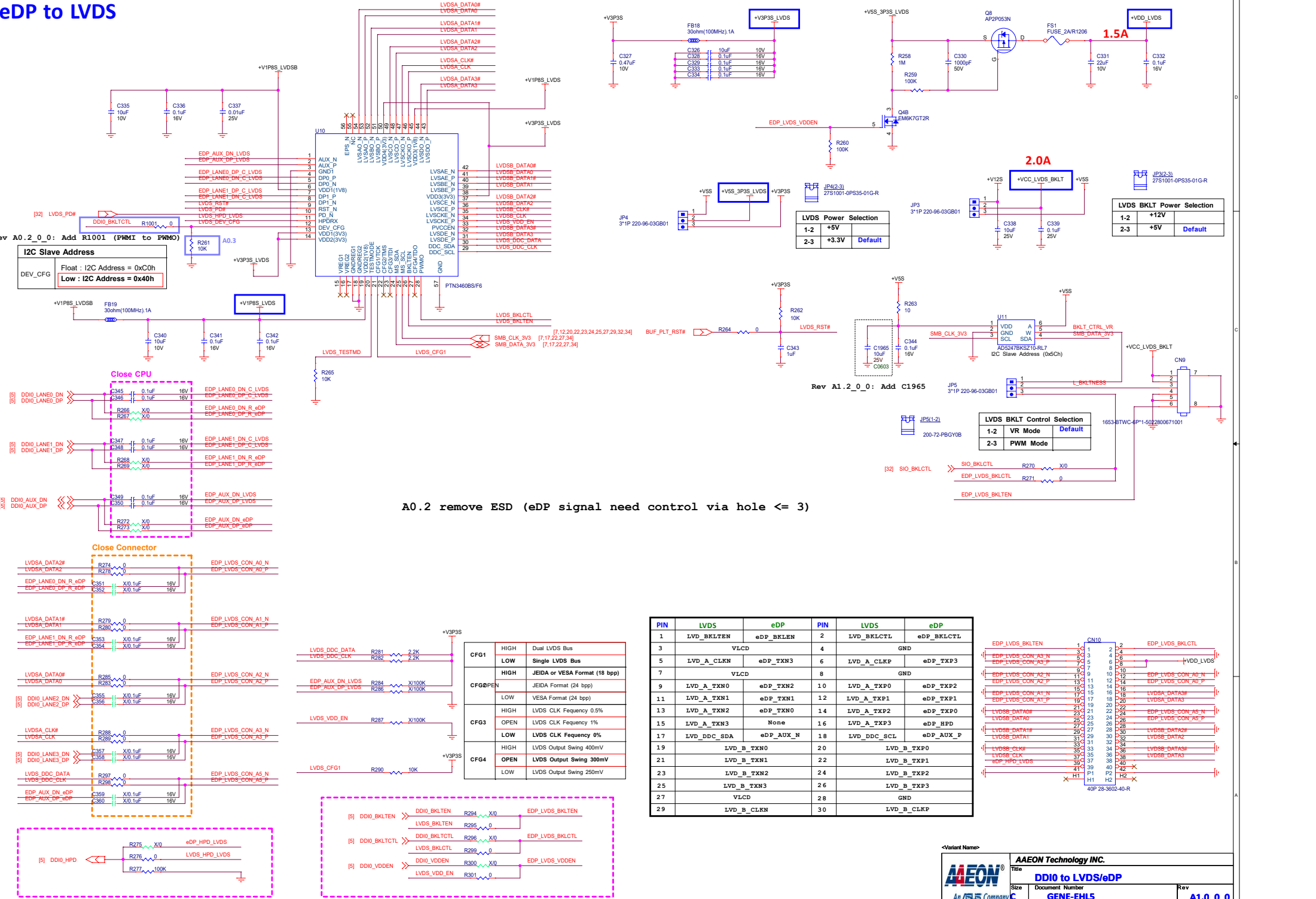
eSPI Interface supports 1.8V only



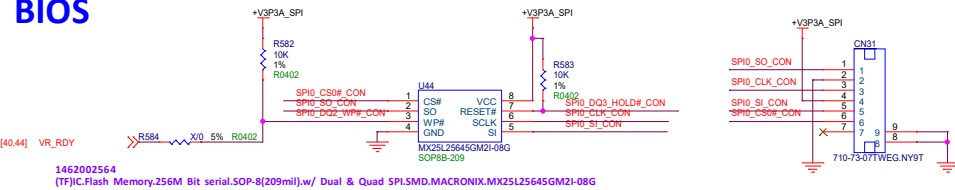
For GP\_E06:  
The pull-up rail will be V1P8A or V3P3A,  
depending on what I/O voltage the GPIO is configured to.



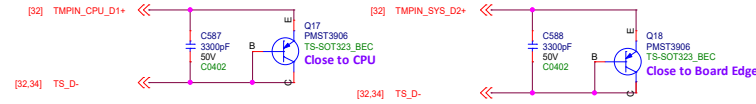
eDP to LVDS



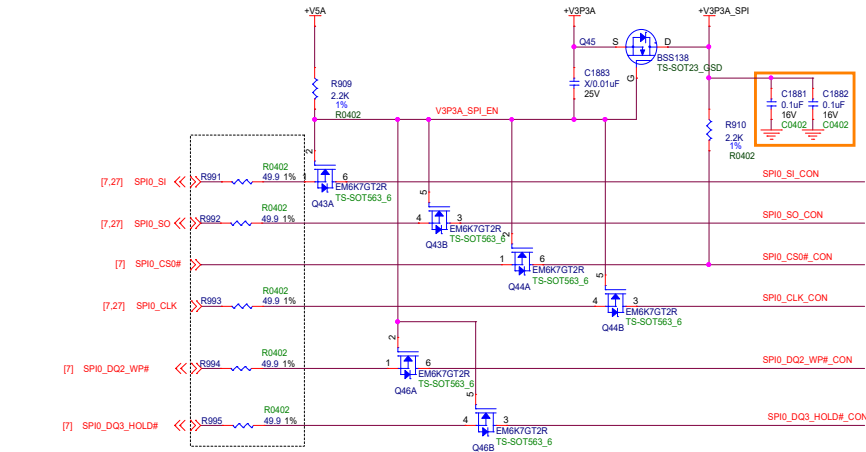
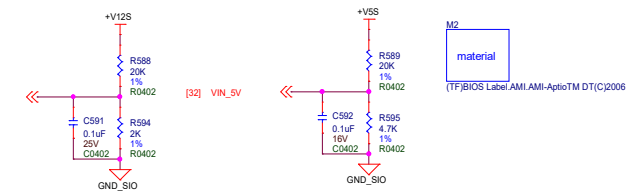
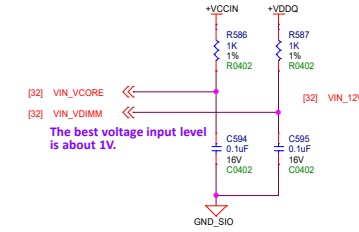
# BIOS



## Temperature Monitor(CPU, SYS)

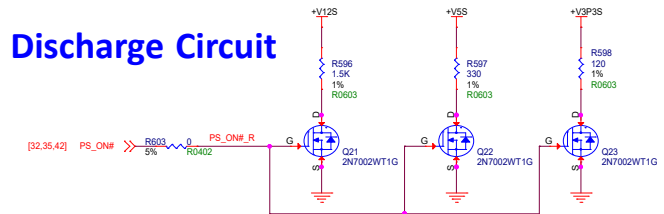


## Voltage Monitor

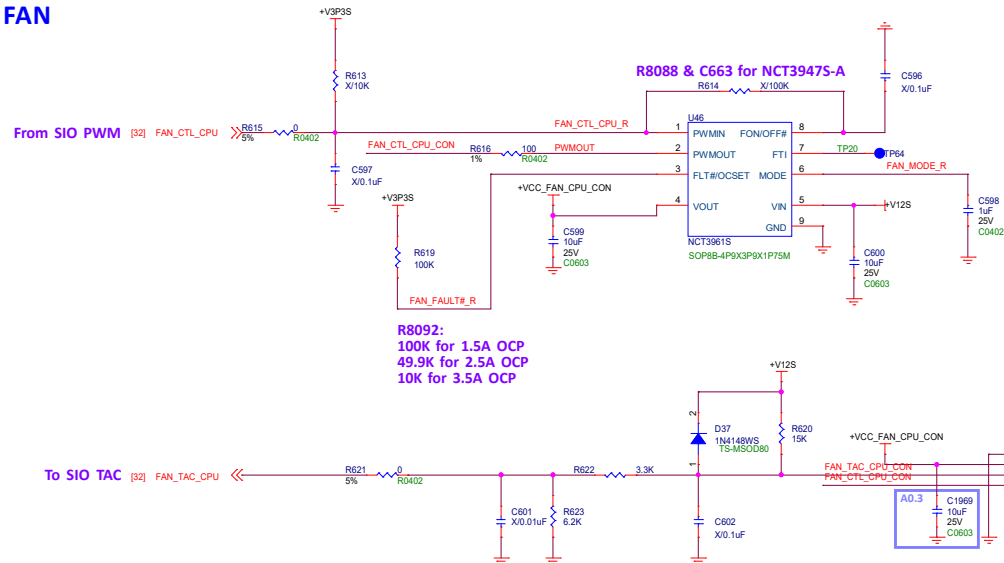


Rev.A0.2\_0\_0: Change SPI Circuit

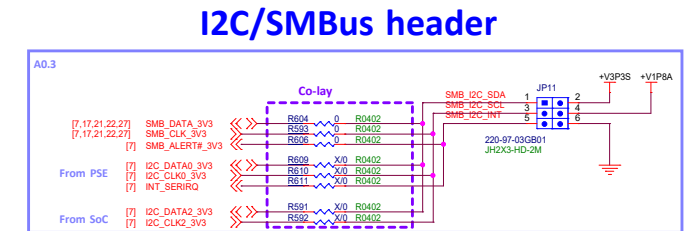
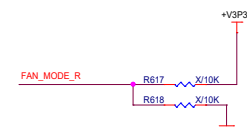
## Discharge Circuit



**FAN**



FAN_Mode	
	Mode
High	PWM mode
Low	DC mode
Float i ng	Auto-detect



## eSPI Connector (Debug Card)

