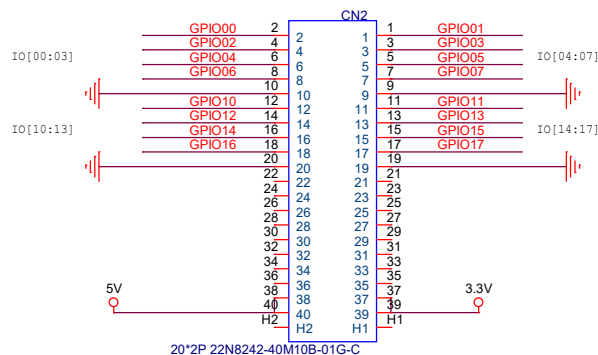
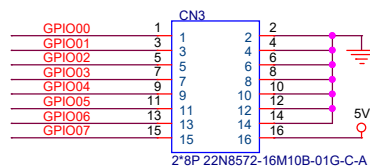


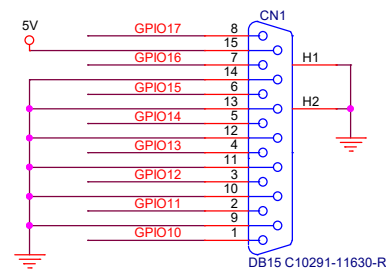
After 40 p SMD flip down layer, DIO - P1 (GPIO00), to received the IO board of P2 (GPIO00)



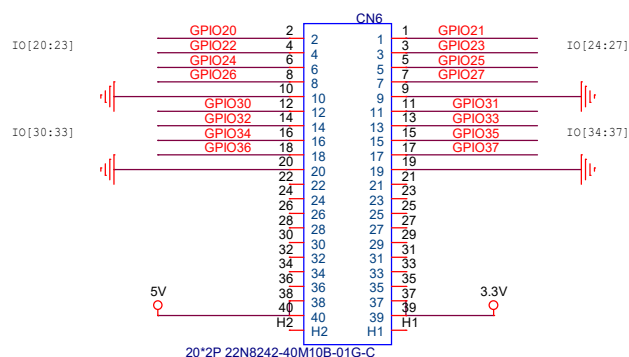
DB15--J1



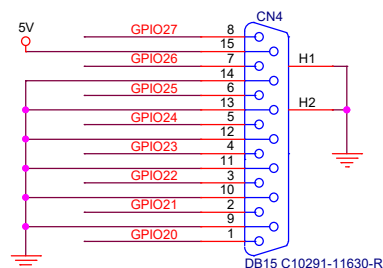
DB15--J2



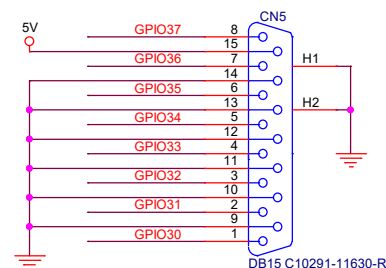
After 40 p SMD flip down layer, DIO - P1 (GPIO20), to received the IO board of P2 (GPIO20)



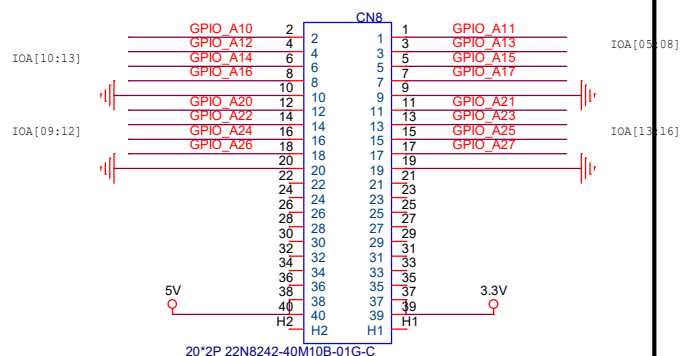
DB15--J3



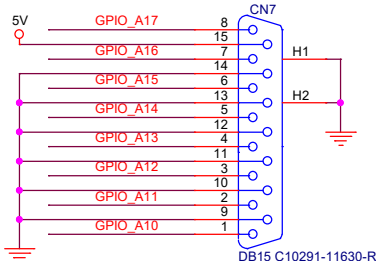
DB15--J4



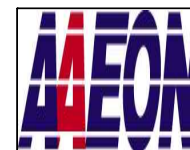
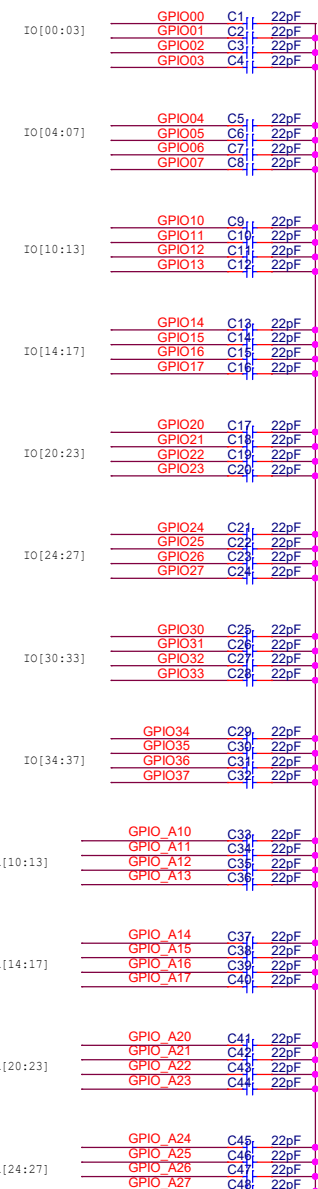
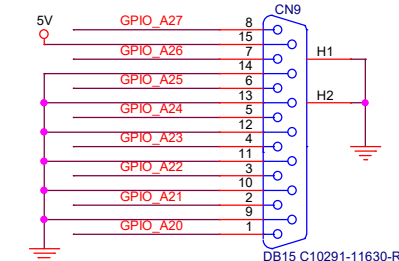
After 40 p SMD flip down layer, DIO - P1 (GPIOA10), to received the IO board of P2 (GPIOA10)



DB15--J5



DB15--J6



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