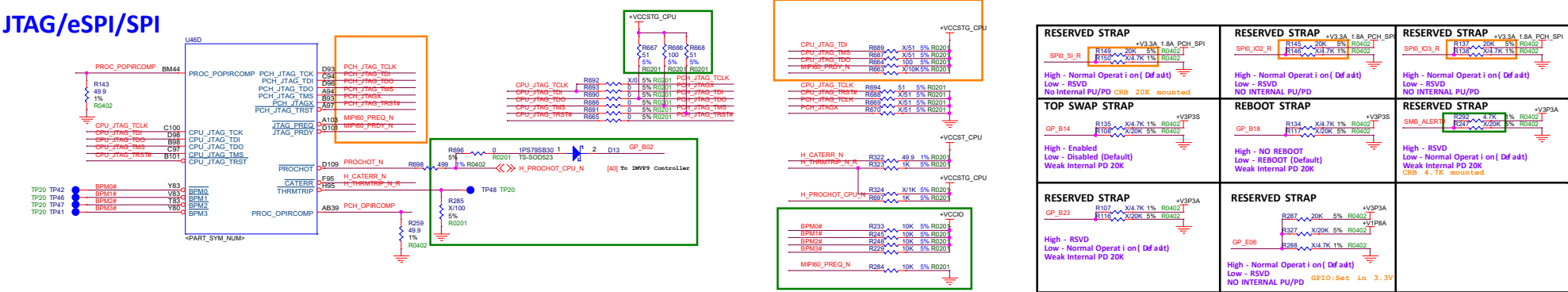
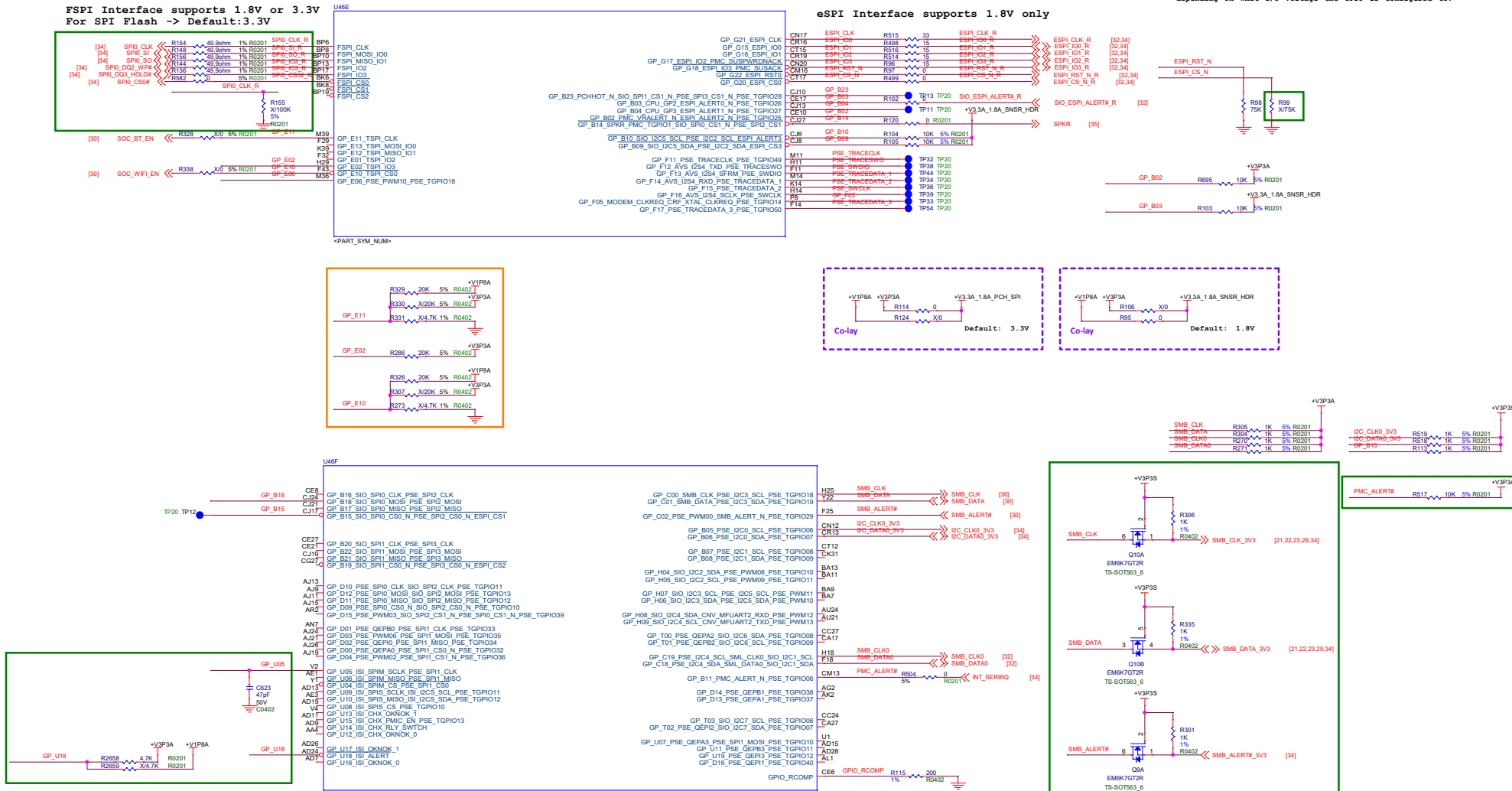


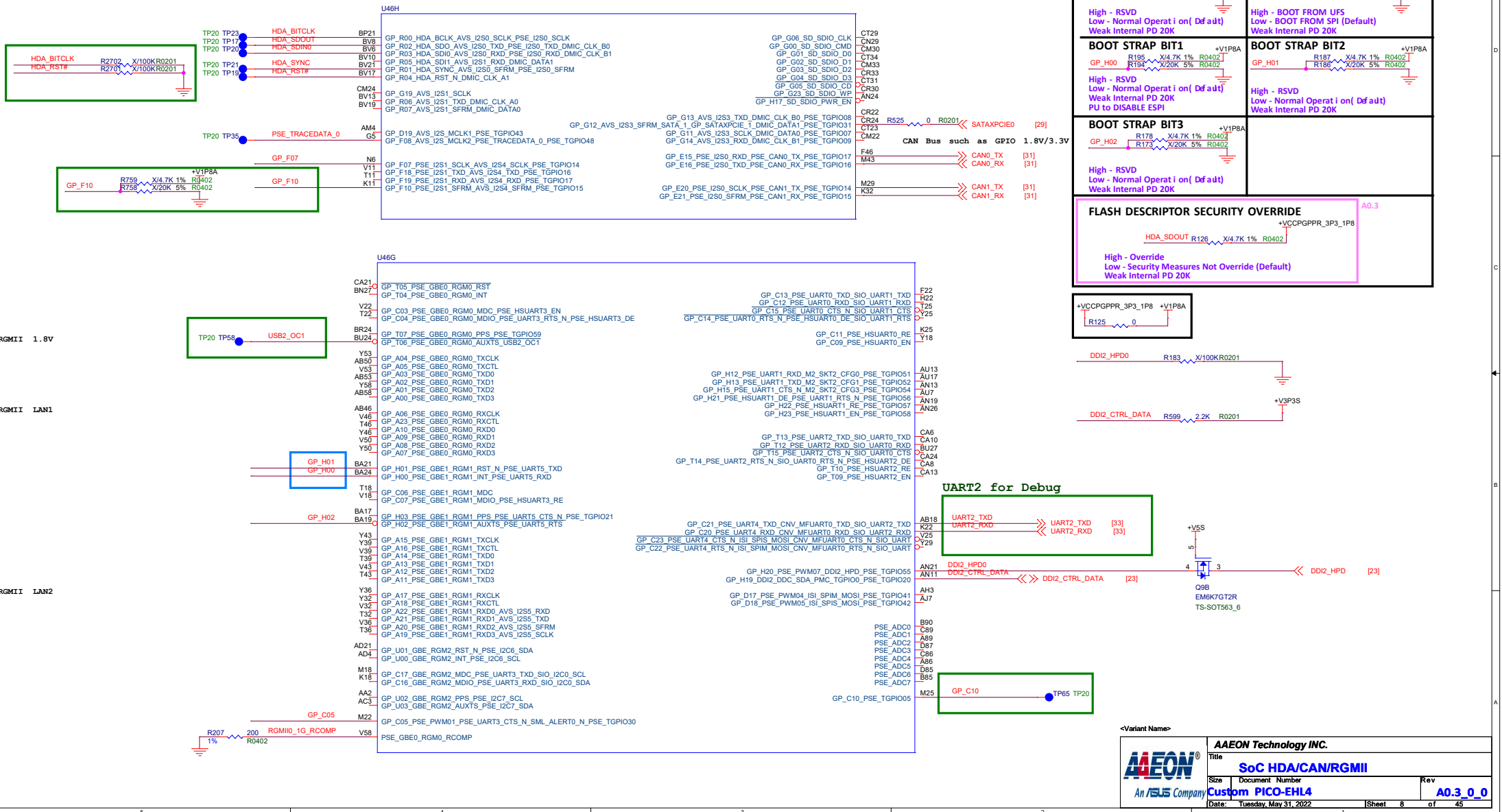
SoC JTAG/eSPI/SPI



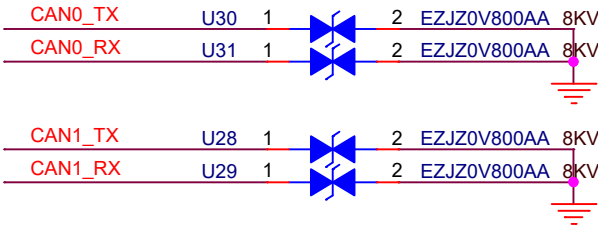
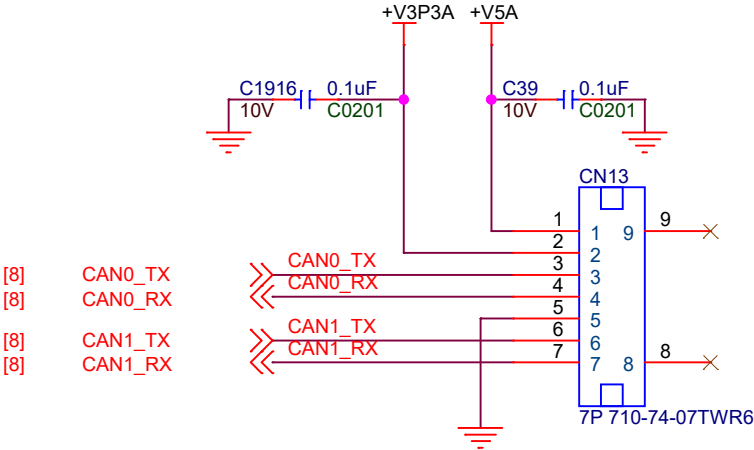
For GP E06:
The pull-up rail will be V1P8A or V3P3A,
depending on what I/O voltage the GPIO is configured to.




SoC HDA/CAN/RGMII



CAN BUS



<Variant Name>

| | | |
|---|-----------------------|----------------|
|  | Aaeon Technology INC. | |
| | CAN Bus | |
| Size | Document Number | Rev |
| A | PICO-EHL4 | A0.3_0_0 |
| Date: | Tuesday, May 31, 2022 | Sheet 31 of 45 |

