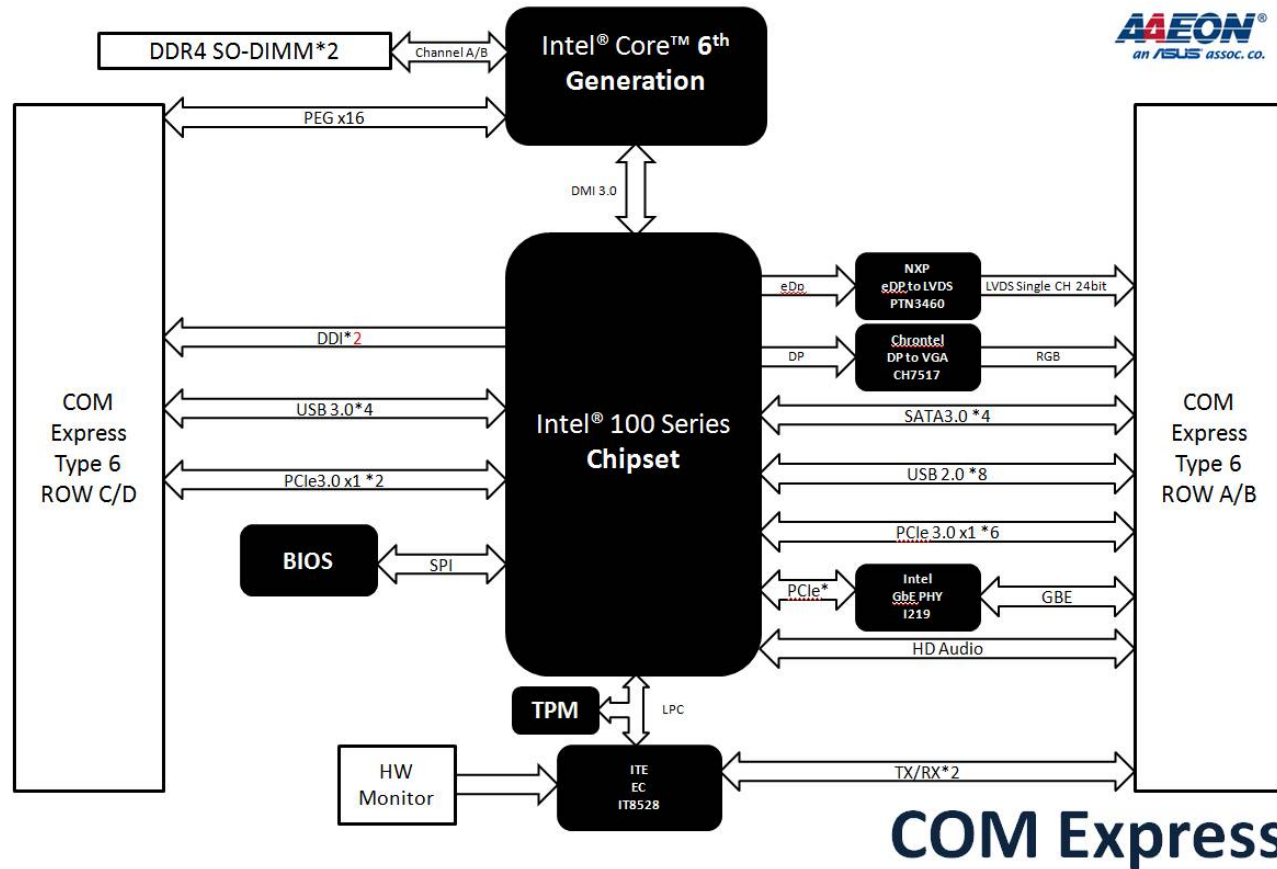


# COM-SKH Rev. A1.3 BLOCK DIAGRAM



**AEON**  
an ASUS assoc. co.

Page	Description
01)	TITLE PAGE
02)	System Setting
03)	Power Map
04)	Power Sequence
05)	PROCESSOR 1-DMI/PEG
06)	PROCESSOR 2-DDR4
07)	PROCESSOR 3-DDI/EDP
08)	PROCESSOR 4-CFG/RSVD
09)	PROCESSOR 5-PWR1
10)	PROCESSOR 6-PWR2
11)	PROCESSOR 7-VSS
12)	DDR4 SO-DIMM-A
13)	DDR4 SO-DIMM-B
14)	PCH 1-SPI/SM/LINK/UART/I2C
15)	PCH 2-CLK
16)	PCH 3-DMI/USB2.0/PCIE/USB3
17)	PCH 4-DDI/USB3/LPC/eSPI
18)	PCH 5-PCIE/SATA/FAN
19)	PCH 6-HDA/SMB/MISC/I2S/RTC
20)	PCH 7-PWR
21)	PCH 8-VSS
22)	LAN I219
23)	Row A/B & C/D
24)	SPT TPM
25)	EC -ITE8528/H/W Monitor/WDT
26)	DP to LVDS
27)	DP to VGA
28)	POWER +V5A
29)	POWER +V3.3A
30)	POWER SWITCH 5V 3V 1.8_STG
31)	POWER VCCIO/FV1_8A
32)	POWER +VDDQ MEM/VPP
33)	POWER +V1.0A
34)	IMVP8 NCP81245 1
35)	IMVP8 VCCORE
36)	IMVP8 VCCGT
37)	IMVP8 VCCSA
38)	Boot Sequence/iAMT Control
39)	History
40)	History 1
41)	History 2

A1.3 wTPM

**AEON**  
an ASUS assoc. co.

Title <b>TITLE BLOCK</b>		
Size B	Document Number <b>COM-SKHB6</b>	Rev. <b>A0.2</b>
Date: Thursday, June 02, 2022		Sheet: 1 of 41

EC IT8528VG GPIO Pins :

PIN NO.	PIN Name	Multi-Func	Default	GPIO Function
M5	GPA0	PWM0	GPI	EC_AC_PRESENT
N5	GPA1	PWM1	GPI	PM_SUS_STAT#
M6	GPA2	PWM2	GPI	SUS_PWR_ACK
N6	GPA3	PWM3	GPI	FAN_PWM
K6	GPA4	PWM4	GPI	EC_LPC_PME#
J6	GPA5	PWM5	GPI	MOD_SPL_CSEN0#
M7	GPA6	PWM6/SSCK	GPI	BID0
K7	GPA7	PWM7/RIG1#	GPI	CAR_SPI_CSEN0#
A4	GPB0	RXD/SIN0	GPI	SRXD1X
A3	GPB1	TXD/SOUT0	GPI	STXD1X
D2	GPB2	CTX0	GPI	SLP_WLAN#_EC
B4	GPB3	SMCLK0	GPI	
A2	GPB4	SMDAT0	GPI	
F1	GPB5	GA20	GPO	5VSBY1_SW
H4	GPB6	KBRST#	KBRST	CB_WDT
A1	GPB7	RING#/PWRFIL/LPCRST#	GPI	EC_LID#
D1	GPC0	CRX0	GPI	CB_BATLOW#
B3	GPC1	SMCLK1	GPI	EC_CLK_3P3
B2	GPC2	SMDAT1	GPI	EC_DATA_3P3
K13	GPC3	KSO16/SMOSI	GPI	EC_PCH_THRMTRIP
C2	GPC4	TMRIO/WUI2	GPI	BIOS_DIS0#
J10	GPC5	KSO17/SMISO	GPI	BID1
E1	GPC6	TMR1/WUI3	GPI	BIOS_DIS1#
M2	GPC7	PWUREQ#	GPI	EC_PWRBTN#
E6	GPB0	SSCE1#/TM	GPI	
A5	GPB1	DTR1#/ID7	GPI	SYS_RESET#
E7	GPB2	SSCE0#	GPI	
D6	GPB6	DSR0#	GPI	

PCH GPIO Pins :

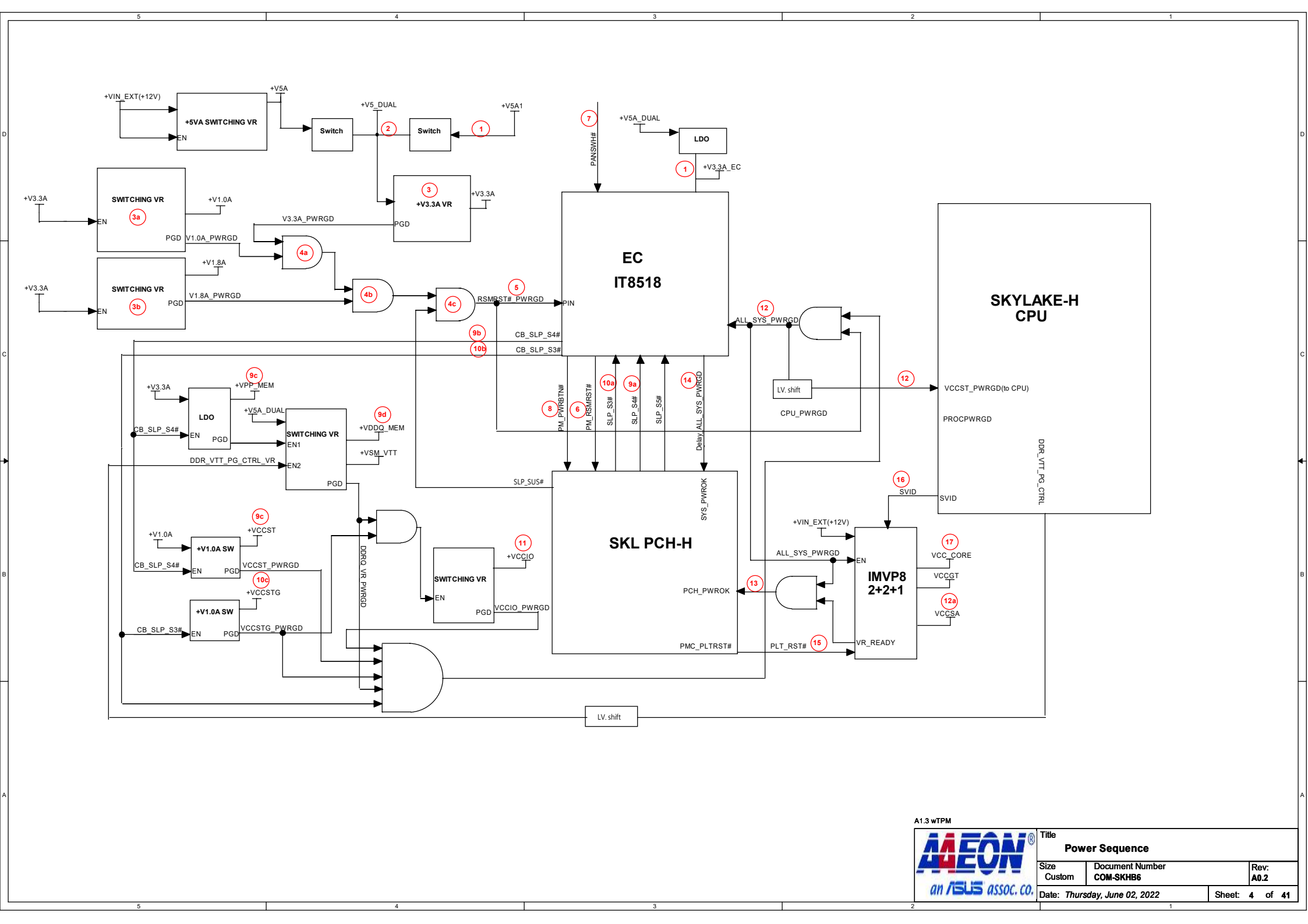
Group	Name	Power Well	Default	GPIO Function
Group A	GPP A0	Primary Core 3.3V		KBRST
	GPP A1			LPC AD0
	GPP A2			LPC AD1
	GPP A3			LPC AD2
	GPP A4			LPC AD3
	GPP A5			LPC FRAMES
	GPP A6			NT_SERIRQ
	GPP A7			PRQ0#
	GPP A8			PM_CLKRUN#
	GPP A9			PCI CLK LPD0
	GPP A10			PCI CLK EC
	GPP A11			EC LPC_PME#
	GPP A12			US_PWR_ACK
	GPP A13			PM_SUS_STAT#
	GPP A14			SUSACK#
	GPP A15			
	GPP A16			
Group B	GPP A17-A23			
	GPP B0			
	GPP B1			SMLT1_ALERT#
	GPP B2			
	GPP B3			
	GPP B4			CLK_PCIE_LAN1_REQ#
	GPP B5			
	GPP B6-10			MPHY_EXT_PWR_GATEB
	GPP B11			SLP_S0W
	GPP B12			PLT_RST#
Group C	GPP B13			HDA_SPKR
	GPP B14			
	GPP B15-22			SMLT1_ALERT#
	GPP B23			
	GPP C0			SMB_CLK
	GPP C1			SMB_CLK
	GPP C2			SMB_ALERT#
	GPP C3			SMLD_CLK
	GPP C4			SMLD_DATA
	GPP C5			SMLD_ALERT#
Group D	GPP C6			
	GPP C7			
	GPP C8-23			

PCH GPIO Pins :

Group	Name	Power Well	Default	GPIO Function
Group D	GPP D0-8	Deep Sleep Power 3.3V		
	GPP D9			SLEEP#
	GPP D10			LDW
	GPP D11			PCIE_CPEP0#
	GPP D12			PCIE_CPEP1#
	GPP D13-16			
	GPP D17-D23			
Group E	GPP E0-3	Primary Core 3.3V		
	GPP E4			SCIB
	GPP E5			SMW
	GPP E6-7			
	GPP E8			SATA_LED#
	GPP E9			USB_OC# 0 1
	GPP E10			USB_OC# 2 3
	GPP E11			USB_OC# 4 5
	GPP E12			USB_OC# 6 7
	GPP F0-14			
Group F	GPP F15	Primary Core 3.3V		
	GPP F16			
	GPP F17			
	GPP F18			
	GPP F19			EDP_VDD_EN
	GPP F20			EDP_BKLT_EN
	GPP F21			EDP_BKLT_CTRL
	GPP F22-23			
Group G	GPP G0-23			
Group H	GPP H0-23			
Group I	GPP I0	Primary Core 3.3V		DPB_HPD
	GPP I1			DPB_HPD
	GPP I2			DPD_HPD
	GPP I3			
	GPP I4			DP_HPD
	GPP I5			DPB_CTRLCLK
	GPP I6			DPB_CTRLDATA
	GPP I7			DPB_CTRLCLK
	GPP I8			DPB_CTRLDATA
	GPP I9			DPD_CTRLCLK
Group J	GPP I10			DPD_CTRLDATA
	GPP J0-23			

A1.3 wTPM



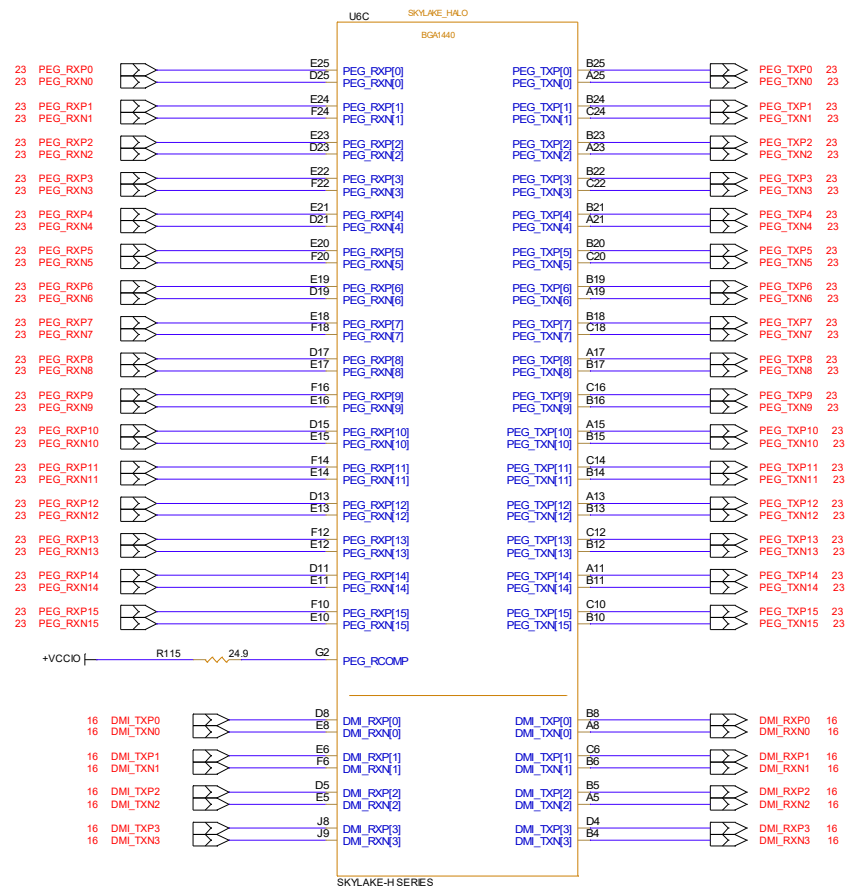


A1.3 wTPM



Title		
Power Sequence		
Size	Document Number	Rev:
Custom	COM-SKHB6	A0.2
Date: Thursday, June 02, 2022		Sheet: 4 of 41

# SKYLAKE-H BGA PROCESSOR (DMI/PEG)

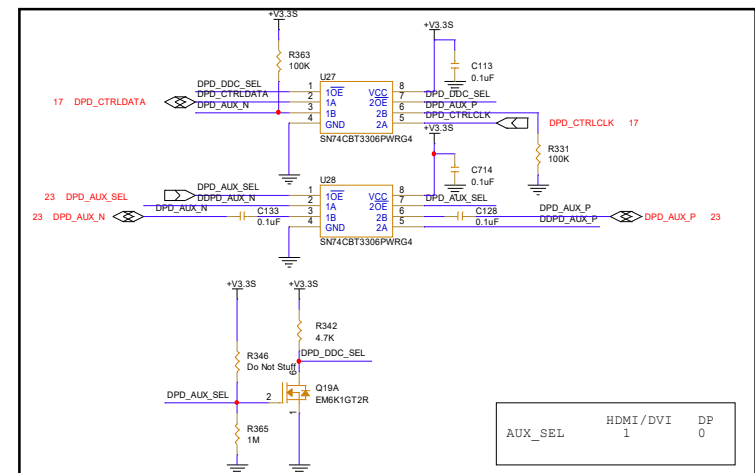
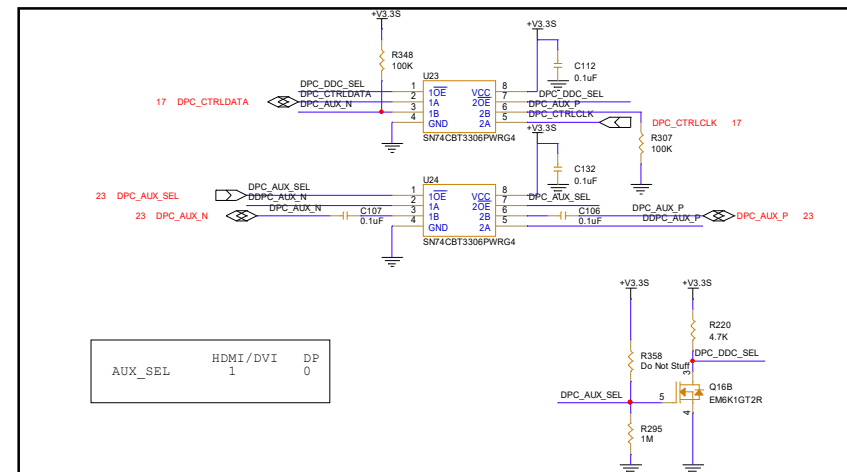


A1.3 WTPM



Title <b>PROCESSOR(1/7)</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <i>Thursday, June 02, 2022</i>		Sheet: <b>5</b> of <b>41</b>

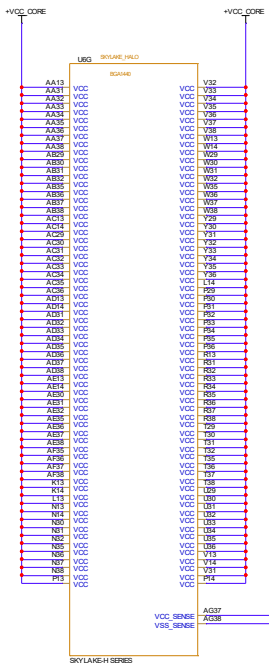
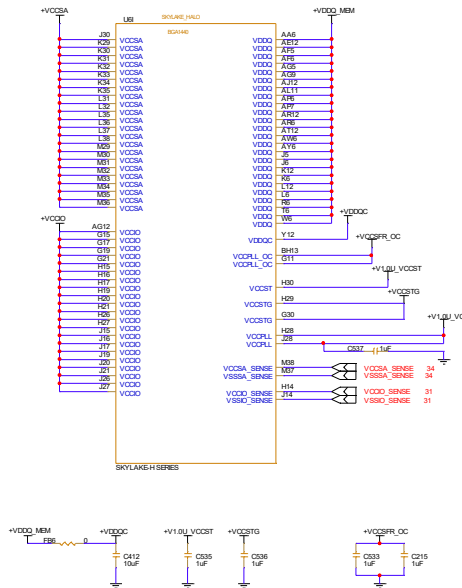
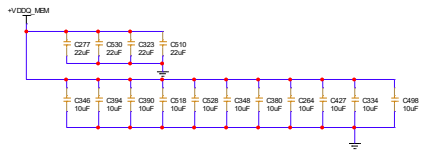
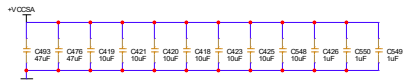
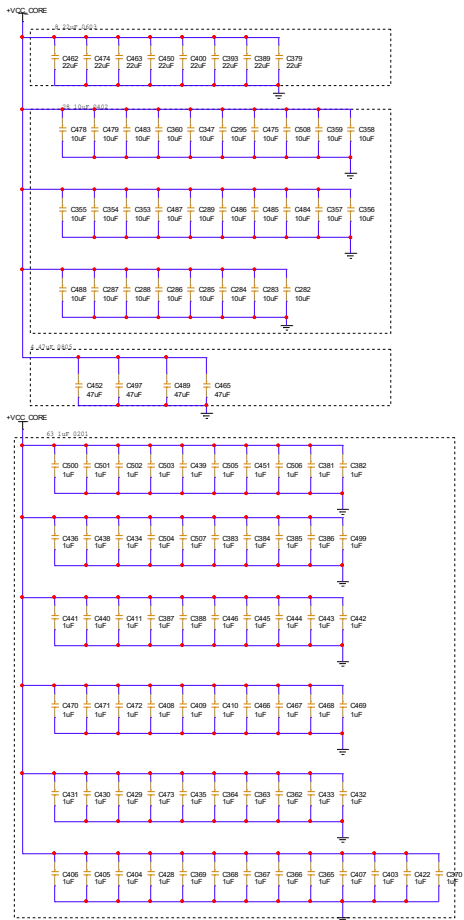




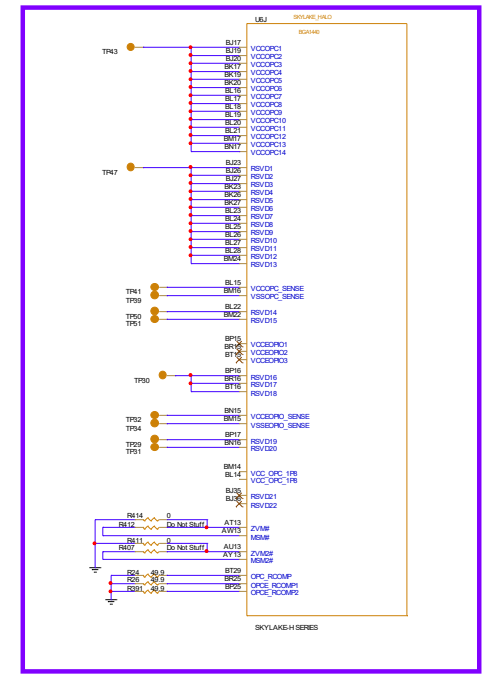




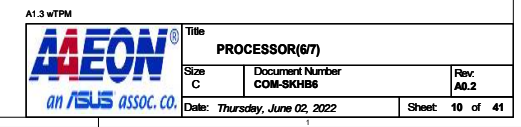


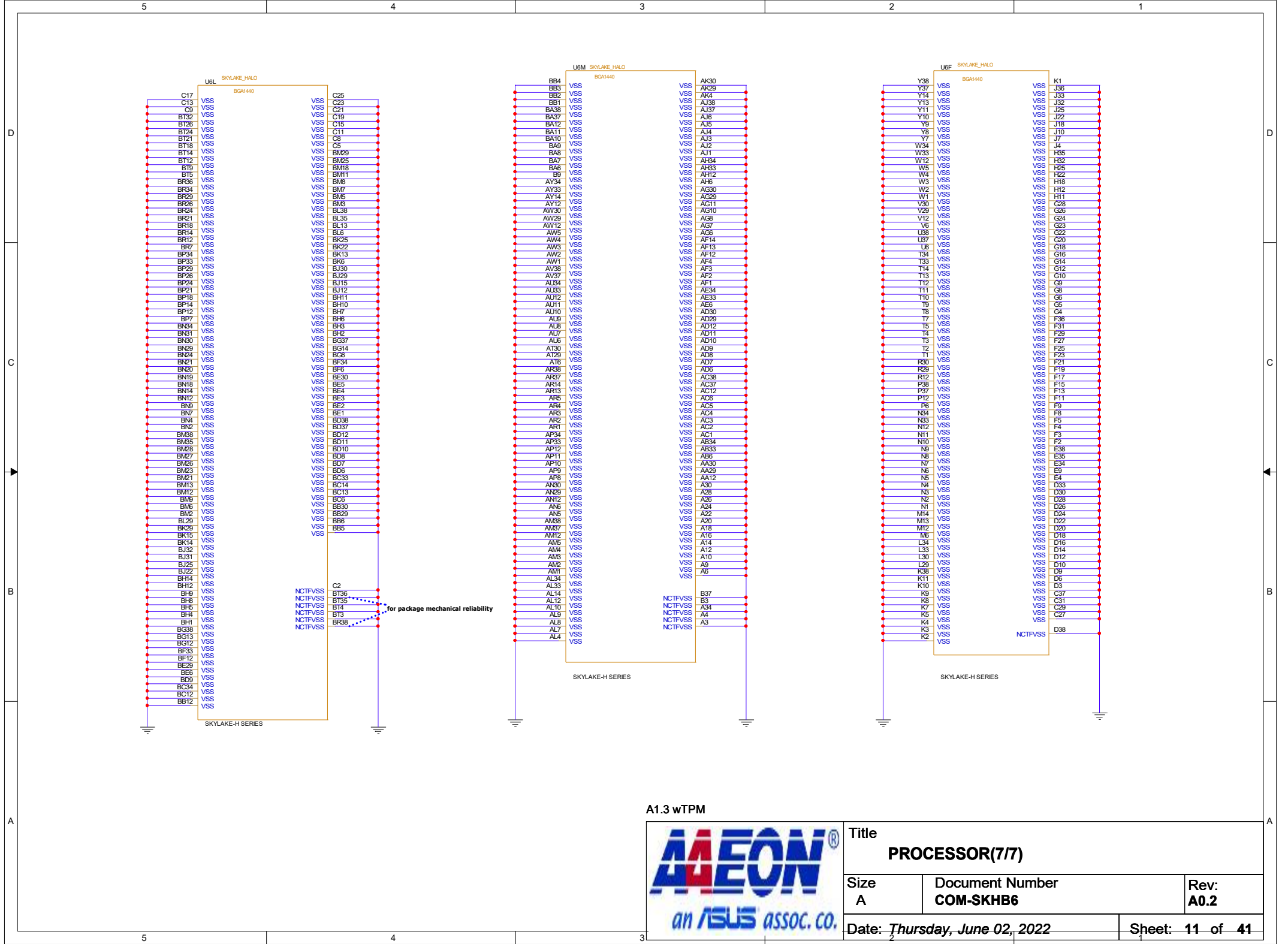


Only Support CPU SKUs without OPC in COM-SKH



A1.3 wTPM



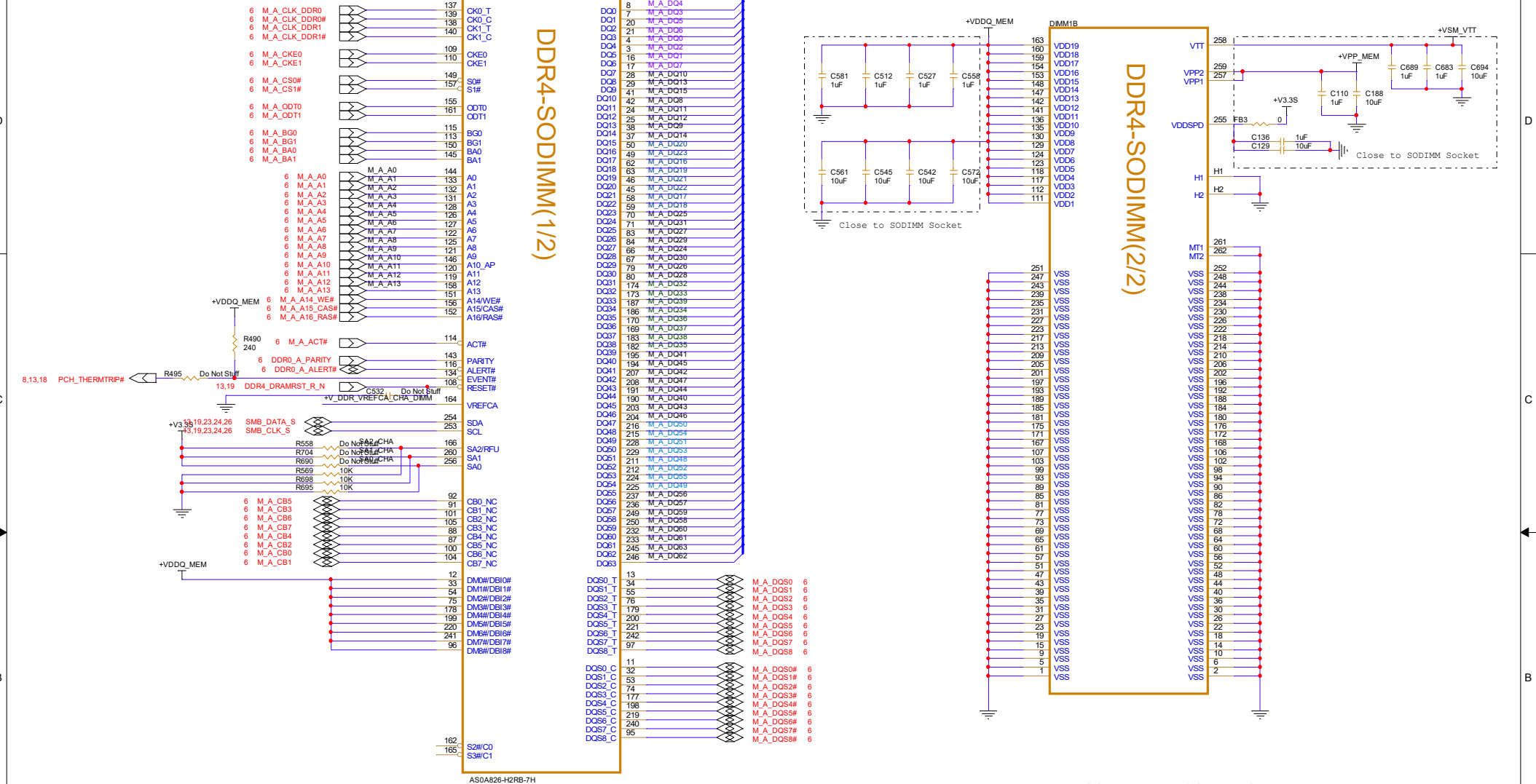


A1.3 wTPM



Title <b>PROCESSOR(7/7)</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <i>Thursday, June 02, 2022</i>		Sheet: <b>11 of 41</b>

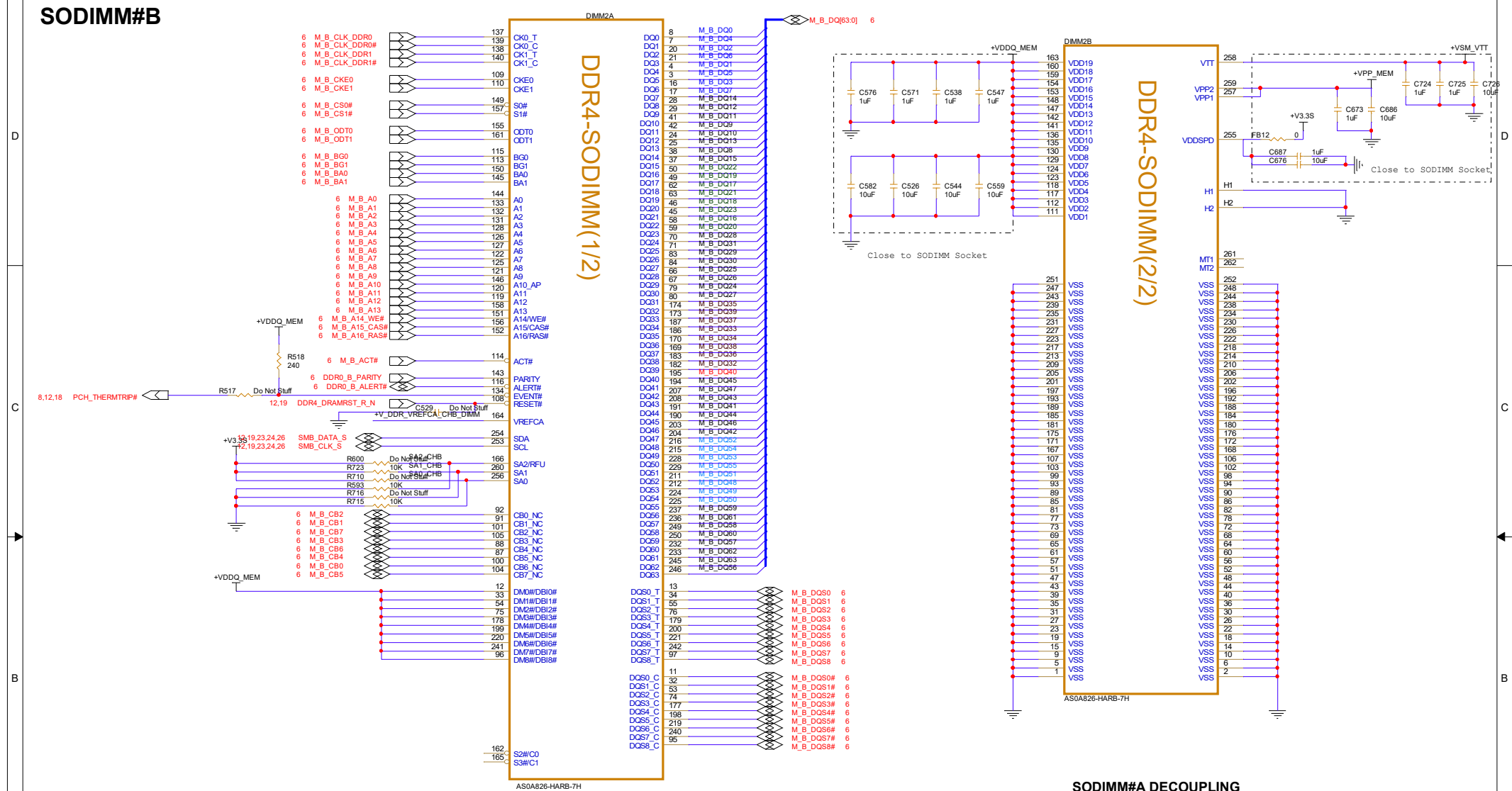
# SODIMM#A



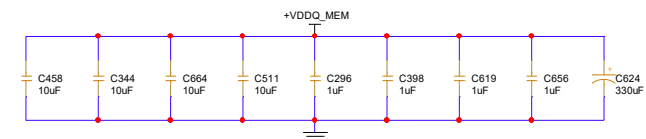
A1.3 wTPM



Title <b>DDR4 SO-DIMM-A</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <i>Thursday, June 02, 2022</i>		Sheet: <b>12 of 41</b>

**SODIMM#B**

## SODIMM#A DECOUPLING



### A1.3 wTPM

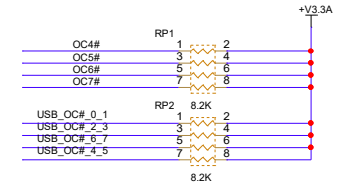
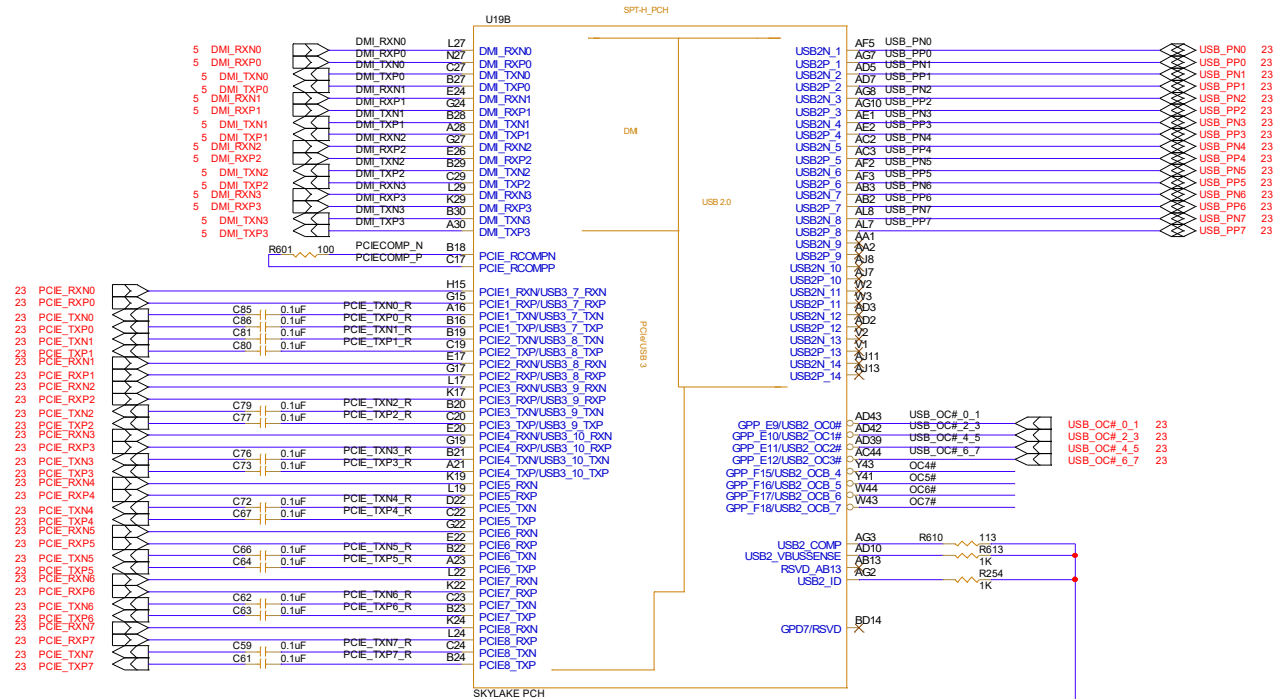


Title		
DDR4 SO-DIMM-B		
Size A	Document Number COM-SKHB6	Rev: A0.2
Date: Thursday, June 02, 2022		Sheet: 13 of 41







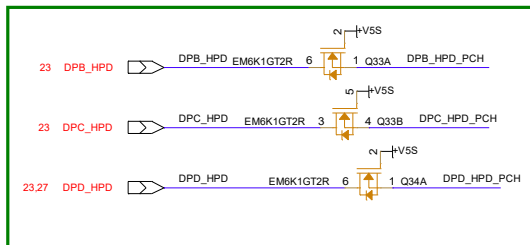
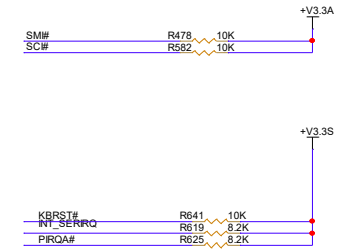
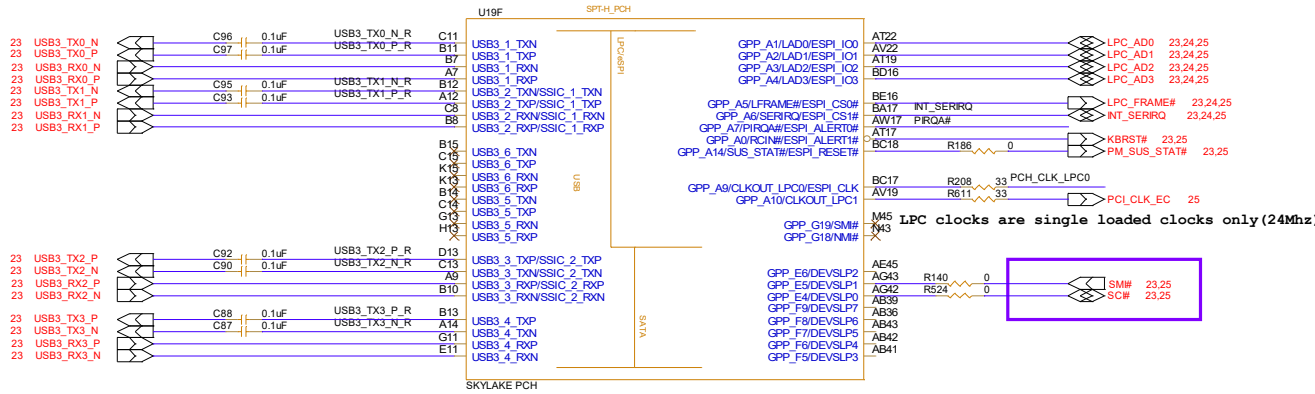
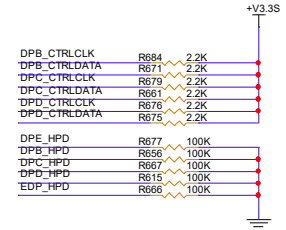
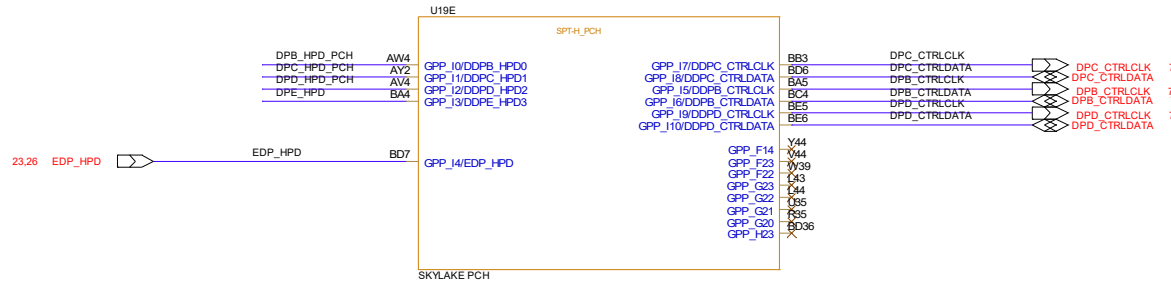


### A1.3 WTPM



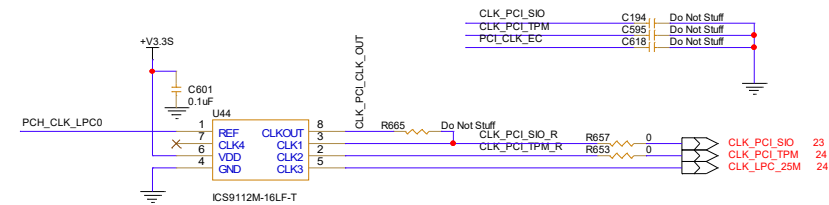
Title <b>PCH(3/8)</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <b>Thursday, June 02, 2022</b>		Sheet: <b>16 of 41</b>

	HPD	DDC	DDI(CPU)
Port1	DDPB_HPDP	DDCB	DDI1
Port2	DDPC_HPDP	DDCC	DDI2
Port3	DDPD_HPDP	DDCD	DDI3



A0.2 ADD

#### PCI\_CLK\_BUFFER



#### A1.3 wTPM



Title <b>PCH(4/8)</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <i>Thursday, June 02, 2022</i>		Sheet: <b>17 of 41</b>

A



PCIE19/SATA6 AND PCIE20/SATA7 ARE ONLY AVAILABLE ON SERVER SKU

A1 3 wTBM



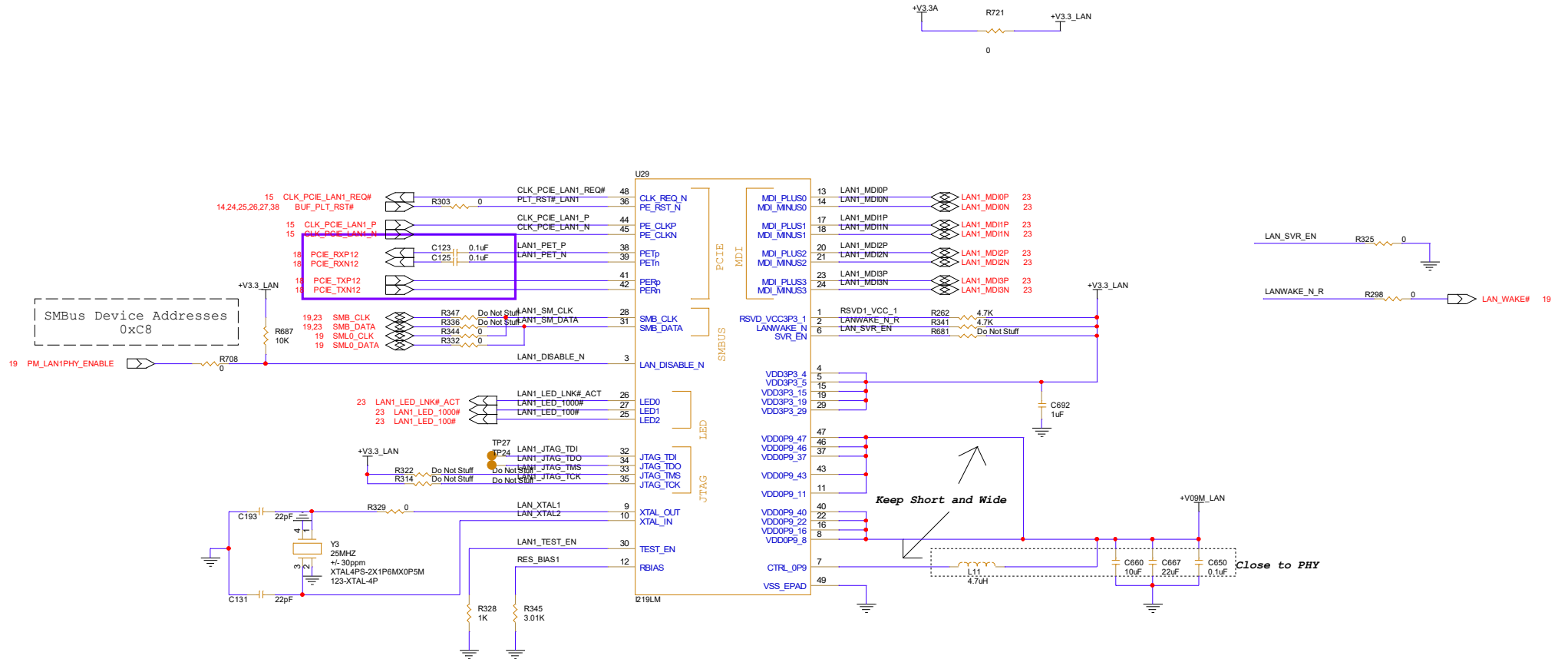
Title <b>PCH(5/8)</b>		
Size A	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: Thursday, June 03, 2022		Sheet: 18 of 41







# Jacksonville -MOBILE



ACTIVITY LED  
Green = LINK UP  
BLINKING = TX/RX ACTIVITY

SPEED LED  
Off = Link 10 Mbps  
Green = Link 100 Mbps  
Orange = Link 1000 Mbps

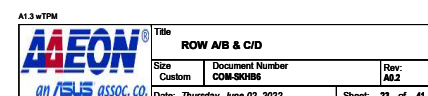
LEDs	Mode	Function
LED0	0100	LINK/ACTIVITY
LED1	0111	LINK 1000
LED2	0110	LINK 100

A1.3 wTPM



Title <b>LAN-I219</b>		
Size <b>A</b>	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <i>Thursday, June 02, 2022</i>		Sheet: <b>22 of 41</b>

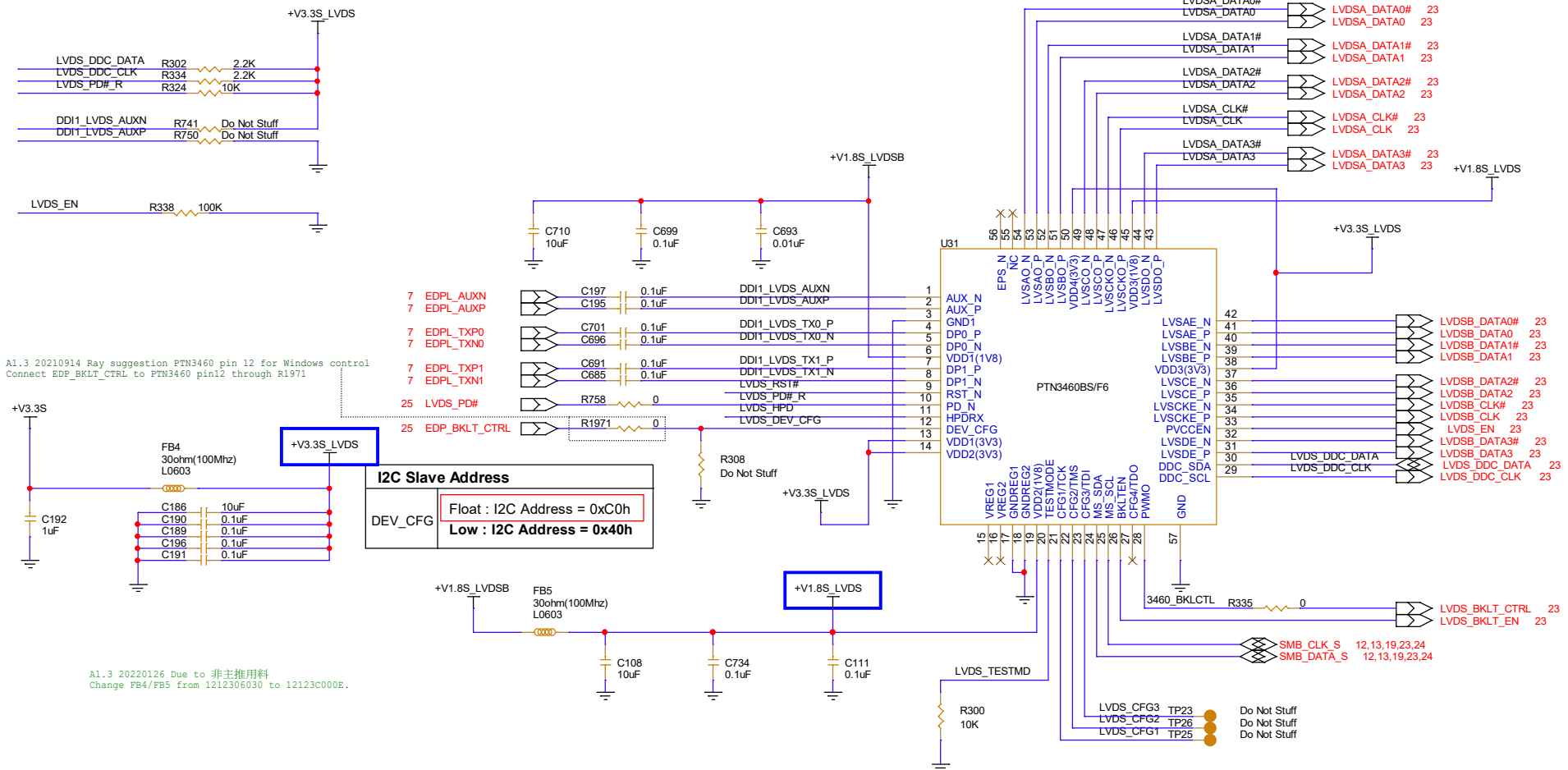









PTN3460



CFG1	HIGH	Dual LVDS Bus
	LOW	Single LVDS Bus
CFG2	HIGH	JEIDA or VESA Format (18 bpp)
	OPEN	JEIDA Format (24 bpp)
CFG3	LOW	VESA Format (24 bpp)
	HIGH	LVDS CLK Frequency 0.5%
CFG4	OPEN	LVDS CLK Frequency 1%
	LOW	LVDS CLK Frequency 0%
CFG5	HIGH	LVDS Output Swing 400mV
	OPEN	LVDS Output Swing 300mV
CFG6	LOW	LVDS Output Swing 250mV
	HIGH	LVDS Output Swing 300mV

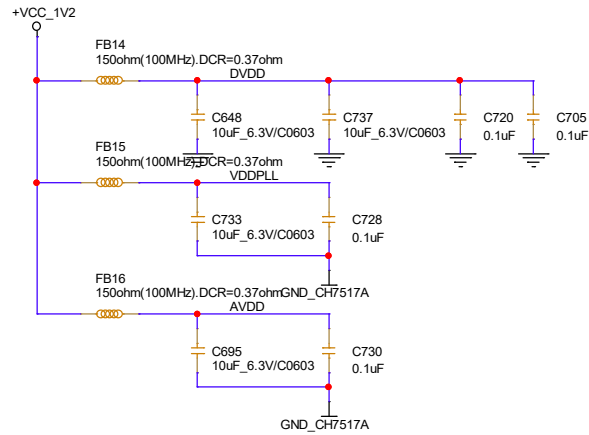
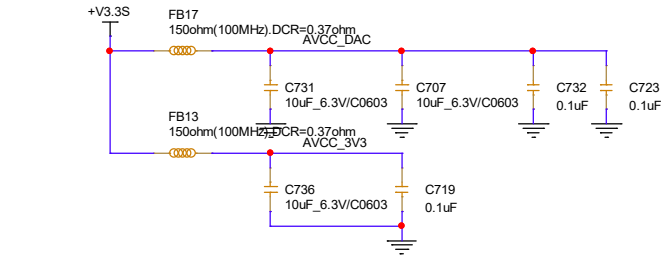


an AELUS assoc. co.

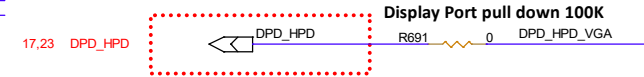
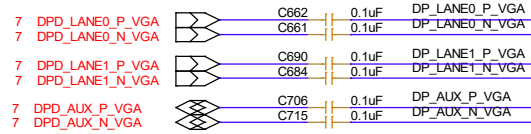
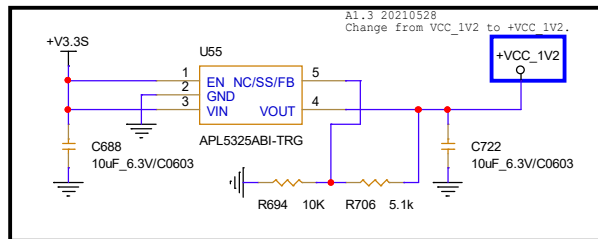
Title <b>DP TO LVDS</b>		
Size B	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: <b>Thursday, June 02, 2022</b>		Sheet: <b>26 of 41</b>

# Note:

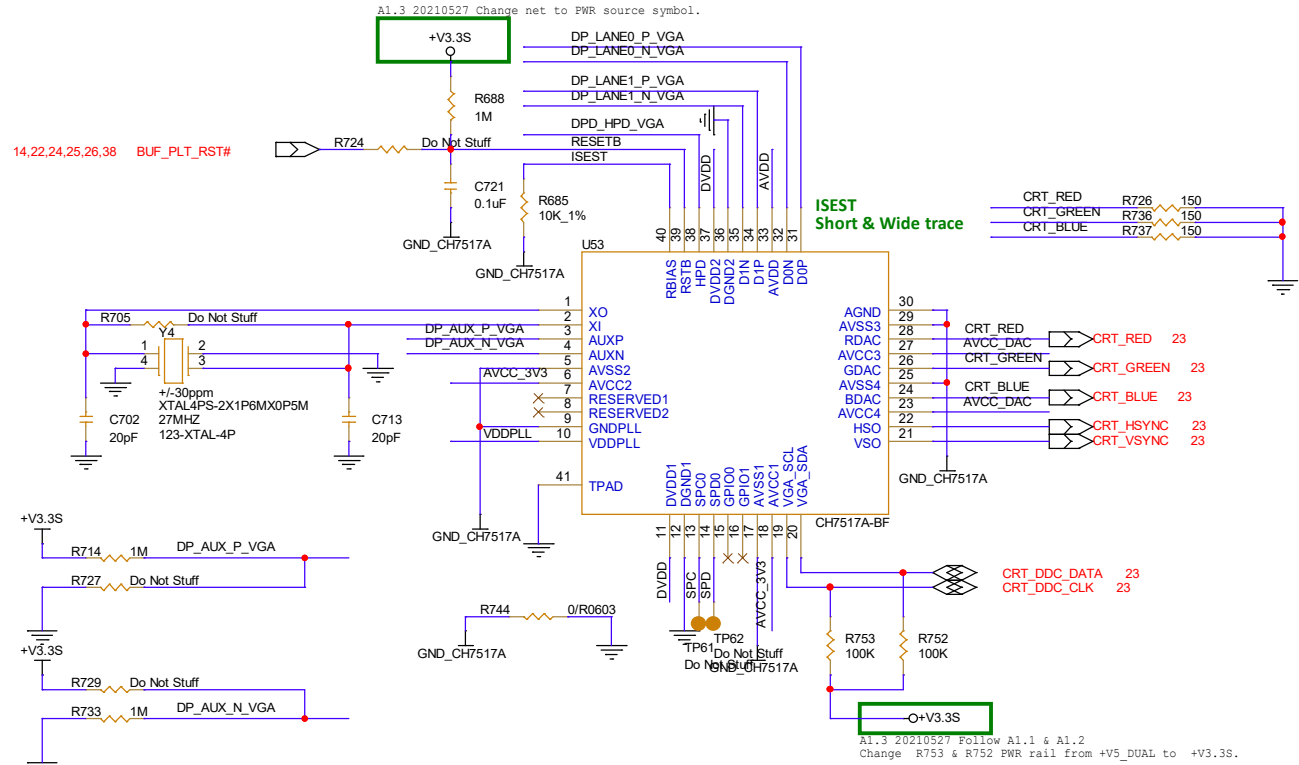
1. +1.05V power spply tolerance range -5%~+25%
2. +3.3V power spply tolerance range +/-10%



## Reserve for Test



## CH7517



A1.3 20210527 Follow A1.1 & A1.2  
Change R753 & R752 PWR rail from +V5\_DUAL to +V3.3S.

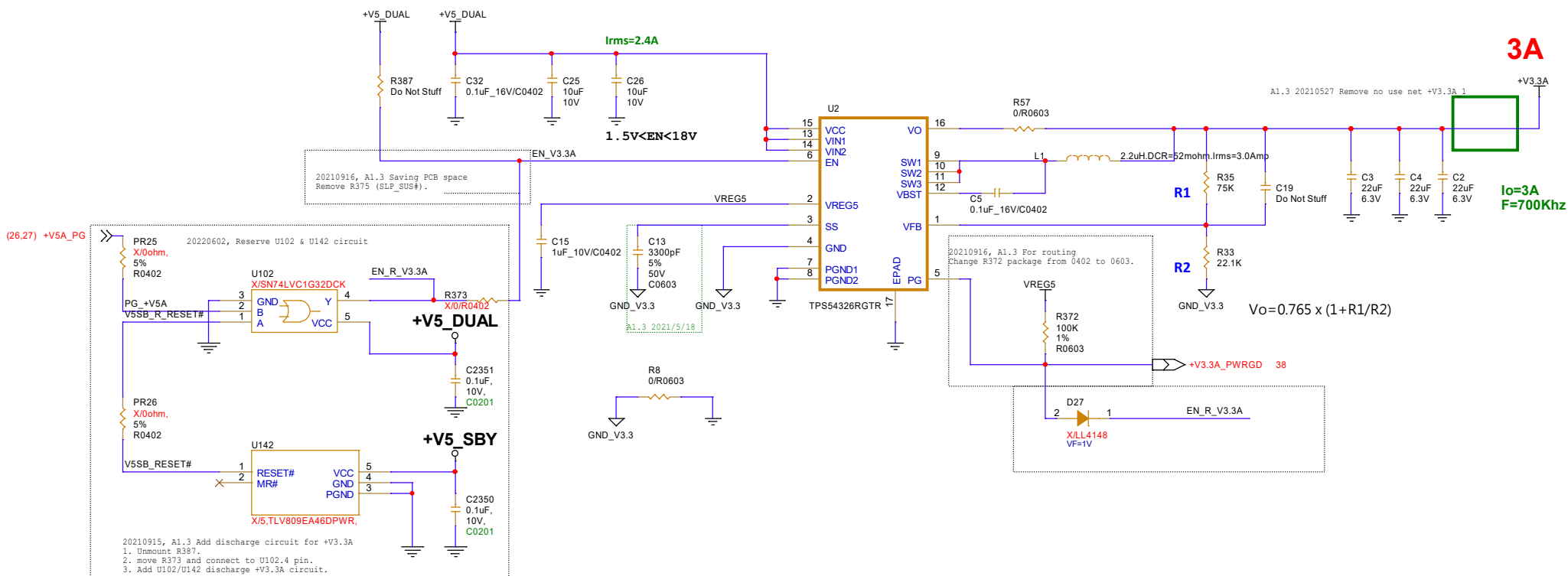
A1.3 wTPM




Title <b>DP to VGA</b>		
Size B	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
Date: Thursday, June 02, 2022		Sheet: 27 of 41



**+V3 . 3A**



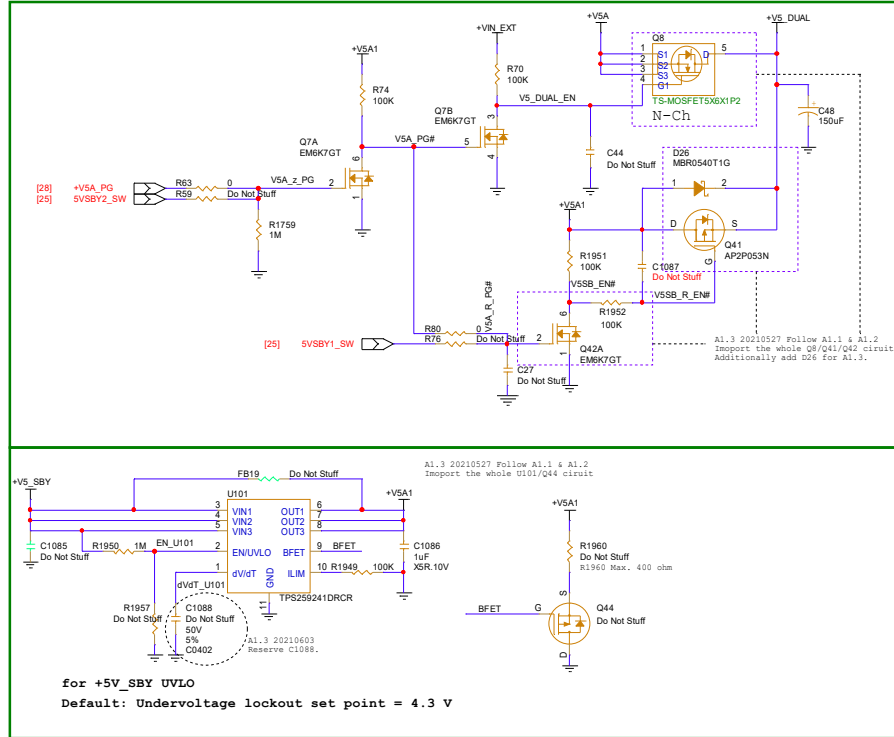
A1.3 wTPM

		Title <b>PPWER SWITCH +V3A</b>	
		Size Custom	Document Number <b>COM-SKHB6</b>
Date: Thursday, June 02, 2022		Rev: <b>A0.2</b>	
		Sheet: 29 of 41	

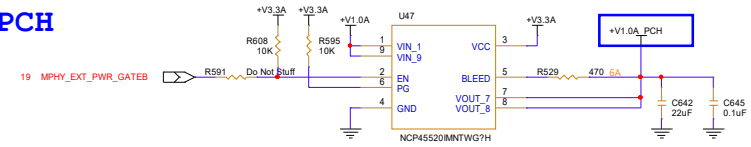


## +V5\_DUAL switch circuit

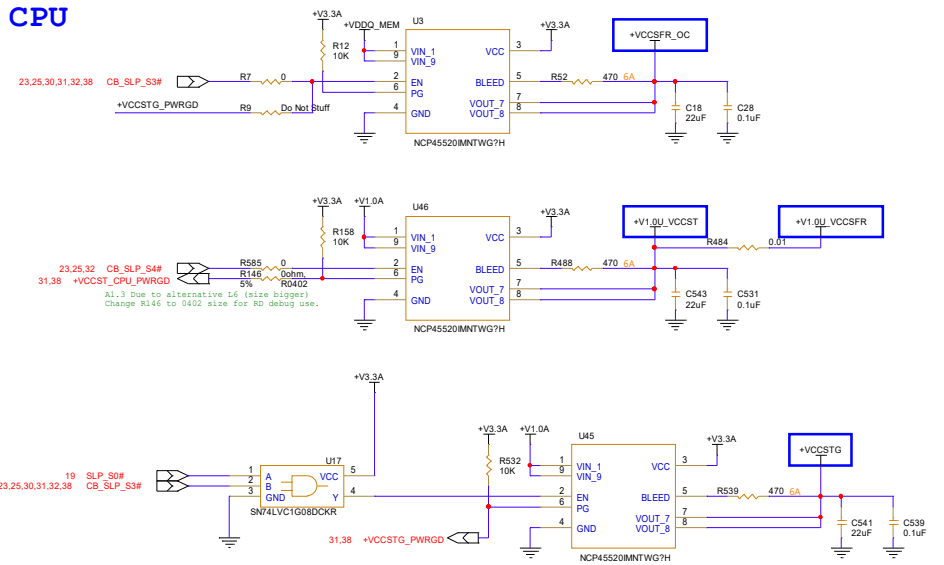
+V5A1, from carrier board  
+V5A, from COM Module



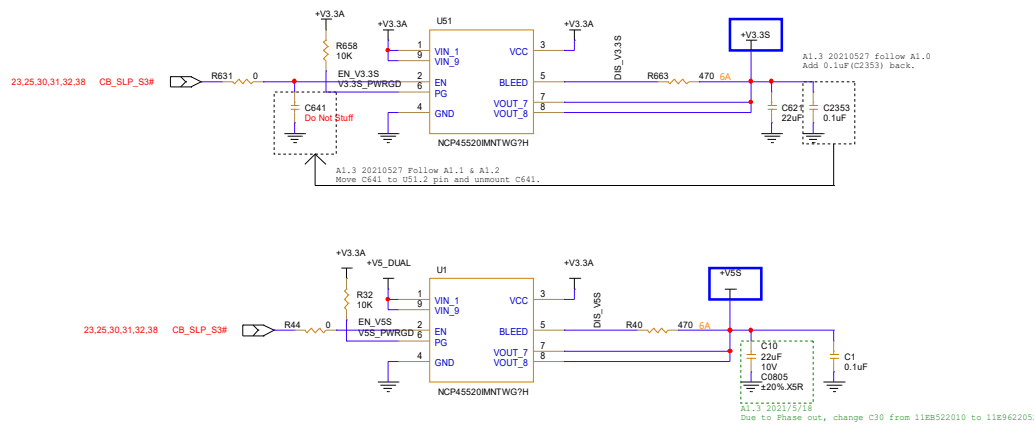
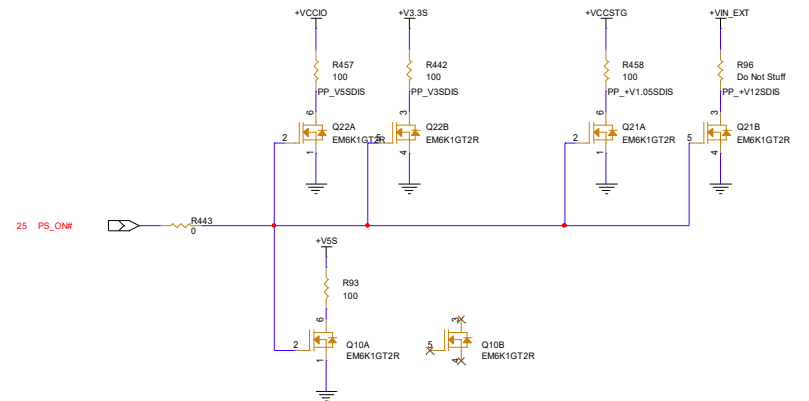
## To PCH



## To CPU

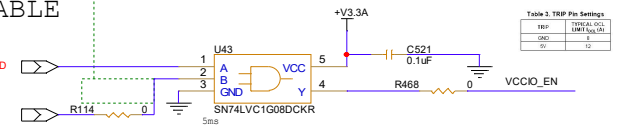


## Discharge

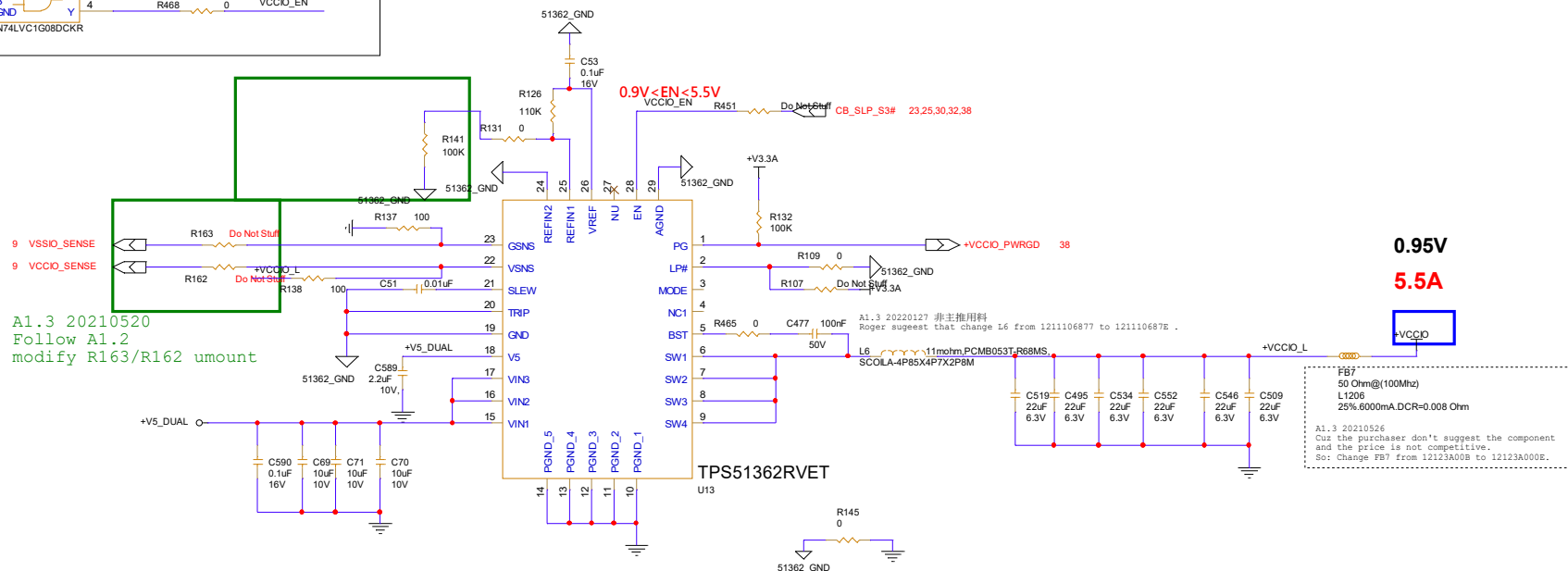
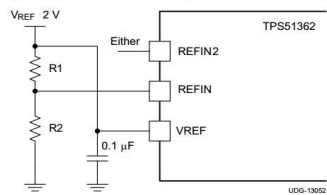


A1.3 Due to alternative L6 (size bigger)  
Remove R117 (+VCCST CPU PWRGD) for saving PCB space.

30,38 +VCCSTG PWRGD



VOLTAGE (V)		V <sub>OUT</sub> OUTPUT VOLTAGE (V)
REFIN PIN (V <sub>REFIN</sub> )	REFIN2 PIN (V <sub>REFIN2</sub> )	
GND	GND	1.05
Float	GND	1.2
GND	Float	1.5
Float	Float	1.35
Resistor dividers	Either GND or Float	Adjustable from 0.6 to 2.0



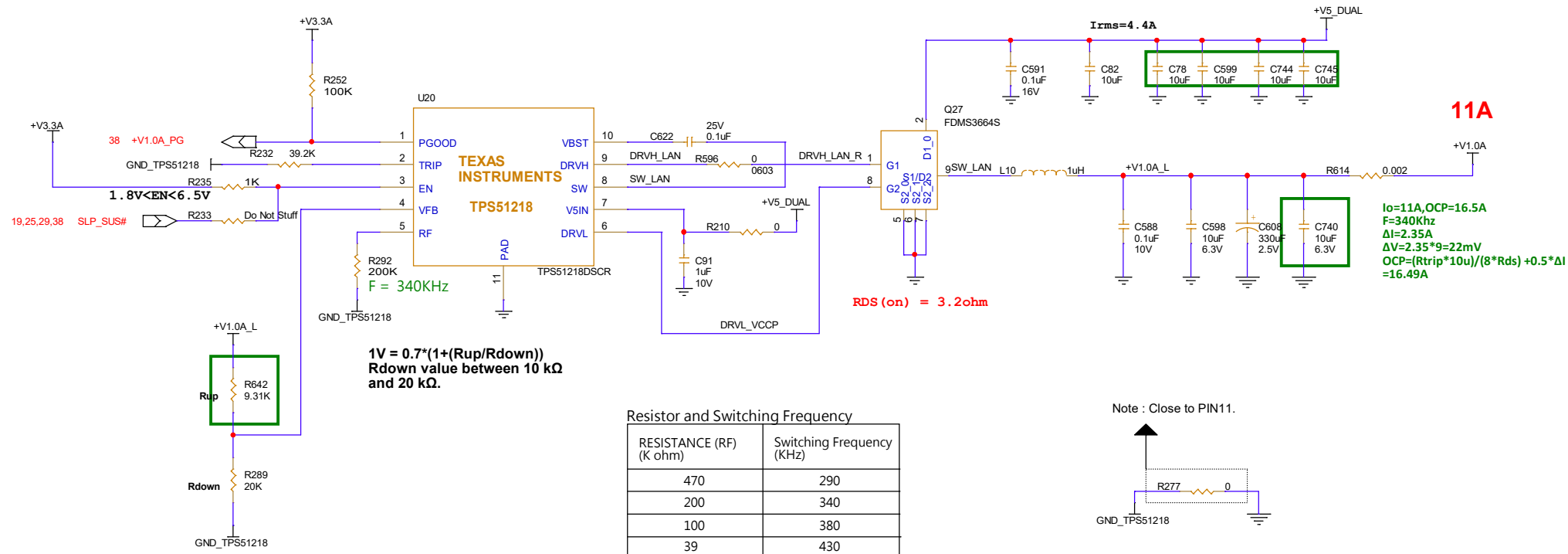
**AEON**<sup>®</sup>  
an ASUS<sup>®</sup> assoc. co.

**+V1.05/+V1.5S**

Rev:  
A0.2

Sheet: 31 of 41

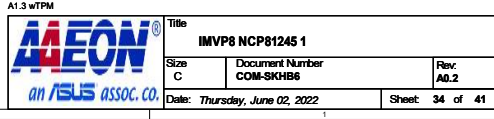


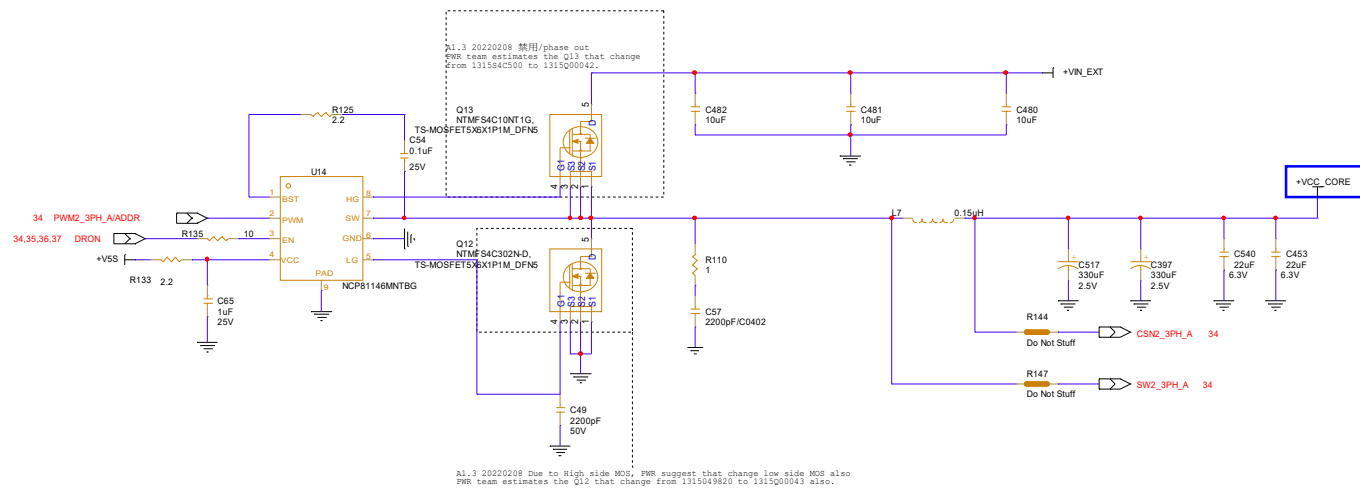
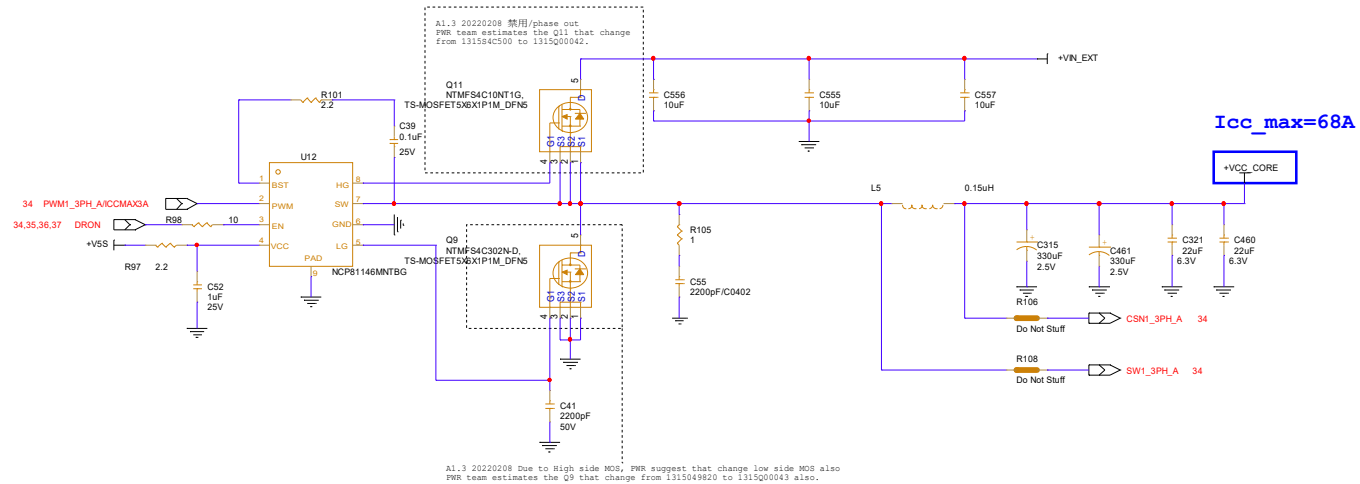


A1.3 wTPM



Title <b>+V1.0A</b>		
Size B	Document Number <b>COM-SKHB6</b>	Rev: <b>A0.2</b>
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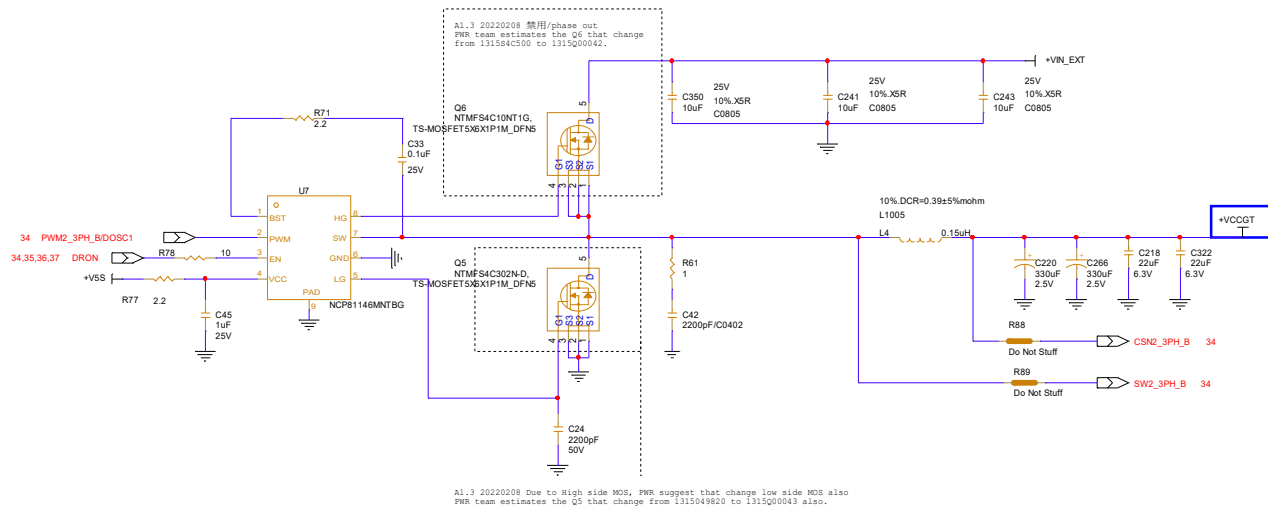
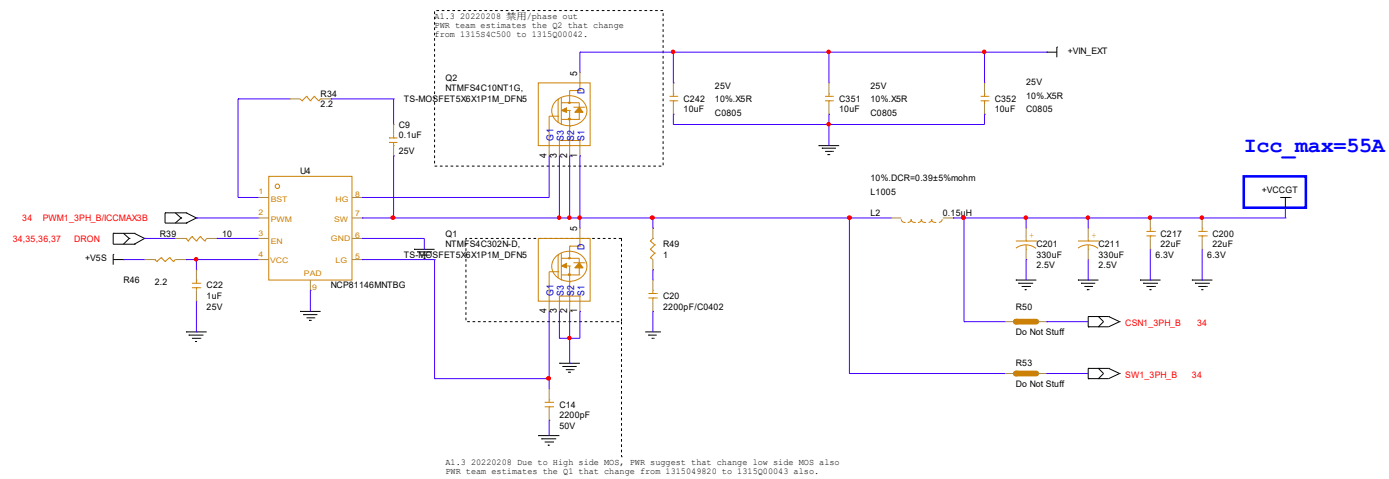




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<b>AMEON</b> an ASUS assoc. co.		Title <b>IMVP8 VCCGT</b>	
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# COM-SKHB6 Revision History

01. Project code: TBD  
02. Model name: COM-SKHB6  
03. Model revision: A1.3\_0\_0  
04. 96-Level: TBD  
05. PCB P/N: 1907SKLH05  
06. PCB thickness: 2.0 mm  
07. PCB stackup: 12 Layer  
08. Panelization: TBD  
09. PCB VIA: NON-HDI  
10. Material: TBD  
11. PCB Dimension: TBD


\*U19 is PCH. Must modify the BOM when create BOM.  
\*U6 is CPU. Must modify the BOM when create BOM.

## Revision History

## Page 1

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.	Bug ID
1		First Release	2015/	HW			A1.0	
2	30	Phase Out	2021/05/18	HW	The part is phase out.	Change C10 from 11EB522010 to 11E9622052.	A1.3	
3		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1121501691 to 1121501690 for C5, C21, C32, C83, C290, C378, C395, C494, C591, C661, C662, C684, C690, C705, C706, C715, C719, C720, C721, C723, C728, C729, C732.	A1.3	
4		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11E9610080 to 1129610080 for C6, C23, C496, C598, C740.	A1.3	
5		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11E9501091 to 11EB501040 for C7, C15, C75, C91, C98, C110, C126, C136, C185, C192, C261, C296, C297, C398, C425, C512, C527, C538, C547, C549, C550, C554, C558, C566, C568, C571, C576, C581, C585, C596, C602, C615, C619, C620, C643, C656, C671, C673, C683, C687, C689, C692, C724, C725.	A1.3	
6		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11E9522790 to 1129522790 for C8.	A1.3	
7		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1124533282 to 11A4433180 for C13.	A1.3	
8		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11C1501080 to 11C1501030 for C31.	A1.3	
9		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1129747091 to 1129747090 for C38, C313.	A1.3	
10		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1129710391 to 1129710390 for C122, C212, C464	A1.3	
11		ePLM	2021/05/18	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1129701791 to 1129701790 for C244, C265, C361, C693.	A1.3	
12		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11C4547140 to 1124547190 for C276, C391	A1.3	
13		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 11E4433090 to 1124433090 for C735.	A1.3	
14		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1651526001 to 16515X0003 for DIMM2	A1.3	
15		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 12123A00009 to 12123A0000E for FB7. 非主推用科/價格不具優勢	A1.3	
16		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 121112207G to 1211122079 for L1.	A1.3	
17		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1907SKLH01 to 1907SKLH05 for PCB1.	A1.3	
18		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1050539214 to 1050504324 for R73.	A1.3	
19		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Keeping PR1, PR2 are 1540782282.	A1.3	
20		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 1462001280 to 1462001282 for SPI1. BIOS PN: 1462CSKH-A0	A1.3	

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COM-SKHB6 Revision History

- 01. Project code: TBD
- 02. Model name: COM-SKHB6
- 03. Model revision: A1.3\_0\_0
- 04. 96-Level: TBD
- 05. PCB P/N: 1907SKLH05
- 06. PCB thickness: 2.0 mm
- 07. PCB stackup: 12 Layer
- 08. Panelization: TBD
- 09. PCB VIA: NON-HDI
- 10. Material: TBD
- 11. PCB Dimension: TBD

\*U19 is PCH. Must modify the BOM when create BOM.  
\*U6 is CPU. Must modify the BOM when create BOM.

Revision History

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Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.	Bug ID
21		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 144SKLPC0 to 1440QM1700 for U19.	A1.3	
22		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 14S9346000 to 14S9346001 for U31.	A1.3	
23		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 14407517A0 to 14407517A1 for U53.	A1.3	
24		ePLM	2021/10/13	HW	Due to original part phase out and optional function.	Change from 14909665T0 to 14909665T2 for U54.	A1.3	
25		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 14S3010700 to 1430053251 for U55	A1.3	
26		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 12310025A5 to 1231X00003 for Y3	A1.3	
27		ePLM	2021/05/19	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Change from 12310027A3 to 1231X00004 for Y4	A1.3	
28	Page 23	ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	***ePLM BOM change from Capaciotr to Resistor. Change from 12310027A3 to 105B700009. so change C155->R763 C157->R764 C703->R765 C704->R766 C708->R768 C709->R767 C711->R769 C712->R770 C625->R777 C626->R778 C635->R771 C636->R772 C637->R773 C638->R774 C639->R775 C640->R776 C627->R779 C628->R780 C629->R781 C630->R782 C631->R783 C632->R784 C633->R785 C634->R786 And add net name DPD_LANE*_CN_*, DPC_LANE*_CN_*, DPB_LANE*_CN_*	A1.3	
29		ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Confirm with Jeff C. that R757 change to unmount. ePLM R757 mount ver. A1.1 R757 unmount ver. A1.2 R757 unmount	A1.3	
30		ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Confirm with Jeff C. that Q37 change to mount. ePLM Q37 unmount ver. A1.1 Q37 mount ver. A1.2 Q37 mount	A1.3	
31		ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Follow ver. A1.2 that umount R162,R163,R719	A1.3	
32		ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Follow ePLM / A1.1 / A1.2 Change R251 from 0 ohm to 33ohm.	A1.3	
33		ePLM	2021/05/20	HW	Follow the ePLM BOM of 9697SKLH05-S to change the parts. Standard P/N: COM-SKHB6-A10-0001	Follow ePLM BOM modification and change from SW1 to SW1(1,2OFF).	A1.3	

## COM-SKHB6 Revision History

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## Revision History

## Page 3

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.	Bug ID
34	31	ePLM	2021/05/26	HW	Cuz the purchaser don't suggest the component and the price is not competitive.	Change FB7 from 12123A00B to 12123A000E.	A1.3	
35	25	A1.2	2021/05/27	HW	Follow A1.2 modification	C567 move close to FB2/U16.5	A1.3	
36	25	A1.2	2021/05/27	HW	Follow A1.2 modification	C100 unmount	A1.3	
37	25	A1.2	2021/05/27	HW	1. Follow A1.2 modification 2. Richard request	1. Unmount R804 & add R1961. 2. Add R1962 (unmod).	A1.3	
38	25	A1.2	2021/05/27	HW	Follow A1.2 modification	Original A1.2 add U100 circuit, but Ray suggest that remove U100 whole circuit and add new design.	A1.3	
39	30	A1.2	2021/05/27	HW	Follow A1.2 modification	1. Move C641 to U51.2 pin and unmount C641. 2. Import the whole Q8/Q41/Q42 circuit 3. Import the whole U101/Q44 circuit 4. Additionally add D26 for A1.3.	A1.3	
40	30	A1.2	2021/05/27	HW	Follow A1.2 modification	Change R753 & R752 PWR rail from +V5_DUAL to +V3.3S for right PWR rail.	A1.3	
41	30	A1.2	2021/05/27	HW	Follow A1.2 modification	Add Board ID (R1953-R1956)	A1.3	
42	24	PWR rail	2021/05/27	HW	Change to correct PWR rail.	Change from +V3.3S to +V3.3A_EC. Change R682 & R699 from 47K to 10K and keep unmounting.	A1.3	
43	23	PWR rail	2021/06/04	HW	Remove A1.1 design for PCB spacing.	Remove R1947 (0ohm) for DPD_HPD.	A1.3	
44	14	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change U50.5 from +V3.3S to +V3.3A	A1.3	
45	19	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change BA11 from EC_RSMRST# to PM_RSMRST#	A1.3	
46	23	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change NET from +V5A1 to +V5_SBY	A1.3	
47	24	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change Q43 from 1315EMK10 to 1315013810.	A1.3	
48	25	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change Q25 to 1315013810.	A1.3	
49	25	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	FSCK & FSCK_F connect to Q26B	A1.3	
50	27	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change R753 & R752 PWR rail from +V5_DUAL to +V3.3S.	A1.3	
51	27	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Change from VCC_IV2 to +VCC_IV2.	A1.3	
52	30	PWR rail	2021/06/04	HW	follow A1.1 & A1.2 design	Reserve C1088	A1.3	
53	38	ESS	2021/09/14	HW	Ray suggestion that add accelerated discharge RSMRST# circuit for RTC problem and modify U10 & U39 connection for discharge RSMRST#.	Remove R454 and add U102 whole circuit. And remove R449. Change U39.1 connection from U10.4 to EC_RSMRST#(from EC). Change U10.4 connection from U39.1 to RSMRST_PWRRG0# (To EC).	A1.3	
54	25	禁用/ 一物二號	2021/09/08	HW	Follow A1.2 modification	Change C648,C688,C695, C707,C722,C731, C733,C736,C737 from 11EA810030 to 11EB610050.	A1.3	
55	24	禁用	2021/09/08	HW	part EOL	Change LPC1 from 1655912032 to 1655X00036.	A1.3	
56	28	禁用	2021/09/13	HW	料件撤減禁用	PWR team estimates the Q23 that change from 1315D4C500 to 1315038640 (Non-P2P).	A1.3	
57	35, 36	禁用	2021/09/13	HW	禁用/phase out	PWR team estimates the Q11,Q13,Q2,Q6 that change from 1315S4C500 to 1315030042. Due to PWR suggestion, PWR team estimates the Q9,Q12,Q1,Q5 that change from 1315049820 to 13150300043 also.	A1.3	
58	26	ESS	2021/09/14	HW	Ray suggestion PTN3460 pin 12 for Windows control	Connect EDP_BKLT_CTRL to PTN3460 pin12 through R1971	A1.3	
59	29, 38	Discharge	2021/09/15	HW	Add discharge circuit for +V3.3A	1. Unmount R387. 2. move R373 and connect to U102.4 pin. 3. Add U102/U142 discharge +V3.3A circuit. 4. Add B27 for discharging RSMRST#. 5. Add D28 for discharge path.	A1.3	
60	26	PCB space	2021/09/16	HW	Saving PCB space and smooth routing.	Remove R375 (SLP_SUS#). Change R372 package from 0402 to 0603.	A1.3	
61	30	A1.0	2021/09/23	HW	Follow A1.0.	Add 0.1uF(C2353) back.	A1.3	
62	28	Forbidden	2021/10/27	HW	禁用/phase out	After the result of PWR E&E measurement, change R211 from 56.2k(10SA56229) to 35.7k(0402) for ocp adjust.	A1.3	
63	34	Dr.MOS EOL	2021/10/27	HW	After verifying Dr.MOS, change OCP value.	Horse suggest that Change R440 From 18K(0402) to 16K(0402) for OCP adjust	A1.3	
64	19, 20, 26, 15, 31, 30	Component	2022/01/26	HW	非主龍用料	This suggested that 1.Change C95 from 1655902122 to 1655802133. 2.Change F54/F56/F610 from 1212306030 to 12123C000E. 3.Change Y2 from 12310024A2 to 12310024A5. And change C102 and C105 from 124418090 to 11A4312041 after vendor's verification. 4.Roger suggest that change Q14, Q20 from 1315762010 to 121110687E. 5.Roger suggest that change L6 from 1211106877 to 121110687E. 6. Remove R117 (+VCC5V1_CPU_PWRRG0) for saving PCB space. 7.Change R146 to 0402 size for RD debug use.	A1.3	

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HISTORY		
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