

Preliminary Specification, AAEON reserves the right to change this specification without notice.

GENESYS-APL7

3.5" GENE Board

Engineering Specification

Rev. A10 2019/02/19

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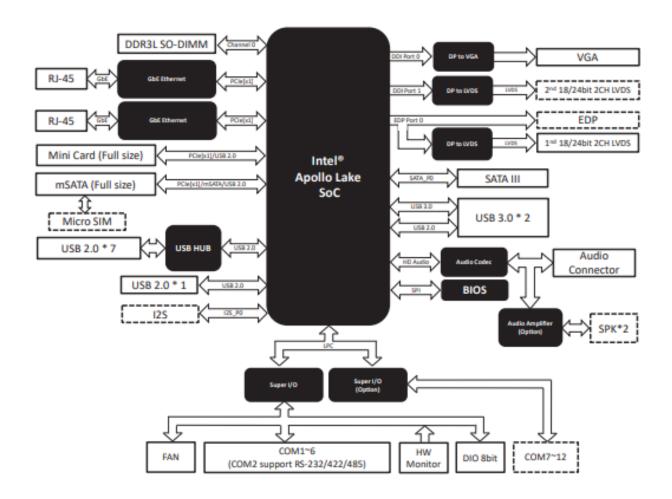
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Revision History

Rev	Brief Description of Changes	Date of Issue	Author(s)
A1.0	Final spec	2019/02/19	Alan Lan

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1. Block Diagram



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2. SPECIFICATIONS

Engineering Specification				
Product Name		GENE-APL7		
System				
Form Factor	Size	3.5" SubCompact Board		
Processor	Туре	Intel® Pentium® N4200/ Celeron® N3350 Processor SoC		
System Memory	Туре & сар.	DDR4 1866MHz SODIMM x 1,Max up to 8GB.		
Chipset	Туре	Intel® Pentium® N4200/ Celeron® N3350 Processor SoC		
I/O Chips	Туре	Fintek F81866		
Ethernet	Controller	2 x 10/100/1000M Realtek RTL-8111E Ethernet		
Wake on LAN		Yes		
BLOS	Interface	SPI		
BIOS	Capacitor	AMI BIOS – 128M bit ROM		
Matchdon Times	Chipset	Fintek F81866		
Watchdog Timer	Function	255 levels		
RTC	Chipset	Intel® Pentium® N4200/ Celeron® N3350 Processor SoC		
H/W Status	Chipset	Fintek F81866		
Monitor	Function	VIN1=Vcore / Vin2 = DDR		
Expansion Interface		1 x Full Size Mini Card 1 x Full Size Mini Card (colay mSATA)		
Battery		Lithium battery		
Power Supply Request		+12V DC, AT or ATX		
Power Consumption		N4200 @ DDR4 8GB, 12V@1.61A		
Unit Size		7"x5.1"x1.7" (178mmx 129mmx 43mm) for passive cooler		
Gross Weight		-		
Operating Temp		32°F ~ 122°F (0°C ~ 50°C)		
Storage Temp.		-40°F ~ 176°F (-40°C ~ 80°C)		
Humility		0%~90% relative humidity, non-condensing		



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MTBF		TBD	
Chipset		Intel® Pentium® N4200/ Celeron® N3350 Processor SoC	
Memory		- Thiole 1 Gradine 14-200/ Colorone 140000 1 10003301 000	
I/O			
Solid Storage Disk interface		1 x Half Size Mini Card for mSATA 1 x 2.5" HDD Bay	
Display		1 x VGA	
Ethernet		2 x RJ45	
Serial Port		COM 1: RS-232 COM 2: RS-232/RS-422/RS-485	
USB	Detail	2 x USB 3.0/2.0 Ports (Real IO), 2 x USB 2.0 Ports (by Front IO),	
Expansion		1 x Full Size Mini Card	

3. OS Support Request

Operation System	Support	Notes
Windows 10 (64 bit)	V	
Linux	V	Ubuntu 16.04.4

4. Mechanical Drawing



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5. Qualification Plan

Formal certification CE / FCC Class A

Drop test: Reference ISTA PROJECT 1A

Random Vibration / 5-500Hz; 2grms

6. Ordering Information / Product Configuration

TBD

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