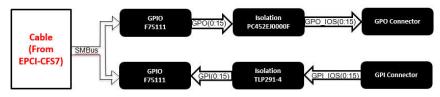


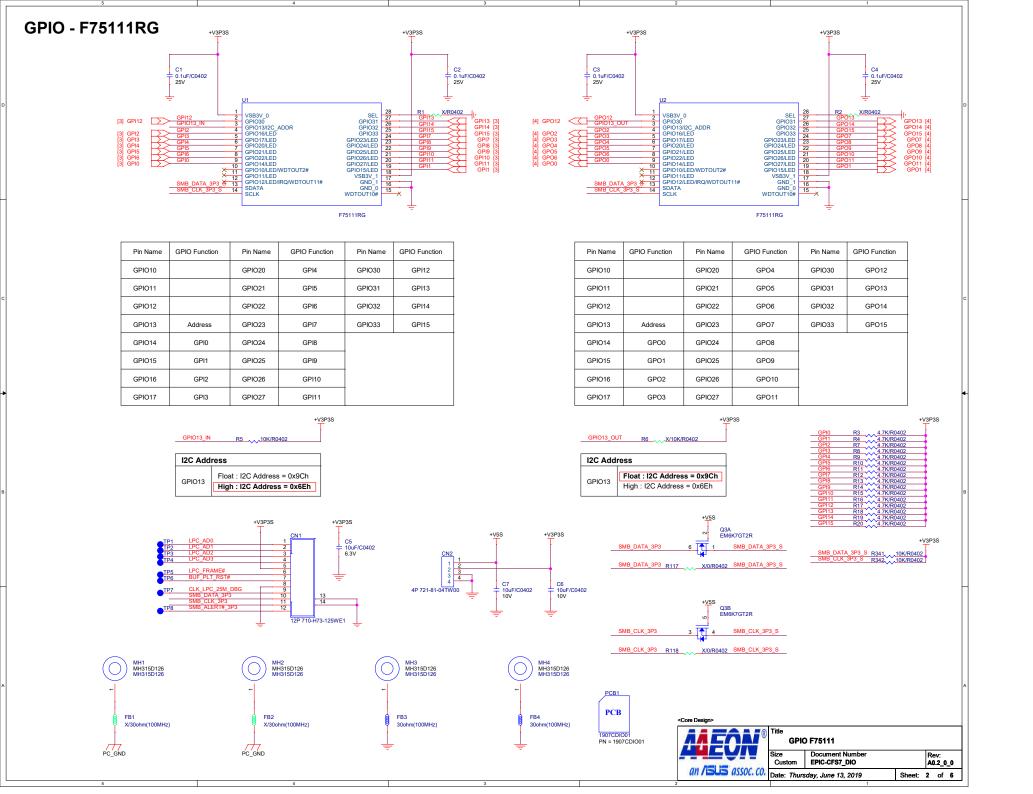
## **Sub Compact Board**

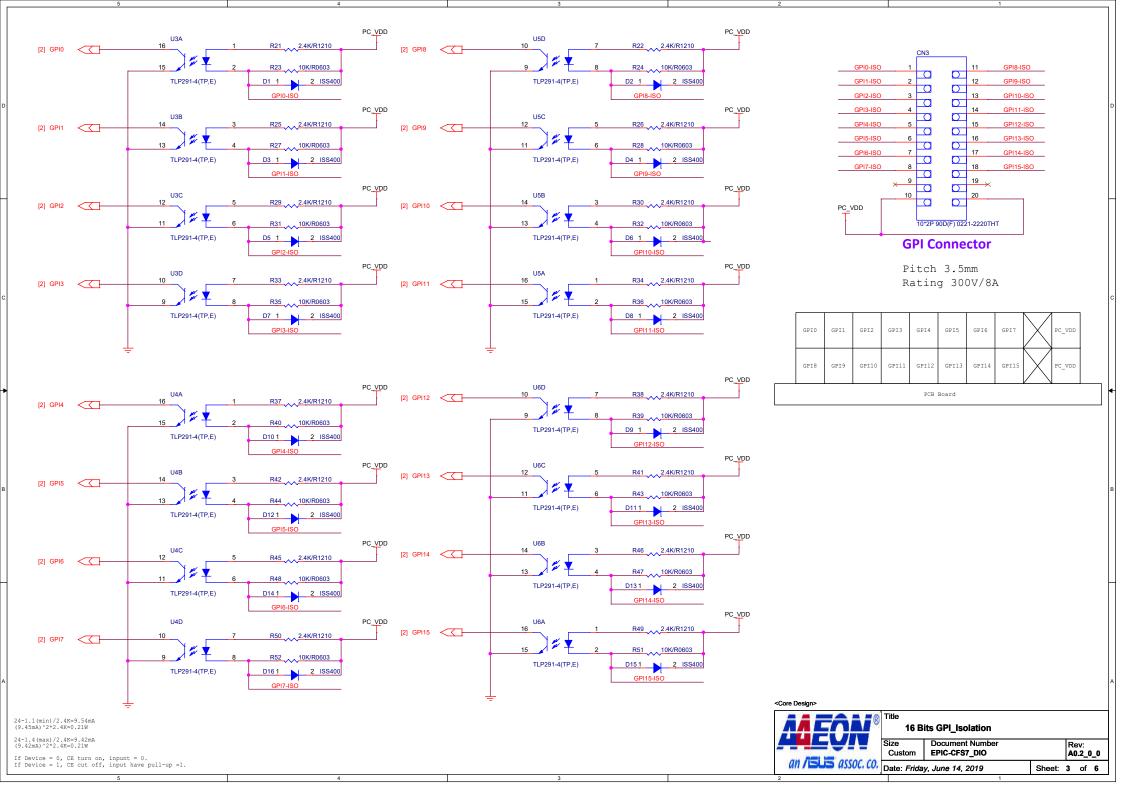
## EPIC-CFS7-DIO A0.2 Block Diagram

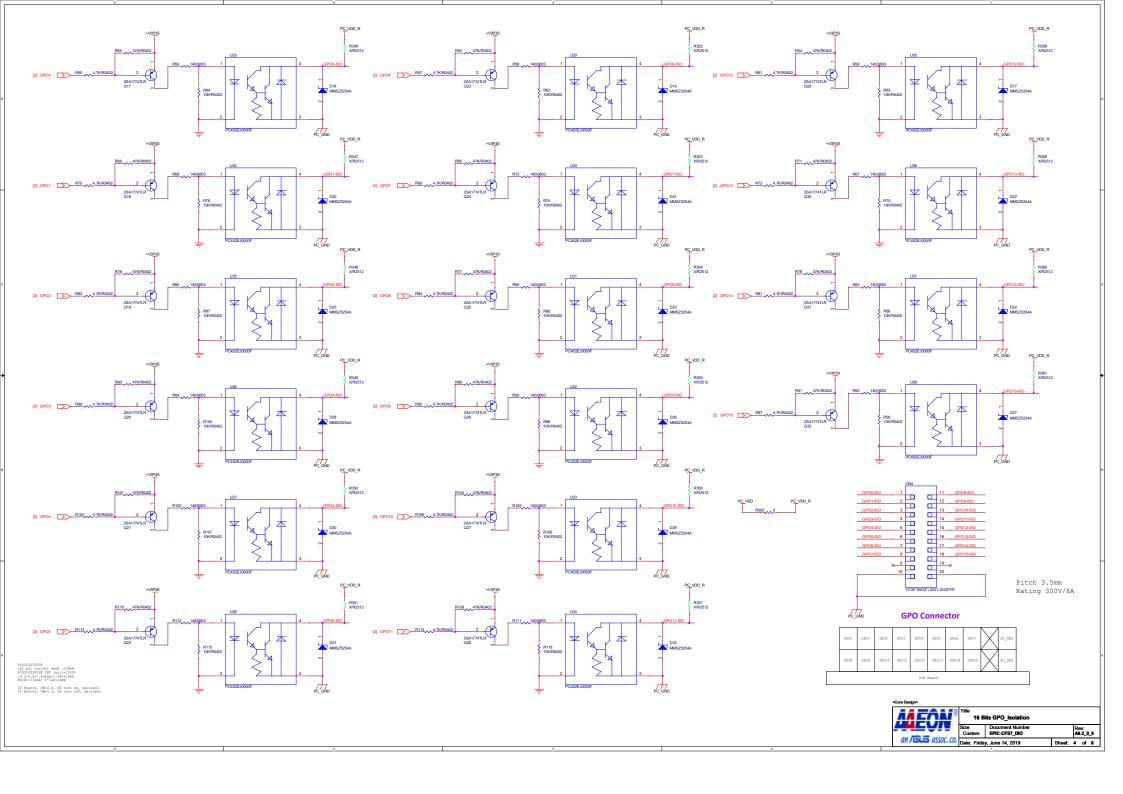


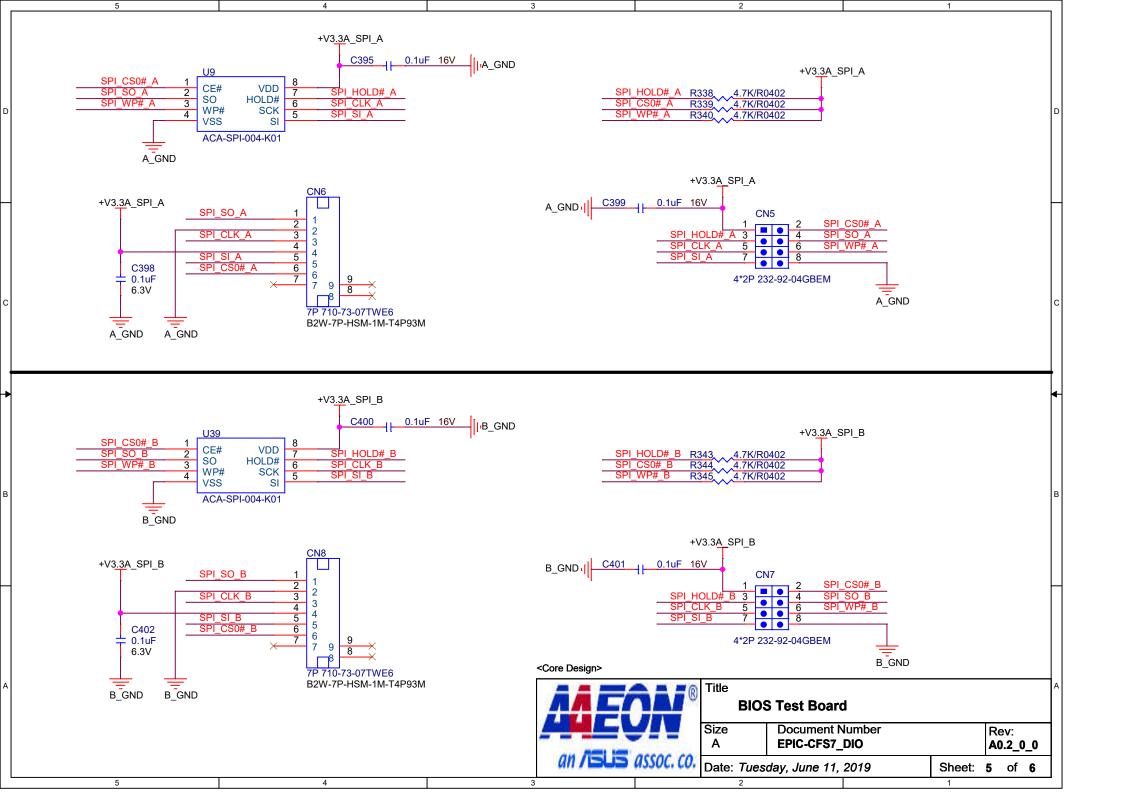


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Date	Revision	Page	Modification	Reason
2018/12/04	A0.1	1~6	First Release.	
2019/05/08	A0.2	516	Del (1).CII, CN2(16559090034) (2).CAI4(16559090034) (2).CAI4(1655907130) (3).CAI4(1655907130) (3).CAI4(1655907130) (3).CAI4(1655907130) (3).CAI4(1655907200) (4).CAI4(1655907200)	Del EC Test Board.
2019/05/08	A0.2	4	Del (1) CN7(1655901000) (2) CN10(1655912002) (2) CN10(1655912003) (4) C25(1124410080) (4) C25(1124410080) (6) JP1 JP2(1653000105) (6) JP1 JP2(1653000105) (7) JP1(2)(1653001070) (8) R1 R2,R7(105A70013) (9) R1 R2,R7(105A70013) (10) SN1(165000700) (11) U8(1440852800) (11) U8(1440852800) (12) U8(1430005302)	Del Post Card Test Board.
2019/05/08	A0.2	3/4	Del (1).C1,C4,C71,C72,C74,C75,C76,C77(1124522881) (2).C2,C2,C3,C5,C5,C7,C3,C25,C30,C31,C32,C59,C51,C52,C53,C70,C73(11C2410730) (2).C2,C3,C5,C5,C7,C3,C25,C30,C31,C32,C59,C51,C52,C53,C70,C73(11C2410730) (3).C4,C4,C45,C45,C49,C44,C73(11E2510032) (5).D4 reserved CAP C47,C55,C55,C57 (6).C45,C54,C54,C54,C54,C54,C54,C54,C54,C54,	Isolation change from ioslation IC to PhotoCoupler.
2019/05/08	A0.2	2	Used all GPI with U1, all GPO with U2.	For GPI/GPO used the same IC.
2019/05/08	A0.2	2	CN2 Pin 1 change from 5VS to +V3P3S.	No need to provide 5V to customer Device.

