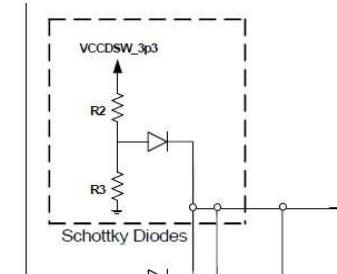
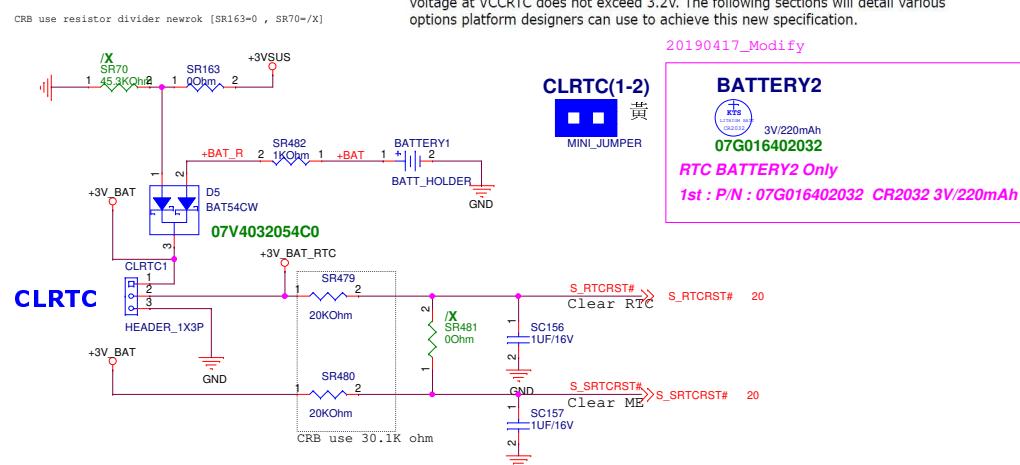
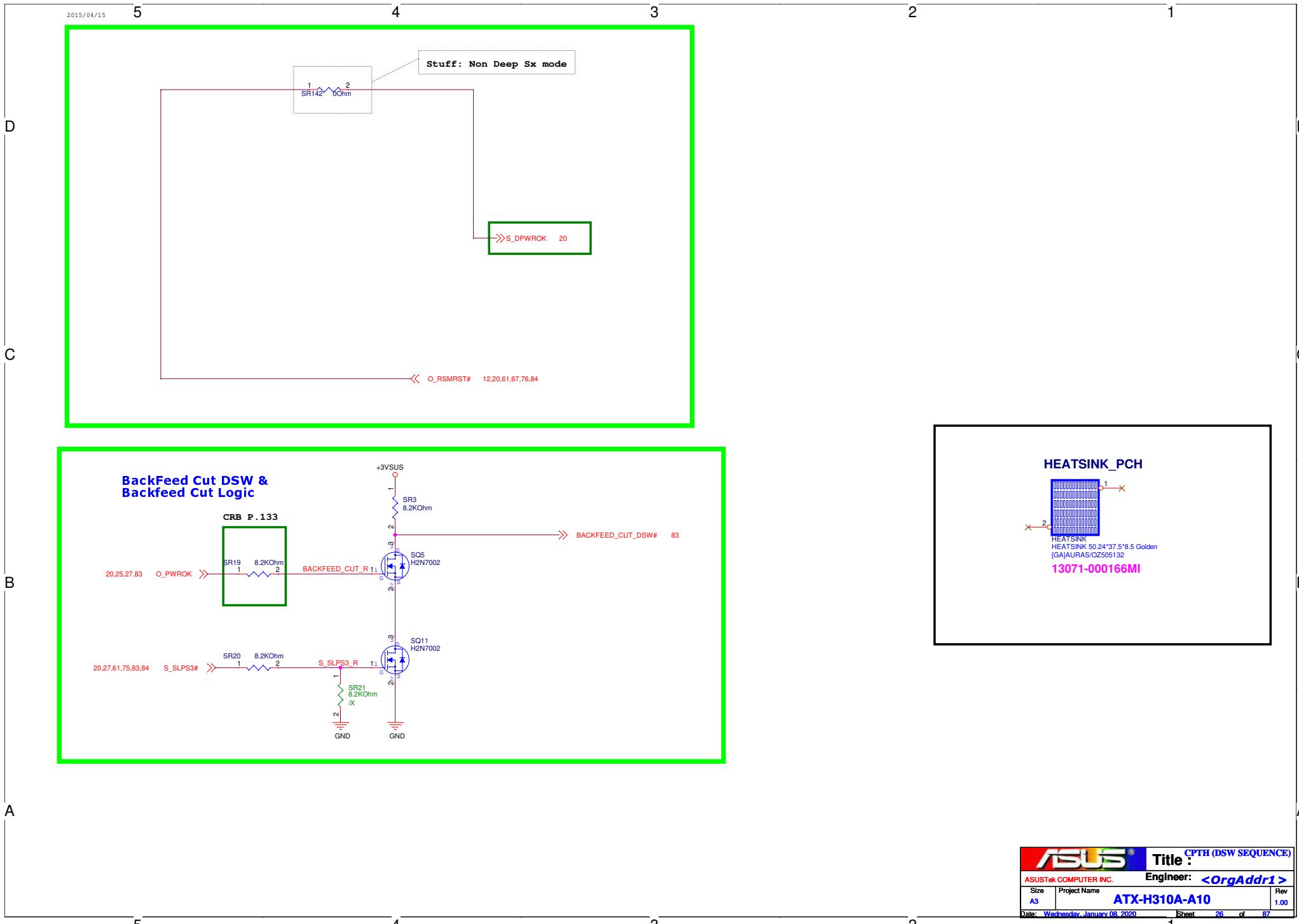


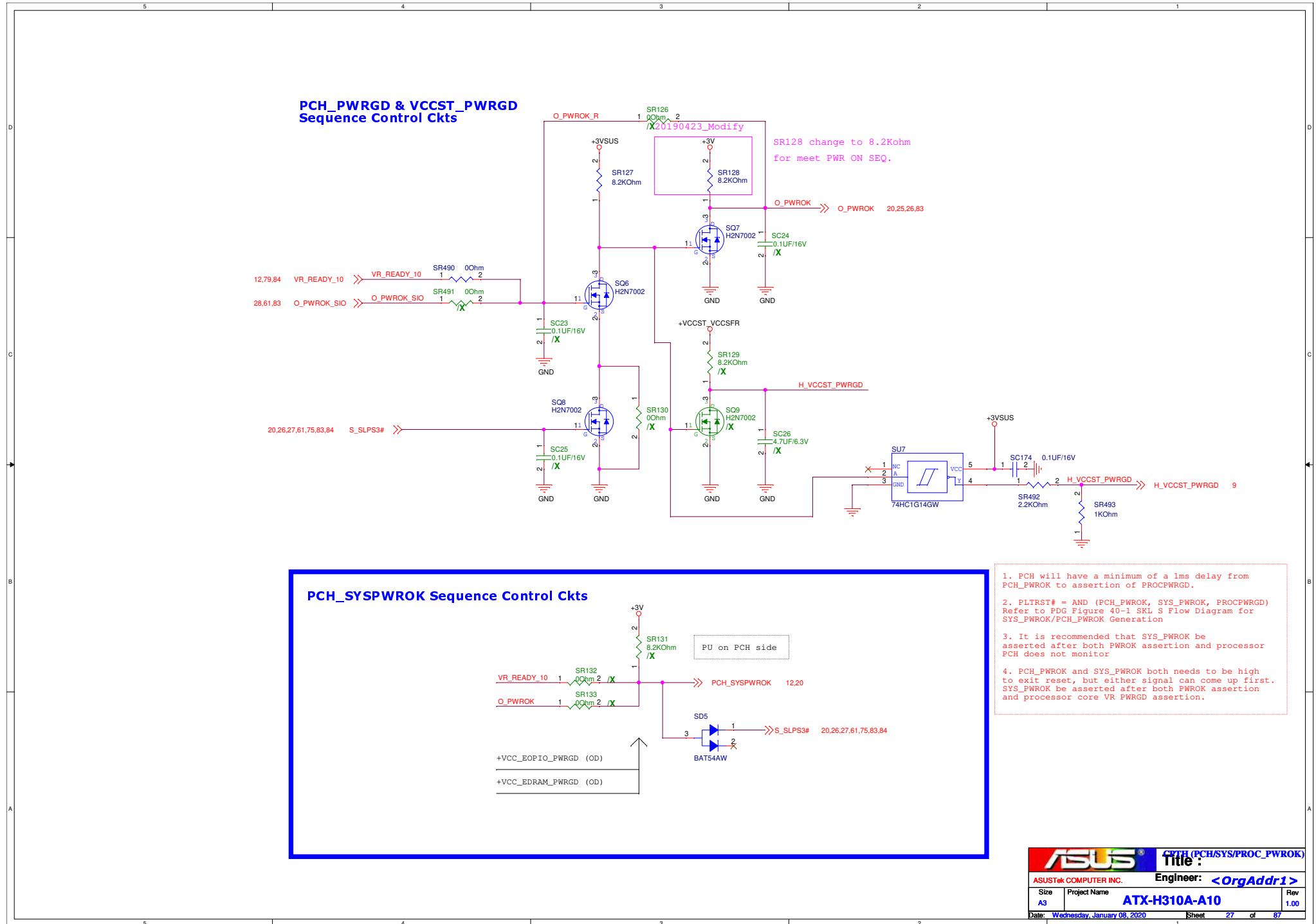
### VCCRTC External Circuit

On SKL, the VCCRTC max voltage is being reduced to minimize leakage on the ESD diodes and prevent RTC oscillator problems. Whether VCCRTC is sourced from Vbatt in G3 or VCCDSW\_3p3 in Non-G3 state, platform designers must ensure the effective voltage at VCCRTC does not exceed 3.2V. The following sections will detail various options platform designers can use to achieve this new specification.

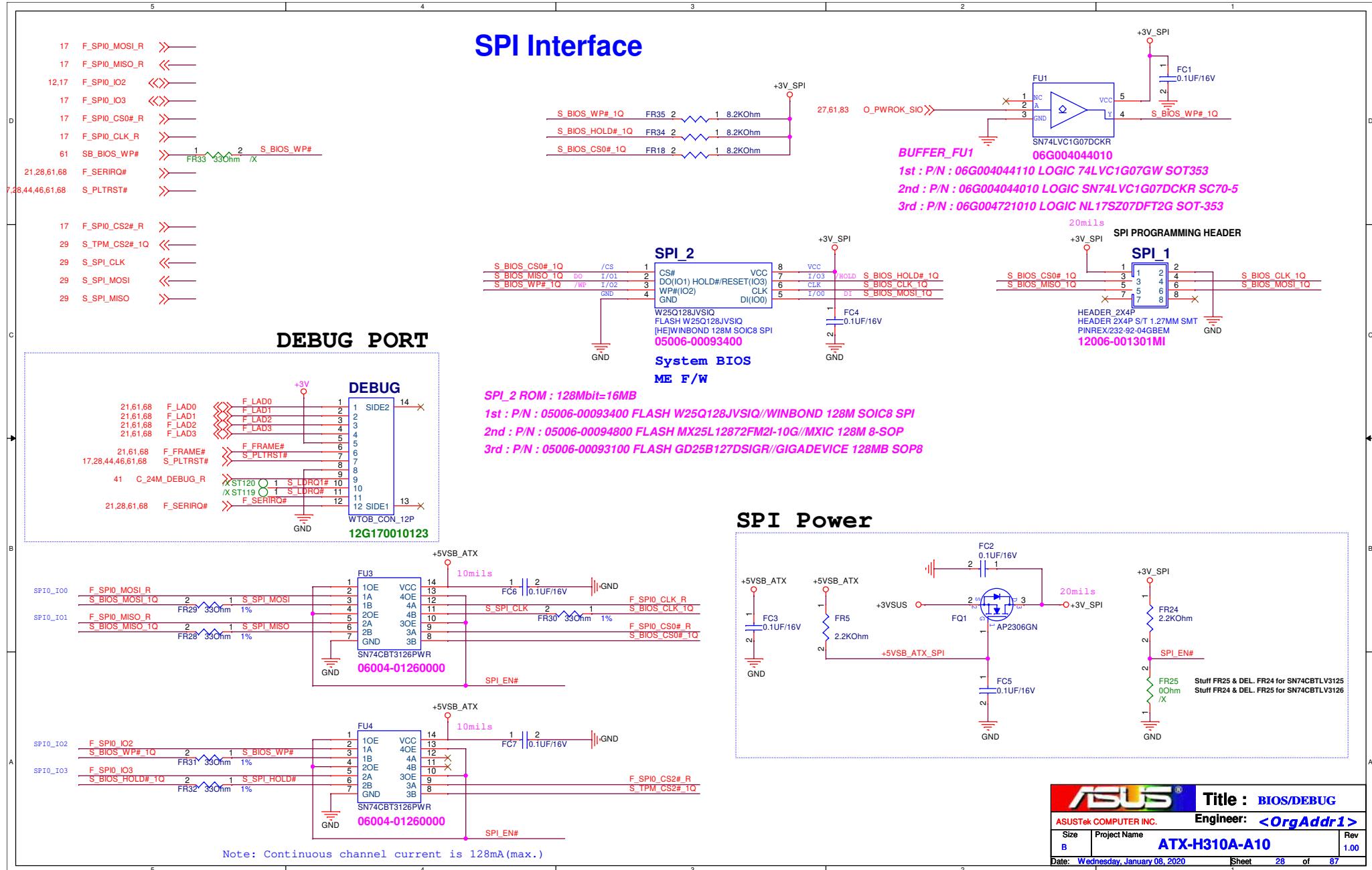
20190417\_Modify

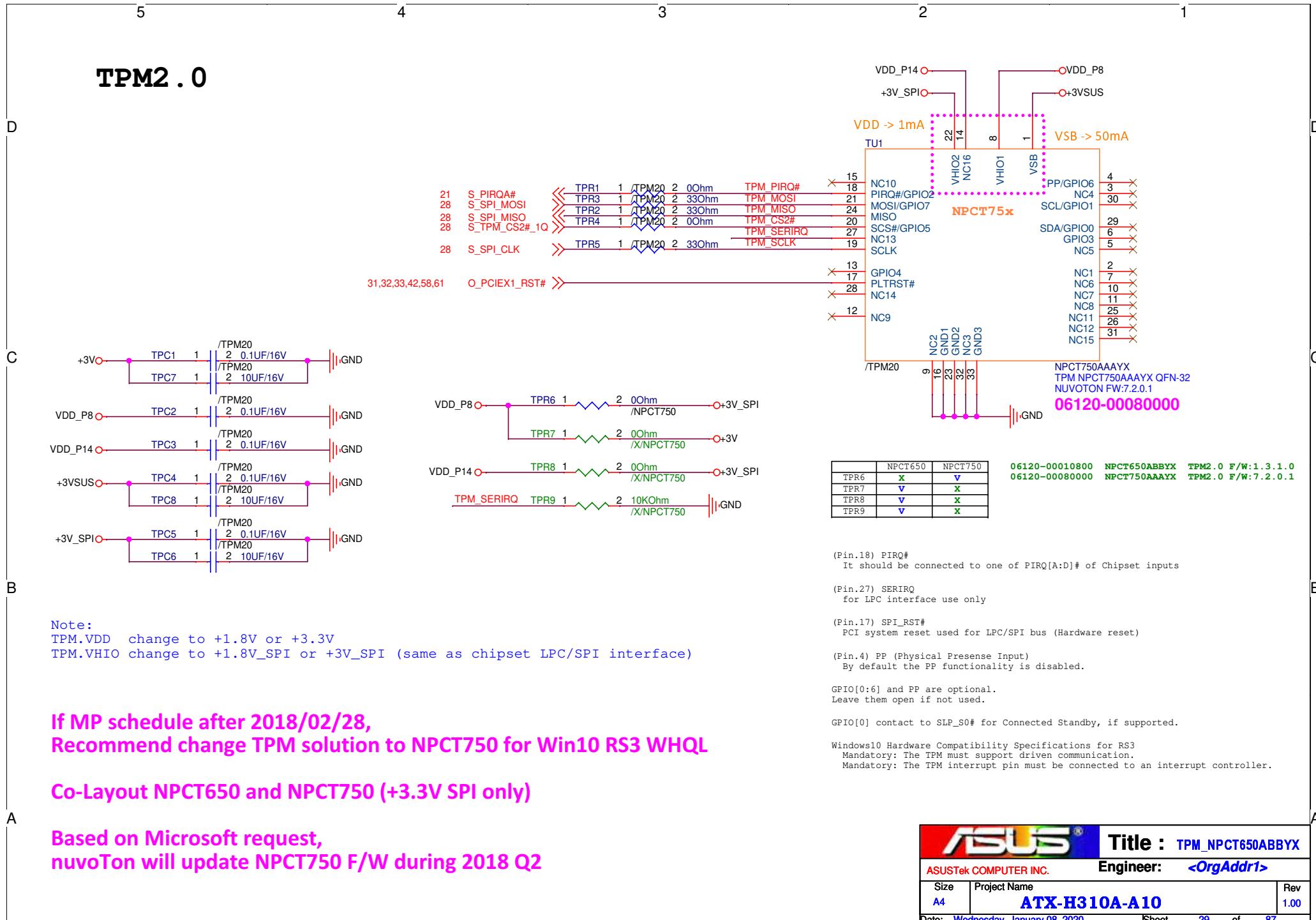


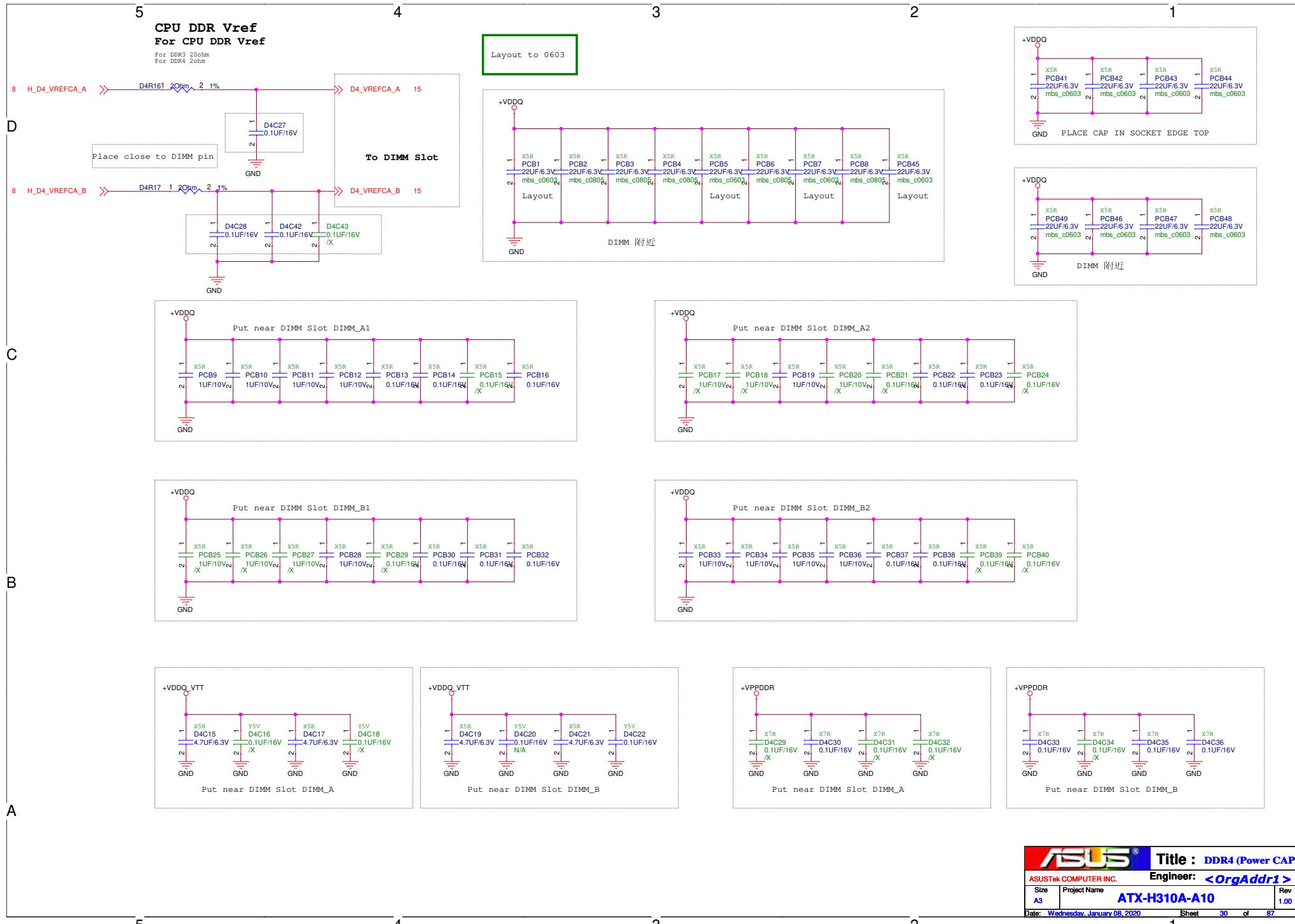


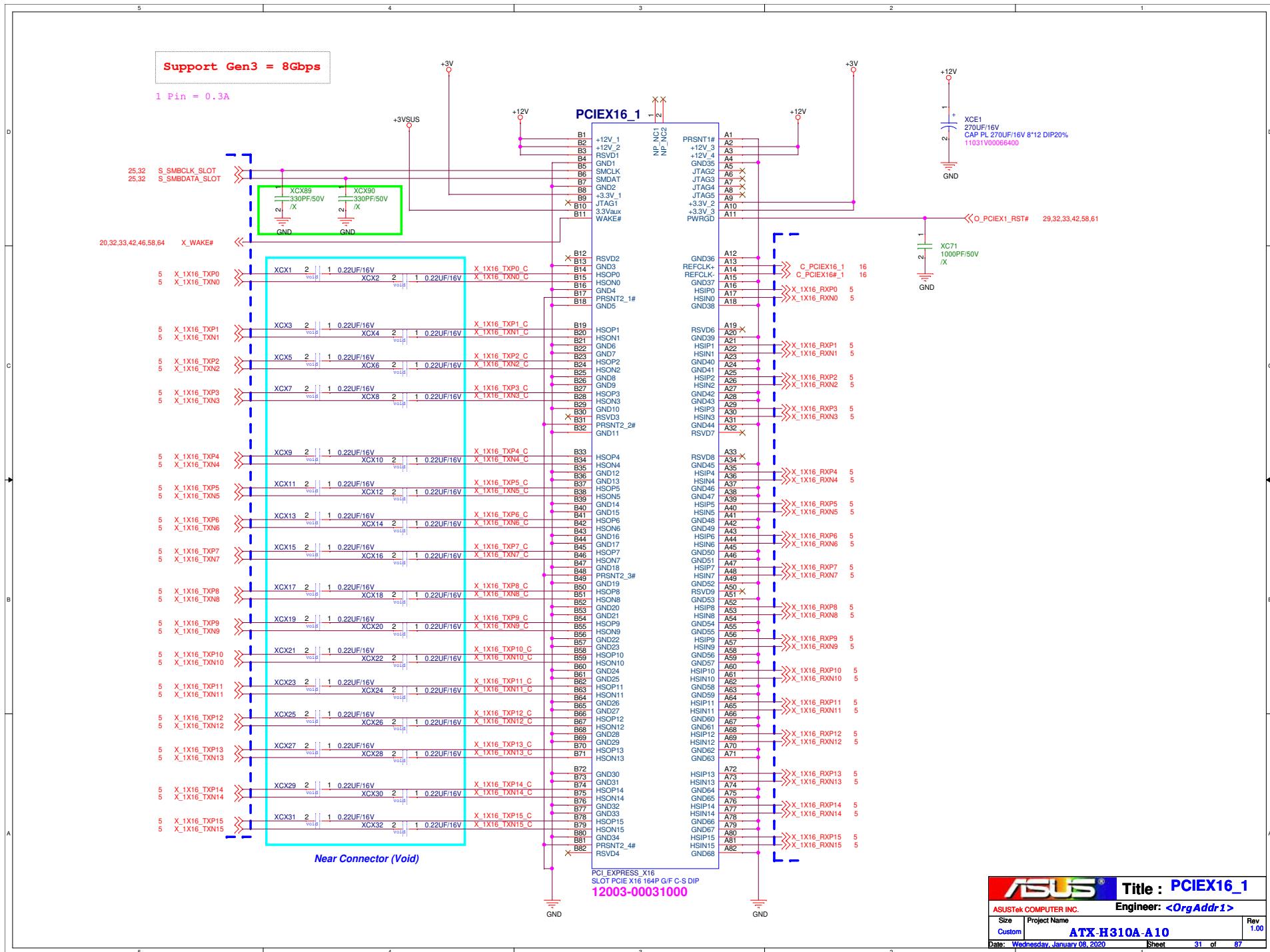


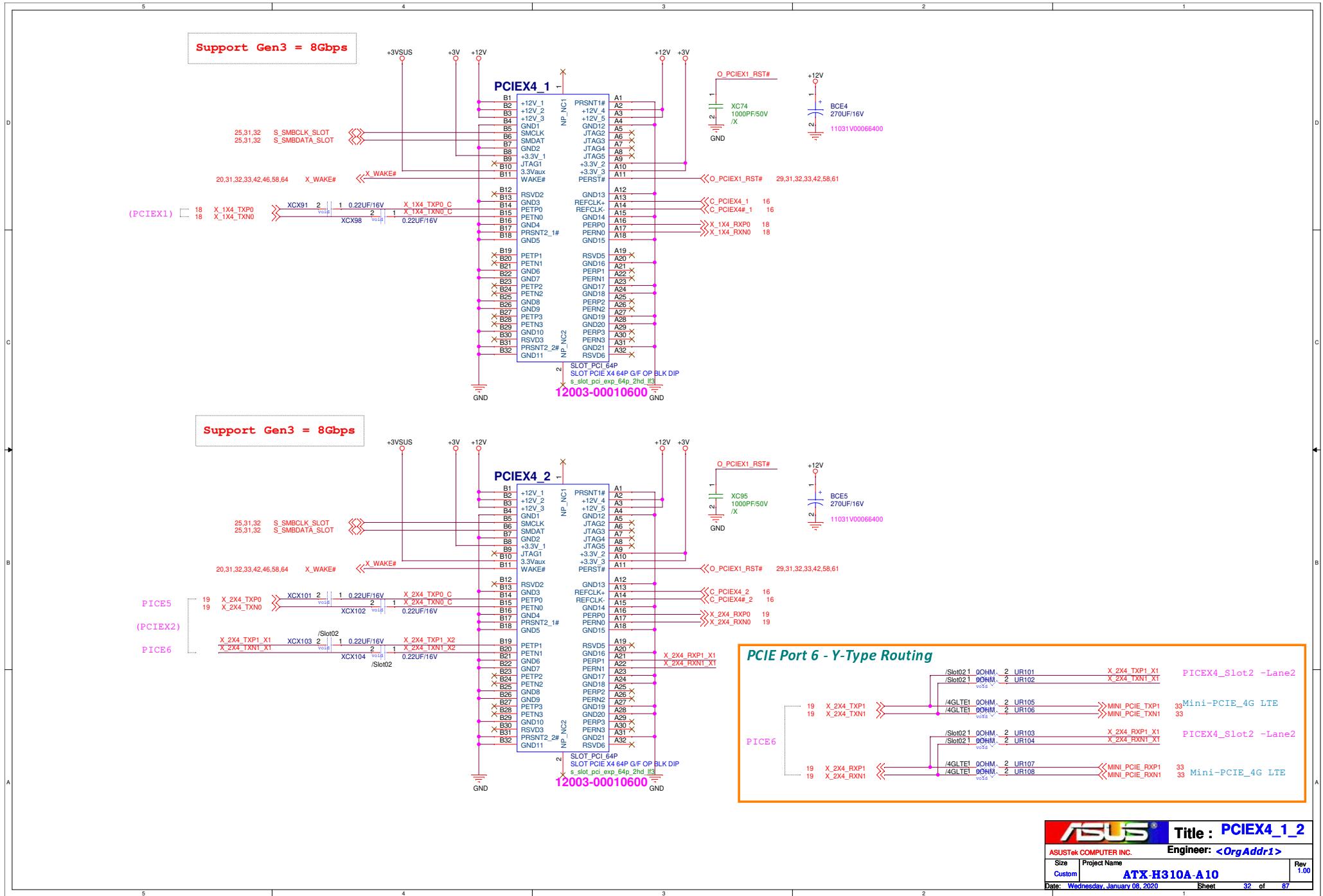
# SPI Interface

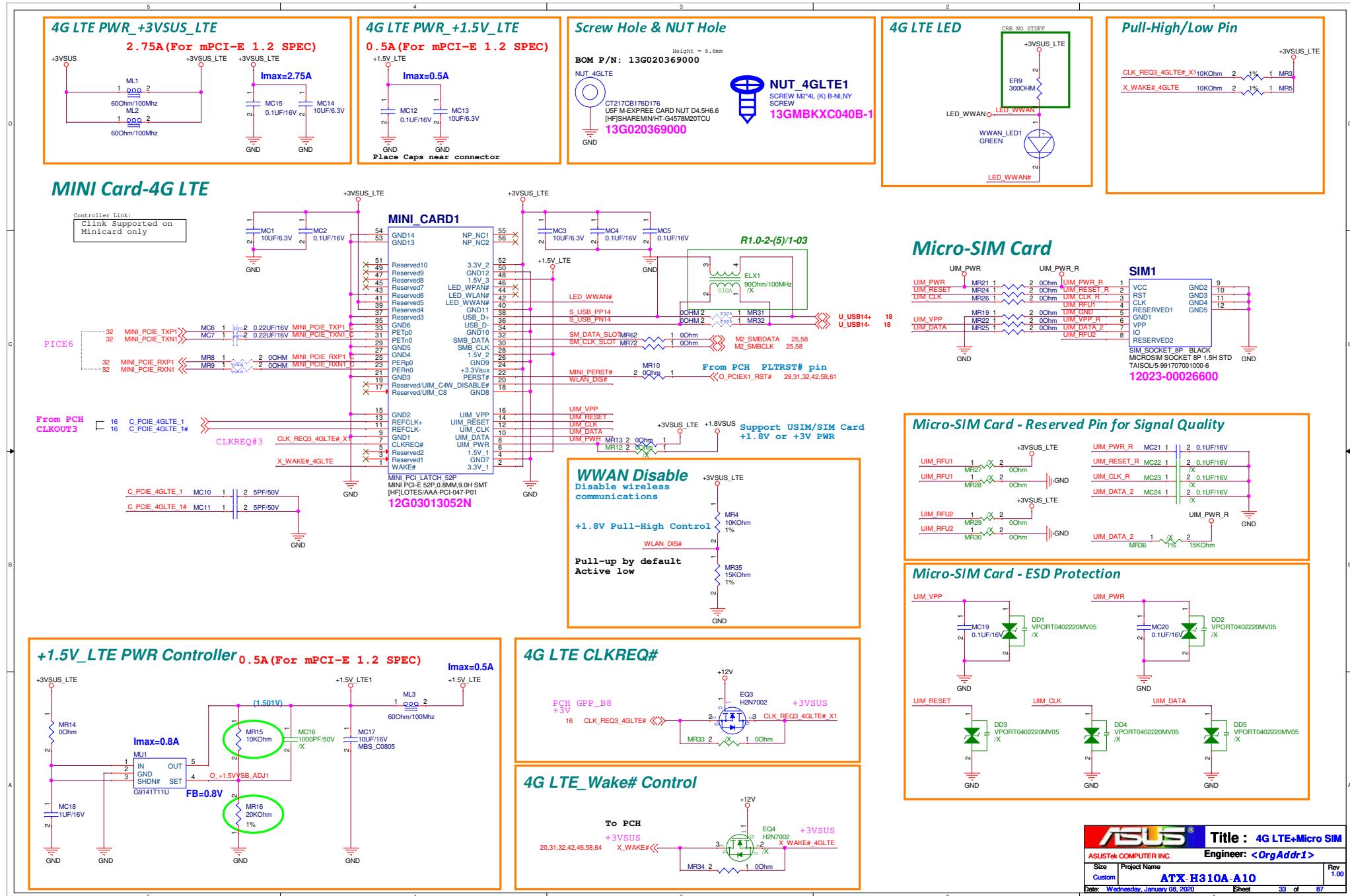


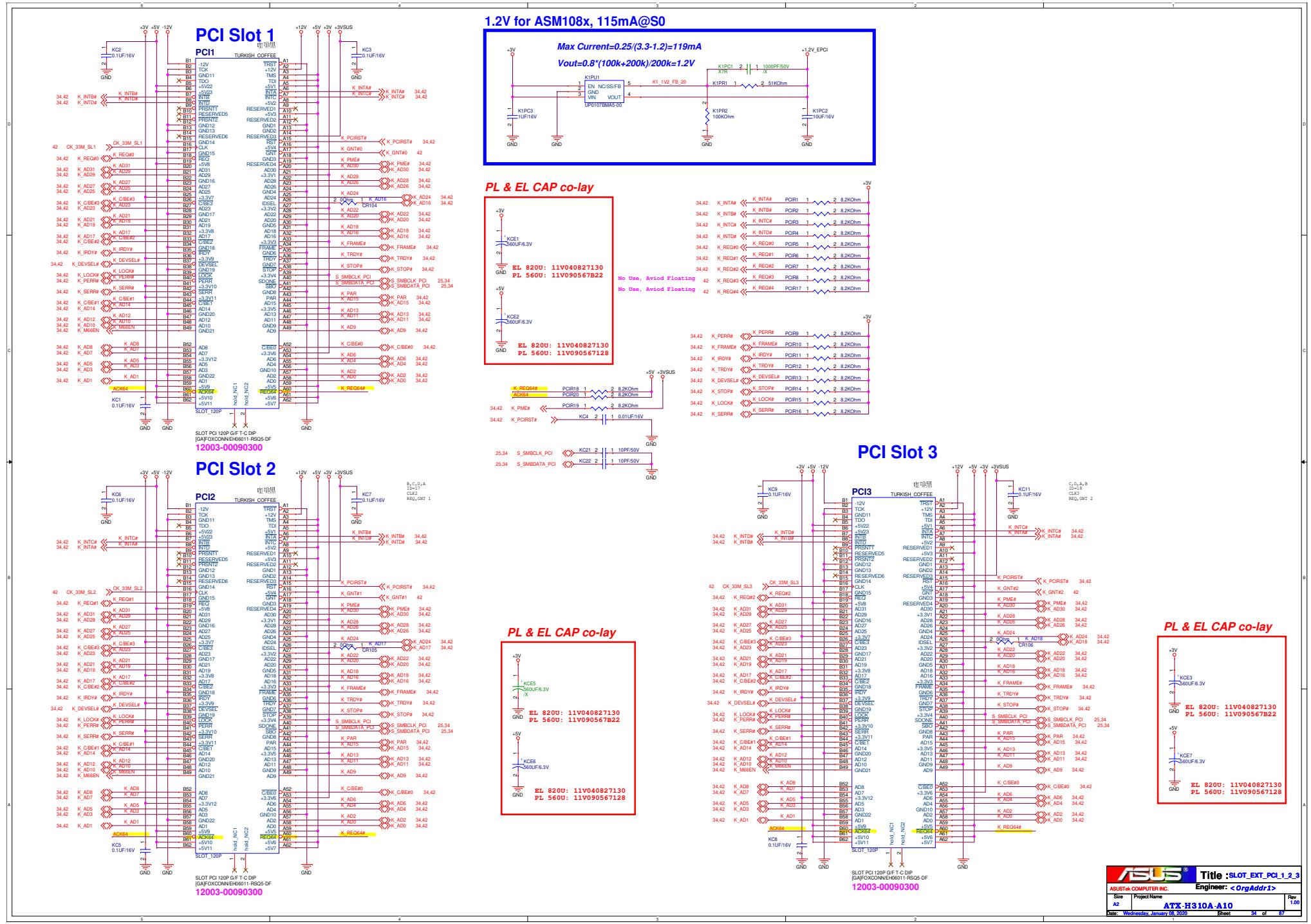


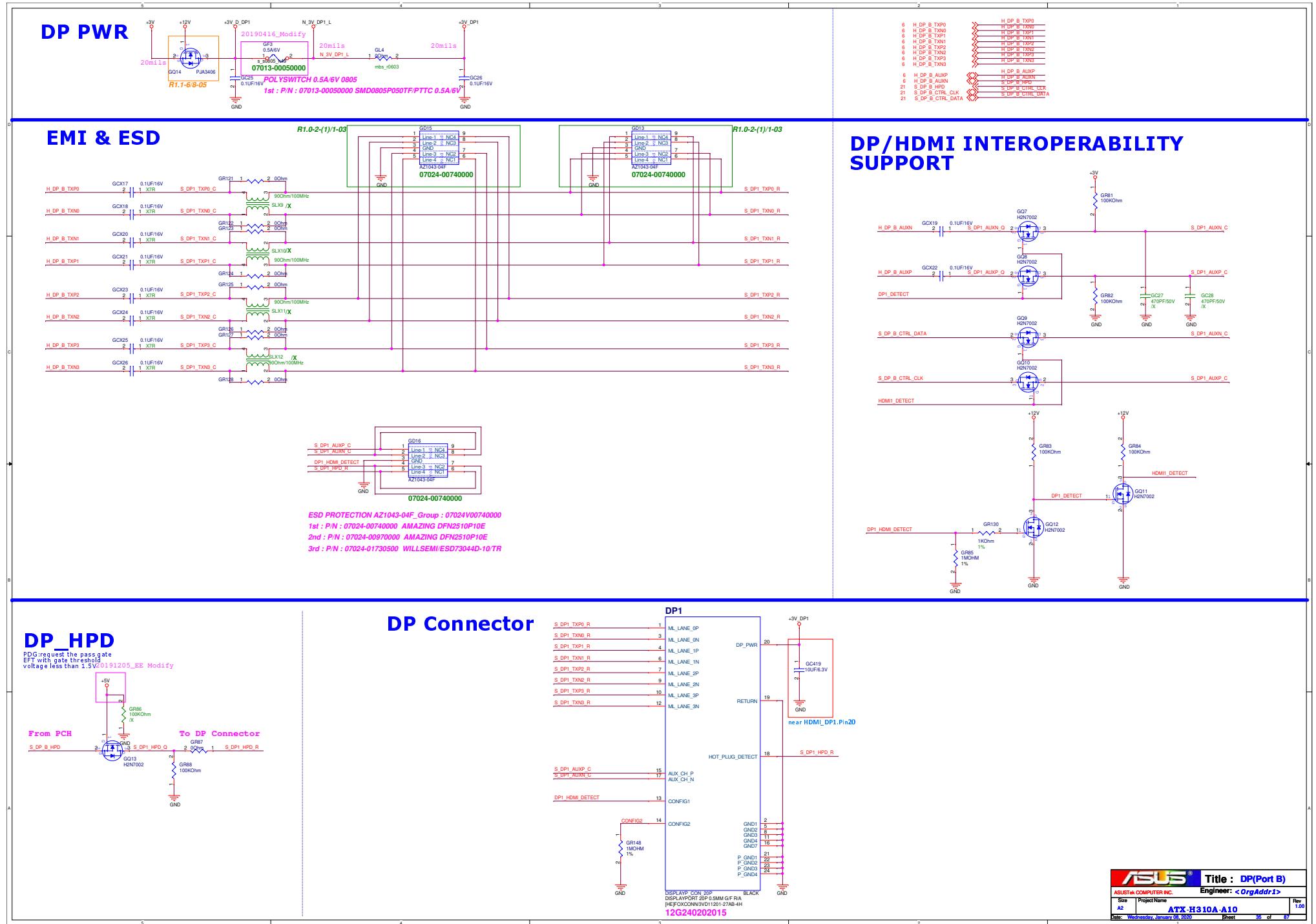


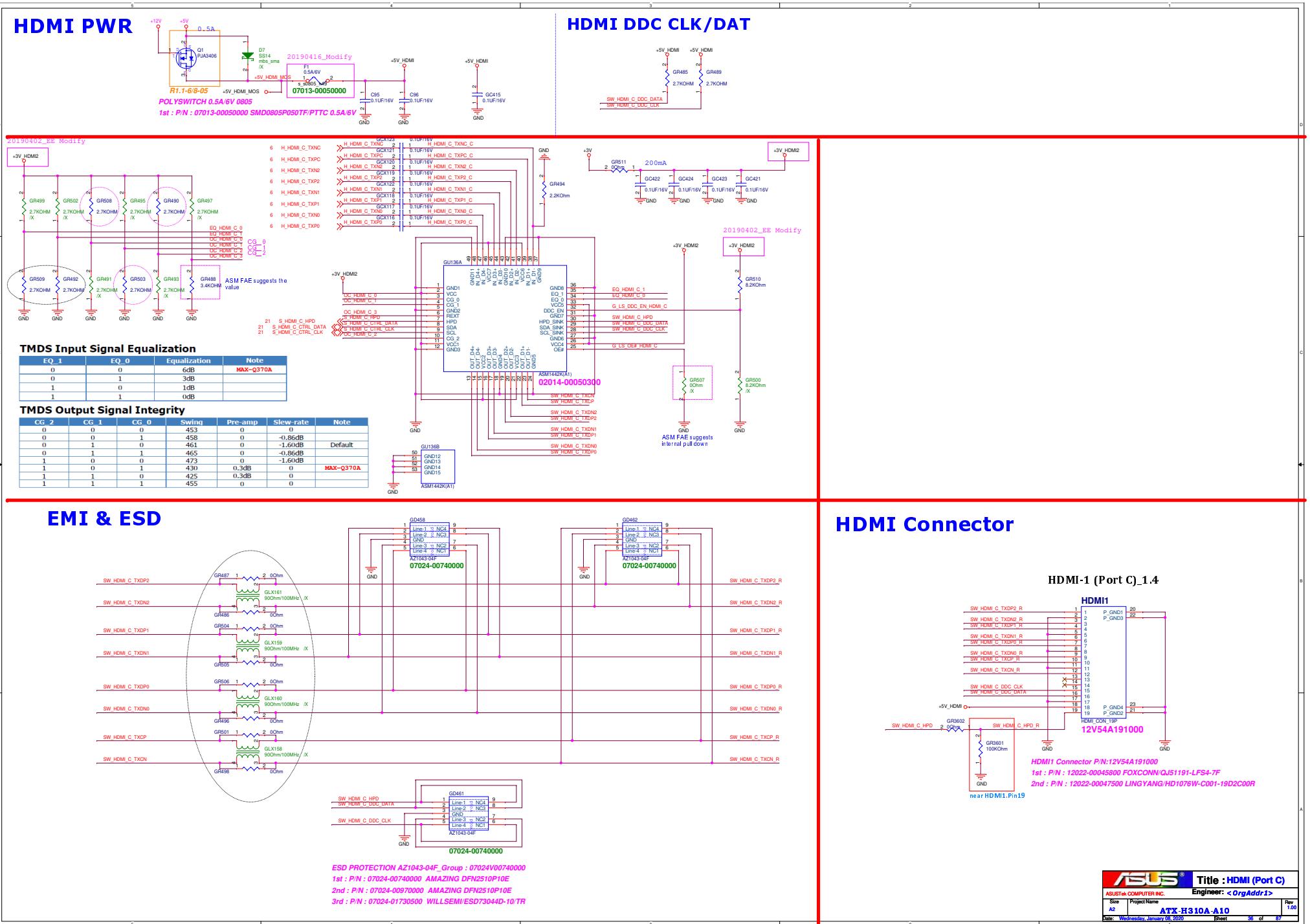


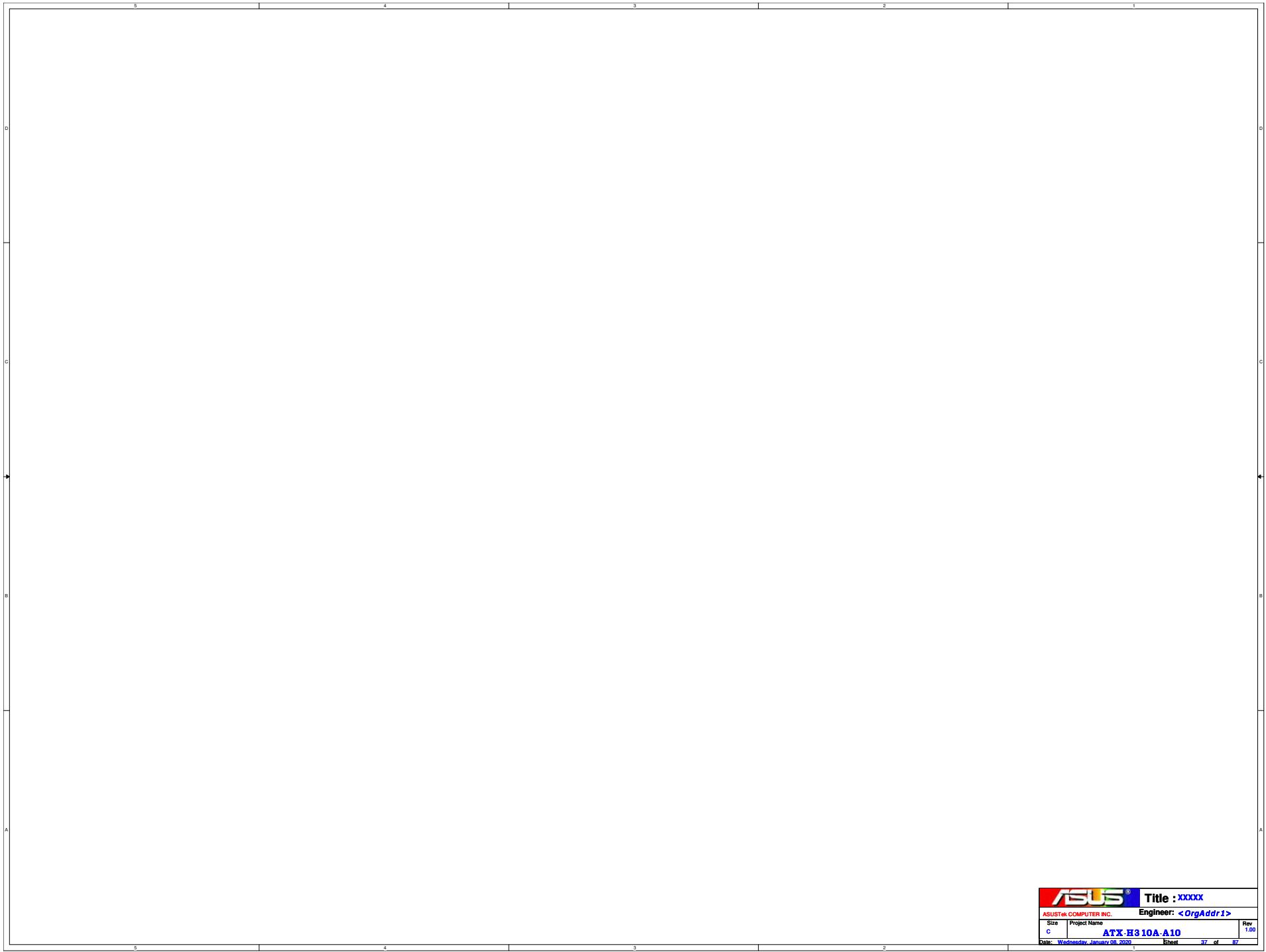






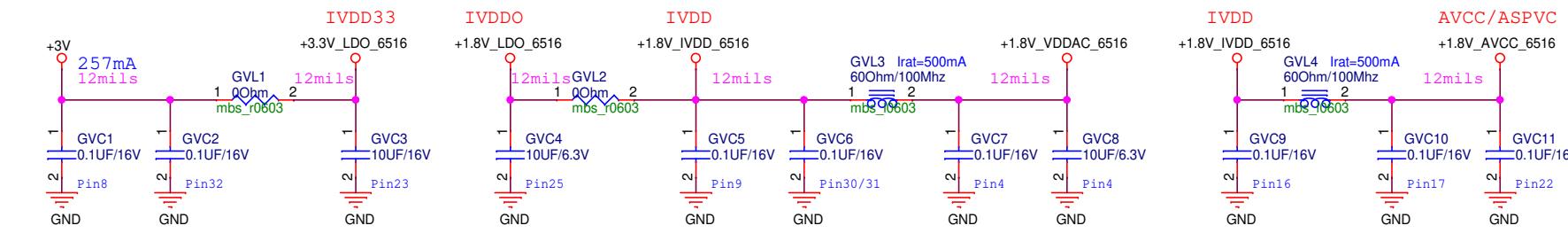




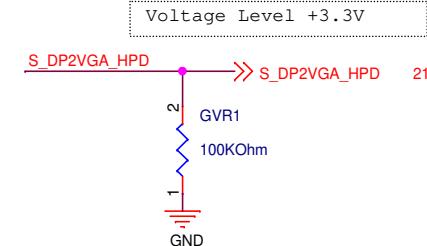


ASUS®		Title : XXXXX	
ASUSTek COMPUTER INC.		Engineer: <OrgAdd1>	
Size	Project Name	Rev	
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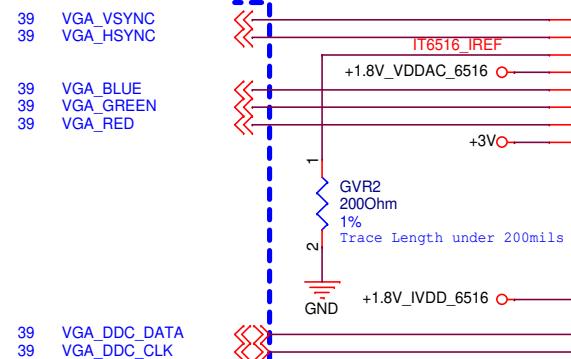
## IT6516BFN/CX Power Caps and Beads



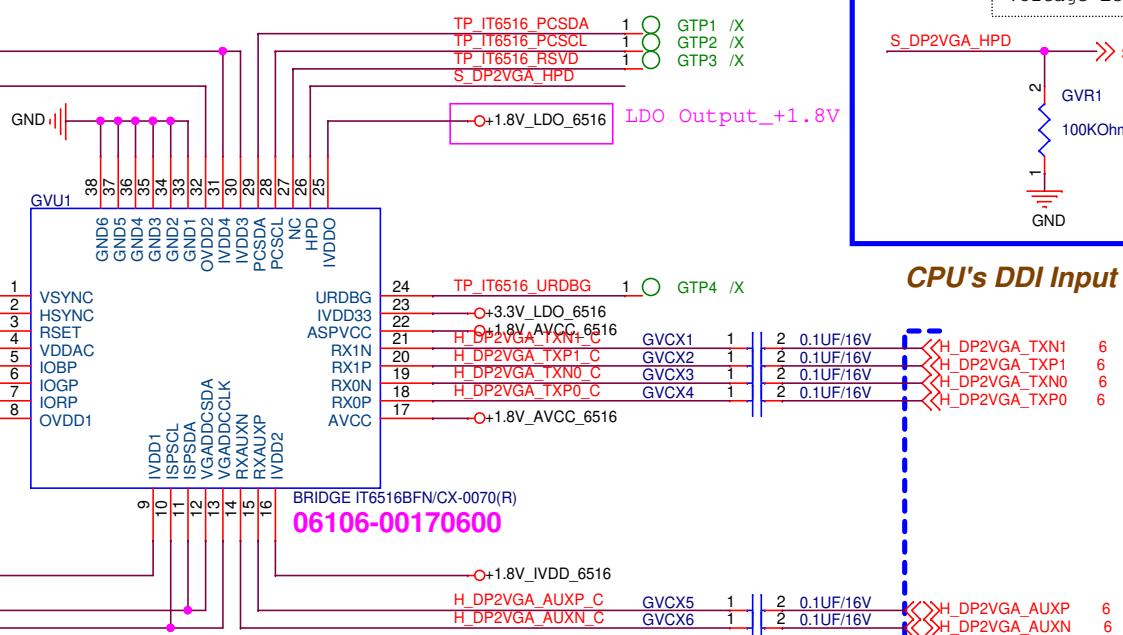
## Hot-Plug Detect



## VGA Output



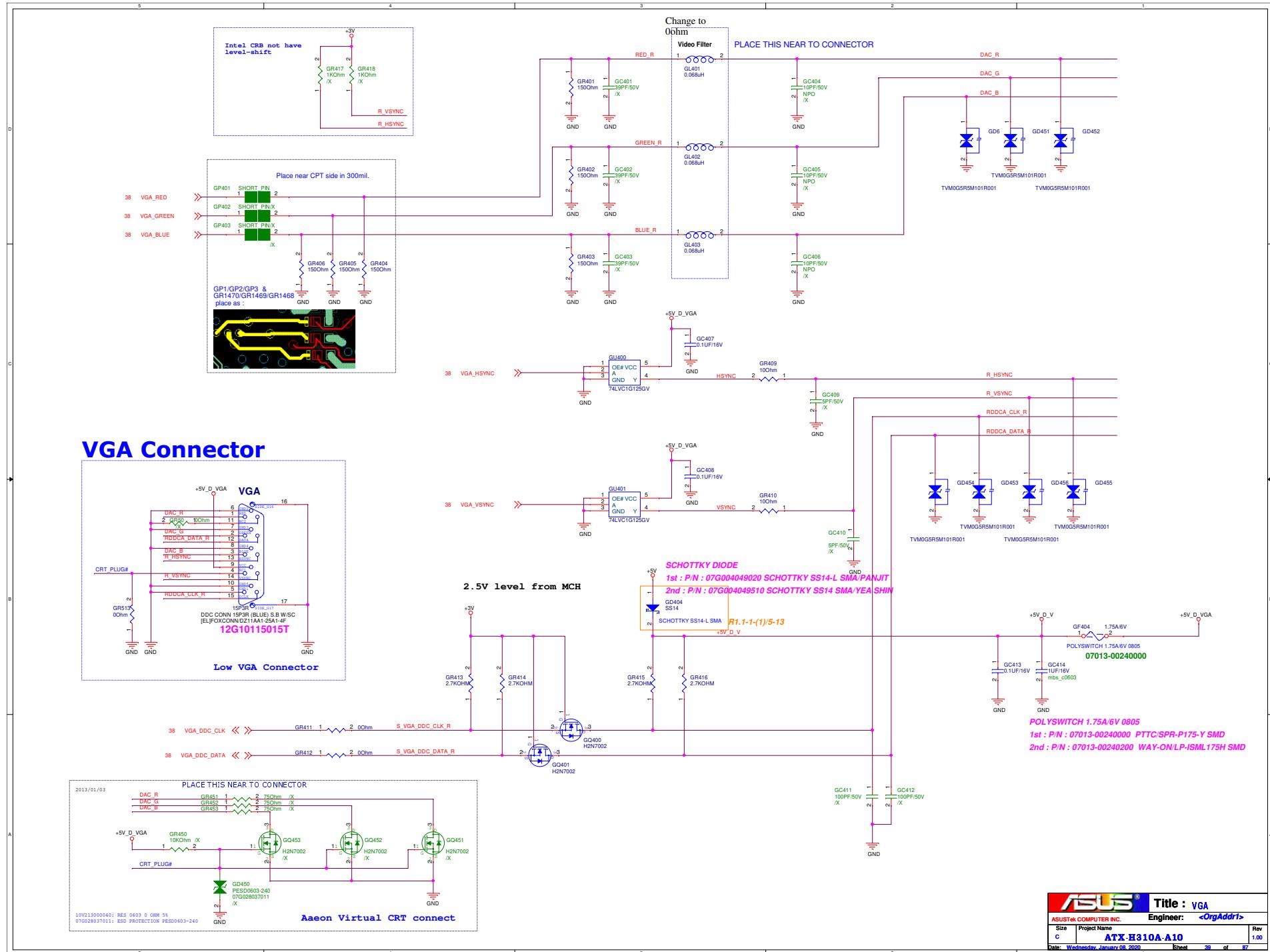
## CPU's DDI Input



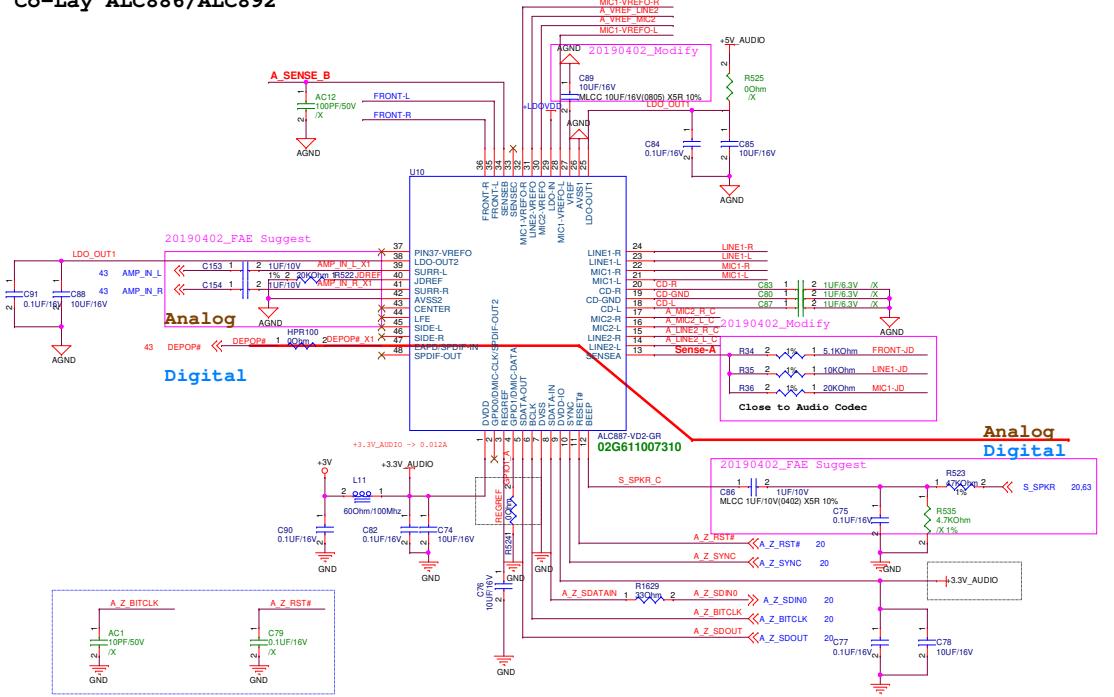
Title : DP to VGA\_IT6516B

Engineer: <OrgAddr1>

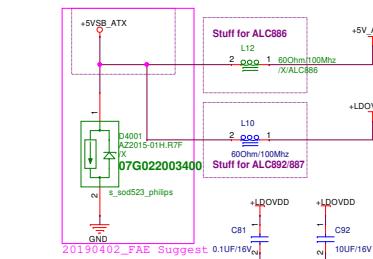
Size	Project Name	Rev
A4	ATX-H310A-A10	1.00
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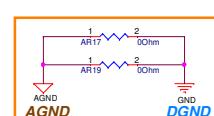
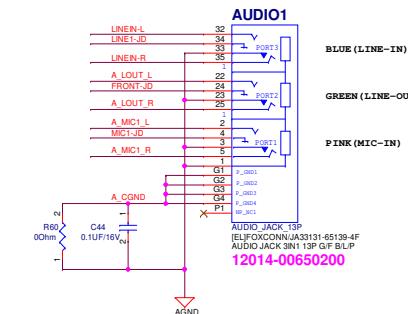
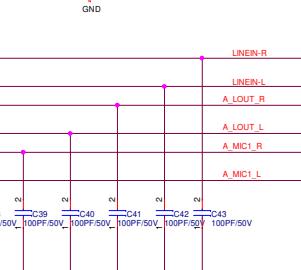
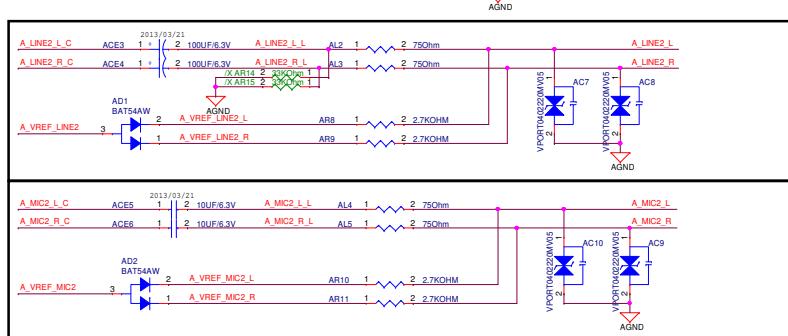
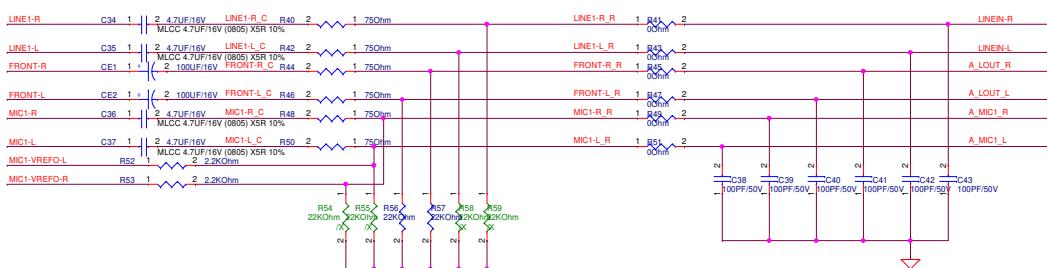
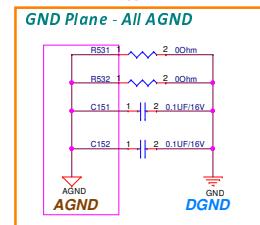
# Co-Lay ALC886/ALC892



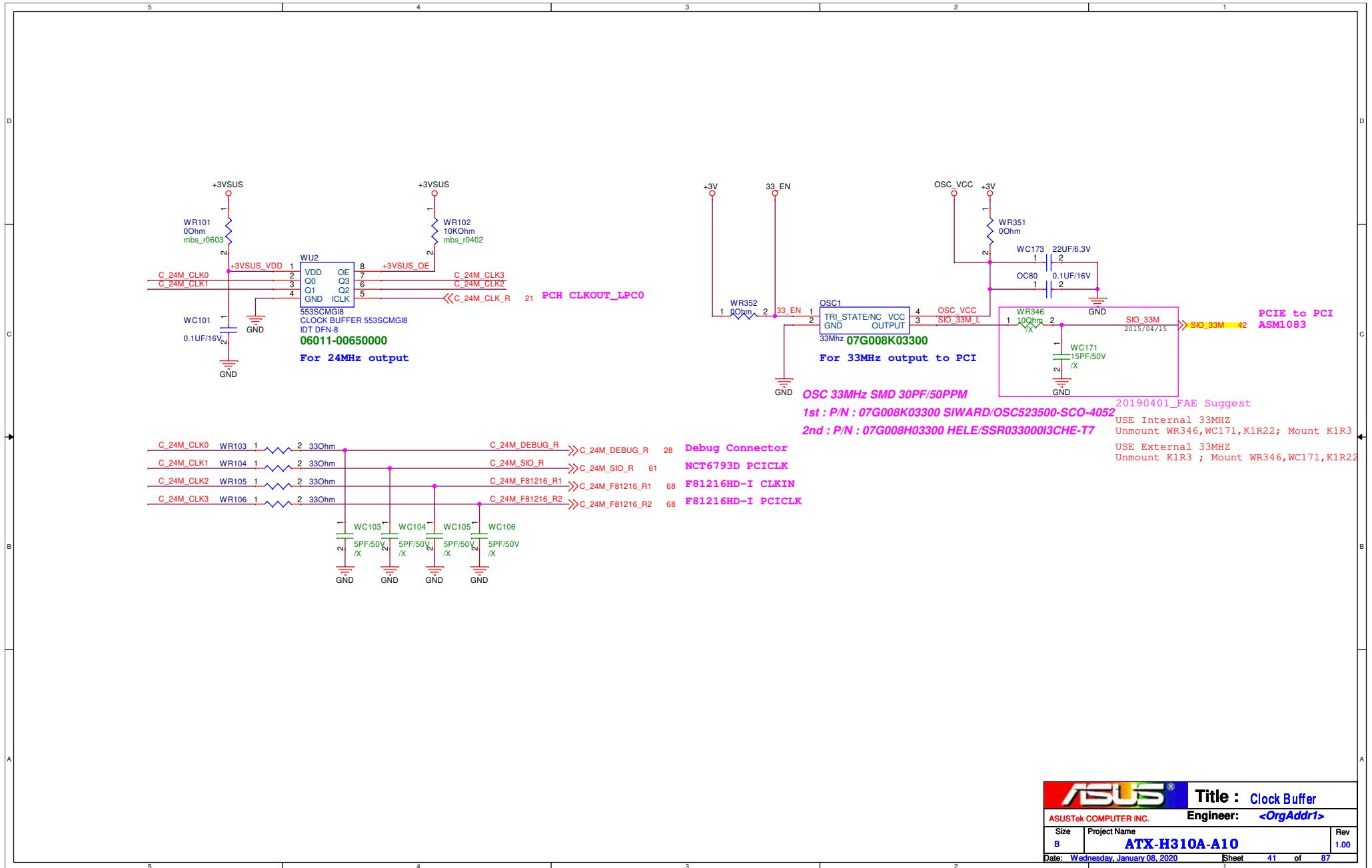
To avoid POP sound when PWR ON

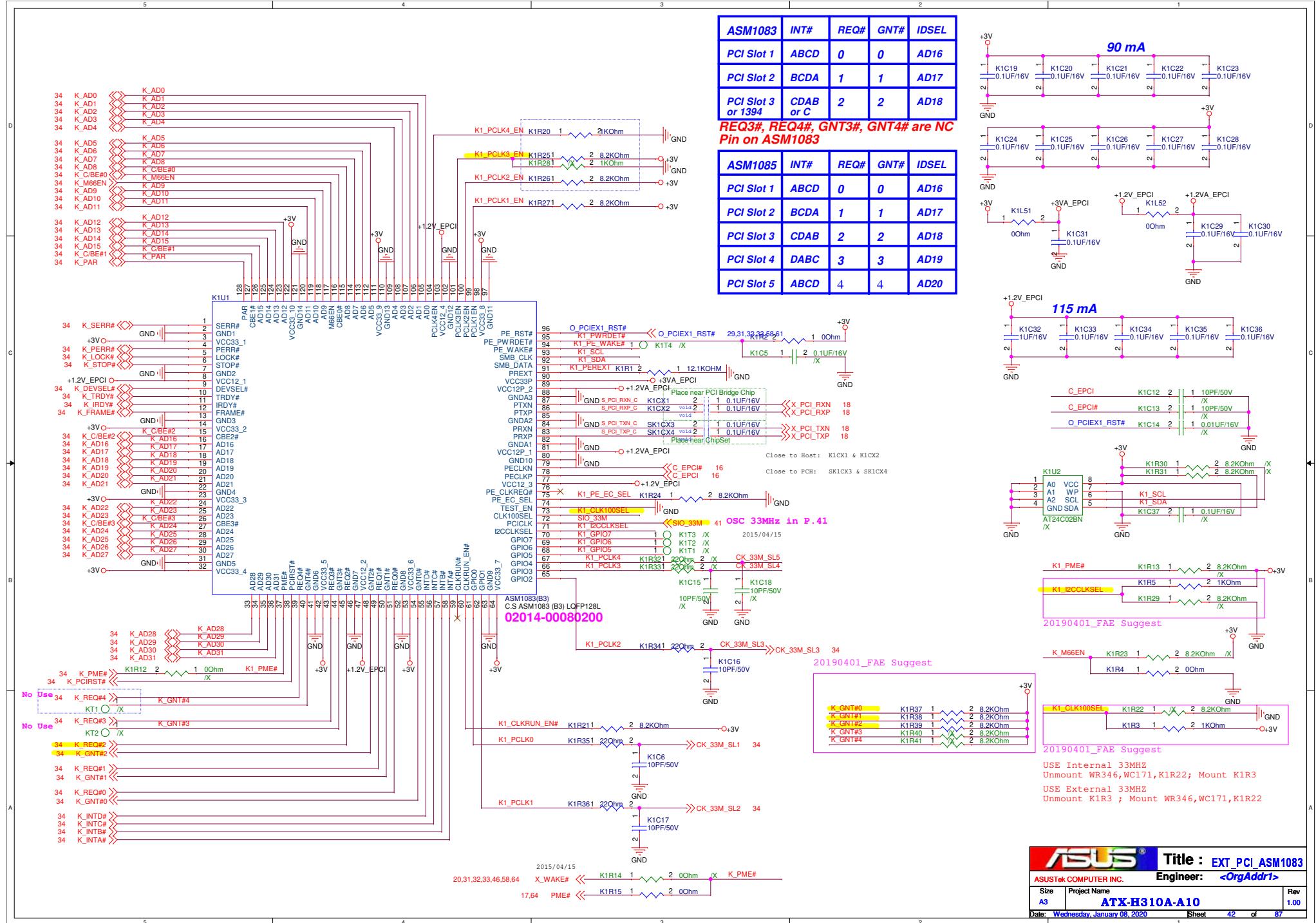


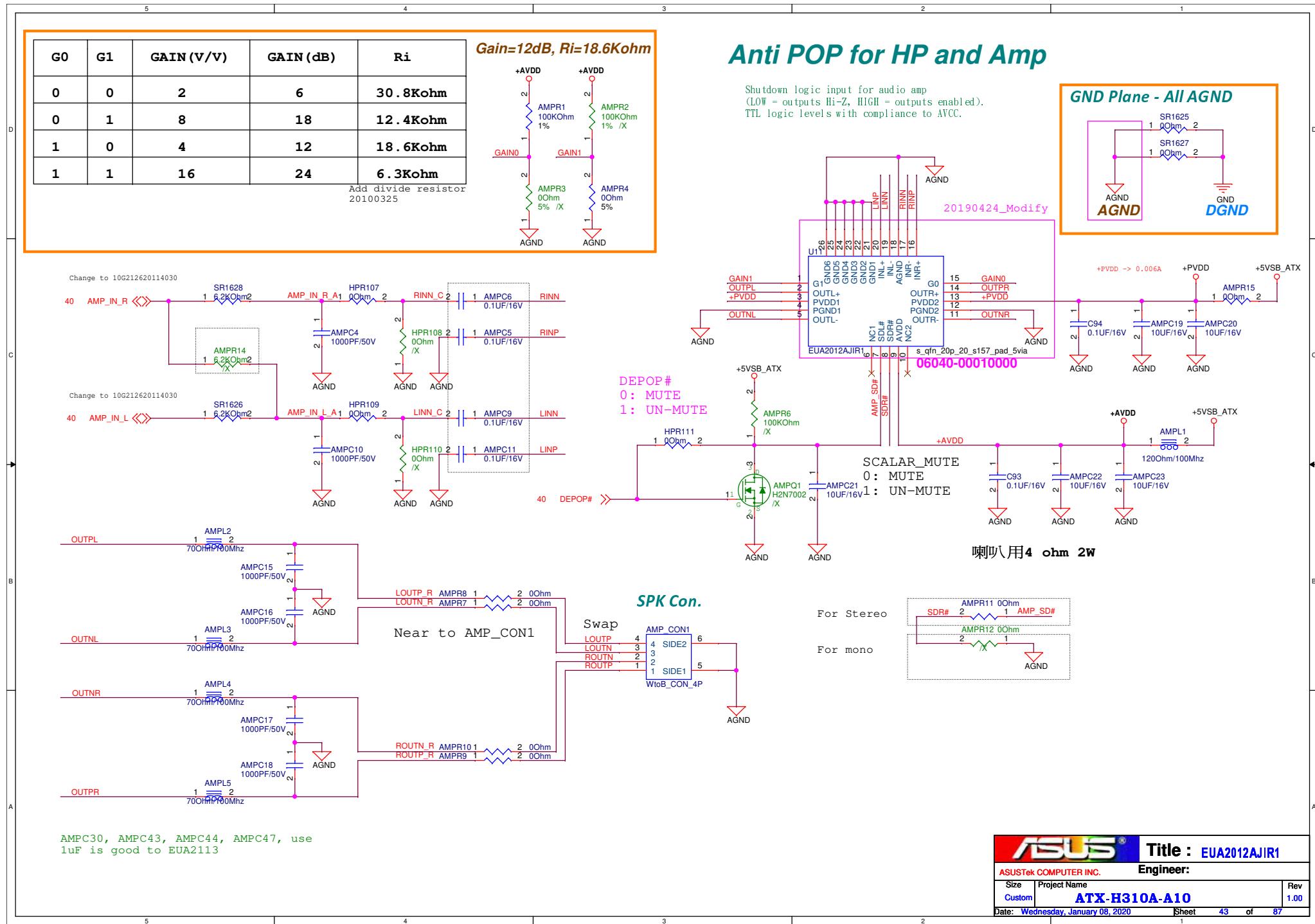
20190401\_FAE Suggest

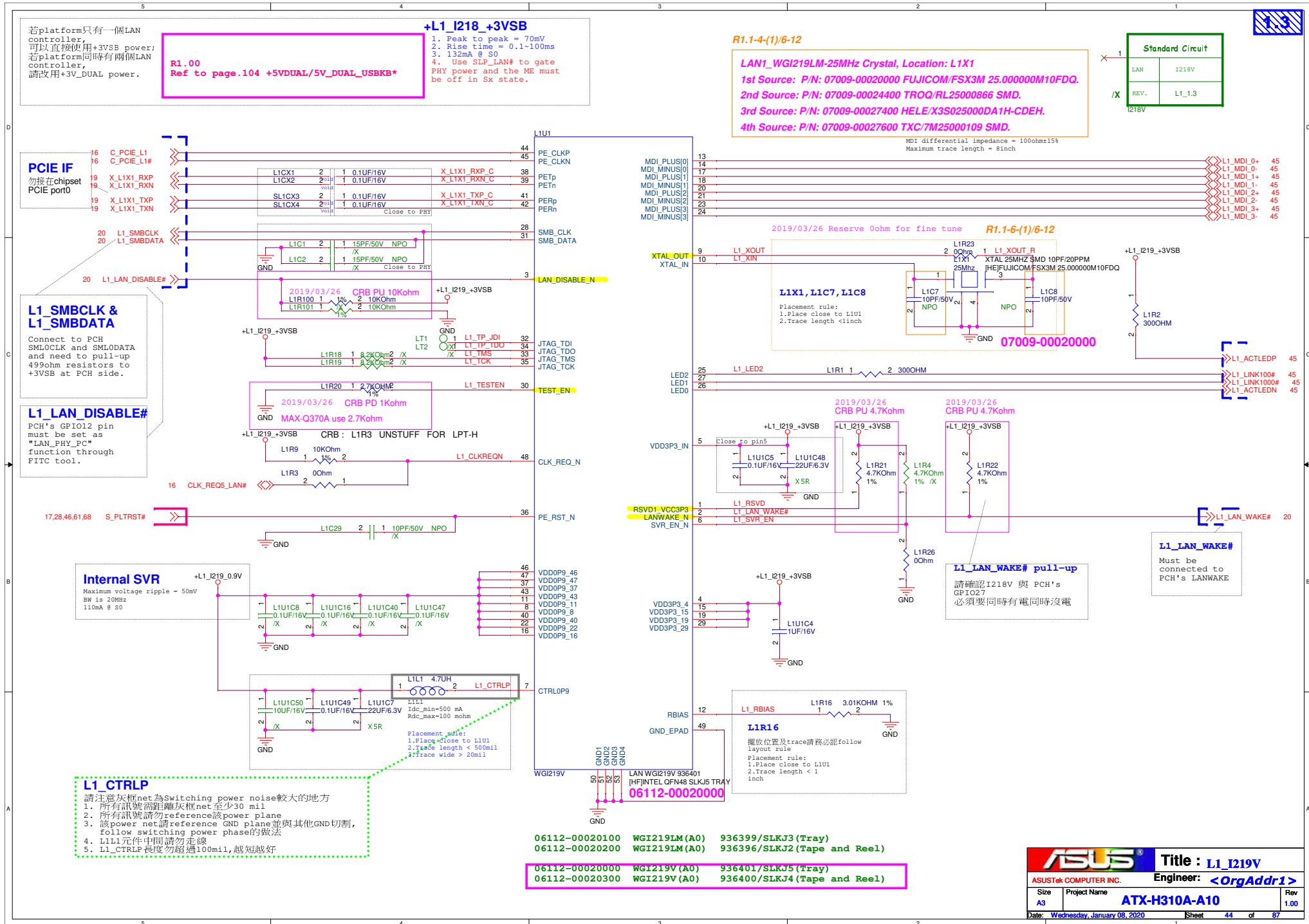


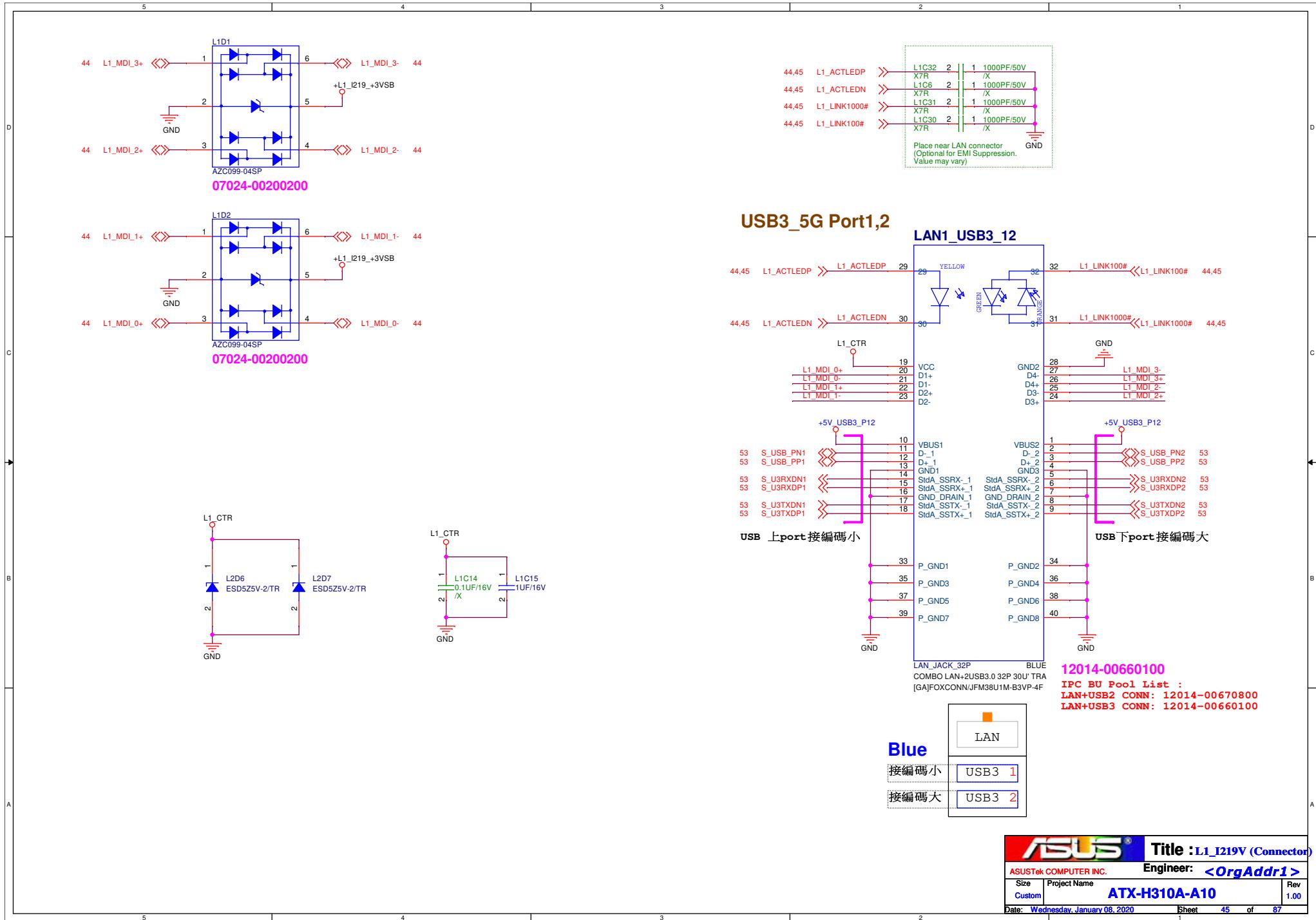
Title : <b>AUDIO CODEC ALC887</b>	
ASUSTek COMPUTER INC.	Engineer: <OrgAddr1>
Size c	Project Name <b>ATX-H310A-A10</b>
Date: Wednesday, January 08, 2020	Rev 1.00
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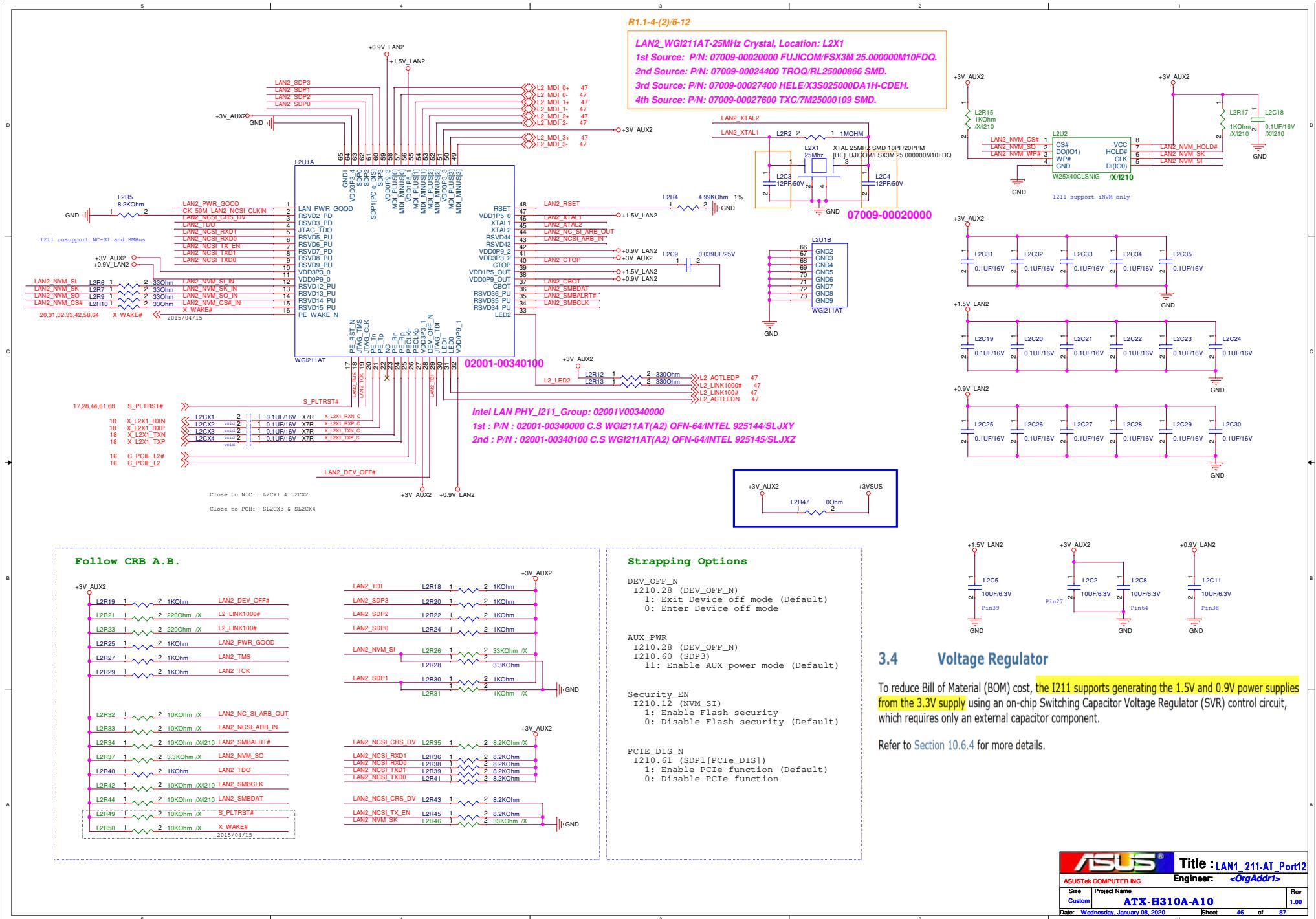


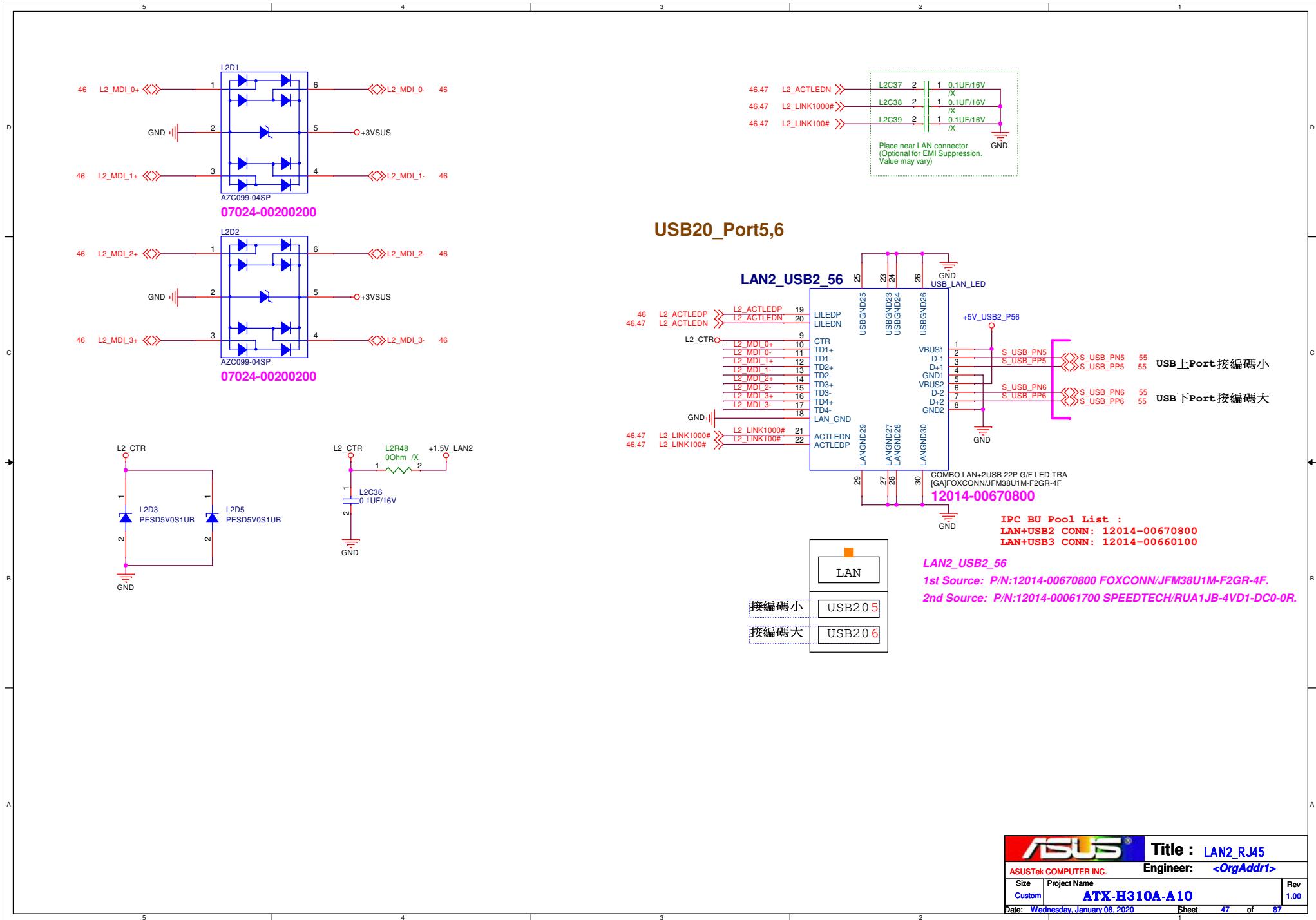


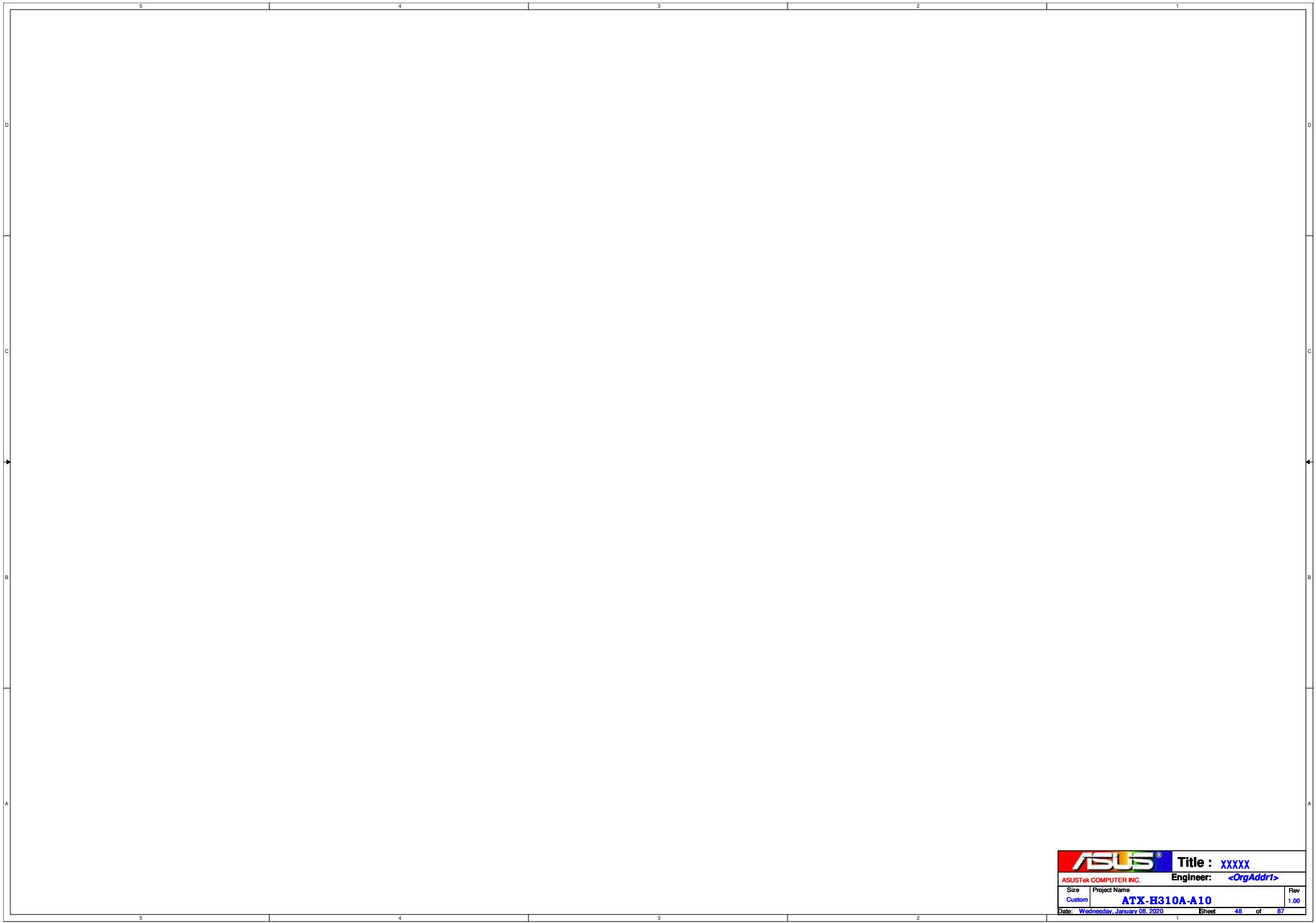




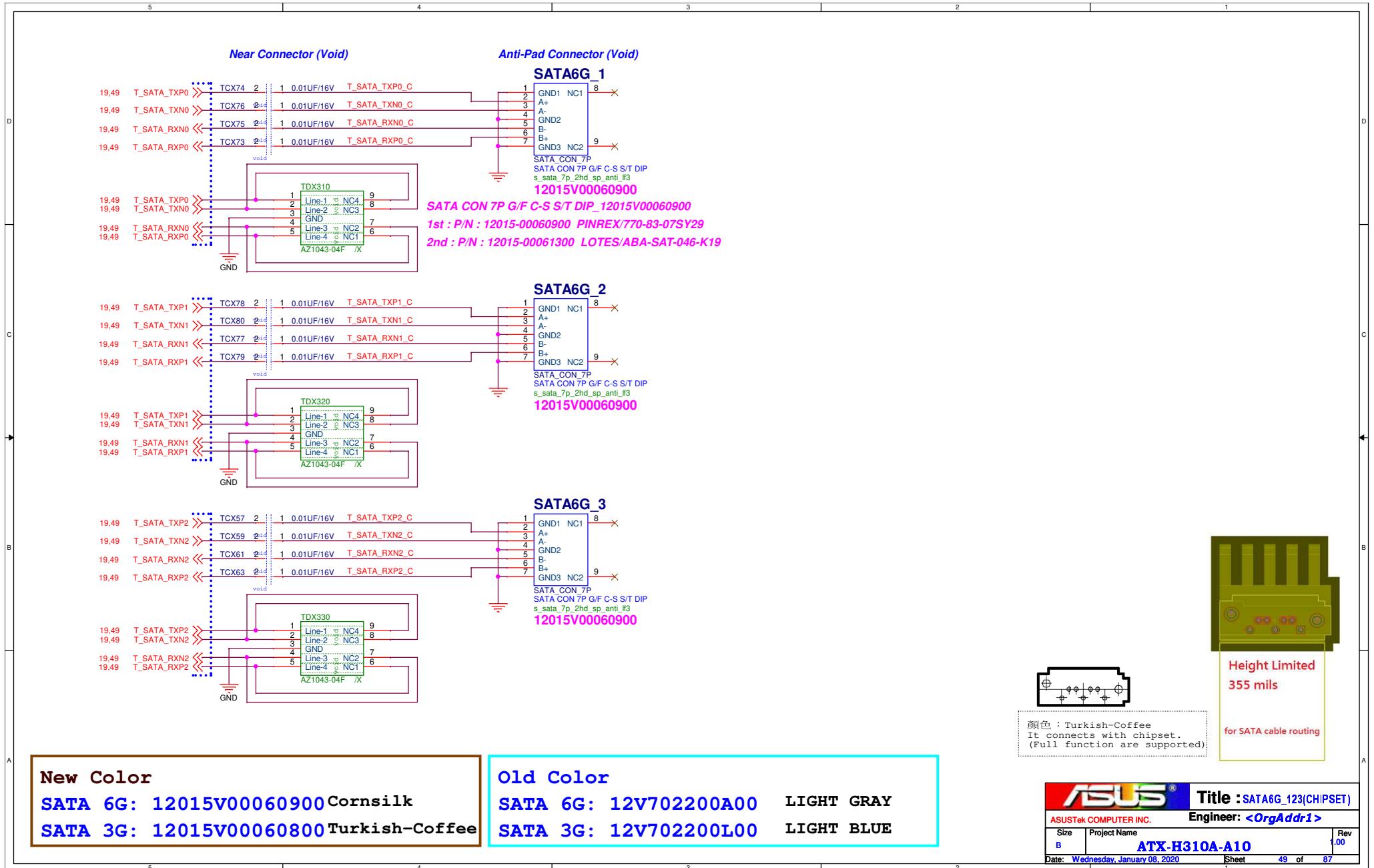


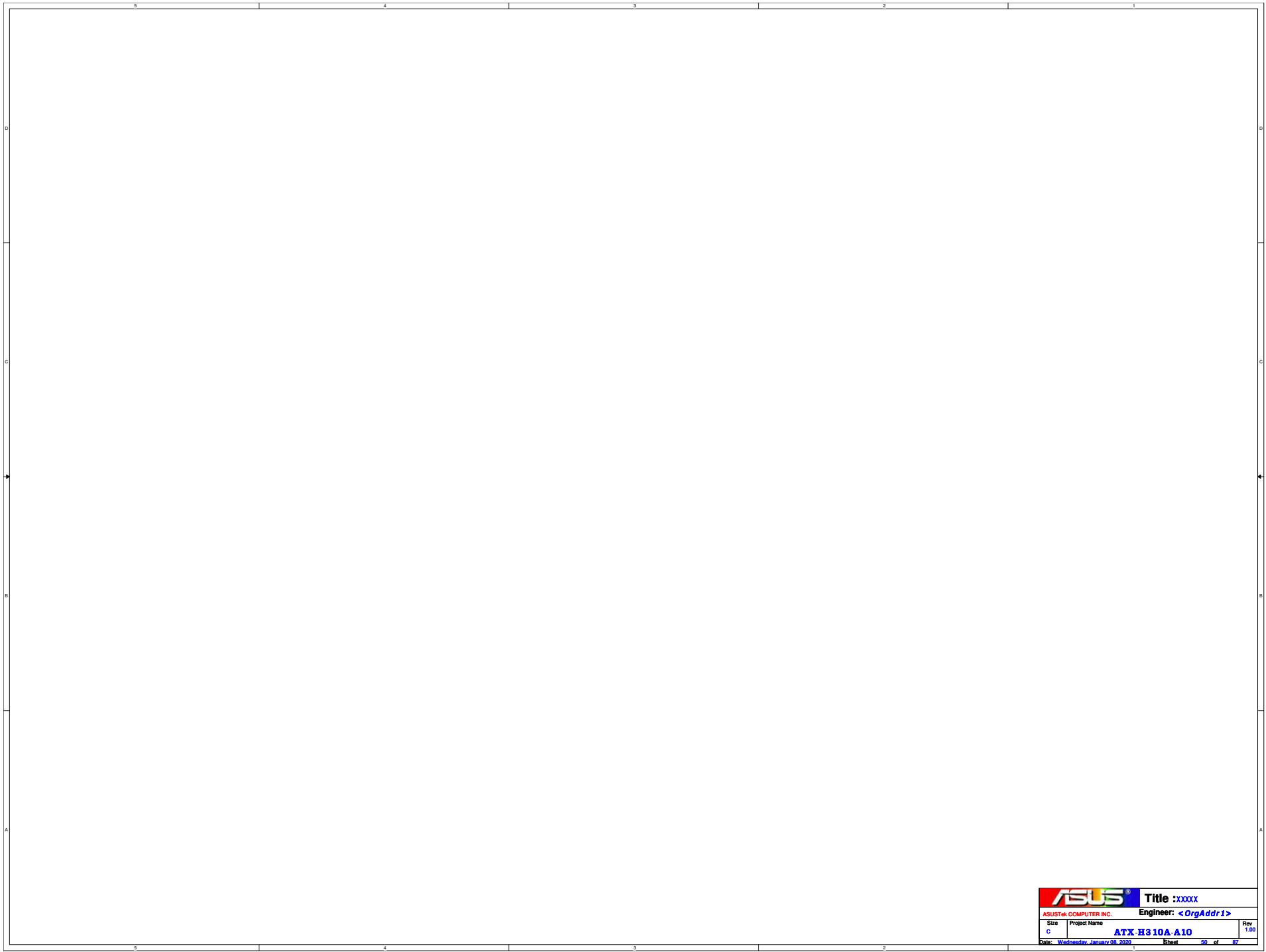


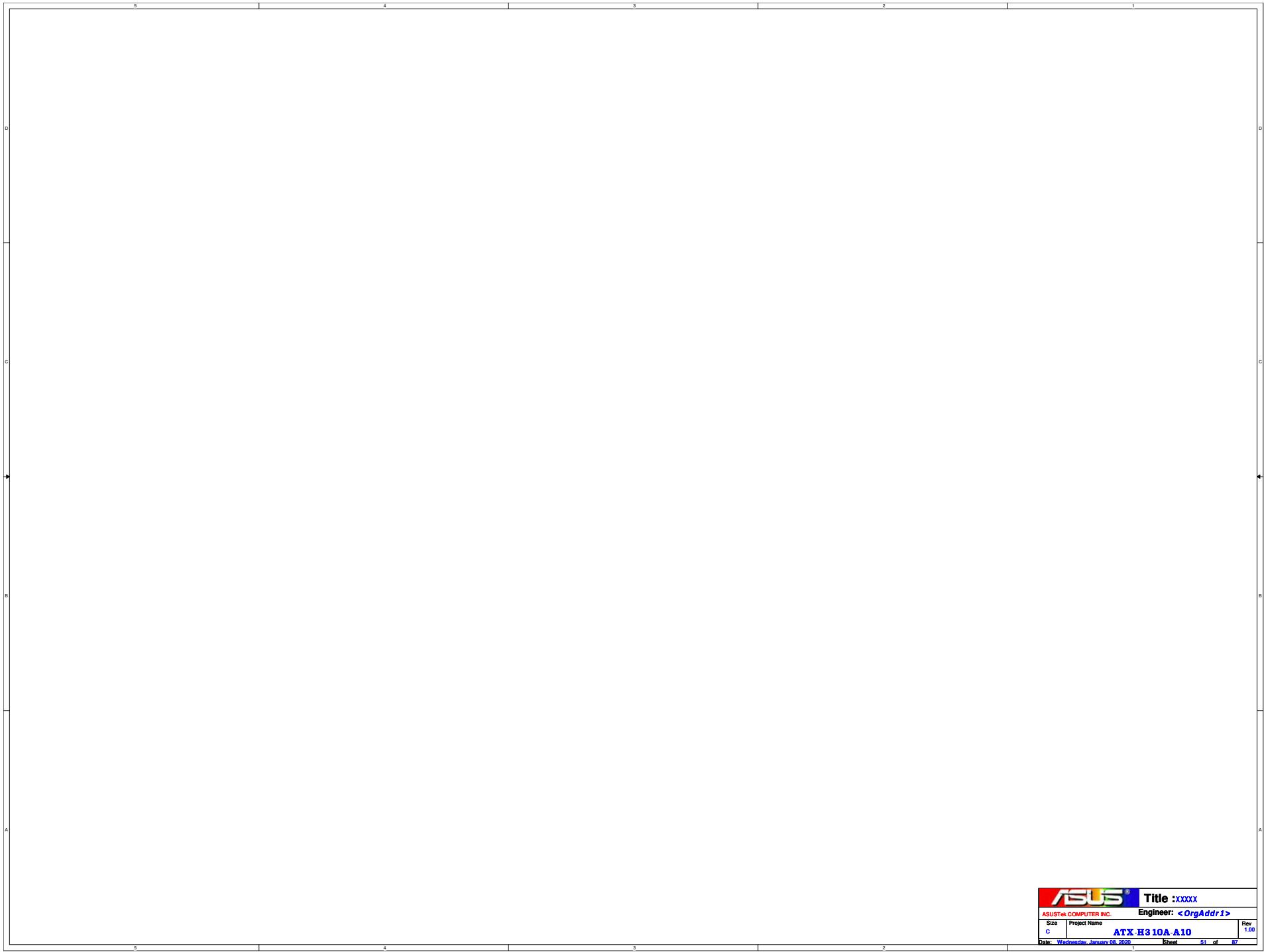


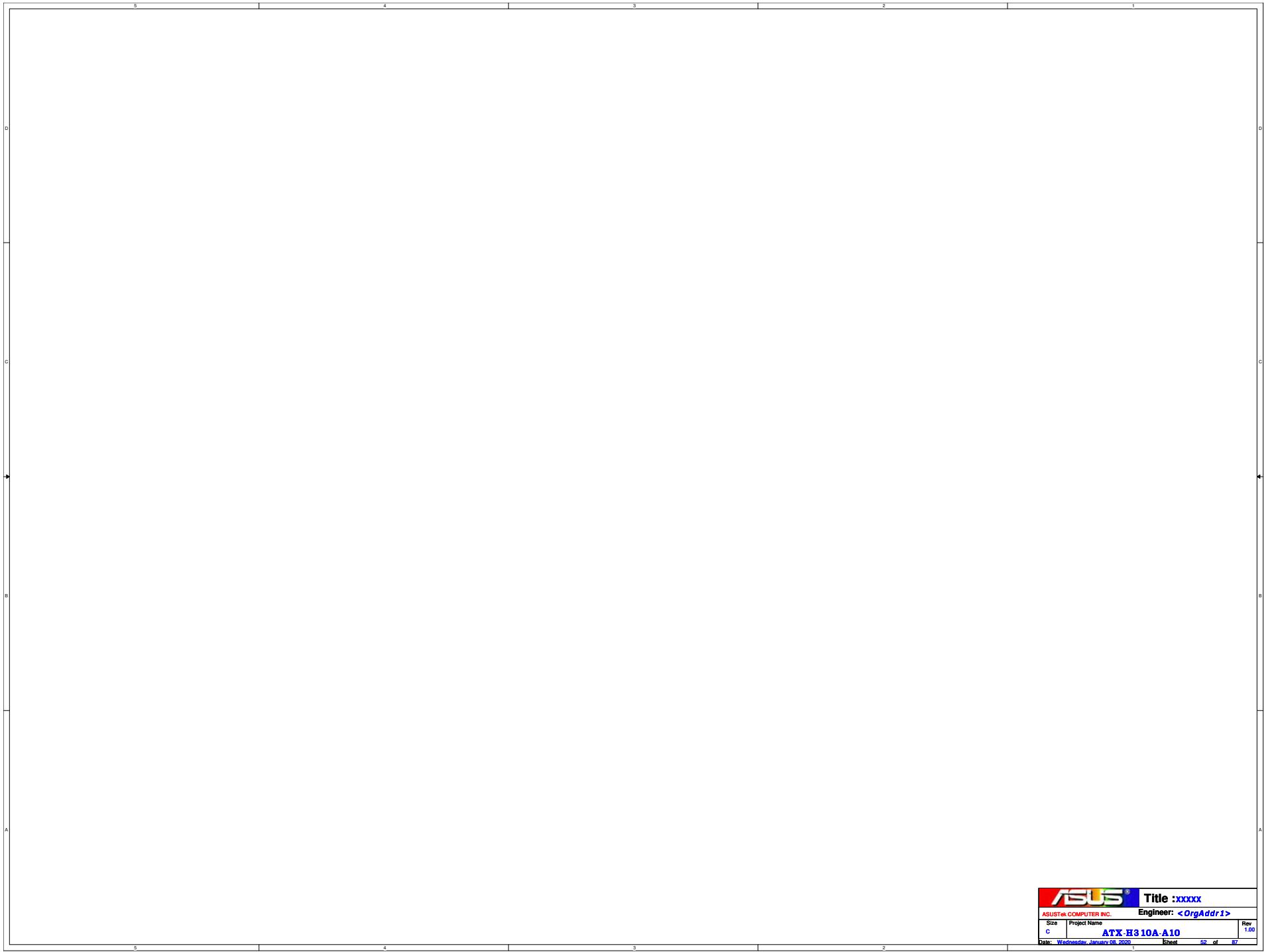


Title : XXXX	
Engineer:	<OrgAddr1>
Size	Project Name
Custom	ATX-H310A-A10
Date: Wednesday, January 08, 2020	Rev 1.00
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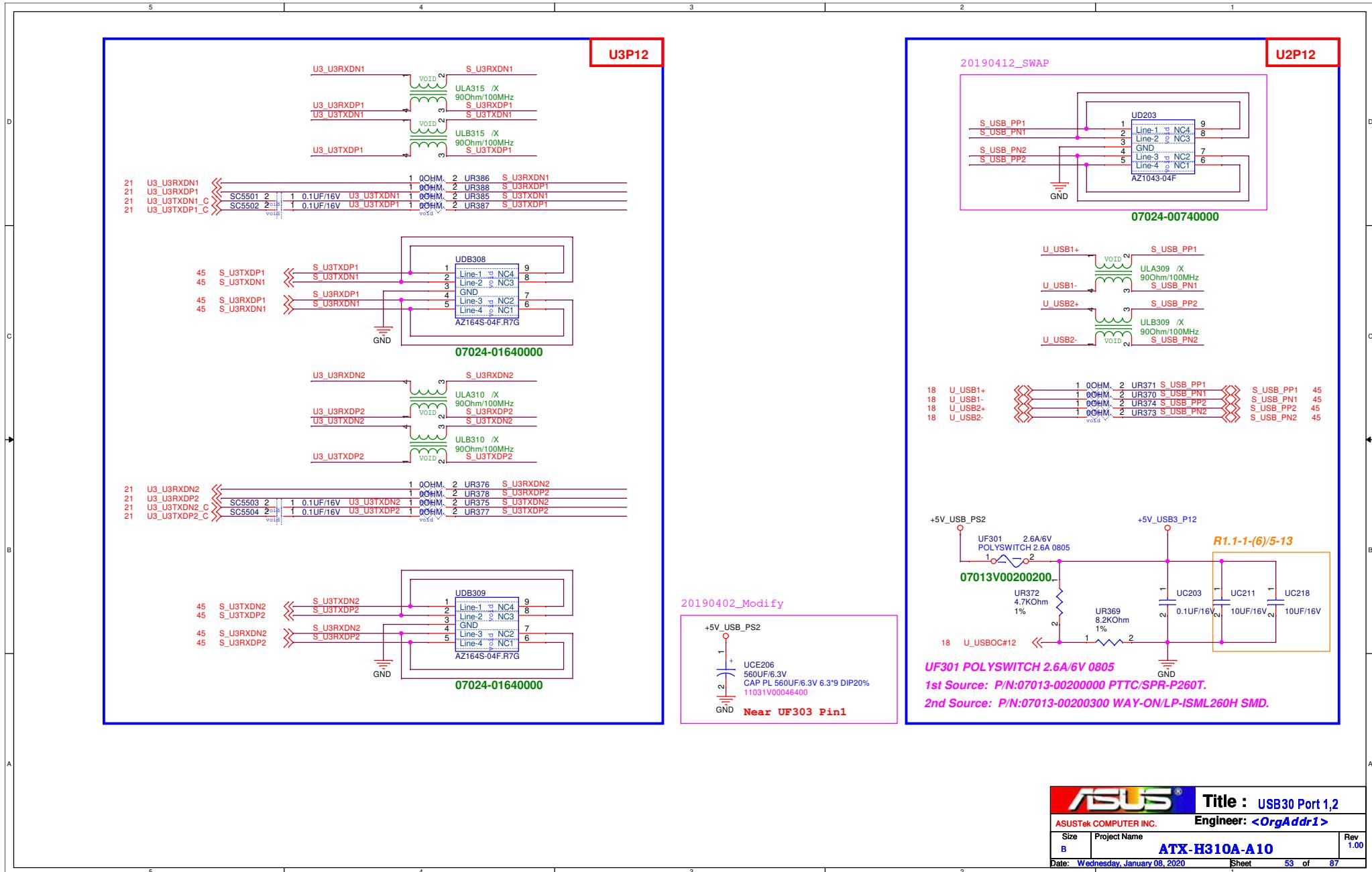


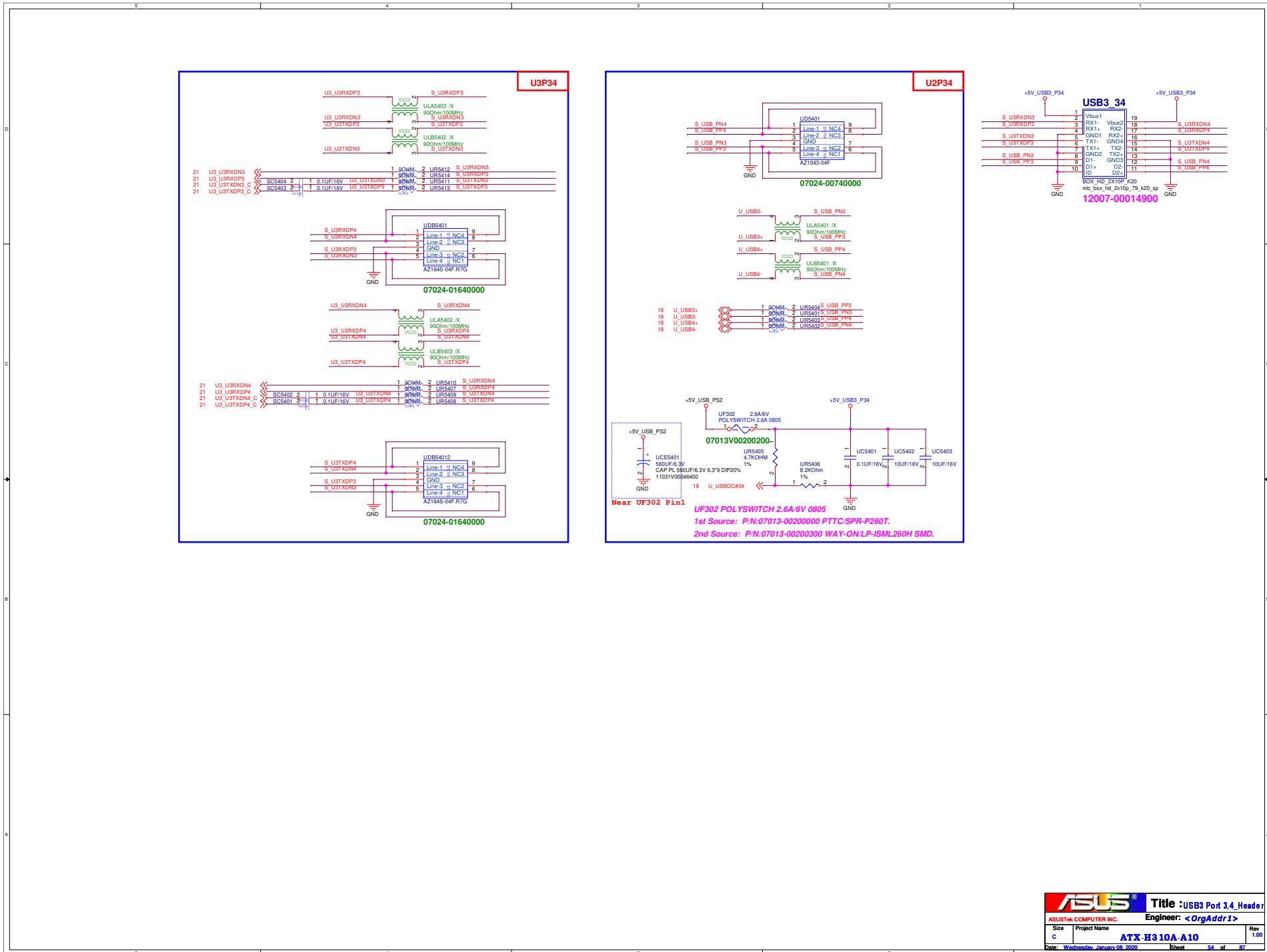


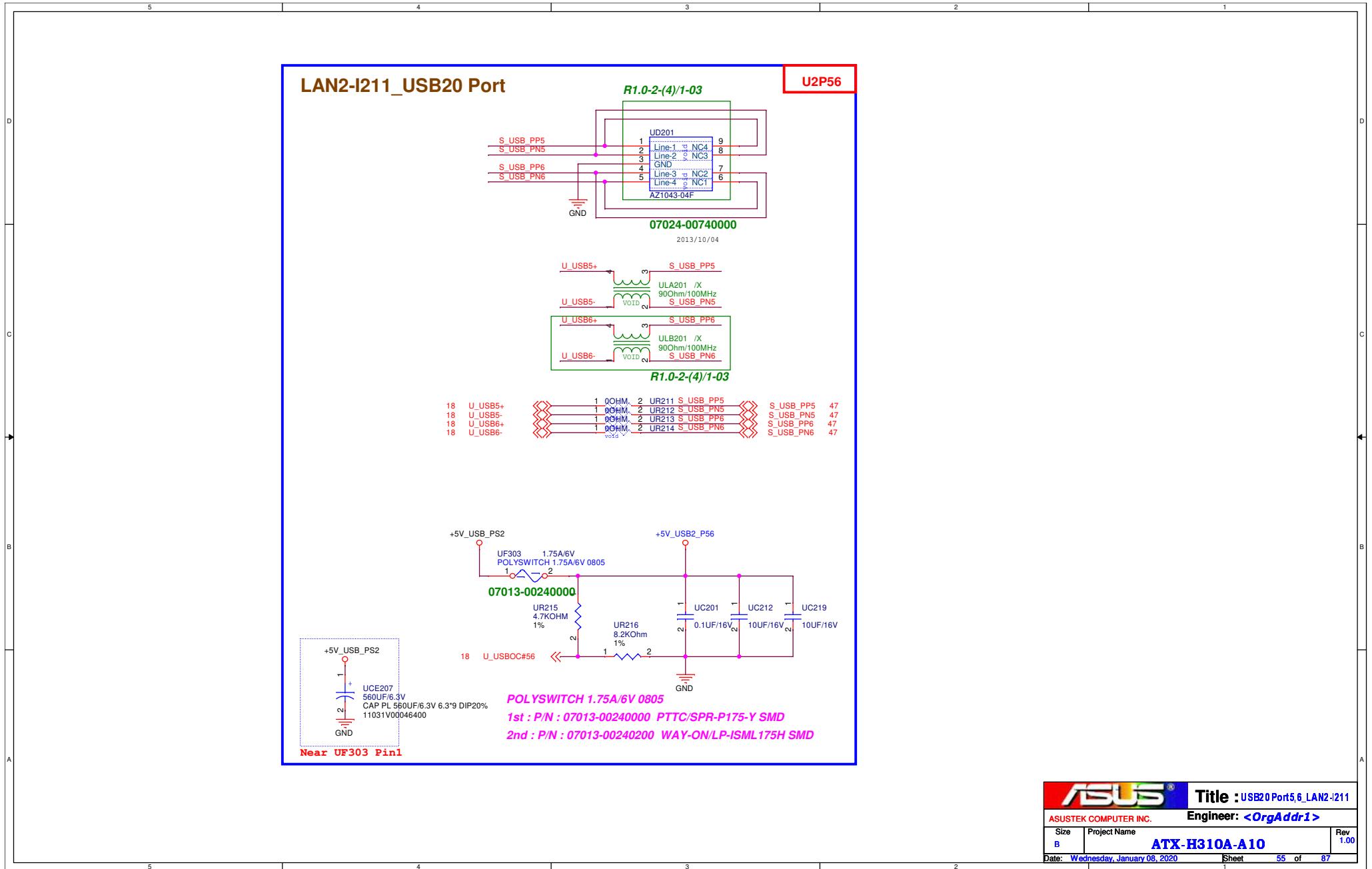


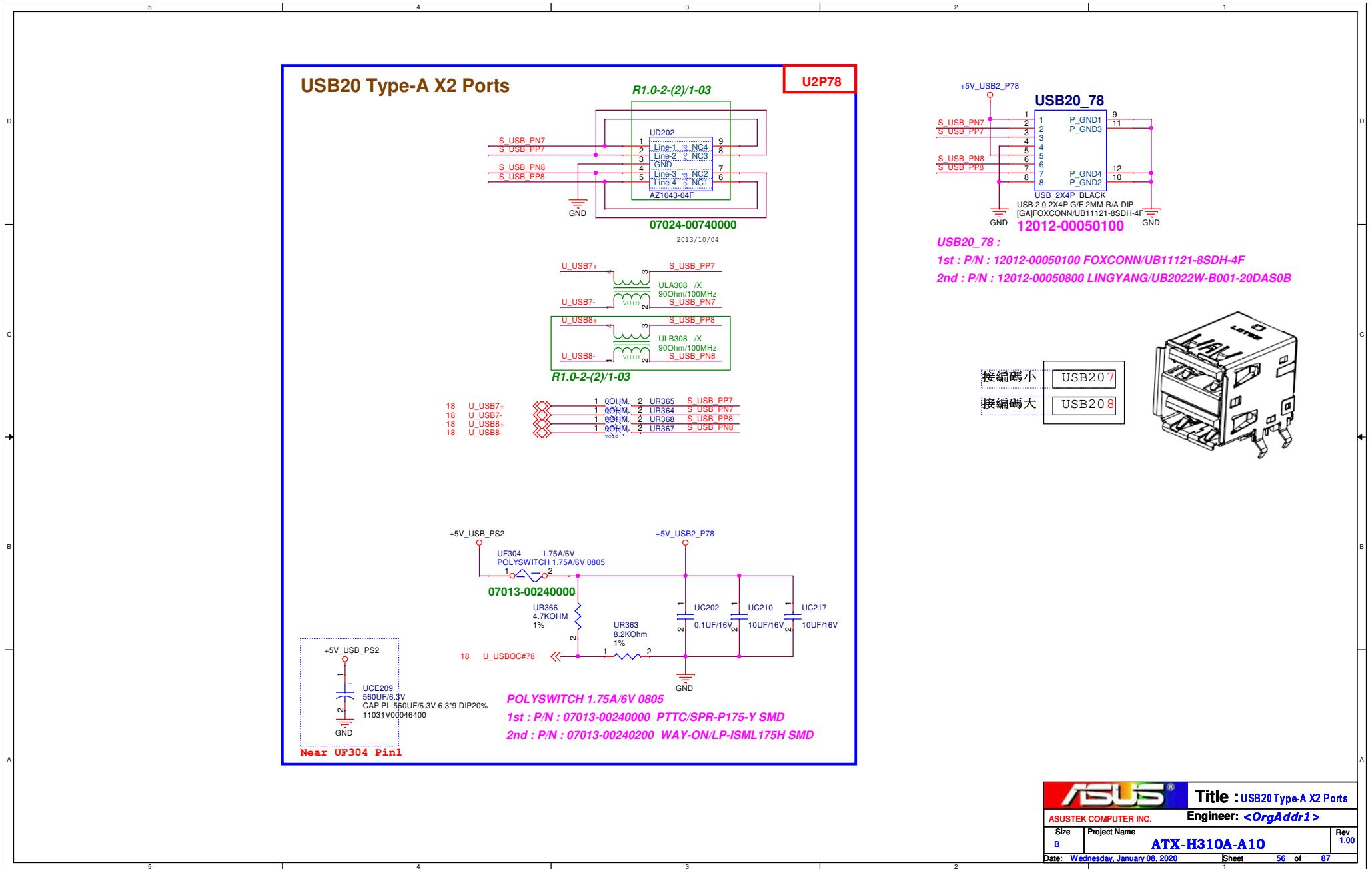


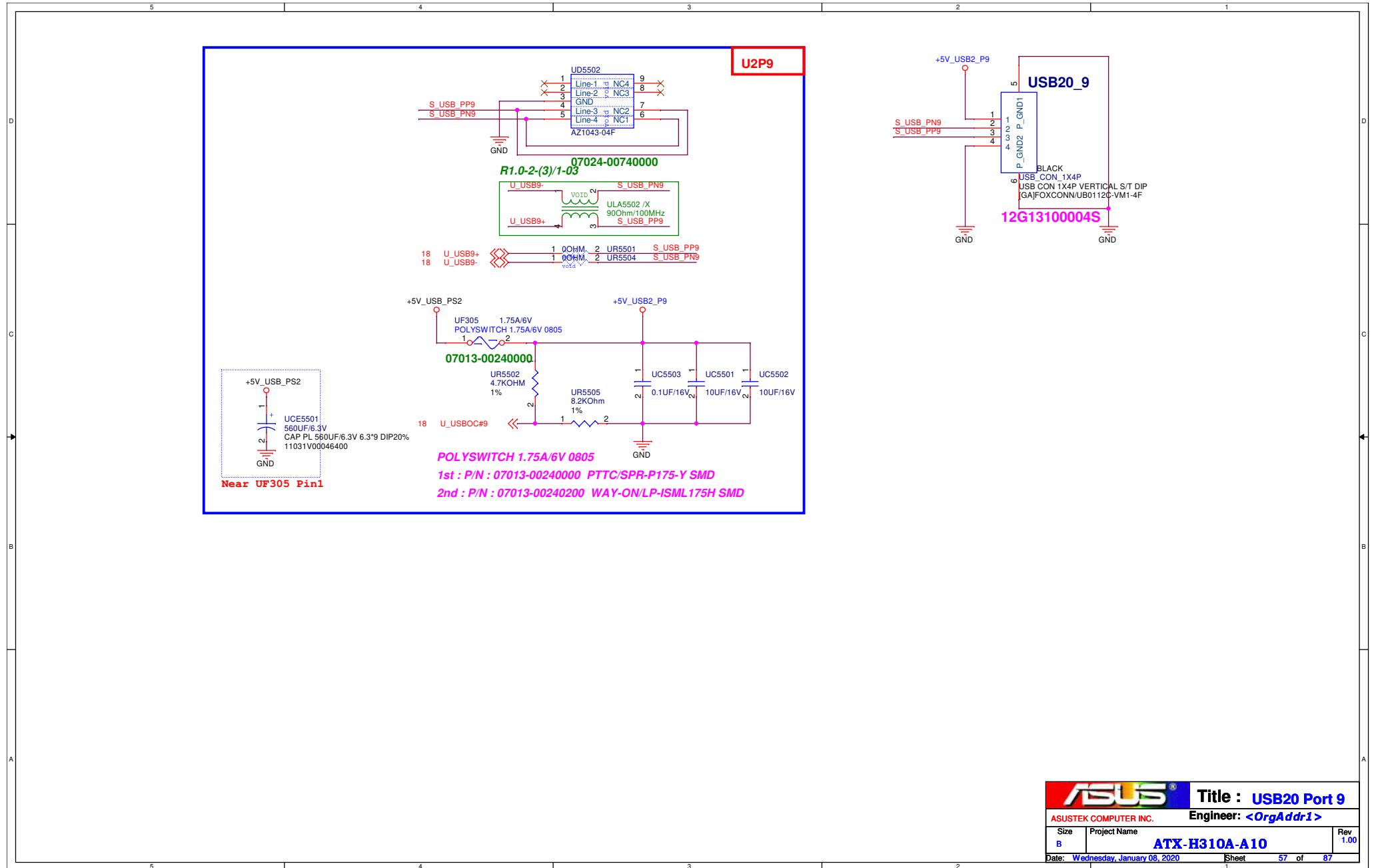
ASUS®		Title :XXXXX	
ASUSTek COMPUTER INC.		Engineer: <OrgAdd1>	
Size	Project Name	Rev	
C	ATX-H310A-A10	1.00	
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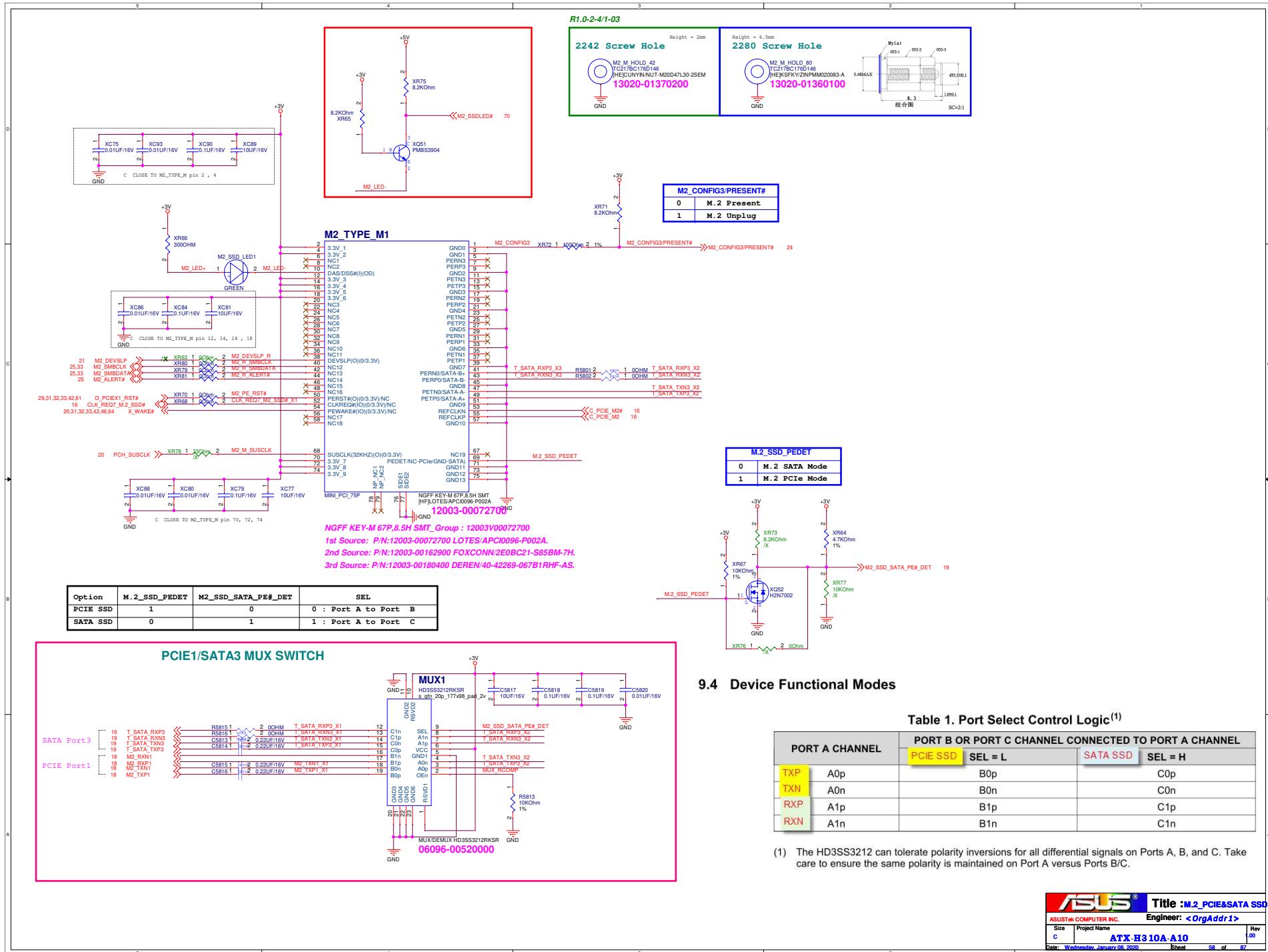


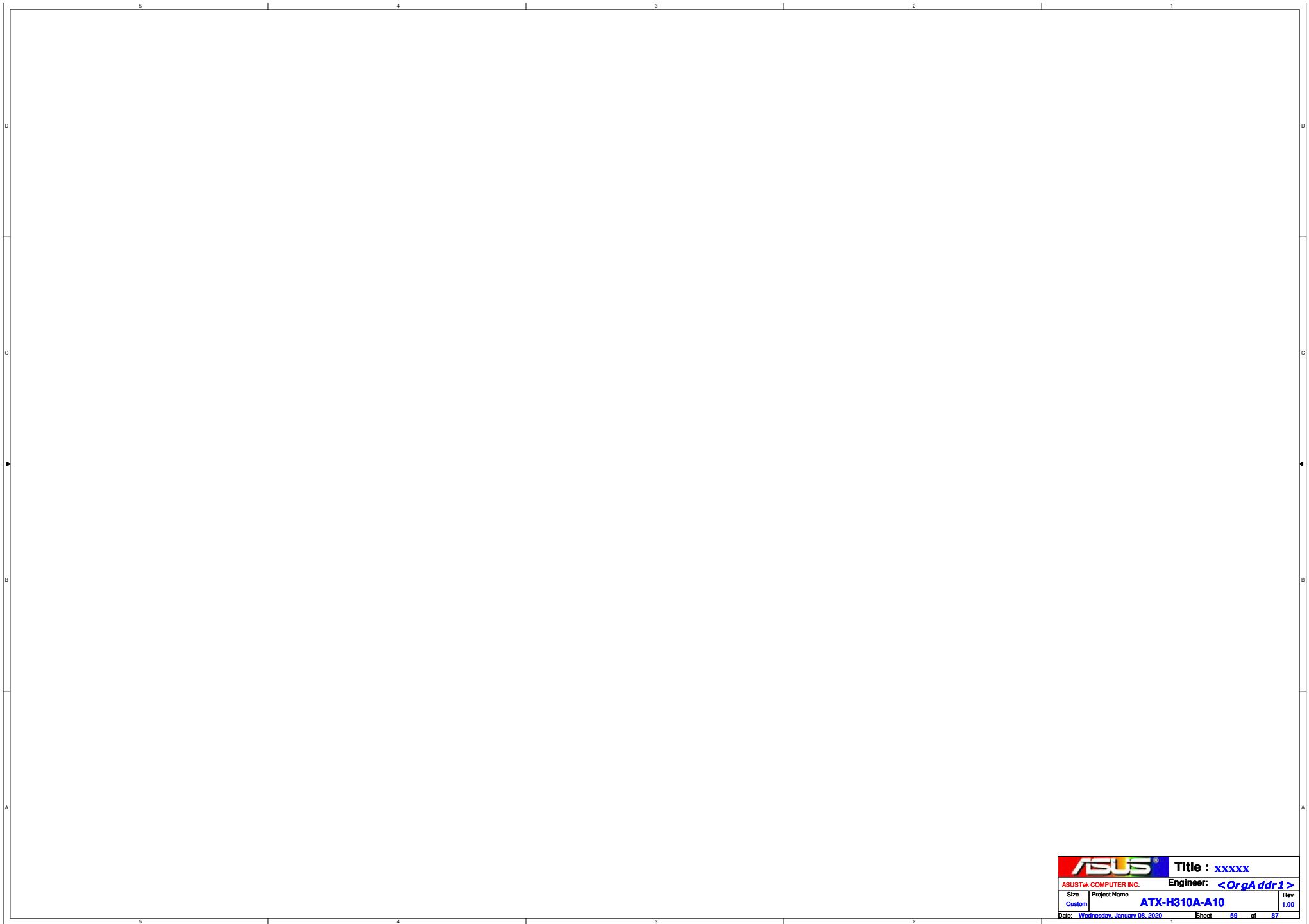






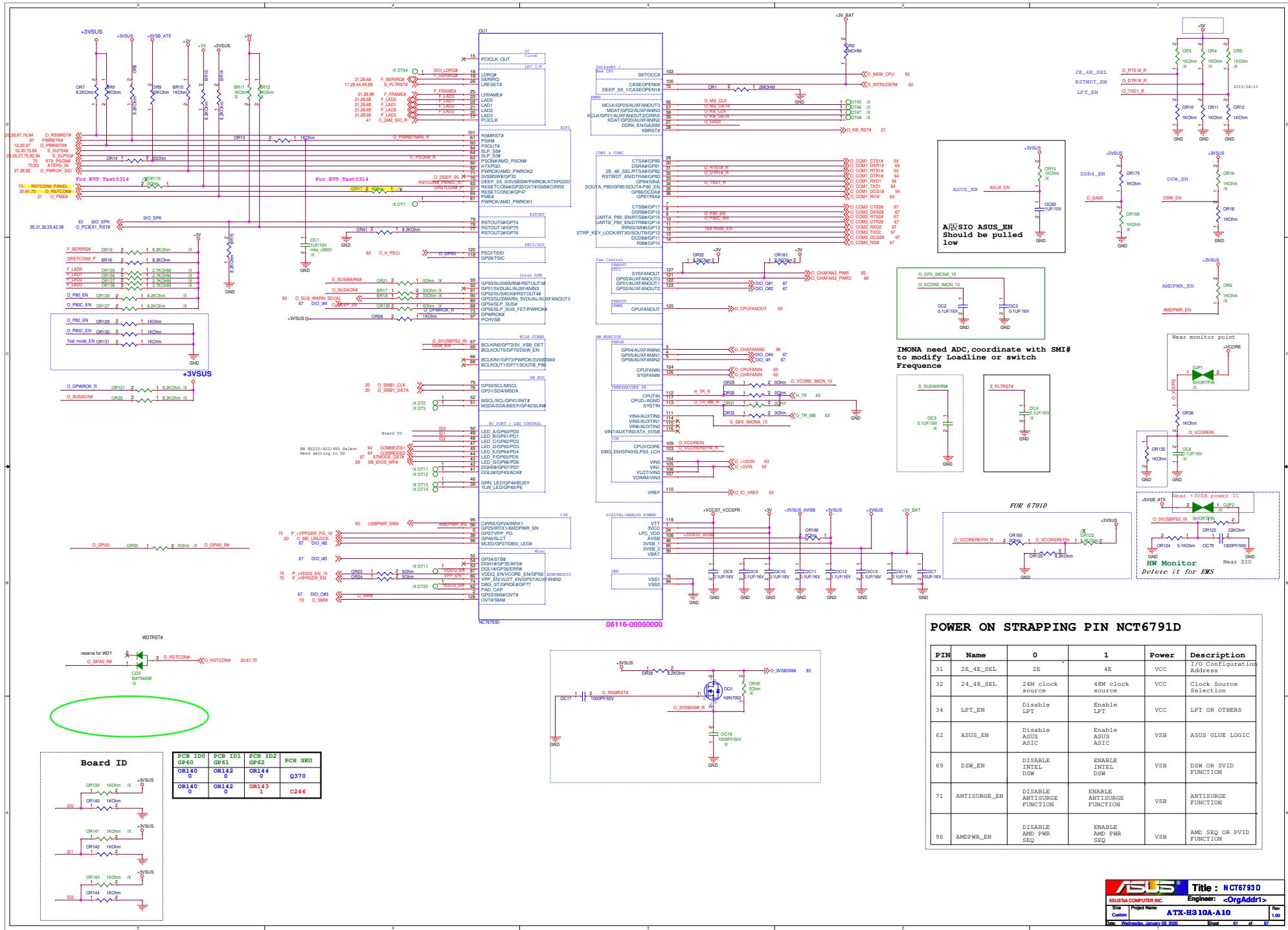


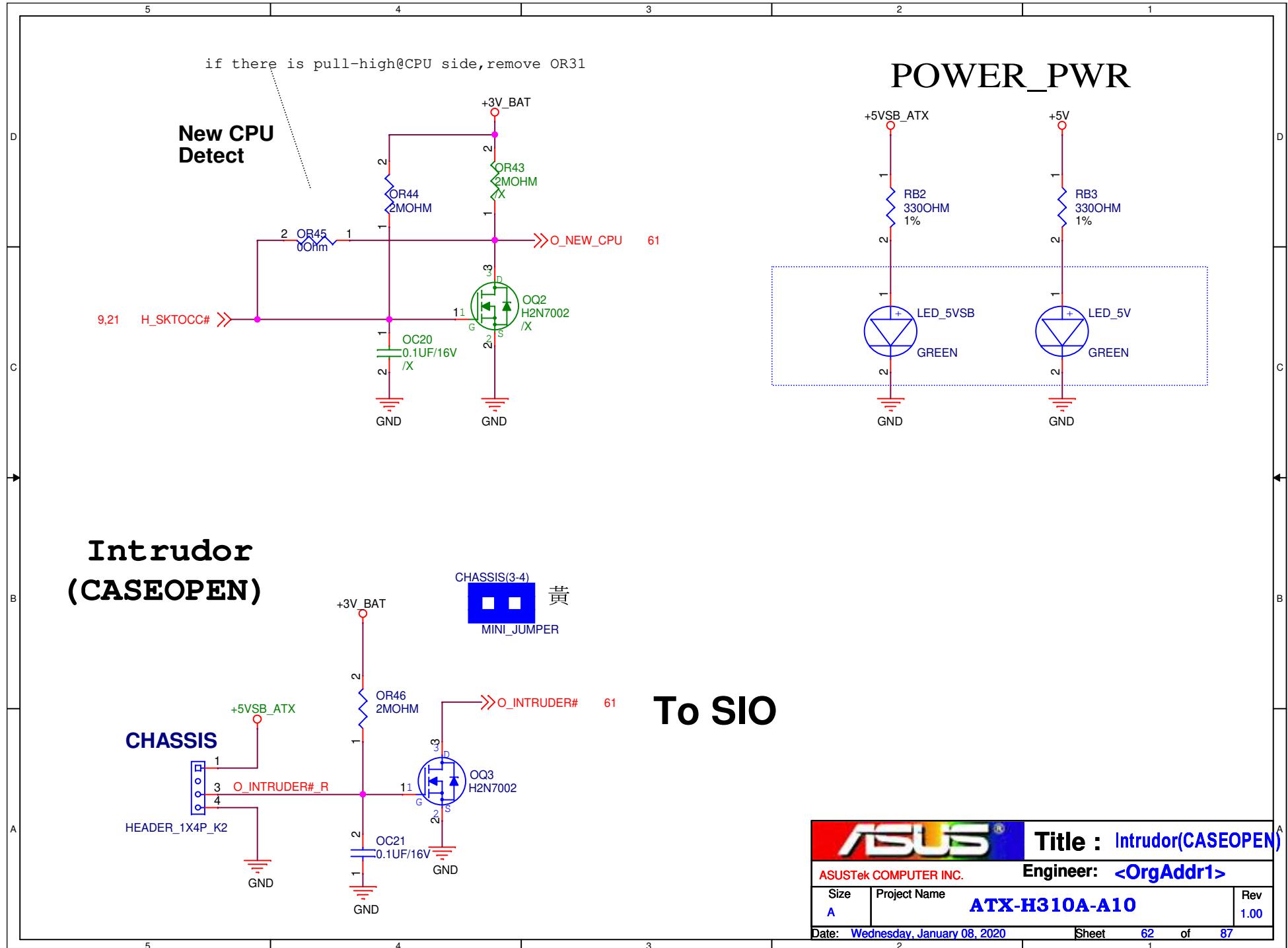


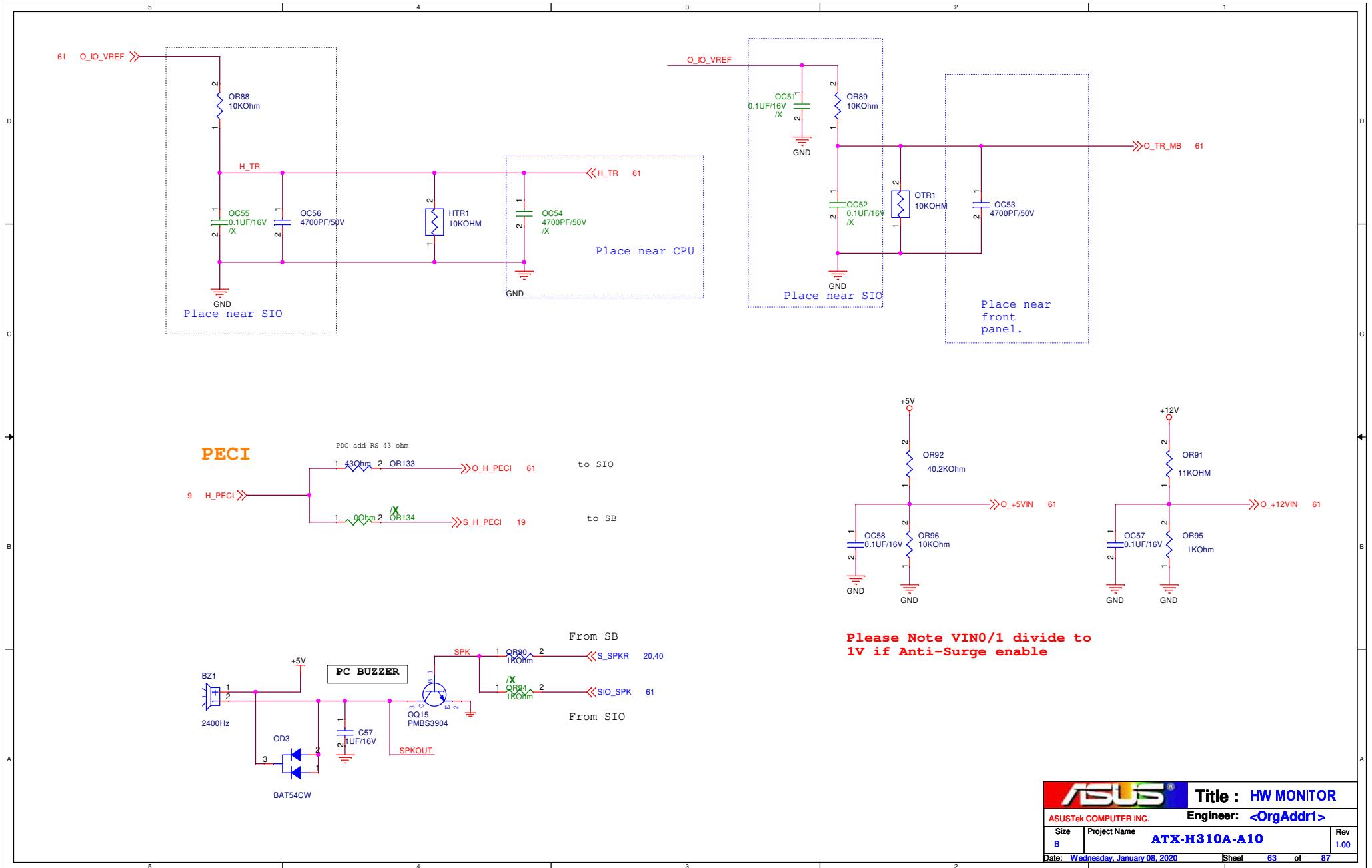




 <b>Title :</b> XXXX		
ASUSTek COMPUTER INC.	<b>Engineer:</b> <OrgAddr1>	
Size <b>B</b>	Project Name <b>ATX-H310A-A10</b>	Rev 1.00
Date: Wednesday, January 08, 2020	Sheet 1 of 87	







ASUS® Title : HW MONITOR		
ASUSTek COMPUTER INC. Engineer: <OrgAddr1>		
Size	Project Name	Rev
B	ATX-H310A-A10	1.00

Date: Wednesday, January 08, 2020

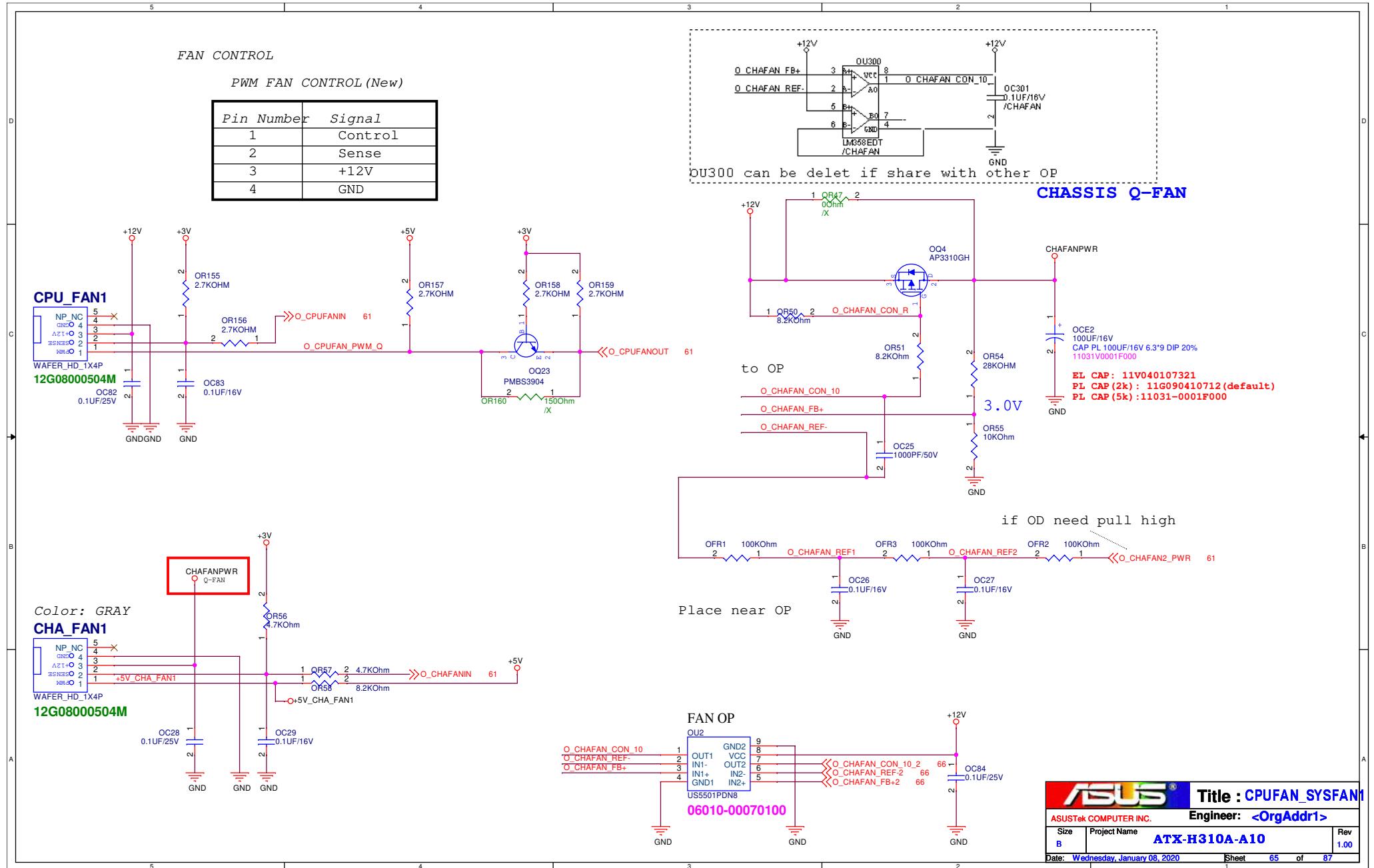
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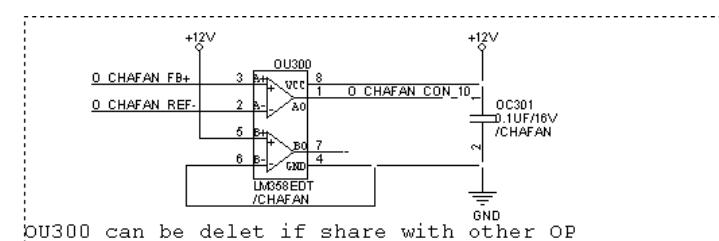
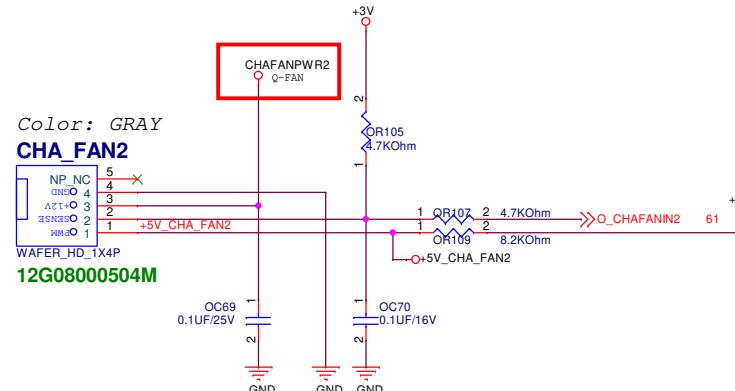




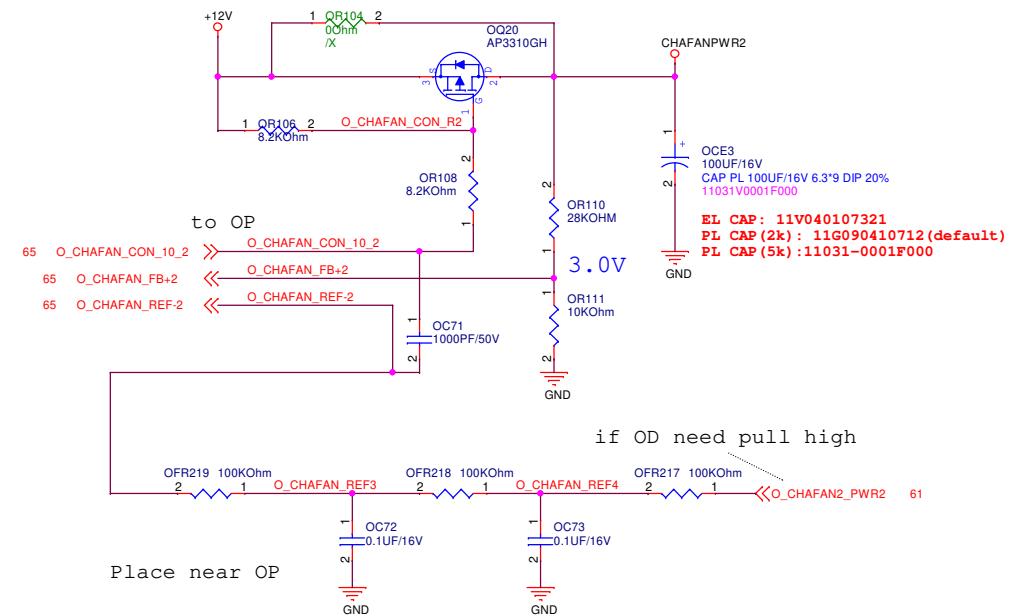
## FAN CONTROL

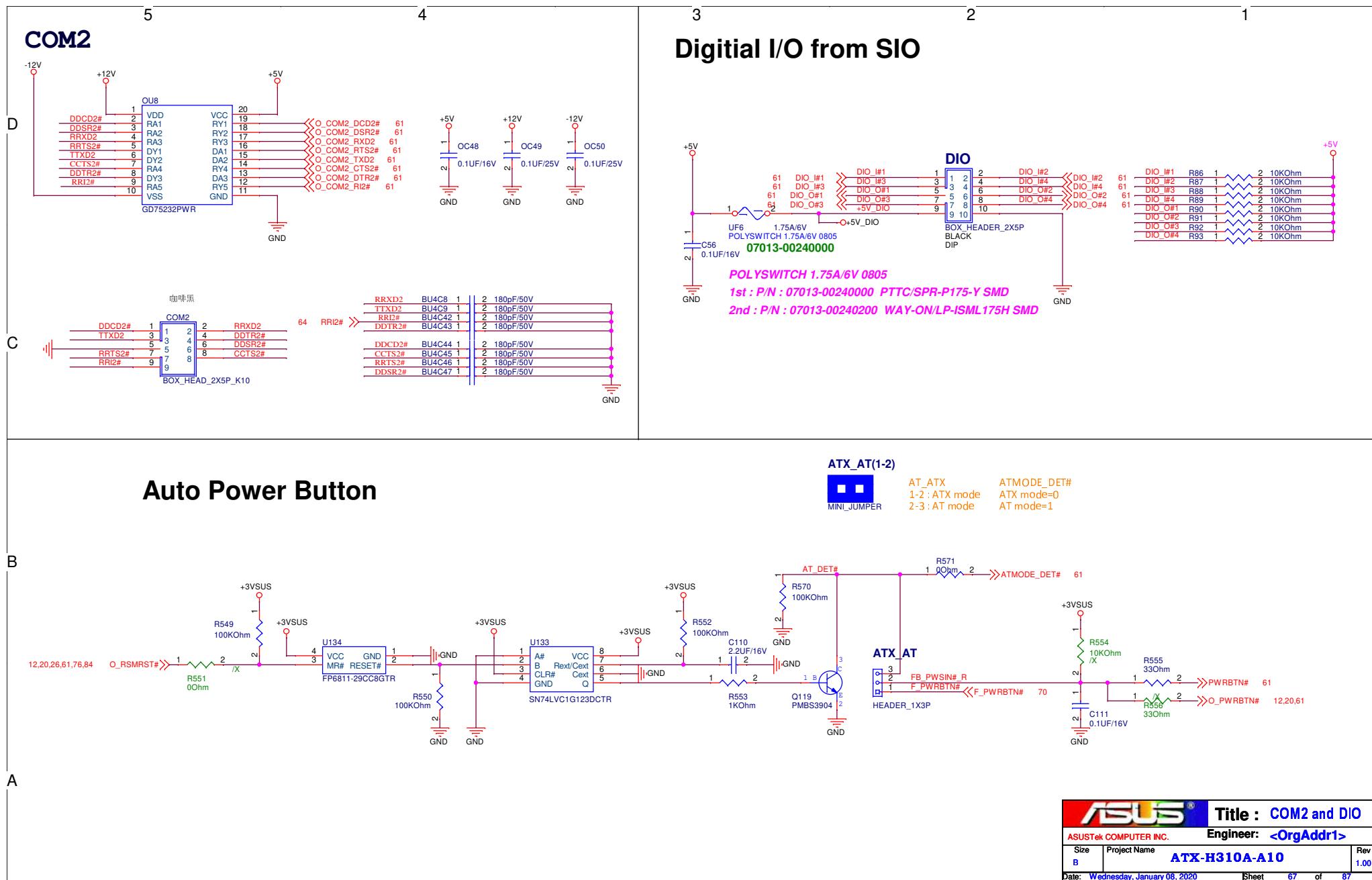
### PWM FAN CONTROL (New)

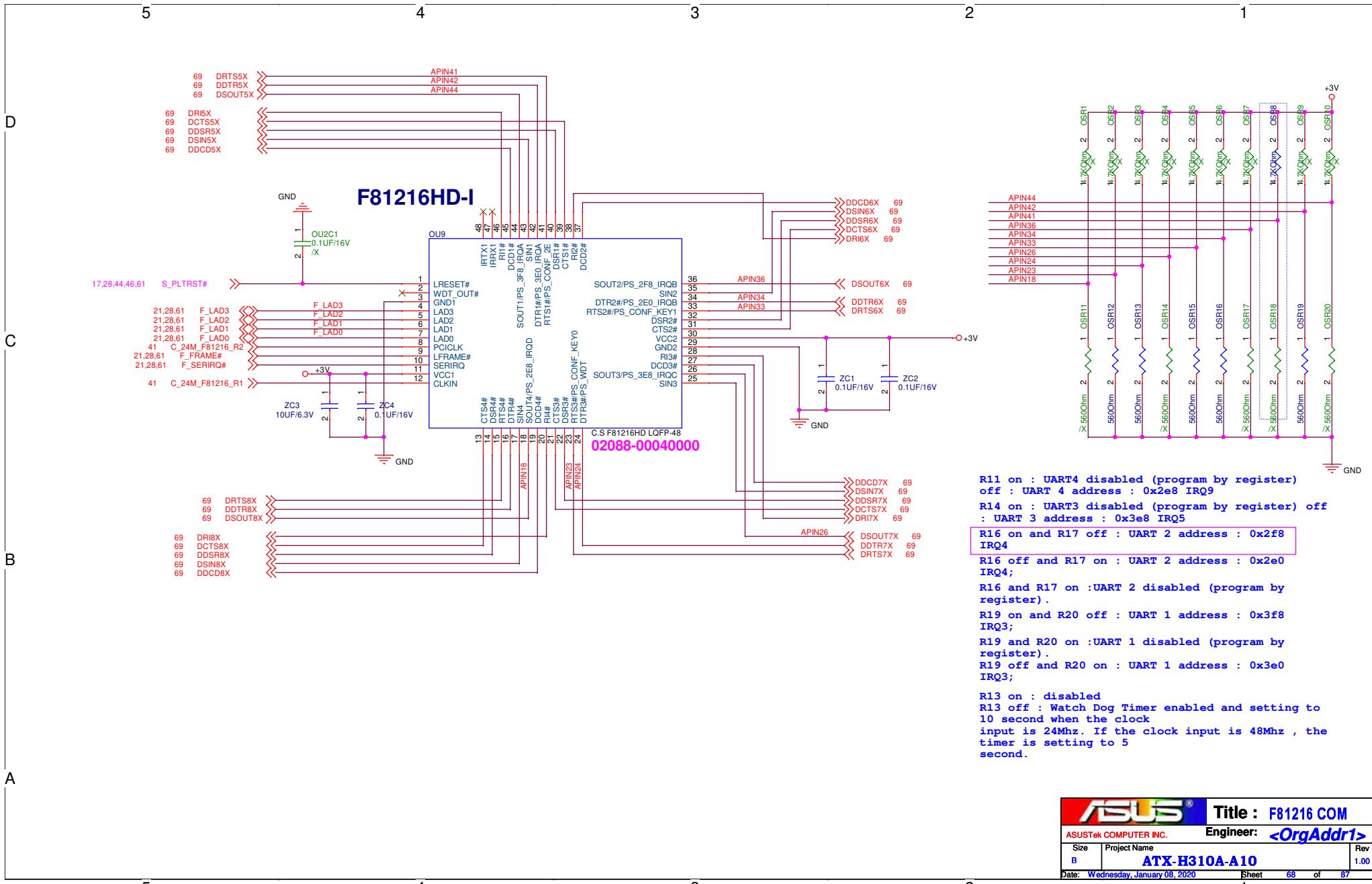
Pin Number	Signal
1	Control
2	Sense
3	+12V
4	GND

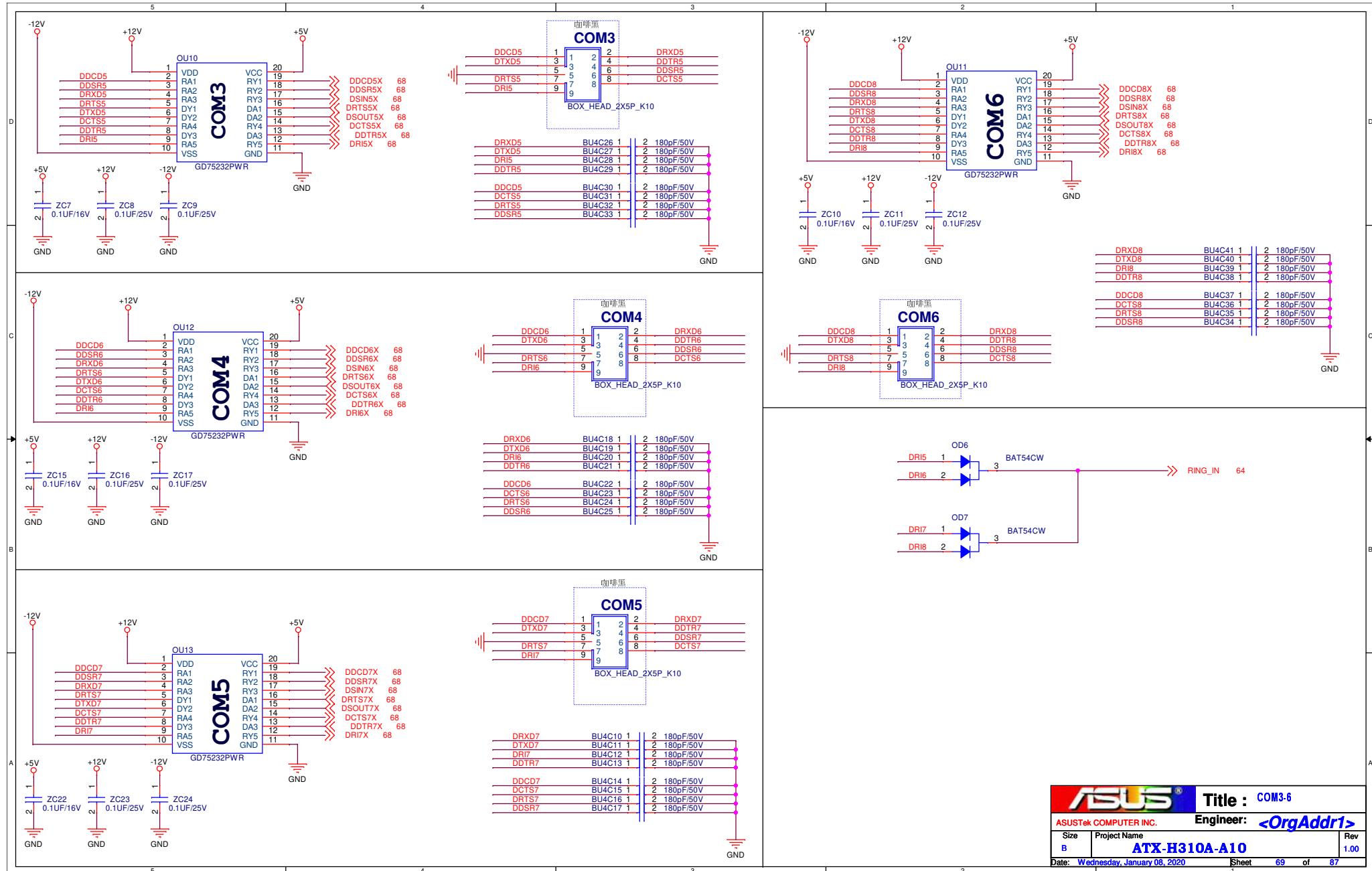


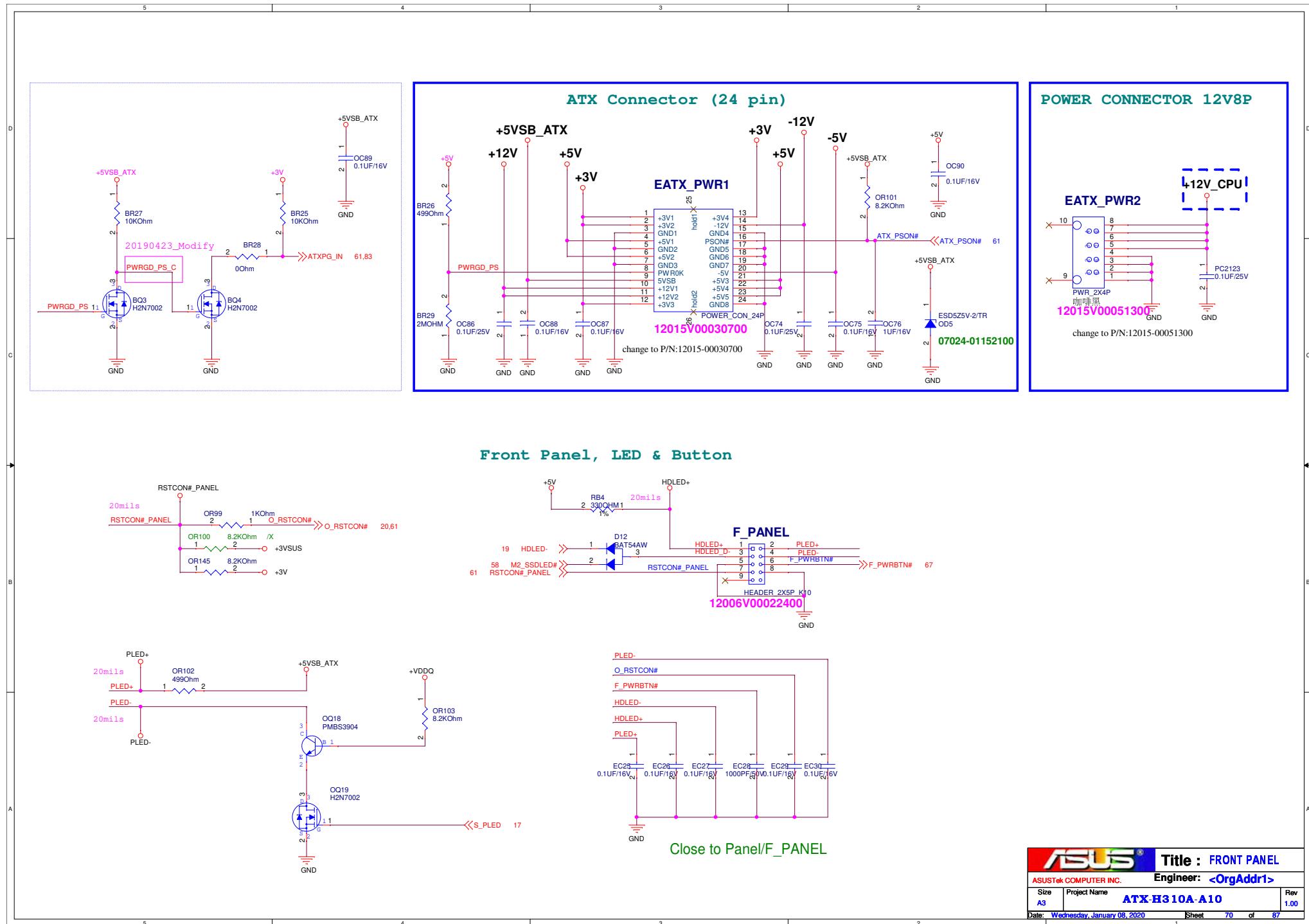
### CHASSIS Q-FAN

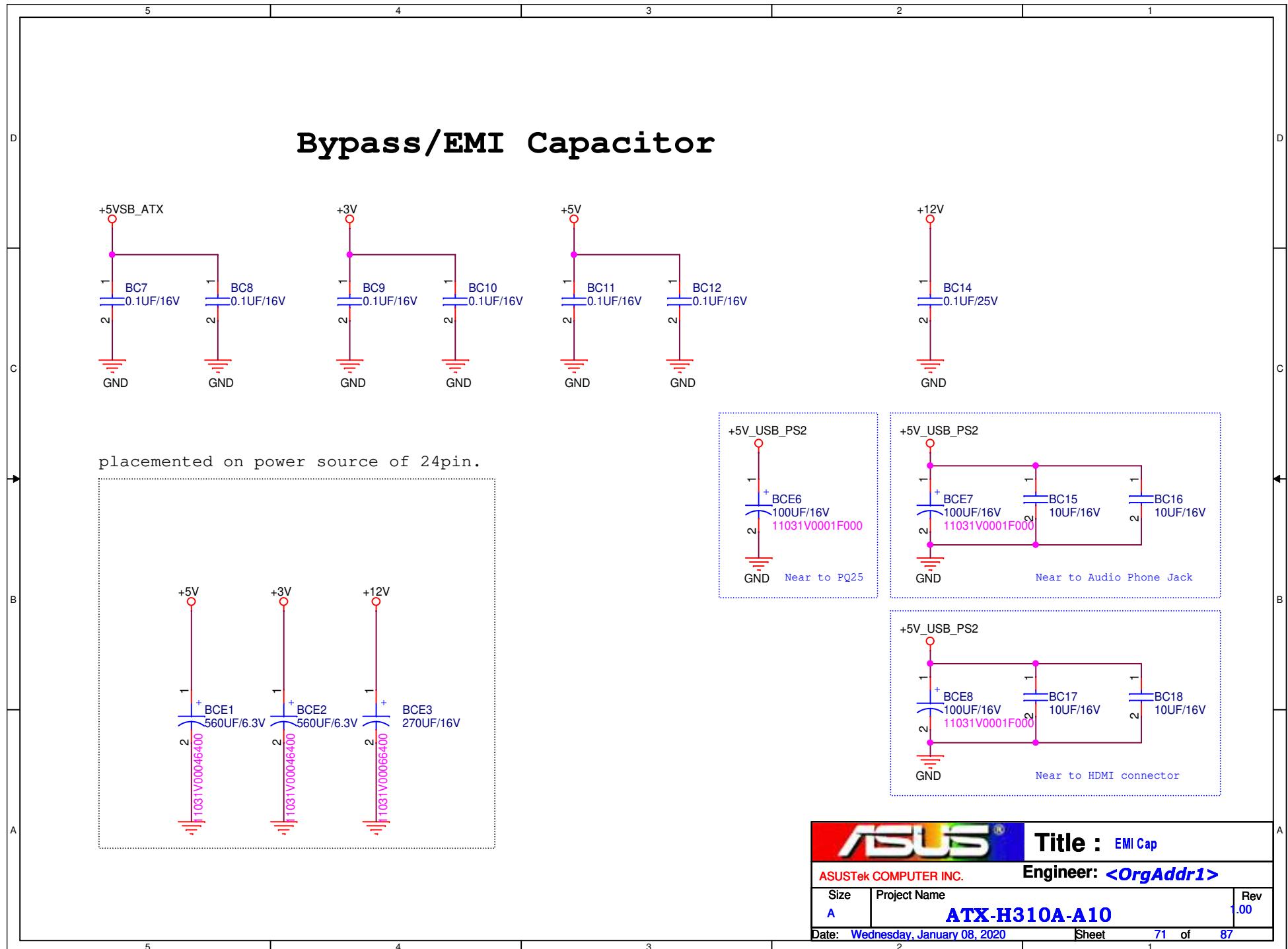


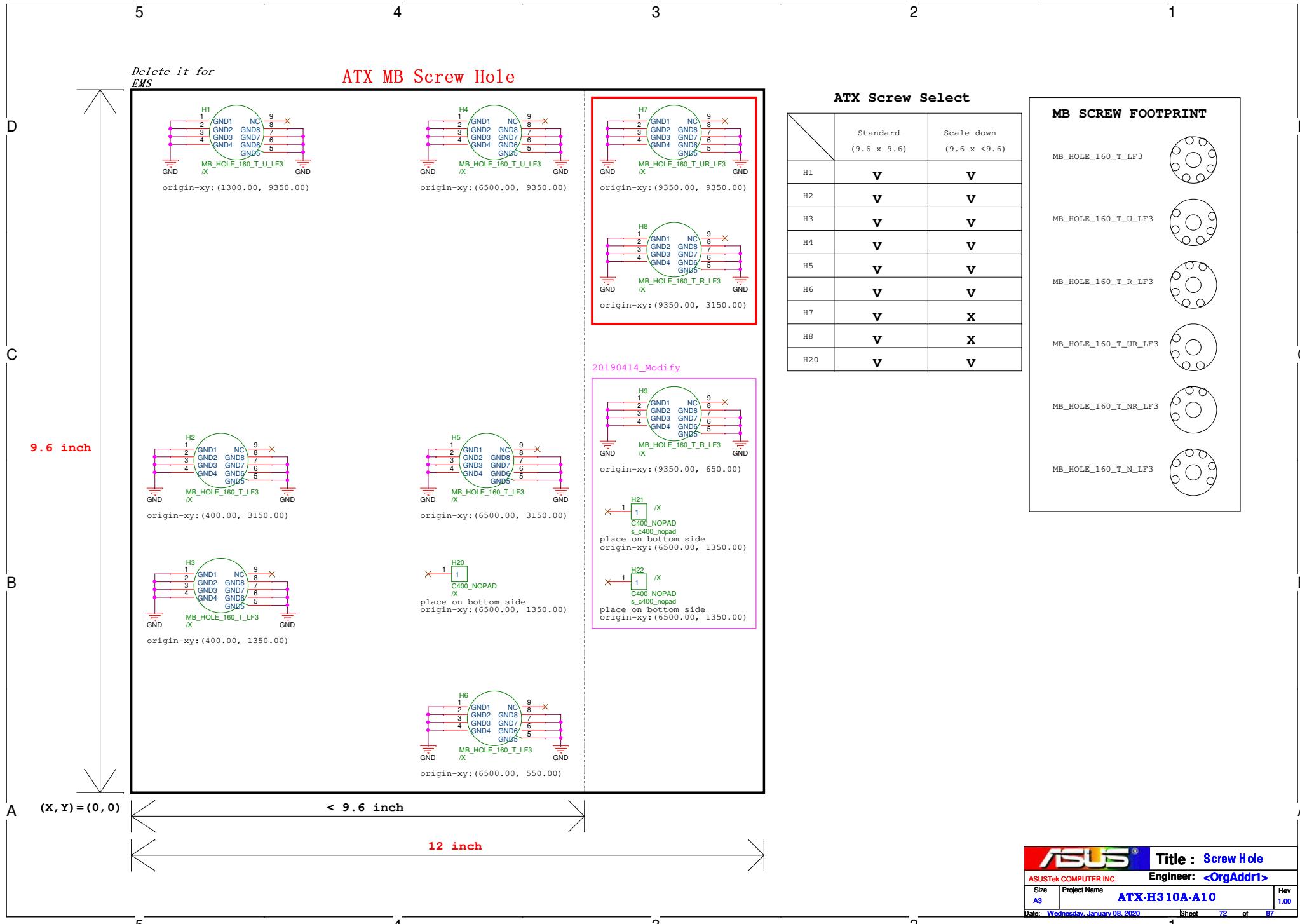




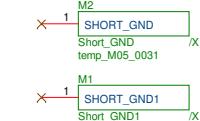
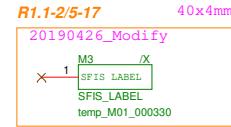








# Selling Point



**The U-DIMM Order of Insertion**



**R1.0-3/1-03**

**20190414\_Modify**

**R1.0-1/12-31**



**ATX-H310A R1.00 PCB-08 :**

**R1.00 PCB\_DF P/N : 08001-14630000**

**R1.00 PCB\_GECS P/N: 08001-14630100**

**R1.00 PCB\_Trustech P/N : 08001-14630200**

**Check PCB P/N**

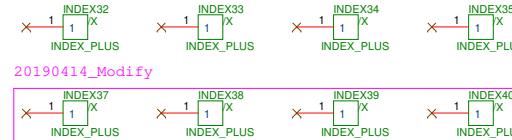
## Fiducial Mask (光學點)

大顆十字光學點

光學點需要 6 ~ 10 顆,  
 LayoutRD 會依空間大小及版本需求  
 擱放所需的光學點  
 所以兩種光學點都需畫入線路中,  
 最後再做刪除.

2012/09/11

小顆十字光學點



**20190414\_Modify**

# Logo

**common Logo for all projects**



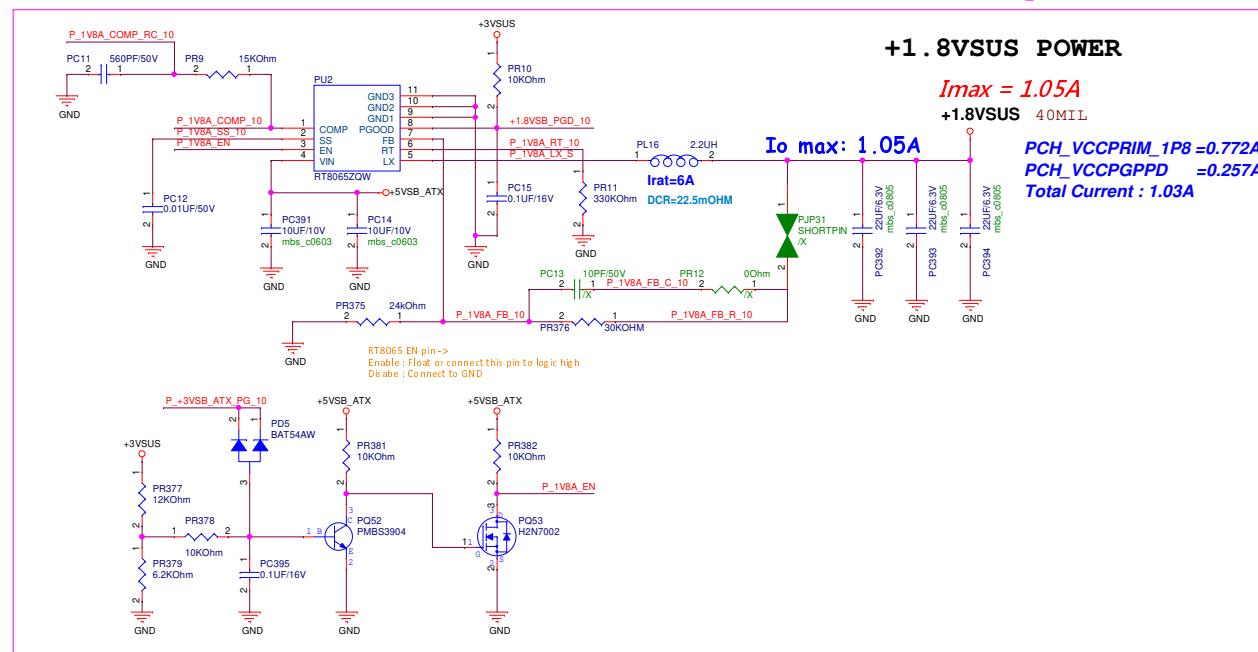
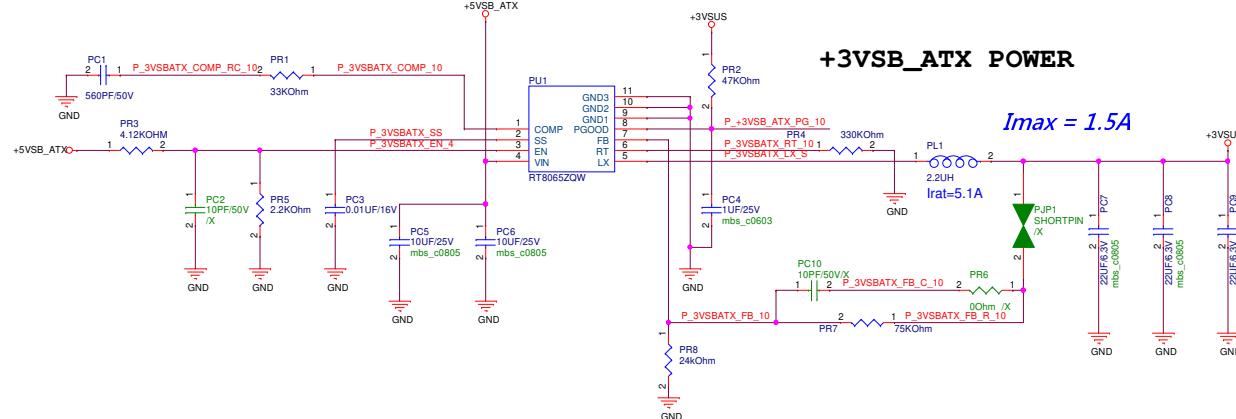
**LOGO2**  
**FCC**  
**FCC**  
**temp\_M01\_000348**

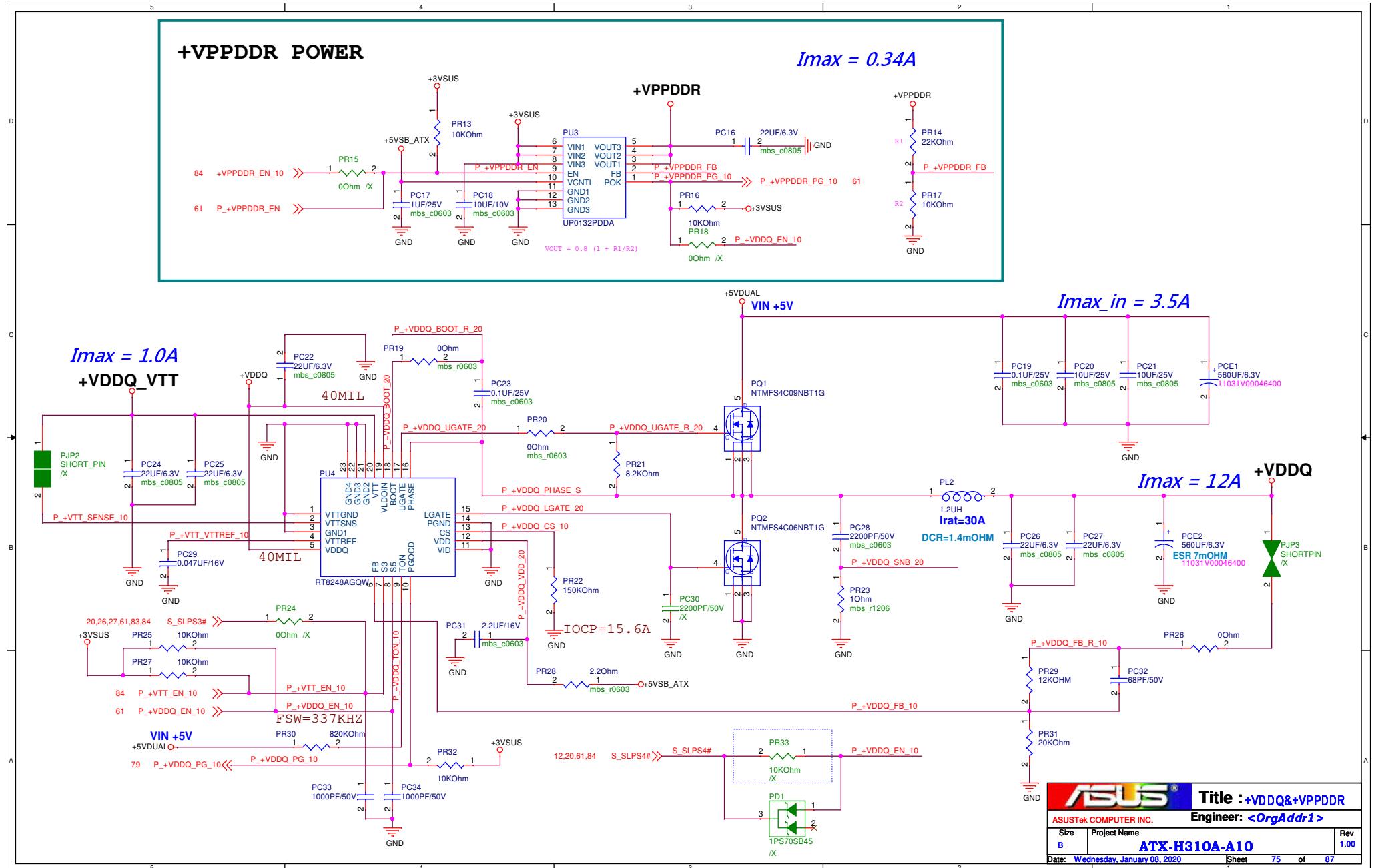


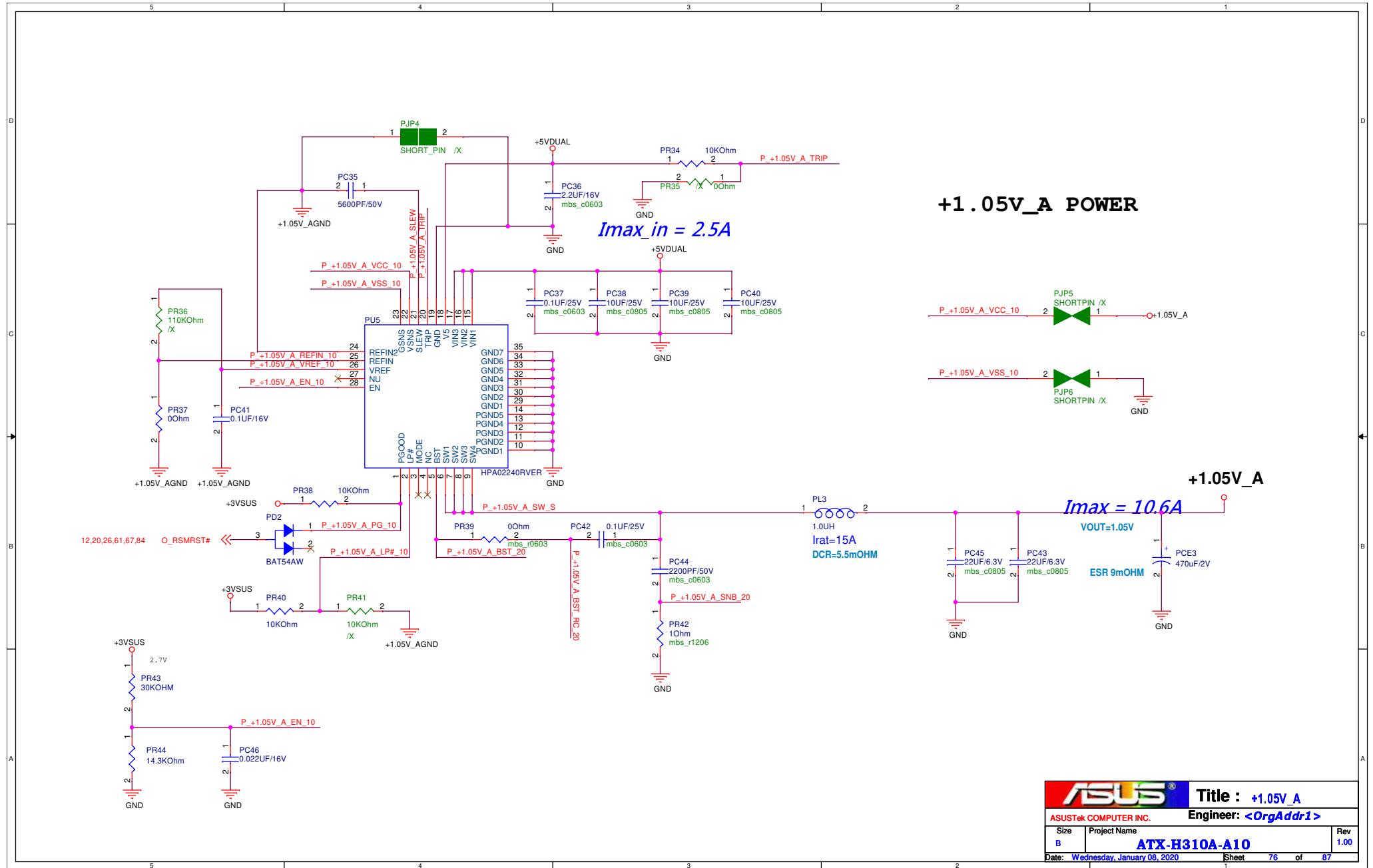
**LOGO5**  
**WEEE\_LOGO**  
**WEEE\_LOGO**

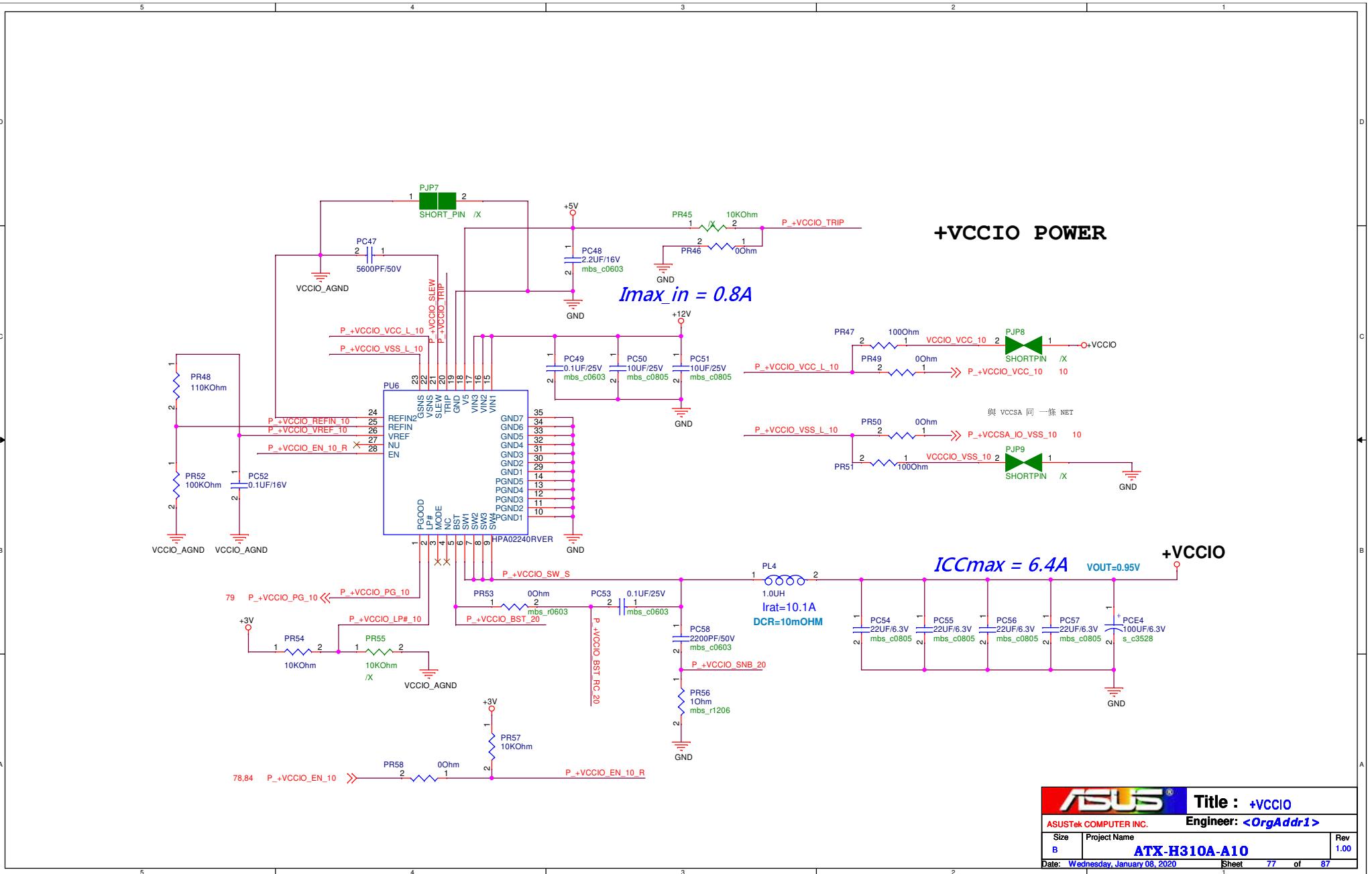
**LOGO6**  
**CE**  
**CE**

**LOGO7**  
**PCB MADE IN CHINA**  
**PCB MADE IN CHINA**

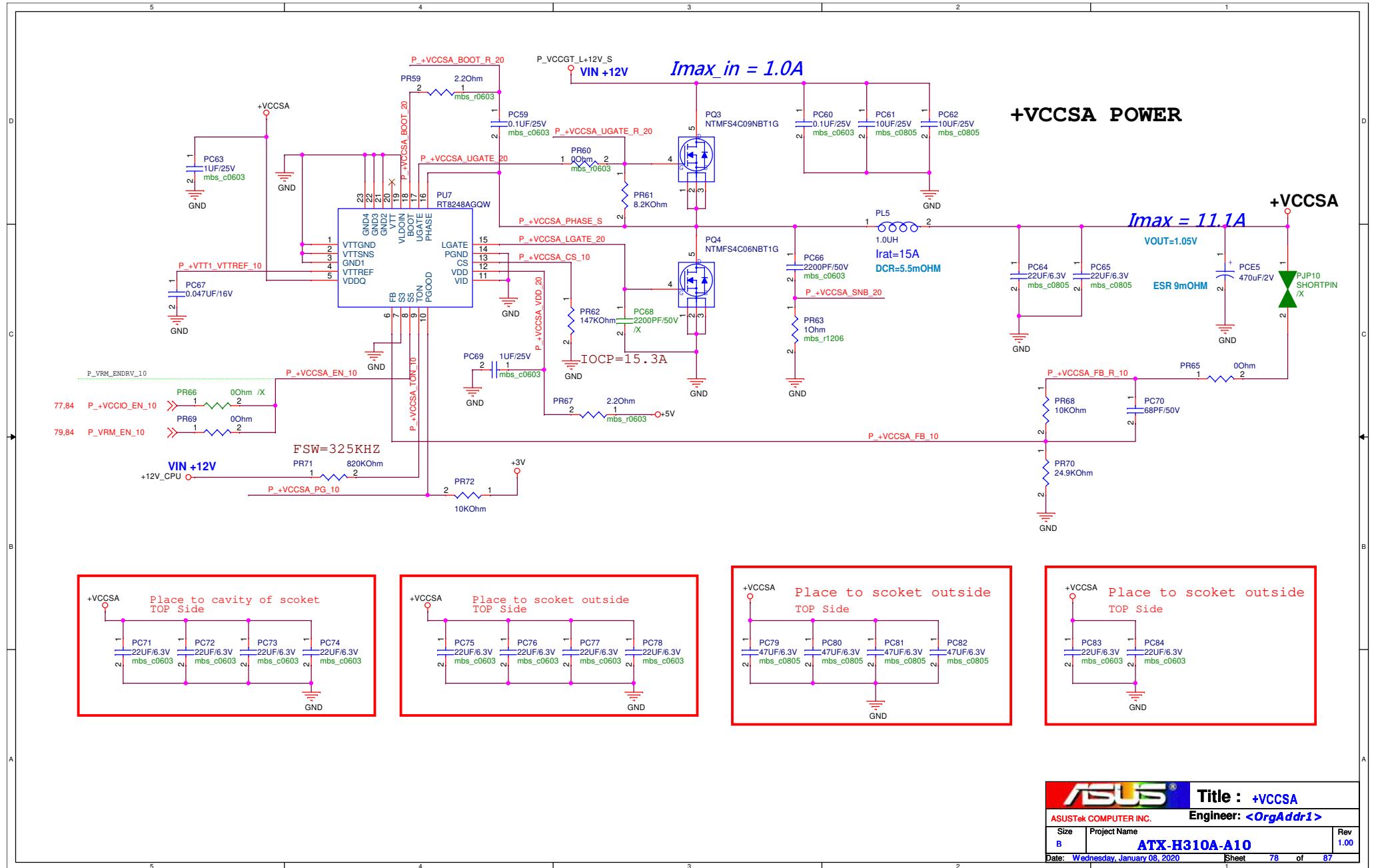


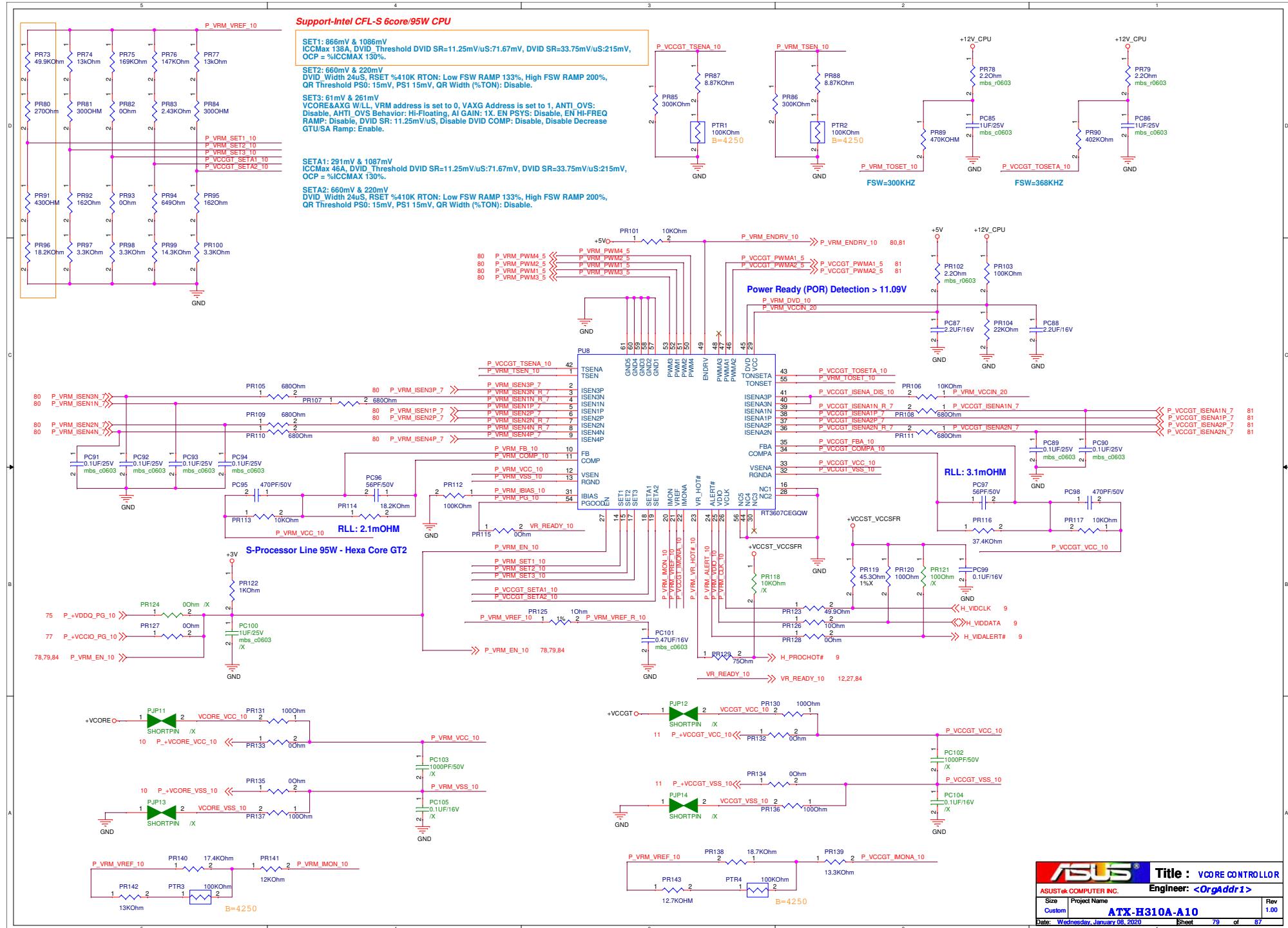


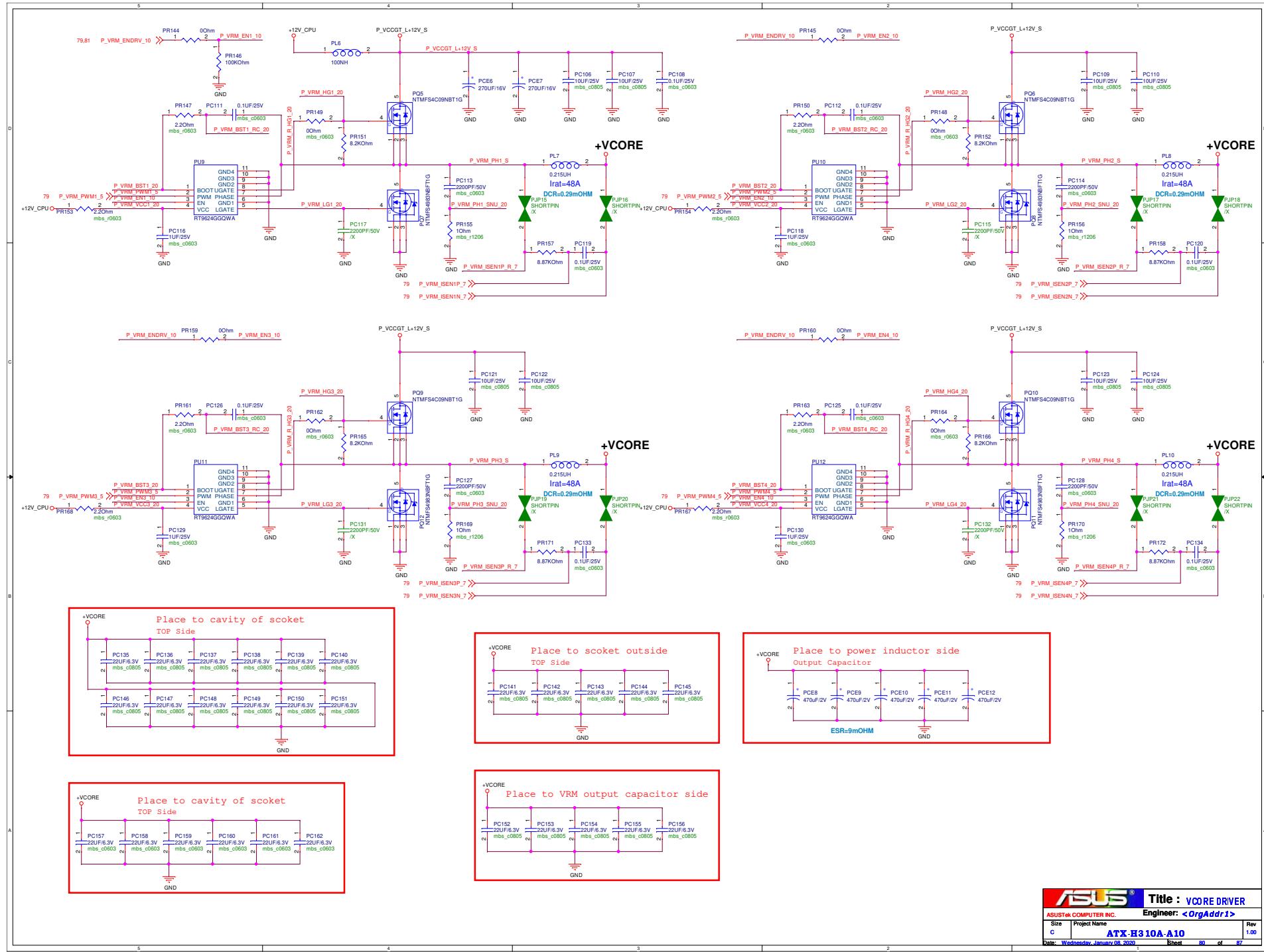


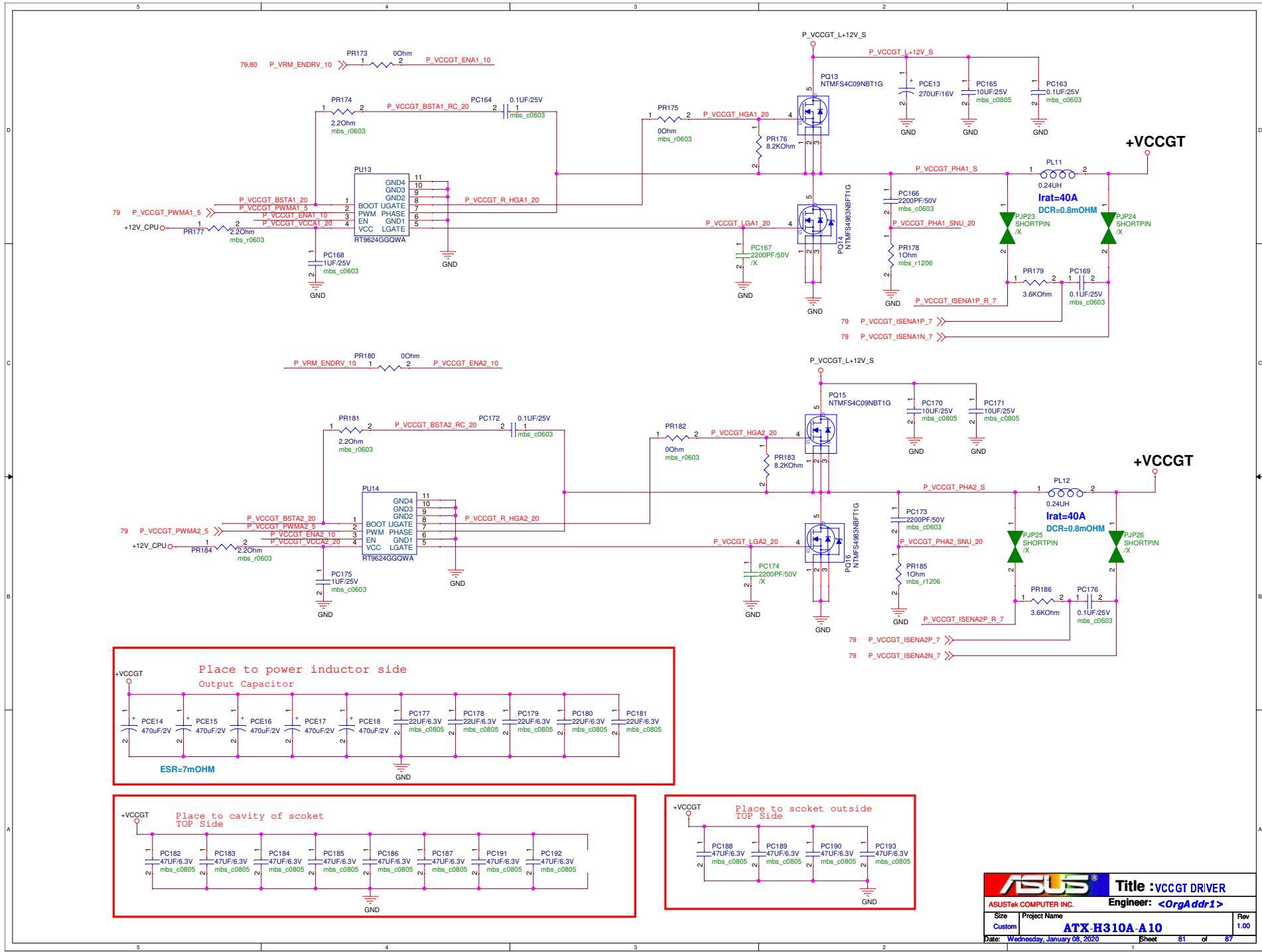


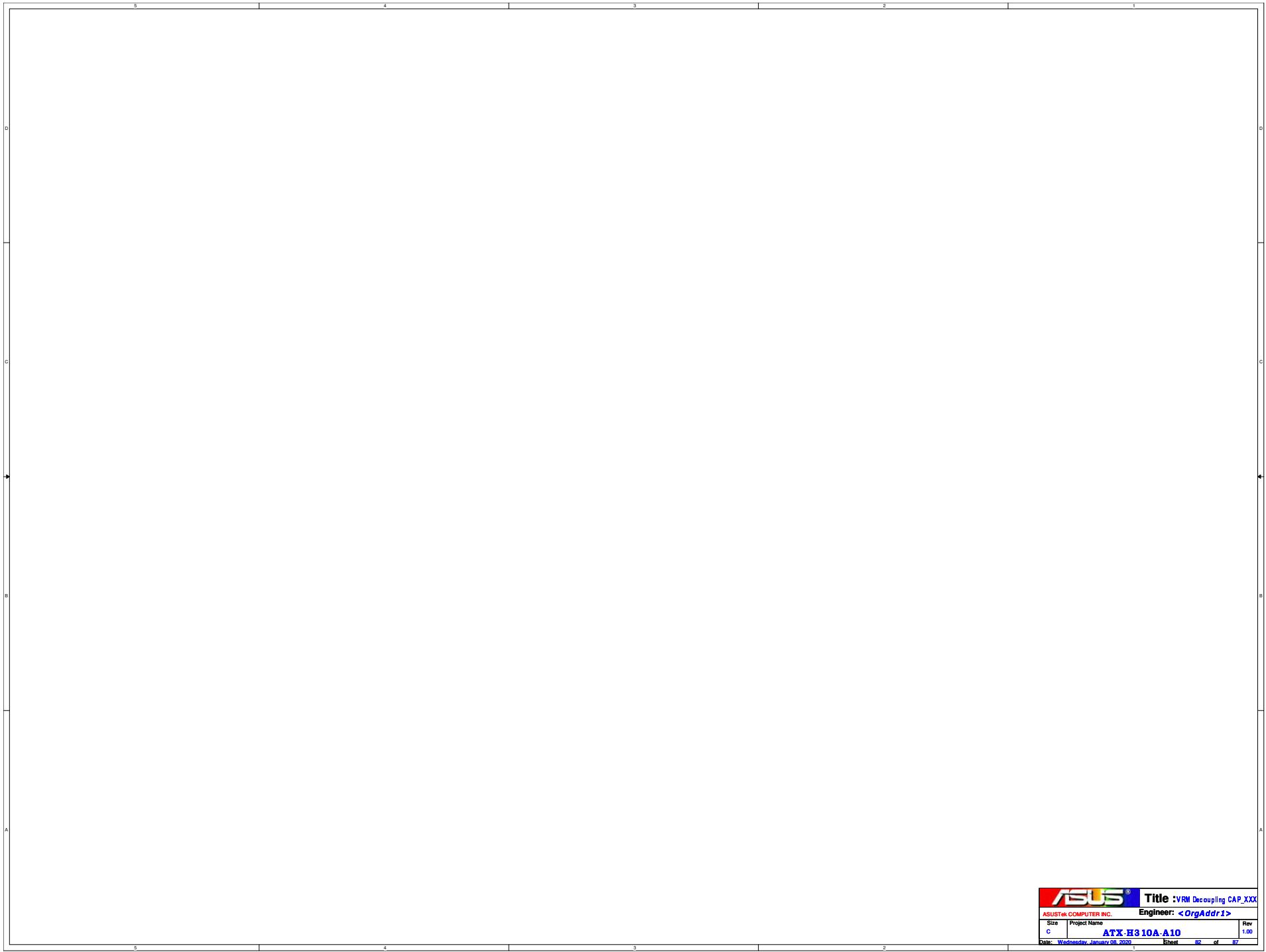
ASUS		Title : +VCCIO
ASUSTek COMPUTER INC.		Engineer: <OrgAddr1>
Size	Project Name	Rev
B	ATX-H310A-A10	1.00
Date: Wednesday, January 08, 2020	Sheet	1 of 87



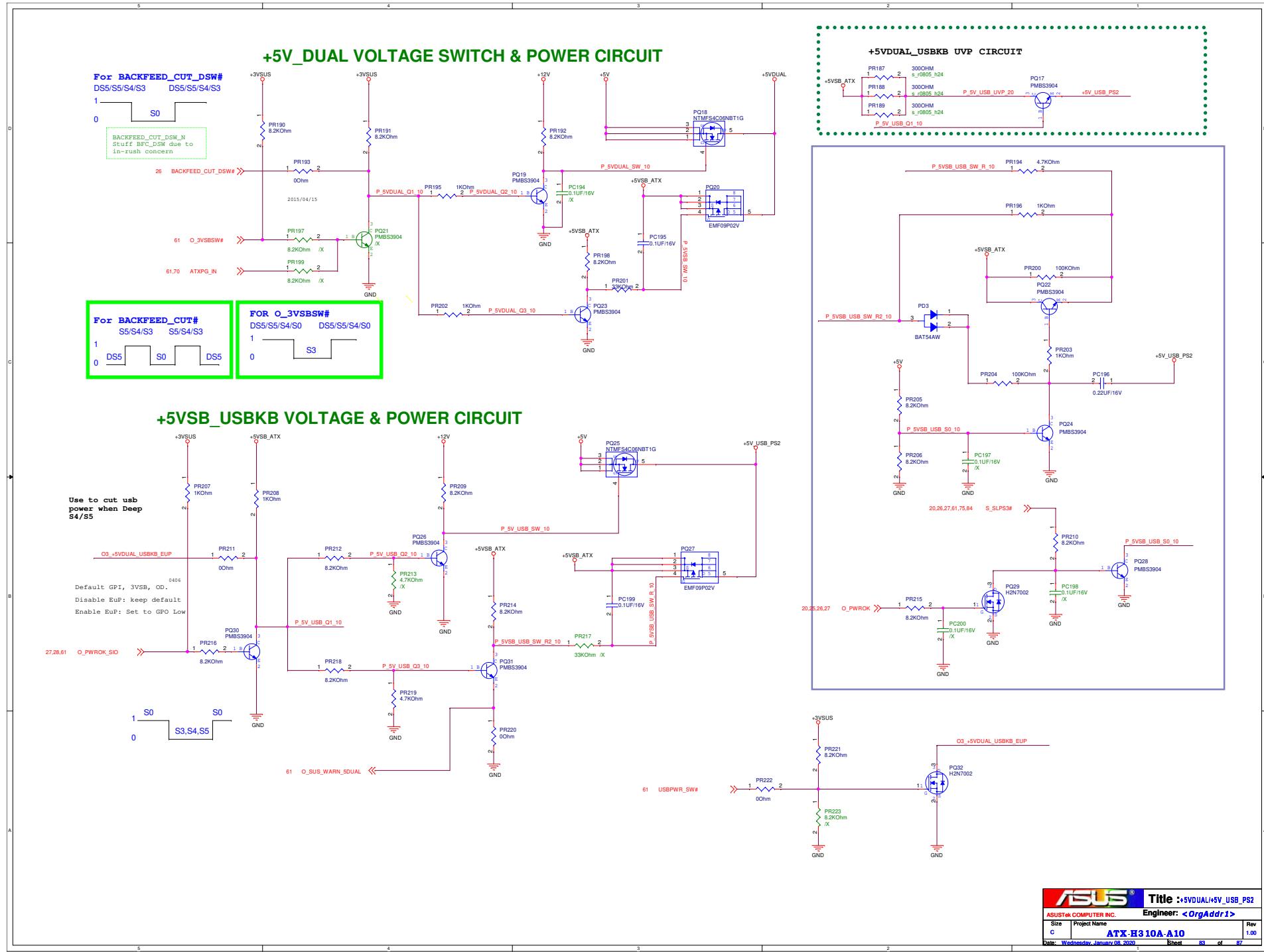


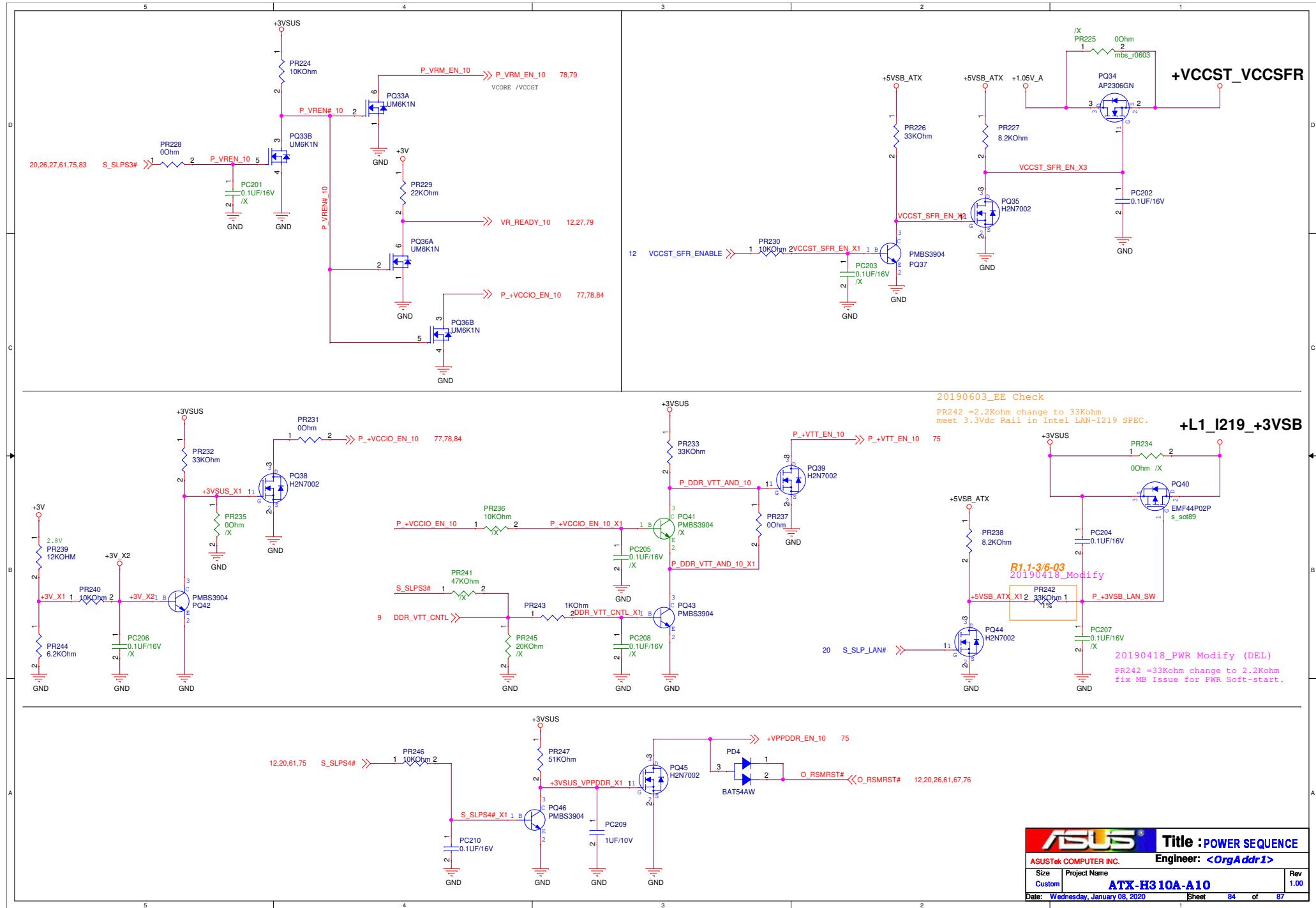


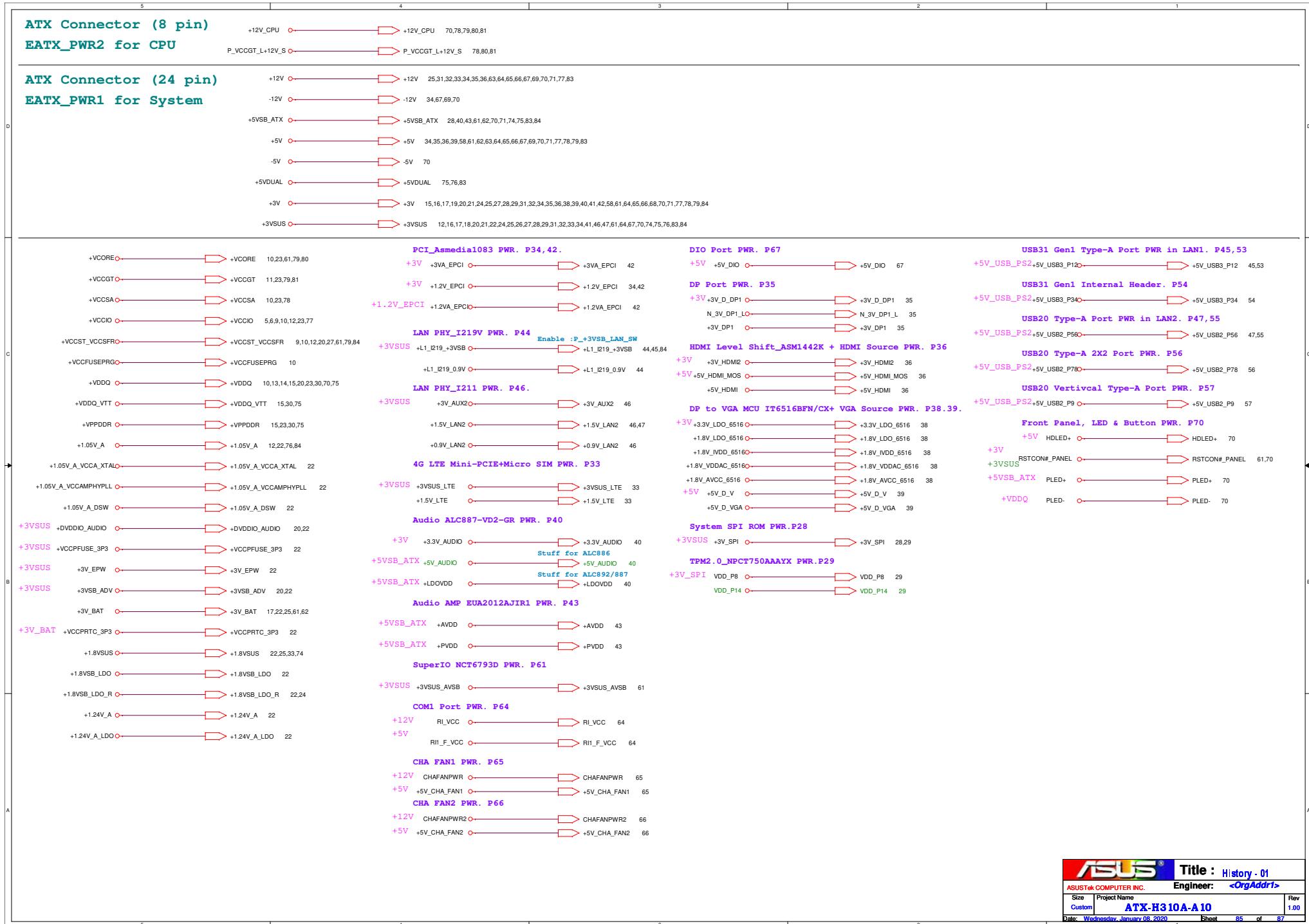


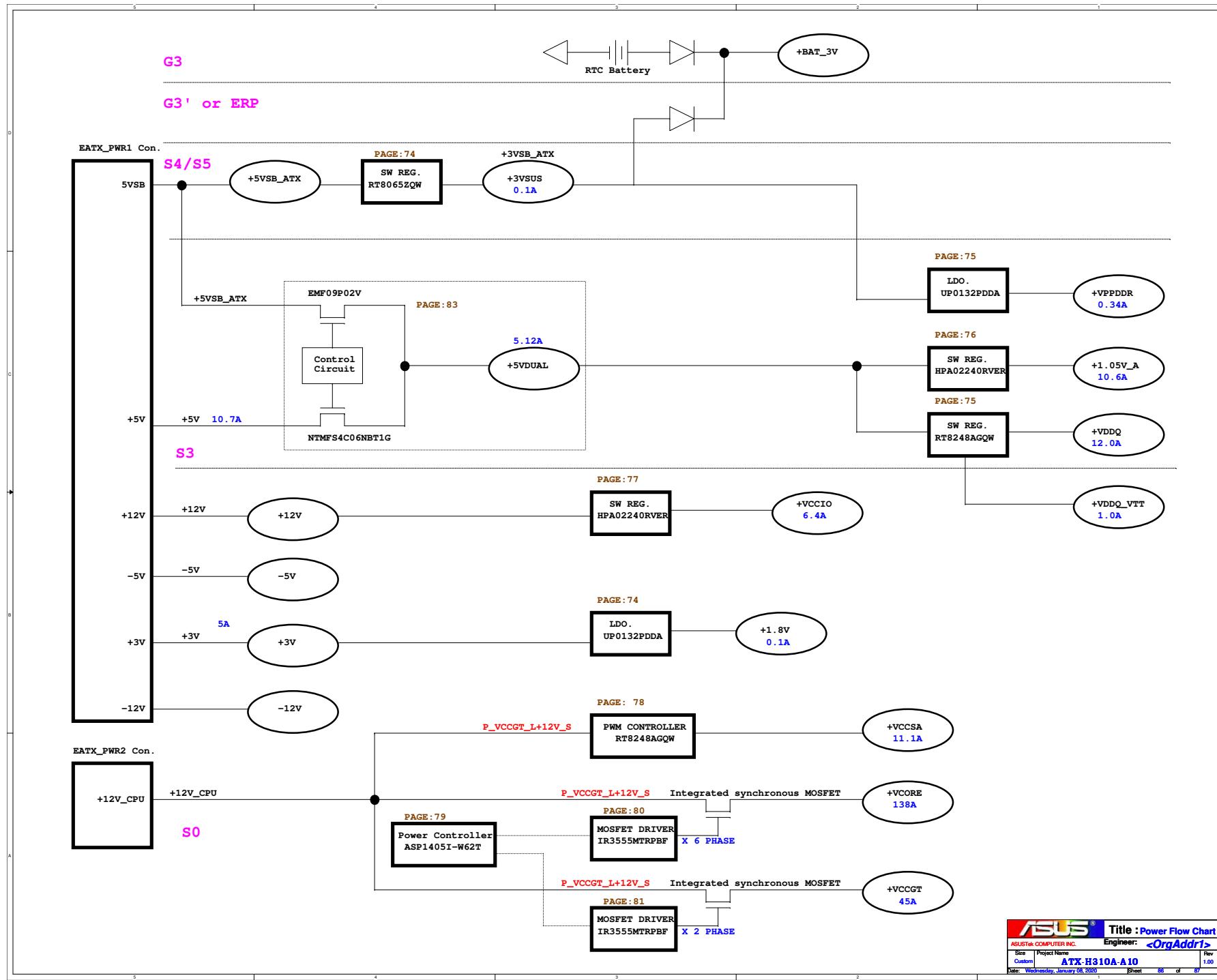


ASUS®		Title : VRM Decoupling CAP_XXX	
ASUSTek COMPUTER INC.		Engineer: <OrgAddrs>	
Size C	Project Name ATX-H310A-A10	Rev 1.00	
Date: Wednesday, January 08, 2020	Sheet 82	of 87	









Rev	Date	Description
1.0 Green	1/10/2019	First Release
	1/21/2019	<p>1_Release ATX-H310A R1.00 .06 PCB Part Number: 08001-14630X00. Page73.</p> <p>(1) R1.01 PCB_DF: 08001-14630000  (2) R1.01 PCB_GECS: 08001-14630100  (3) R1.01 PCB_Trustech: 08001-14630200  (4) R1.01 PCB_CIRCUIT: 08001-14630300</p>
	1/03/2020	<p>2_Follow Layout SWAP0103:</p> <p>(1) SWAP_DP-TX01/12/2_P.N. Page55.  (2) SWAP_USB_Port0_ULB308_U0202. Page56.  (3) SWAP_USB_Port0_ULA502. Page57.  (4) SWAP_USB_Port6_6_ULB201_U0201. Page55.  (5) SWAP_USB_Port4_ELX1. Page53.</p>
		<p>3_Follow IPC HW Leader Define:  Remove PCB_VENDOR_LOGO Symbol. Page73.</p> <p>4_Change M2_M_HOLD_42 from Main Source_EOL to P/N:13020-01370200/CUNYIN/NUT. Page58.</p>

Rev	Date	Description

Rev	Date	Description

Rev	Date	Description