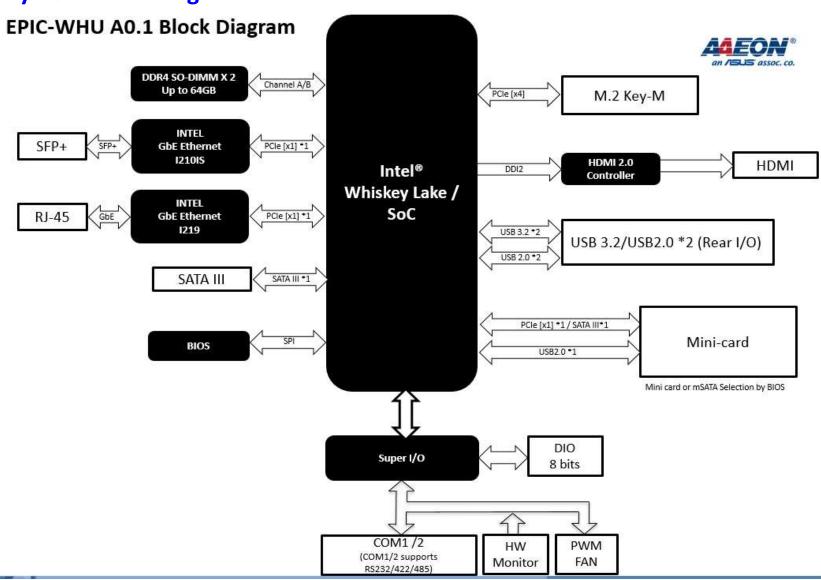


Project Name: EPIC-WHU

Project Number: Version: A0.1_0_0

System block diagram



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1	Cover Sheet
2	System Setting
3	Power Delivery
4	Power Sequence
5	SoC DDI
6	SoC DDR4
7	SoC SPI/LPC/SMBus
8	SoC HDA/SD
9	SoC PCIe/SATA/USB
10	SoC PCIE CLK/ RTC
11	SoC eMMC
12	SoC System
13	SoC Power
14	PCH Power
15	SoC GND
16	SoC Strap
17	SoC RSVD
18	DDR4 SODIMM A
19	DDR4 SODIMM_A DDR4 SODIMM B
20	None
21	None
21	
	HDMI 2.0 Converter
23	LAN1_I210IS
24	LAN2_I219LM
25	None
26	M.2(Key M)
27	None
28	MiniCard/mSATA(Full)
29	SATA/DIO
30	USB3.0
31	SUPERIO_FINTEK 81966
32	POWER INPUT,MISC
33	COM1~COM2
34	FAN/BIOS/ LPC/ HW Monitor
35	None
36	TANDBY POWER
37	None
38	PWR_+VREG5,+V3.3A
39	PWR_+V1P05A/ +VCCIO
40	PWR_+VDDQ/+VDDQ_VPP/+V1P8A
41	PWR_+VCCSTG/+VCCSFR_OC
42	PWR_+V12S/ +V5S/ +V3P3S
43	PWR_IMVP8 Controller
44	PWR_+VCCCORE
45	PWR_+VCCGT/ +VCCSA
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AAEON Technology INC.			
itle			
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Custom

SoC GPIO Pins:

Name	Power Well	Default	GPIO Function	Location
Name	Fower Wen	Delault	GFIO Function	Location
GPIO S0 SC[00]	+V1.8S Core	20k.L	SATA GP[0]	BA12
GPIO S0 SC[01]	+V1.8S Core	20k,L	SATA GP[1]	AY14
GPIO S0 SC[07]	+V1.8S Core	20k.H	N.C.	BD5
GPIO S0 SC[55]	+V1.8S Core	20k,L	N.C.	BD12
GPIO_S0_SC[56]	+V1.8S Core		-	BC12
GPIO S0 SC[57]	+V1.8S Core	20k,H	N.C.	BD14
GPIO S0 SC[58]	+V1.8S Core	20k,L	N.C.	BC14
GPIO_S0_SC[59]	+V1.8S Core	20k,L	LVDS_RBIT0	BF14
GPIO S0 SC[60]	+V1.8S Core	20k,L	LVDS RBIT1	BD16
GPIO S0 SC[61]	+V1.8S Core	20k,H	LVDS RBIT2	BC16
GPIO S0 SC[92]	+V1.8S Core	20k,H	LVDS RBIT3	BH30
GPIO_S0_SC[93]	+V1.8S Core	20k,H	LVDS_RBIT4	BG30
GPIO_S0_SC[94]	+V1.8S Core	20k,L	N.C.	AU32
GPIO S0 SC[95]	+V1.8S Core	20k,L	N.C.	AT32
GPIO_S5[00]	+V1.8A Suspend	20k,H	WAKE RI#	B18
GPIO_S5[01]	+V1.8A Suspend	20k,H	GPIO_PME#	B16
GPIO_S5[02]	+V1.8A Suspend	20k,H	UIM_DET1	C18
GPIO_S5[03]	+V1.8A Suspend	20k,H	UIM_DET2	A17
GPIO_S5[04]	+V1.8A Suspend	20k,L	SoC SMI#	C17
GPIO_S5[05]	+V1.8A Suspend	20k,L	SoC_SCI#	C16
GPIO_S5[06]	+V1.8A Suspend	20k,L	N.C.	B14
GPIO_S5[07]	+V1.8A Suspend	20k,L	N.C.	C15
GPIO_S5[08]	+V1.8A Suspend	20k,L	N.C.	C13
GPIO_S5[09]	+V1.8A Suspend	20k,L	N.C.	A13
GPIO_S5[10]	+V1.8A Suspend	20k,H	N.C.	C19
GPIO_S5[17]	+V1.8A Suspend	20k,H	N.C.	J24
GPIO_S5[22]	+V1.8A Suspend	20k,L	N.C.	K24
GPIO_S5[23]	+V1.8A Suspend	20k,L	N.C.	N24
GPIO_S5[24]	+V1.8A Suspend	20k,L	N.C.	M20
GPIO_S5[25]	+V1.8A Suspend	20k,L	N.C.	J18
GPIO_S5[26]	+V1.8A Suspend	20k,L	N.C.	M18
GPIO_S5[27]	+V1.8A Suspend	20k,H	N.C.	K18
GPIO_S5[28]	+V1.8A Suspend	20k,H	N.C.	K20
GPIO_S5[29]	+V1.8A Suspend	20k,H	N.C.	M22
GPIO_S5[30]	+V1.8A Suspend	20k,H	N.C.	M24

SMBus/I2C Addresses:

Device	8-bit address	7-bit address
SODIMMA	A0h	50h
AD5247BK LCD Backlight Contoller	5Ch	2Eh
PTN3460 Slave		C0h
F75111 GPIO IC	6Eh	37h

PCB Footprints MOSFET BJT



PCB STACK: Board: FR4 Impedence: 50ohm +/-10% Thickness: 1.8mm +-10%				
Layer 1 : Component (Top)				
Layer 2 : GND (GND1)				
Layer 3 : Signal (IN1)				
Layer 4 : GND (GND2)				
Layer 5 : Signal (IN2)				
Layer 6 : POWER (VCC)				
Layer 7 : Signal (IN3)				
Layer 8 : Signal (IN4)				
Layer 9 : GND (GND3)				
Laver 10 : Solder (Bottom)				

Super I/O GPIO Pins:

Name	Location	5V Tolerance	Power Well	Default mode	Туре	Function
GPIOA0	M5	Y	+V3.3A	GPI	1	BKLTNESS_UP_BTN
GPIOA1	N5	Υ	+V3.3A	GPI	0	BKLTNESS_DOWN_BTN
GPIOA2	M6	Y	+V3.3A	GPI	0	EC_BKLTNESS
GPIOA3	N6	Y	N.C.	GPI	N.C.	N.C.
GPIOA4	K6	Y	N.C.	GPI	N.C.	N.C.
GPIOA5 GPIOA6	J6 M7	Y	+V3.3A +V3.3A	GPI GPI	0	LAN1_DISABLE# EC_PME#
GPIOA6 GPIOA7	K7	Y	+V3.3A +V3.3A	GPI	H	WAKE_RI#
GPIOR/	A4	Y	+V3.3A	GPI	i i	RXD3#
GPIOB1	A3	Ý	+V3.3A	GPI	Ö	TXD3#
GPIOB2	D2	·	N.C.	GPI	N.C.	N.C.
GPIOB3	B4	Υ	N.C.	GPI	N.C.	N.C.
GPIOB4	A2	Υ	N.C.	GPI	N.C.	N.C.
GPIOB5	F1		+V3.3A	GPI	0	PWR+V12A_EN
GPIOB6	H4		+V3.3A	GPI	0	WDT_RST#
GPIOB7	A1	Y	+V3.3A	GPI	1	ACC_OFF_SET0
GPIOC0 GPIOC1	D1 B3	Y	+V3.3A N.C.	GPI GPI	I N.C	PLT_RST#_3P3
GPIOC2	B2	v v	N.C.	GPI	N.C.	N.C.
GPIOC3	K13		+V3.3A	GPI	14.0.	ACC_ON_SET0
GPIOC4	C2		+V3.3A	GPI	Ö	SEL COM1 MD0
GPIOC5	J10		+V3.3A	GPI	Ť	ACC_ON_SET1
GPIOC6	E1		+V3.3A	GPI	Ö	SEL COM1 MD1
GPIOC7	M2		+V3.3A	GPI	0	PWR_BTN_OUT
GPIOD0	N1		+V3.3A	GPI	1	PM SLP S3# 3P3
GPIOD1	N3	Y	+V3.3A	GPI	-	PM_SLP_S4#_3P3
GPIOD2 GPIOD3	M4	Y	+V3.3A +V3.3A	LPCRST# GPI	0	BUF_PLT_RST# EC_SCI#_3P3
	N4 1.2	Y	+V3.3A		0	EC_SCI#_3P3
GPIOD4 GPIOD5	N7	Y	+V3.3A N.C.	GPI GPI	N.C.	EC_SMI#_3P3 N.C.
GPIOD5 GPIOD6	M11	'	N.C.	GPI	N.C.	N.C.
GPIOD7	M12		N.C.	GPI	N.C.	N.C.
GPIOE0	N2	Y	+V5S	GPI	0	DO 1
GPIOE1	A13	Ý	+V5S	GPI	ō	DO 2
GPIOE2	A12	Y	+V3.3A	GPI	0	81438 SD#
GPIOE3	B12	Y	+V3.3A	GPI	0	AMP_SD#
GPIOE4	E2		+V3.3A	GPI	1	PWSIN#
GPIOE5	N8	Y	+V3.3A	GPI	1	ACC_INT# EC_PWRON_PMIC
GPIOE6 GPIOE7	M1	Y	GND	GPI GPI	0	PSON#
	M3	Y	+V3.3A			
GPIOF0 GPIOF1	A11 B11	Y	+V5S +V5S	GPI GPI		DI_1 DI_2
GPIOF2	A10	Ÿ	+V3.3A	GPI	6	EN LISE
GPIOF3	B10	Ÿ	+V3.3A	GPI	ŏ	EN_USB DIS_TOUCH#
GPIOF4	D9	Ý	+V5S	GPI	ΤŤ	DI 3
GPIOF5	B9	Υ	+V5S	GPI	1	DI 4
GPIOF6	B1		N.C.	GPI	N.C.	N.C.
GPIOF7	C1		N.C.	GPI	N.C.	N.C.
GPIOG0	E6	Υ		GPO,H don't pull up	0	W_DISABLE0#
GPIOG1	A5		N.C.	GPO,L /ID7	N.C.	N.C.
GPIOG2	E7			GPI, don't pull up	0	W_DISABLE1#
GPIOG6	D6	Y	N.C.	GPI, don't pull up	N.C.	N.C.
GPIOH0	D8	Y	+V3.3A	GPI/ID0	0	EC_RSMRST#
GPIOH1 GPIOH2	E8 D7	Y	+V3.3A +V3.3A	GPI/ID1 GPI/ID2	6	RXD4# TXD4#
GPIOH3	A9	Y	N.C.	GPI/ID3	N.C.	N.C.
GPIOH4	B8	Ý	N.C.	GPI/ID4	N.C.	N.C.
GPIOH5	A8	Ý	N.C.	GPI/ID5	N.C.	N.C.
GPIOH6	B7	Y	N.C.	GPI/ID6	N.C.	N.C.
GPIOI0	G10			GPI	I	CPU_Temperature
GPIOI1	G13			GPI		SYS_Temperature
GPIOI2	G12			GPI	1	VIN_12V
GPIOI3 GPIOI4	F9 F13			GPI GPI	H	VIN_5V VIN_VDIMM
GPIOI4 GPIOI5	F13		N.C.	GPI	N.C.	N.C.
GPIOIS GPIOI6	F10		IN.O.	GPI	IN.C.	VIN_VCORE
GPIOI7	E13		N.C.	GPI	N.C.	N.C.
GPIOJ0	E12		N.C.	GPI	N.C.	N.C.
GPIOJ1	D13			GPI	1	AutoBtn# SEL
GPIOJ2	D12		N.C.	GPI	N.C.	N.C.
GPIOJ3	C13		N.C.	GPI	N.C.	N.C.
GPIOJ4	B13		+V3.3A	CK32K	0	ADM213_EN
GPIOJ5	C12		+V3.3A	CK32KE	0	COM1_SLEW
GPIOM0	K1			LAD0	1/0	LPC_AD0 LPC_AD1
GPIOM1 GPIOM2	J2 J1			LAD1 LAD2	1/0	LPC_AD1 LPC_AD2
GPIOM2 GPIOM3	J1 H2			LAD2 LAD3	1/0	LPC_AD2 LPC_AD3
GPIOM3	K2			LPCCLK	1/0	CLK_EC_25M
GPIOM5	H1			LFRAME#	H	LPC FRAME#
GPIOM6	G2			SERIRQ	I/O	INT SERIRQ 3P3
			•			

F75111 GPIO Pins:

Name	Location	5V Tolerance	Power Well	Default mode	Туре	Function
GPIOA1	M6	Y	+V3.3A	GPI	1	BKLTNESS UP BTN
GPIOA2	N6	Y	+V3.3A	GPI	0	BKLTNESS DOWN BTN
GPIOA3	M7	Y	+V3.3A	GPI	0	EC BKLTNESS
GPIOA4	N7	Y	N.C.	GPI	N.C.	N.C.
GPIOA5	K7	Y	N.C.	GPI	N.C.	N.C.
GPIOA6	J7	Y	+V3.3A	GPI	0	LAN1 DISABLE#
GPIOA7	M8	Y	+V3.3A	GPI	1	EC PME#
GPIOA8	K8	Y	+V3.3A	GPI	1	WAKE RI#
GPIOB1	A5	Y	+V3.3A	GPI	- 1	RXD3#
GPIOB2	A4	Y	+V3.3A	GPI	0	TXD3#
GPIOB3	D3		N.C.	GPI	N.C.	N.C.
GPIOB4	B5	Y	N.C.	GPI	N.C.	N.C.
GPIOB5	A3	Y	N.C.	GPI	N.C.	N.C.
GPIOB6	F2		+V3.3A	GPI	0	PWR+V12A EN
GPIOB7	H5		+V3.3A	GPI	0	WDT RST#
GPIOB8	A2	Y	+V3.3A	GPI	1	ACC OFF SET1
GPIOC1	D2	Y	+V3.3A	GPI	- 1	PLT RST# 3P4
GPIOC2	B4	Y	N.C.	GPI	N.C.	N.C.
GPIOC3	B3	Y	N.C.	GPI	N.C.	N.C.
GPIOC4	K14		+V3.3A	GPI	1	ACC ON SET1
GPIOC5	C3		+V3.3A	GPI	0	SEL_COM1_MD1
GPIOC6	J11		+V3.3A	GPI	1	ACC ON SET2
GPIOC7	E2		+V3.3A	GPI	0	SEL_COM1_MD2
GPIOC8	M3		+V3.3A	GPI	0	PWR_BTN_OUT
GPIOD1	N2		+V3.3A	GPI	- 1	PM SLP S3# 3P4
GPIOD2	N4	Y	+V3.3A	GPI		PM SLP S4# 3P4
GPIOD3	M5	Y	+V3.3A	LPCRST#	1	BUF_PLT_RST#
GPIOD4	N5	Y	+V3.3A	GPI	0	EC_SCI#_3P4
GPIOD5	L3		+V3.3A	GPI	0	EC_SMI#_3P4
GPIOD6	N8	Y	N.C.	GPI	N.C.	N.C.
GPIOD7	M12		N.C.	GPI	N.C.	N.C.
GPIOD8	M13		N.C.	GPI	N.C.	N.C.

+V5A Power consumption

Device	Current(A)
USB3.0*3 + USB2.0*2(pin header)	2.7
+VDDQ/+VDDQ_VTT Power	1.71
+V5S	5.55
Total	9.96

+V5S Power consumption

Device	Current(A)
eDP/LVDS panel	1
COM port *2	1
DIO	0.5
SATA	1
Audio ALC269	1.05
HDMI *2	1
Total	5.55

+V3P3A Power consumption

۰	VOI DAT OWEI COIISUI	приоп
	Device	Current(A)
	WhiskeyLake	0.203
	RTL8111G *2	0.74
	M.2(Key E)	2
	Super I/O F81804	0.01
	+V1P8A Power	2
	+VDDQ_VPP	1
	PCIE [x4]	0.375
	+V3P3S	6.159
	Total	12.487

+V3P3S Power consumption

Device	Current(A)
Audio ALC269	0.035
Super I/O F81804	0.01
M.2(Key B)	1
eDP/LVDS panel	1.5
PTN3460	0.57
PTN3365 *2	0.044
PCIE [x4]	3
Total	6.159

+V12A Power consumption

Device	Current(A)
+V5A Power	
+V3P3A Power	
+VCCCORE Power	
+V1P05A Power	
+VCCIO Power	
+V12S	4.6
Total	2

v 123 Power Consum	Juon
Device	Current(A)
LVDS panel	1
FAN	0.5
COM port *2	1
PCIE [x4]	2.1
Total	4.0

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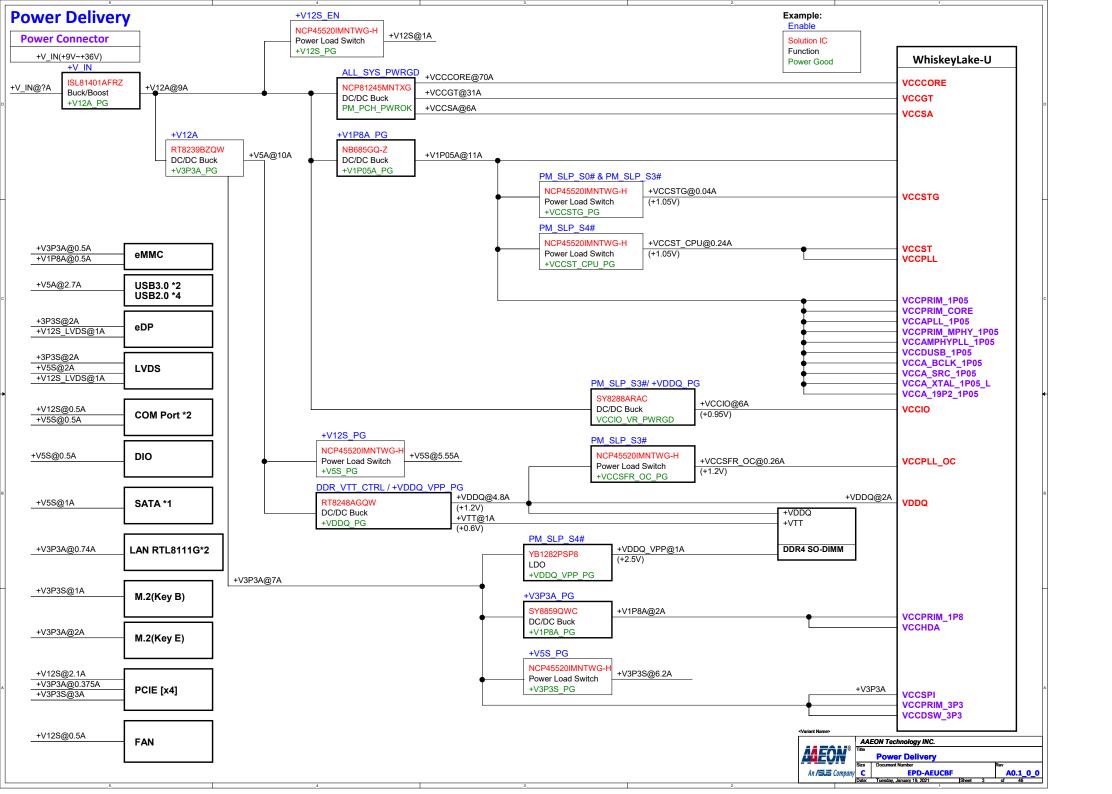


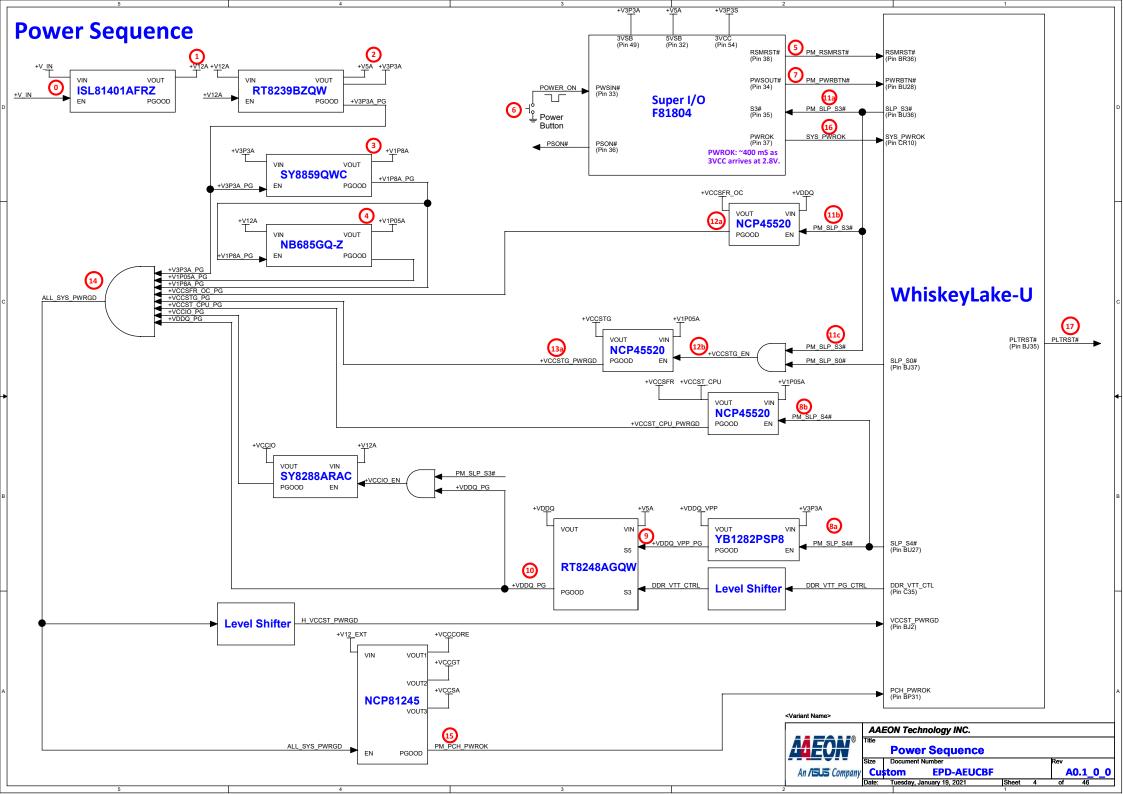
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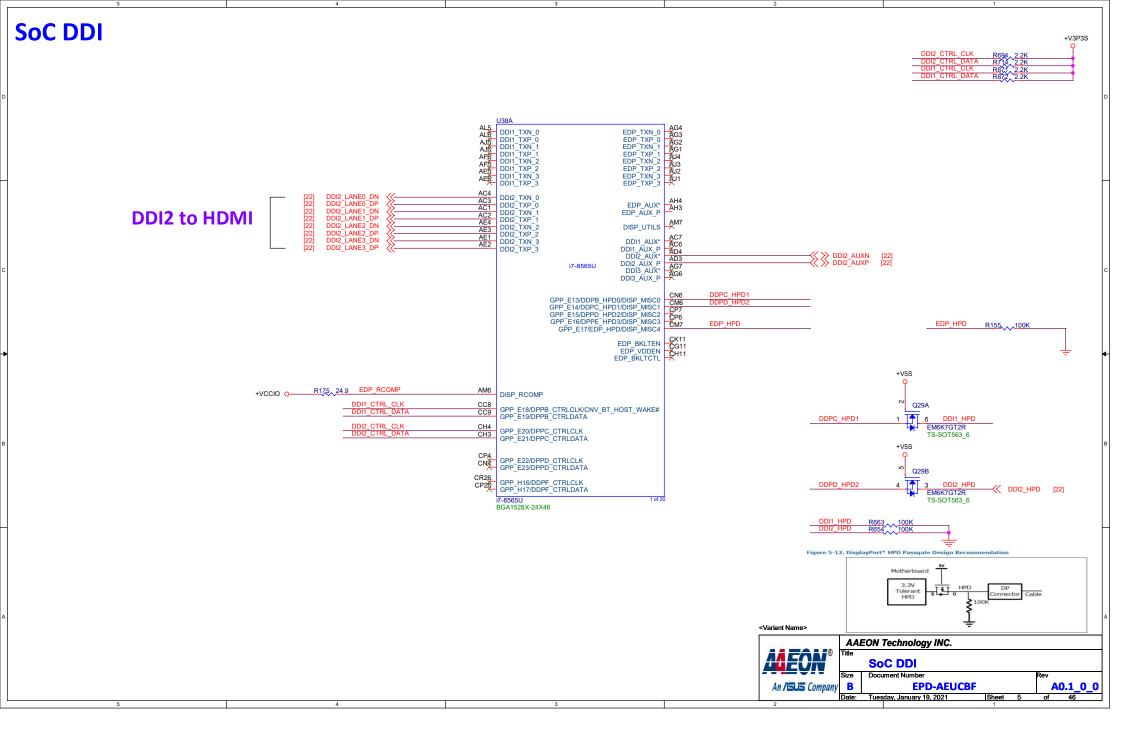
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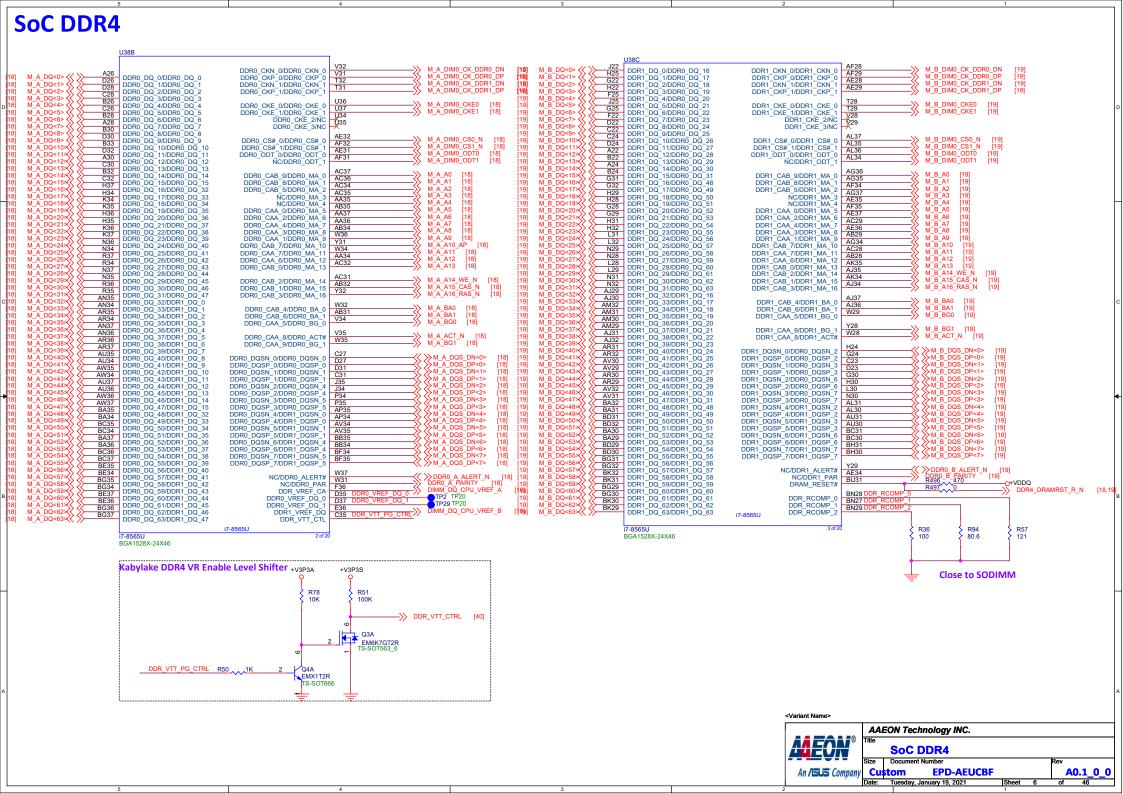
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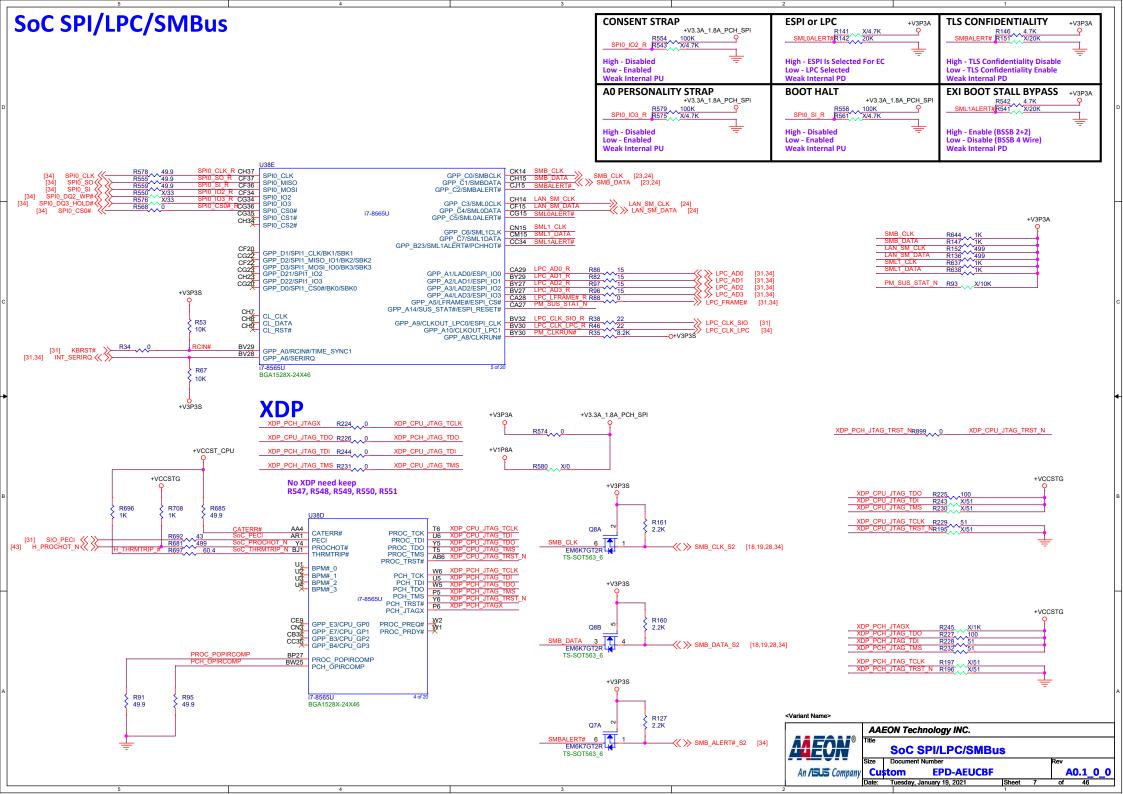
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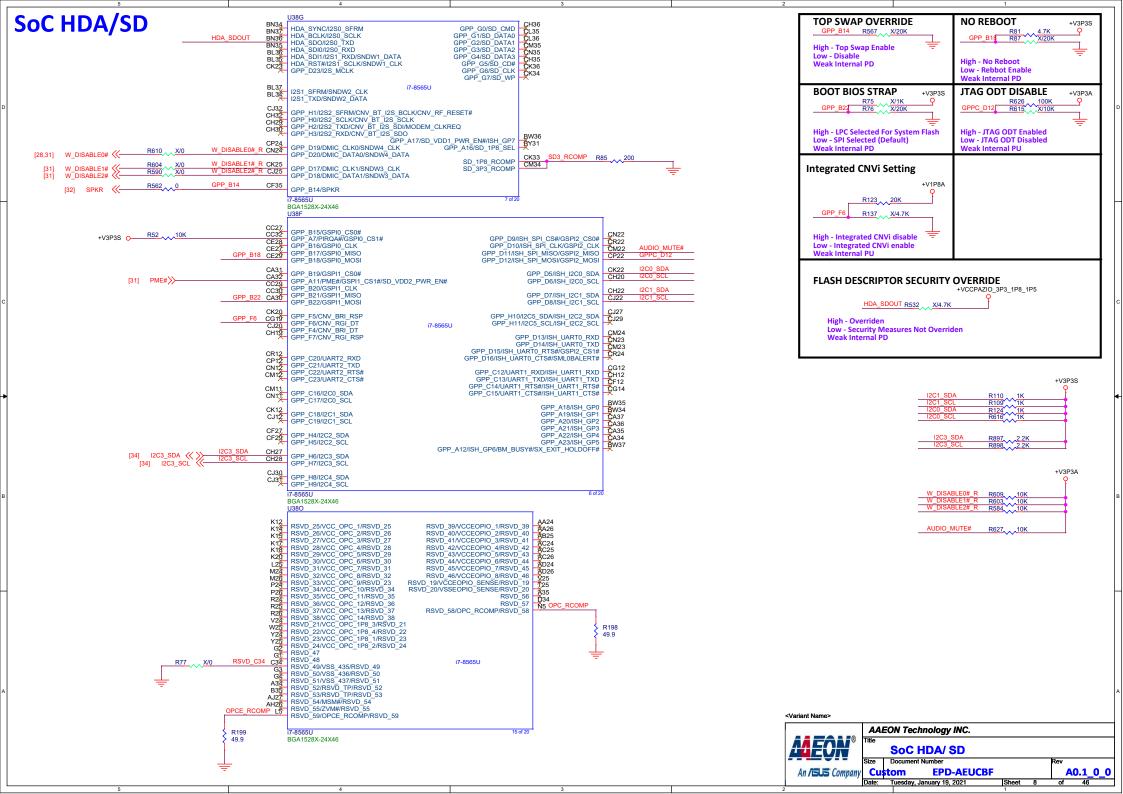


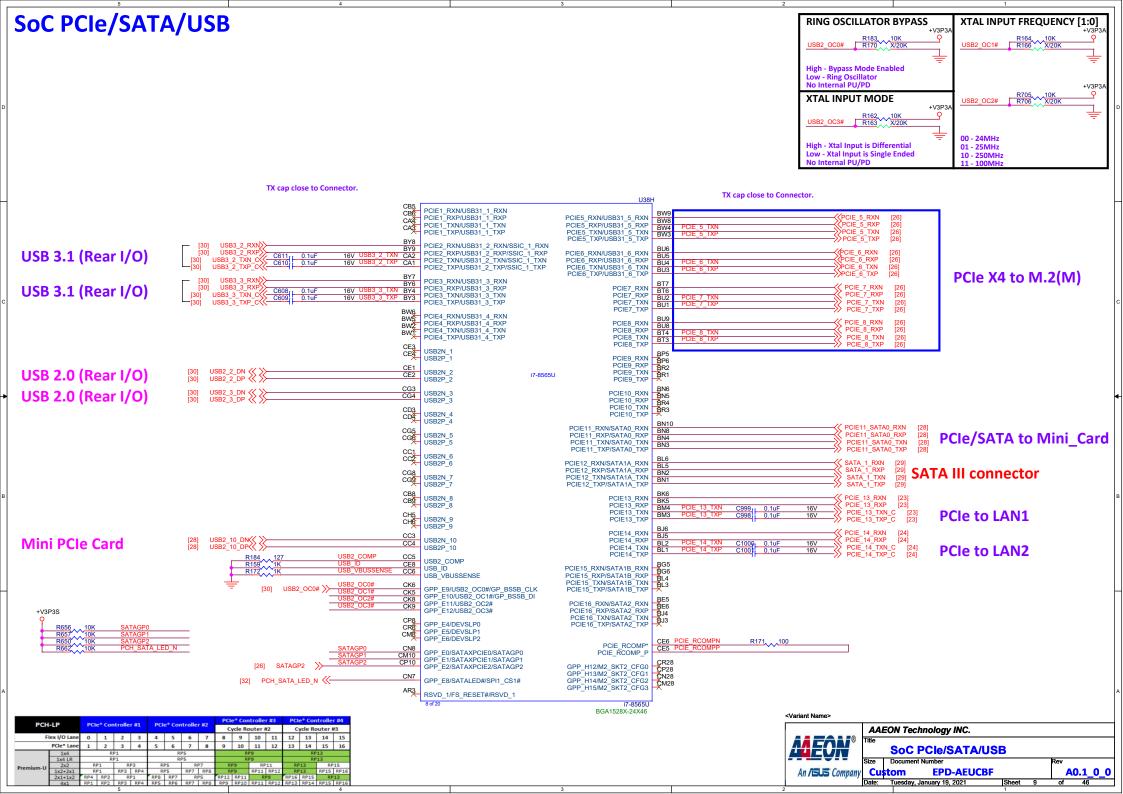


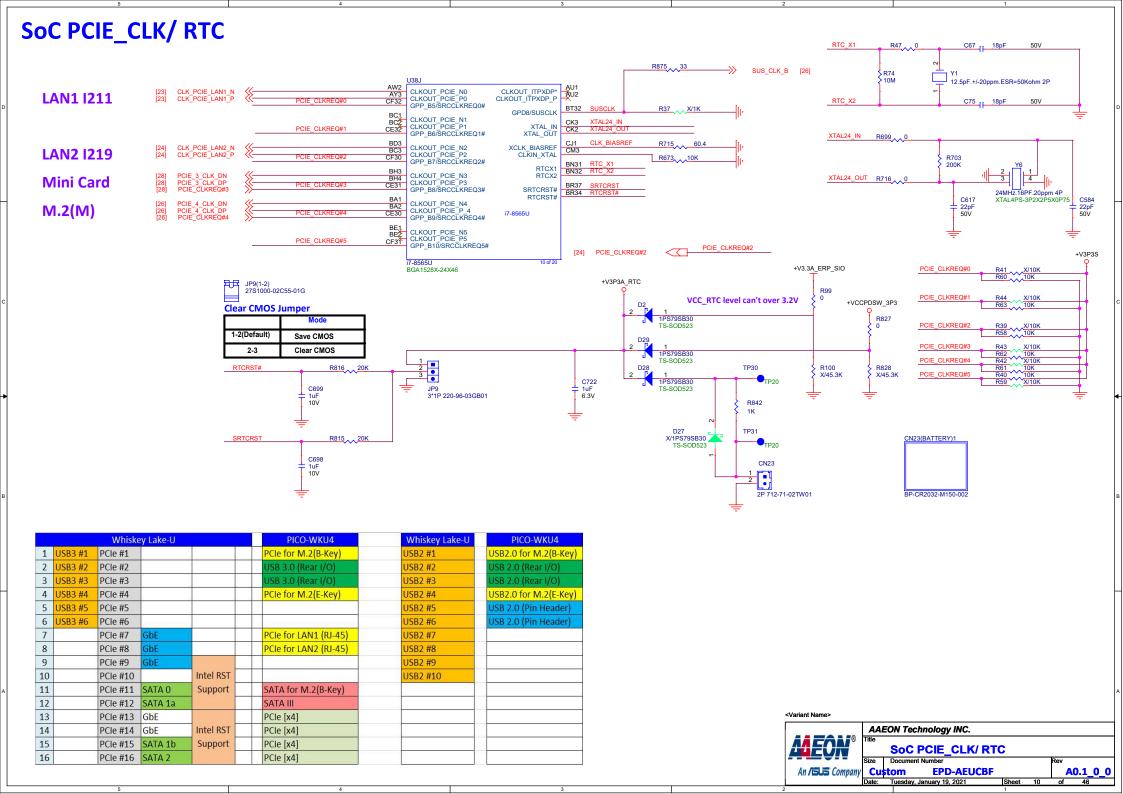






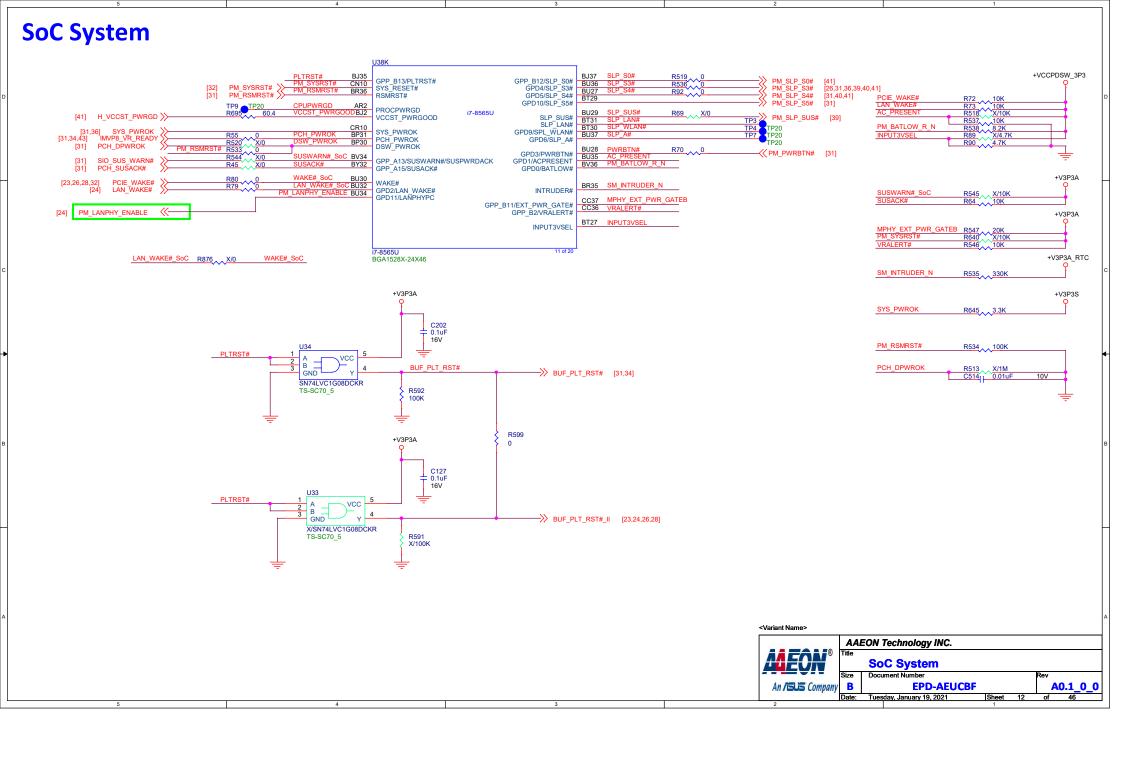


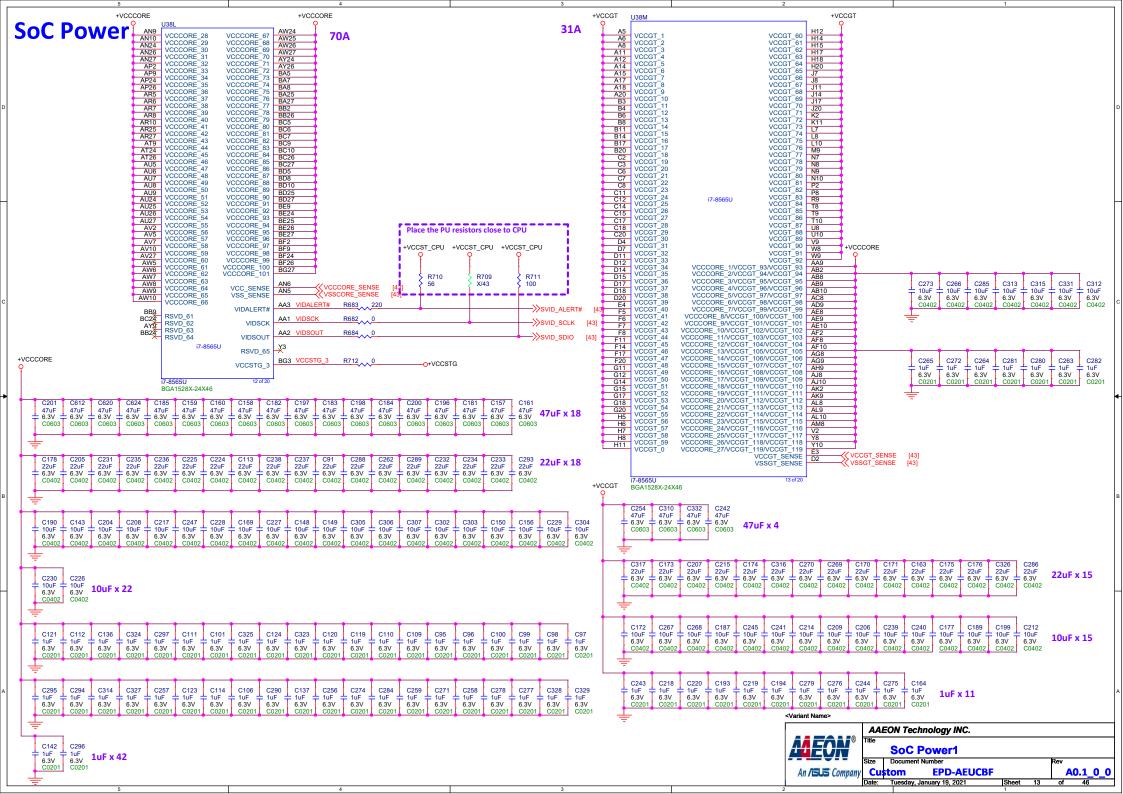


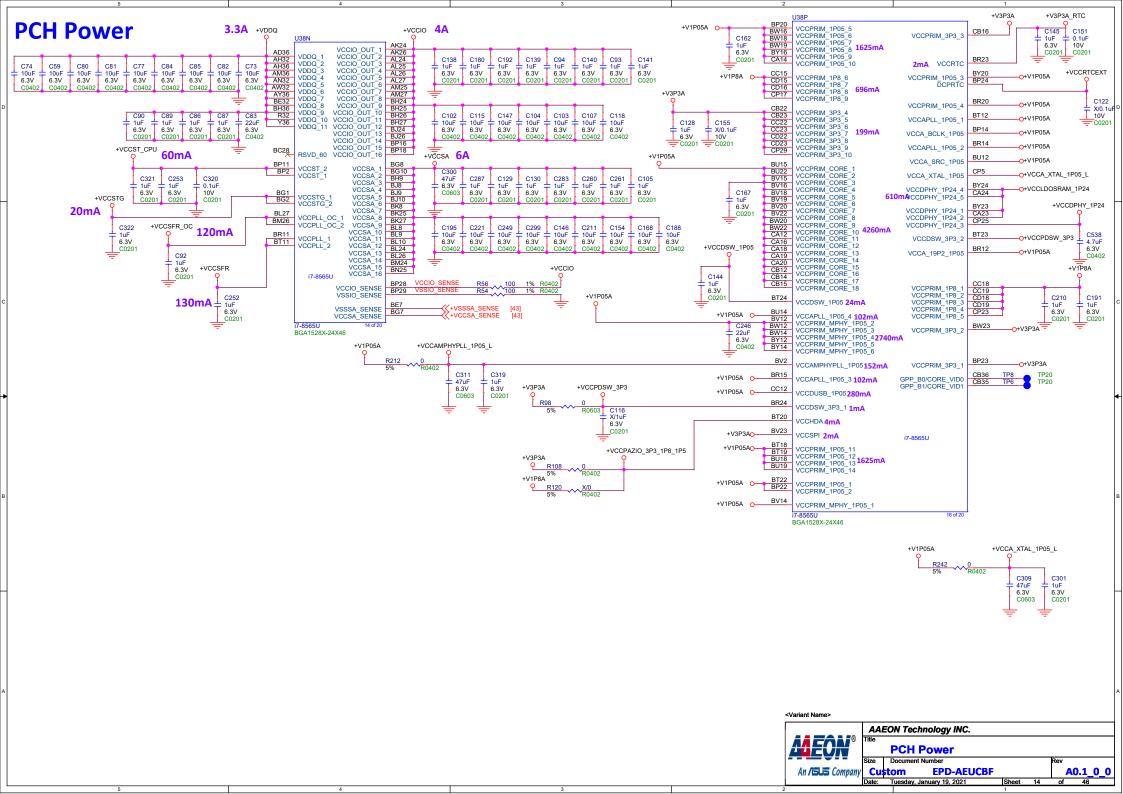


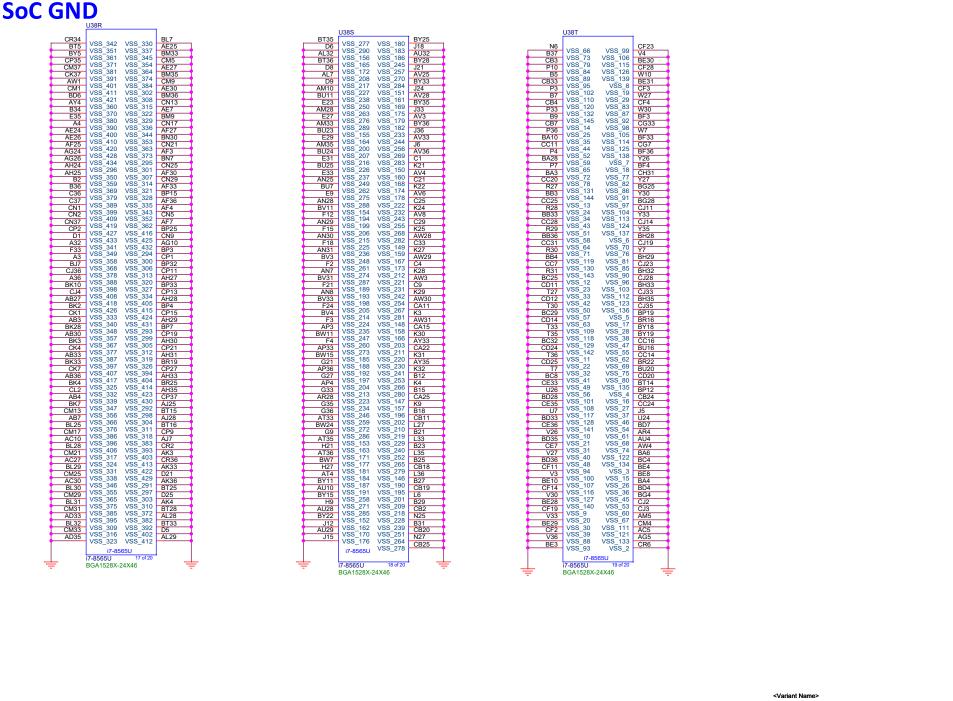
XTAL INPUT MODE XTAL FREQUENCY SELECT +V3P3A R106 4.7K R107 X/20H +V1P8A **SoC eMMC** R516 X/10K R539 X/20K High - 24MHz Low - 38.4/19.2MHz (Default) High - Xtal is Attached Low - Xtal Input is Single Ended CNV_WR_D0N CNV_WR_D0P MAF/SAF STRAP GPP_H18/CPU_C10_GATE# → GPPC_H18_VCCIO_LPM [41] CNV_WR_D1N CNV_WR_D1P +V3P3A GPP_H19/TIMESYNC_0 CN32 CM32 CNV_WT_D0N CNV_WT_D0P CP33 CN33 CNV_WT_D1N CNV_WT_D1P **High - SAF Enabled** Low - MAF Enabled i7-8565U Weak Internal PD BV35 GPD_7 CN31 CP31 CNV_WR_CLKN CNV_WR_CLKP GPD7 GPP_F3 ÇN20 CNV_WT_CLKN CNV_WT_CLKP GPP_D4/IMGCLKOUT0/BK4/SBK4 GPP_H20/IMGCLKOUT1 GPP_F12/EMMC_DATA0 GPP_F13/EMMC_DATA1 GPP_F14/EMMC_DATA2 GPP_F16/EMMC_DATA4 GPP_F16/EMMC_DATA4 GPP_F16/EMMC_DATA4 GPP_F18/EMMC_DATA6 GPP_F18/EMMC_DATA6 CNV_WT_RCOMP1 CP32 CR32 CNV_WT_RCOMP_1 CNV_WT_RCOMP_2 CP20 CK19 GPP_F0/C CG17 GPP_F1 GPP_F2 GPP_F0/CNV_PA_BLANKING CR14 GPP_C8/UART0_RXD GPP_F19/EMMC_DATA7 CP14 GPP_C9/UART0_TXD GPP_C10/UART0_RTS# GPP_C11/UART0_CTS# GPP_F20/EMMC_RCLK GPP_F21/EMMC_CLK GPP_F11/EMMC_CMD GPP_F22/EMMC_RESET# CJ17 CH12 GPP_F8/CNV_MFUART2_RXD GPP_F9/CNV_MFUART2_TXD GPP_F22/EMMC_RESET# CK15 EMMC_RCOMP R138 200 EMMC_RCOMP CF17 GPP_F23/A4WP_PRESENT BGA1528X-24X46









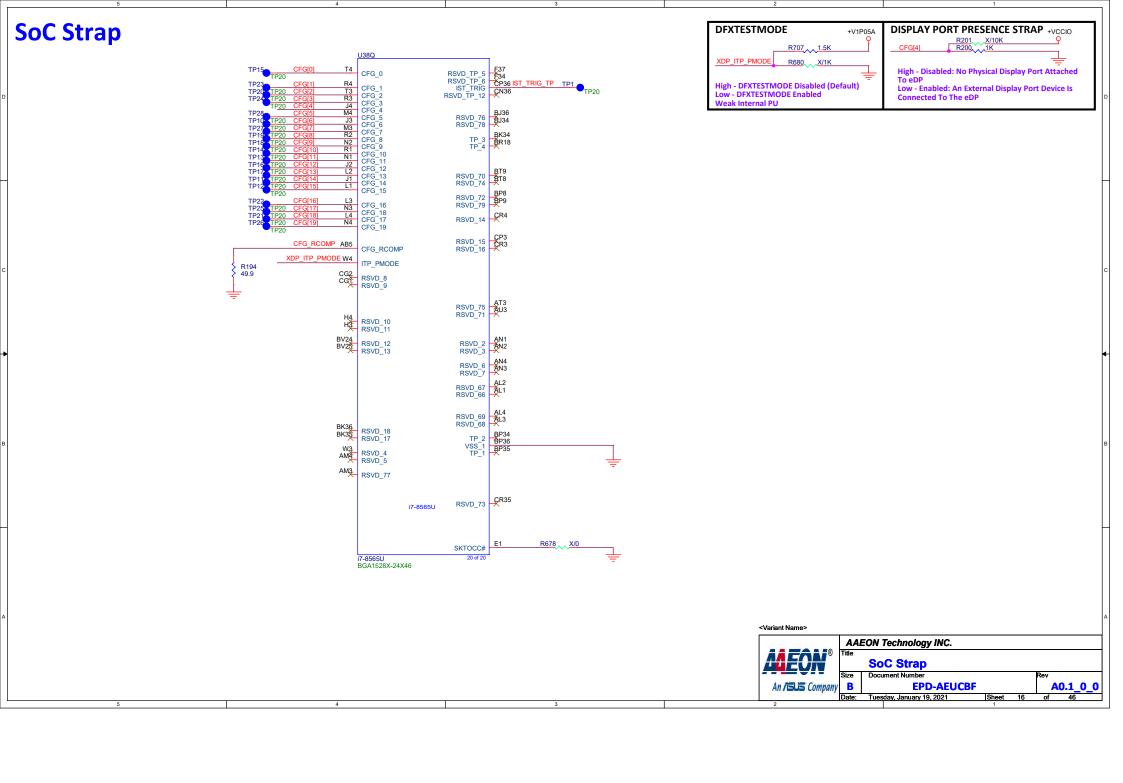


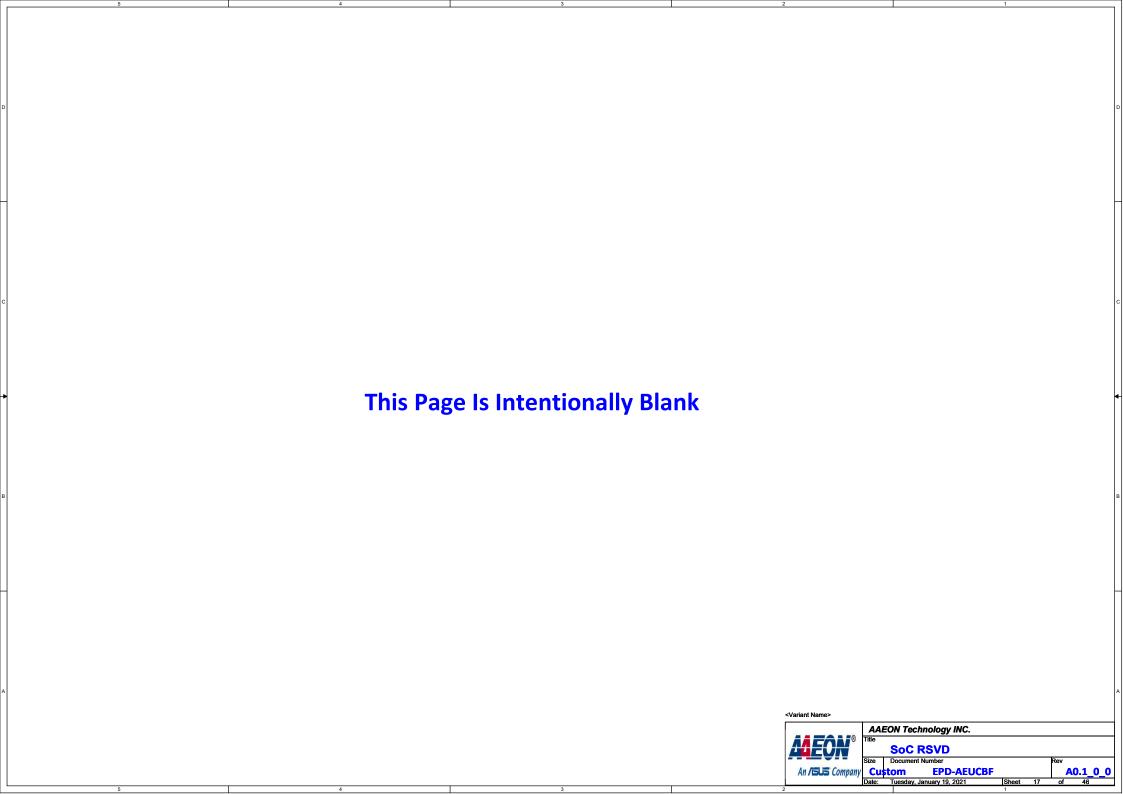
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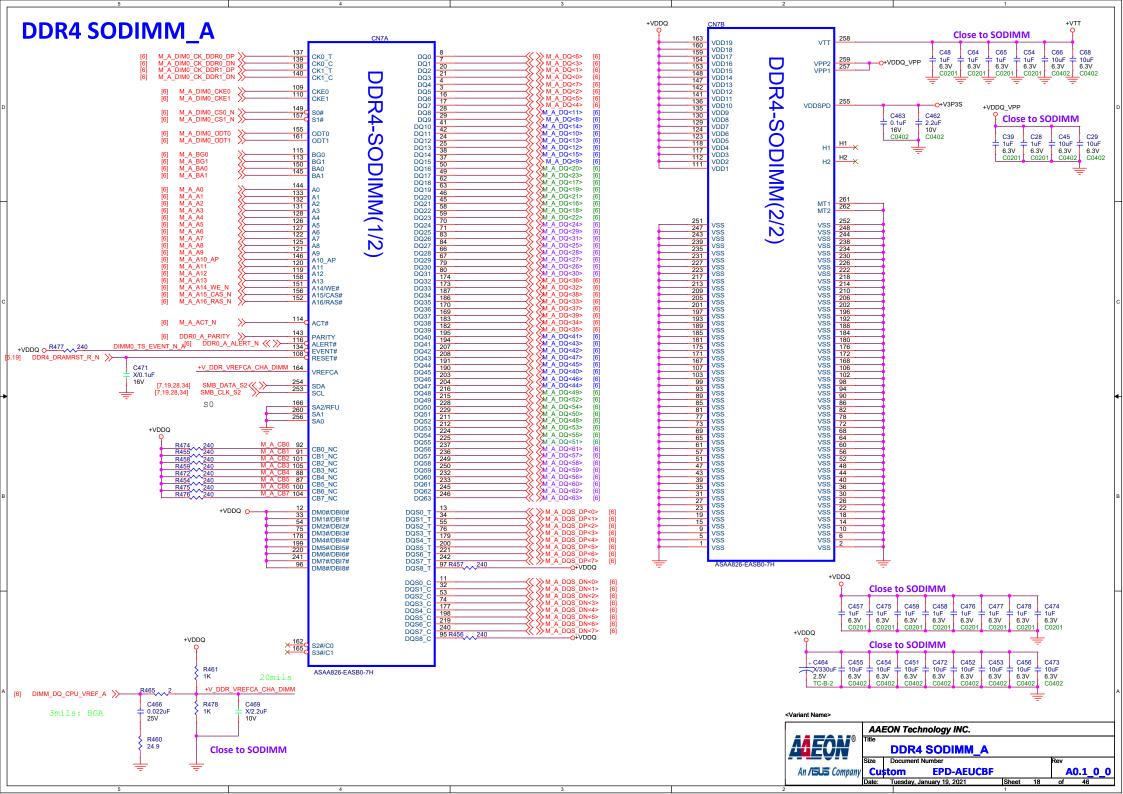
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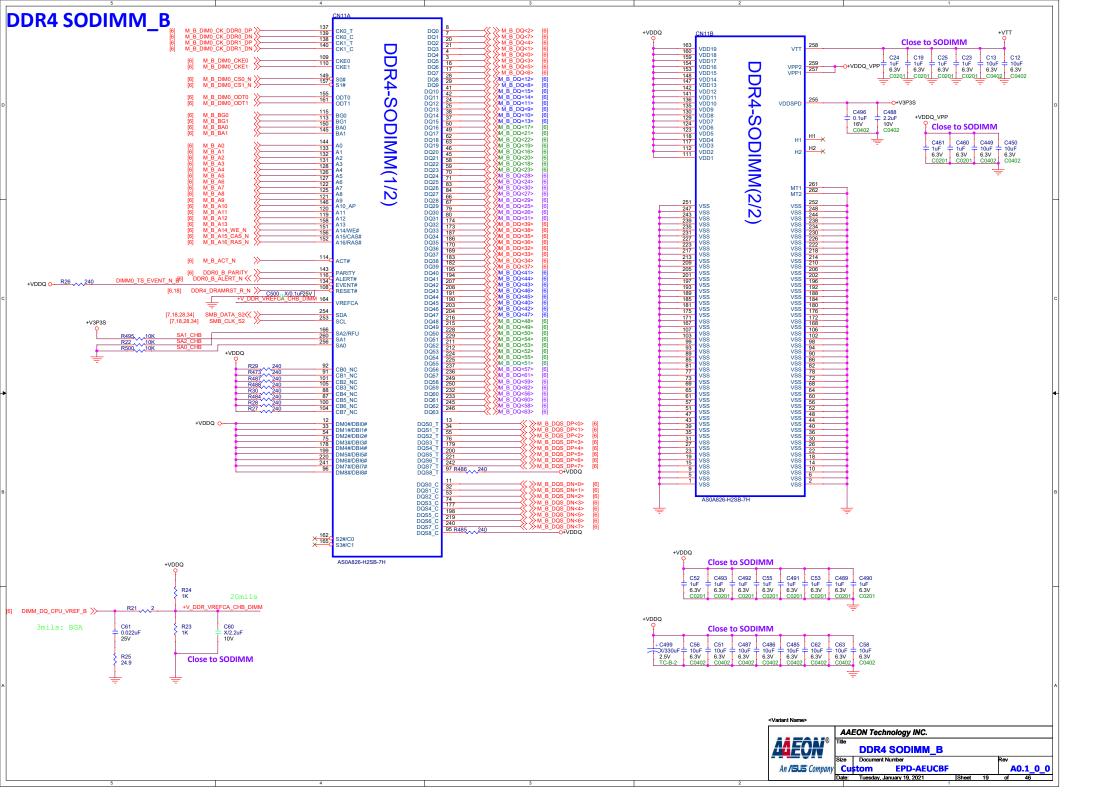
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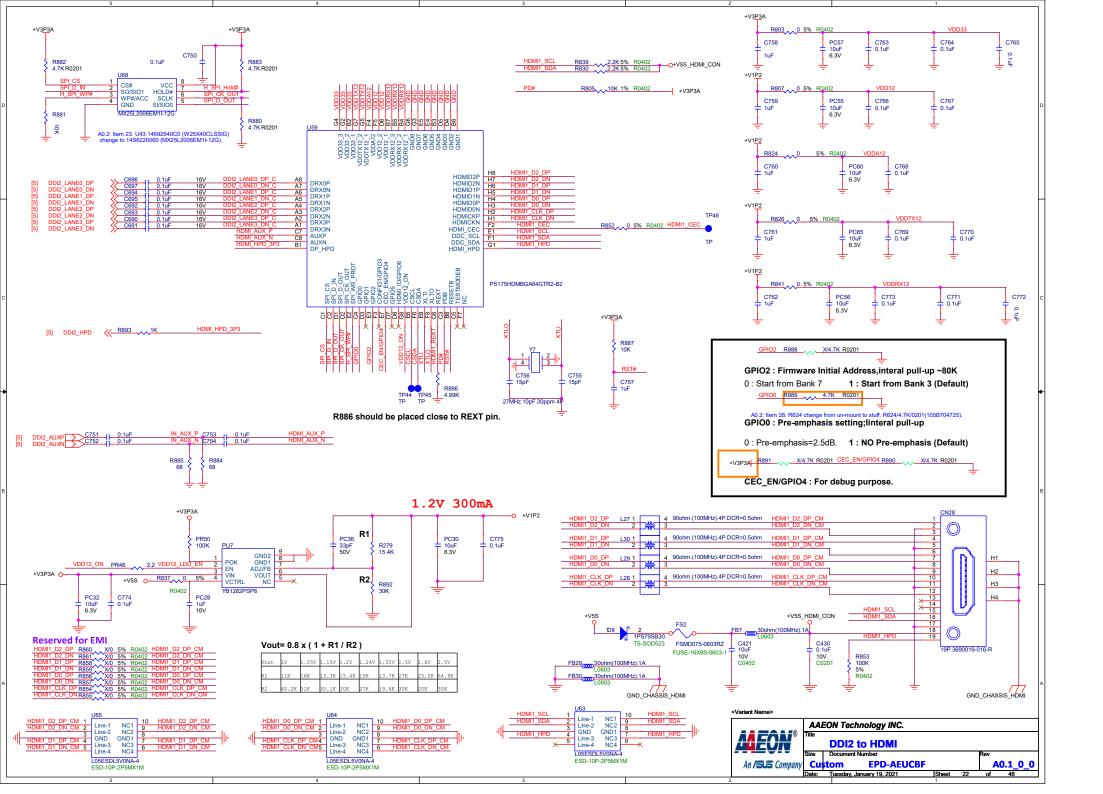


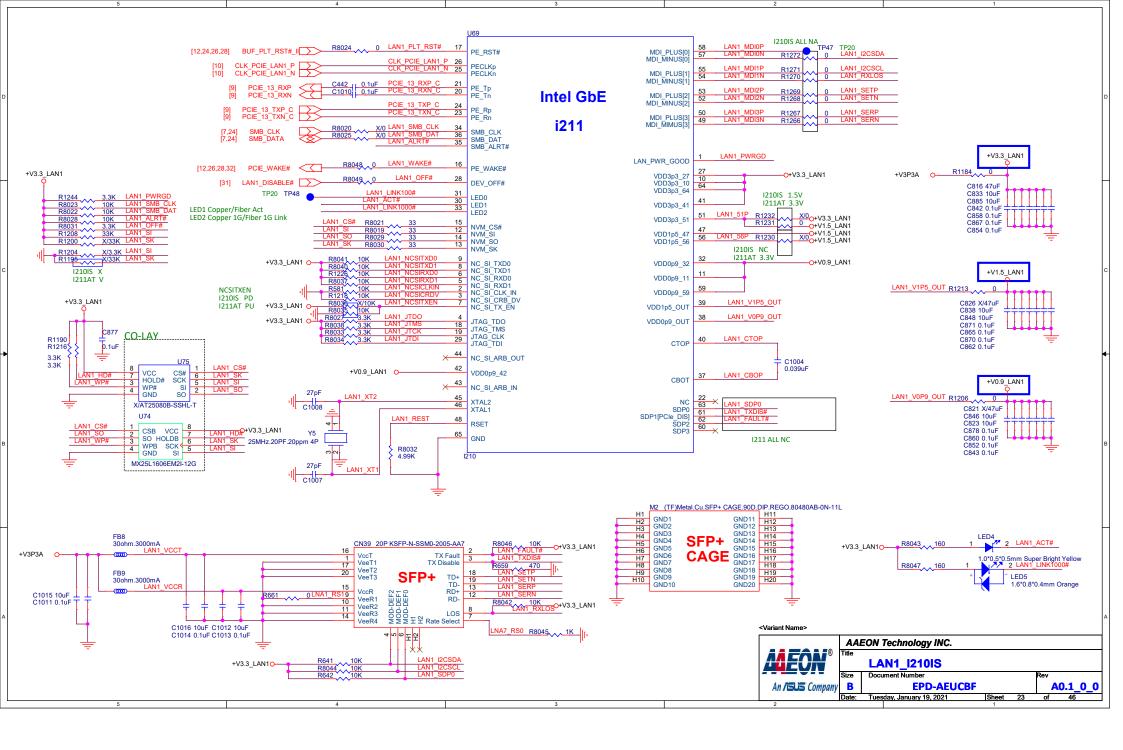
Remove eDP/LVDS

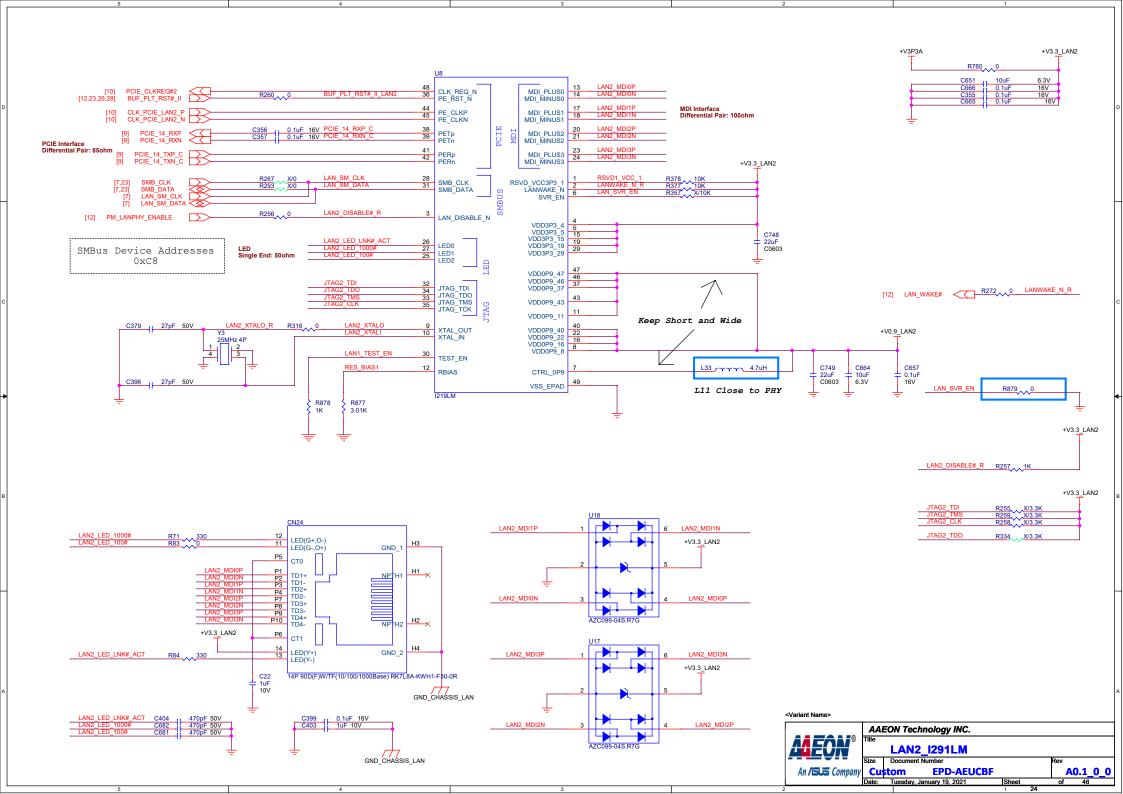
Remove VGA

None
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M.2(Key M) +V3P3S_M2_M CN30 M.2 KEY-M +V3P3S_M2_M R7954 GND_75 10K GND_73 +3.3V_74 69 GND 71 FEDET_OC-PCIE/GND-SATA N/C_67 +3.3V_72 +3.3V_70 68 R8012 0 [9] SATAGP2 <<-SSCLK C948 C953 C952 R8014 10uF 0.1uF 0.1uF X/10K KEY-M R8016 --≪ SUS_CLK_B [10] NC_58 NC_56 PEWAKE# or N/C 52 GND_57 REFCLKP 55 53 51 REFCLKN >> PCIE_WAKE# [12,23,28,32] M2M_CLKREQ# R895 49 GND_51 PETPO/SATA-A+ CLKREQ# or N/C PERST# or N/C PCIE 5 TXP 47 PETNO/SATA-A+ PETNO/SATA-A-# or N/C N/C_48 46 PCIE 5 TXN N/C_48 N/C_46 N/C_44 N/C_42 N/C_42 N/C_40 DEVSLP N/C_36 45 GND_45 PERP0/SATA-B-PCIE_5_RXP +V3P3S 39 PERNO. 37 GND_3 35 PETP1 PCIE_5_RXN PERNO/SATA-R+ GND 39 C992 0.22uF C993 0.22uF PCIE 6 TXP C DEVSLP N/C_36 N/C_34 N/C_32 N/C_30 N/C_26 N/C_26 N/C_24 N/C_24 N/C_24 N/C_22 N/C_20 N/C_24 N/C_24 N/C_24 N/C_24 N/C_30 33 PETN1 GND_33 +V3P3S_M2_M PCIE 6 RXP 29 PERP1 PERN1 27 GND 27 PETP2 25 GND_27 PETP2 PETN2 21 PETN2 C994 0.22uF C995 0.22uF PCIE 7 TXP C PCIE_7_TXN_C +V3P3S M2 M PCIE_7_TXN C945 GND_21 PCIE_7_RXP 19 GND_21 17 PERP2 15 GND_15 C947 C949 PCIE_7_RXP 0.1uF 0.1uF +3.3V_18 +3.3V_16 PCIE_7_RXN R8018 GND 15 13 GND_18 11 PETP3 X/10K +3.3V_14 12 PCIE 8 TXN C DAS/DSS#(I/O)/LED1#(I)(0/3.3V) 8 N/C_8 N/C_6 4 R0402 SSD_LED# [32] R894 X/0 5% GND_9 PERP3 PCIE_8_RXP PCIE 8 RXP 5 PERP3 PERN3 2.5A +V3P3S PCIE_8_RXN 1 GND_3 GND_1 R8017 C938 C950 = 0.1uF C951 D30 AZ5123-01H.R7G 10uF 75P 2E0BC21-S85BM-7H 0.1uF MH2(SCREW)1 MH4 (TF)Screw.M3.0*0.5*4.00mm.IMS.Black.Nylok (TF)BOSS.M3*6.5+1.5mm.Sn <Variant Name> AAEON Technology INC. **44EON® M.2(Key M)**

В

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EPD-AEUCBF

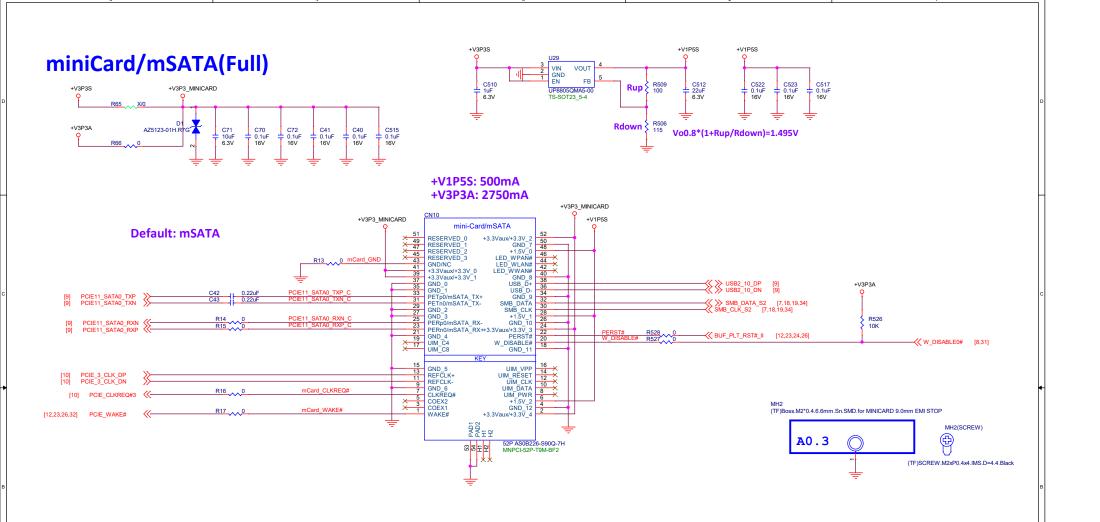
Tuesday, January 19, 2021

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Remove M.2 (Key-E)

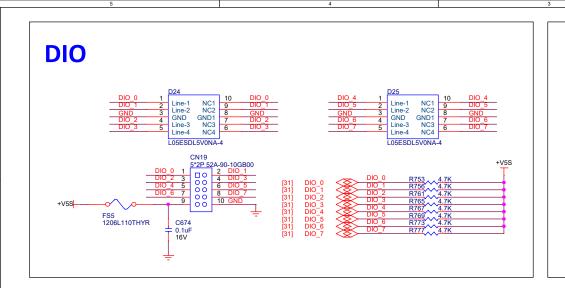
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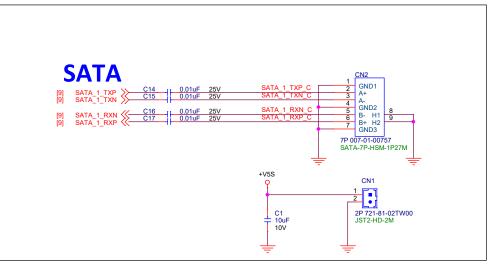
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Remove NanoSIM

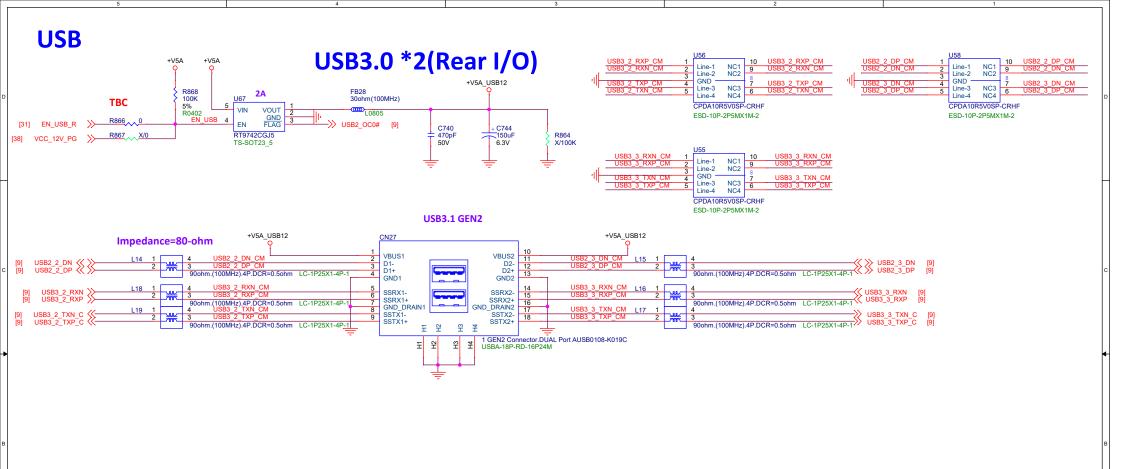
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Title
MiniCard/mSATA(Full)
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<Variant Name>





Remove USB 3.1 x2 + USB 2.0 x2

 AAEON Technology INC.

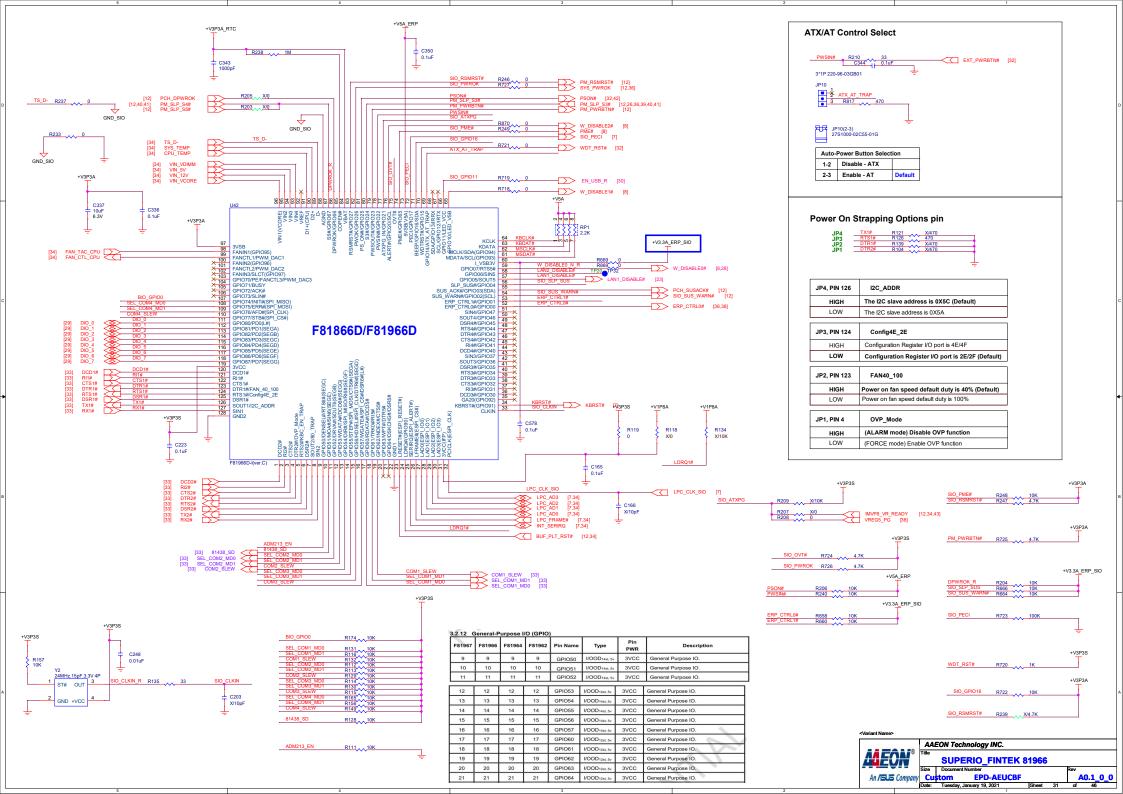
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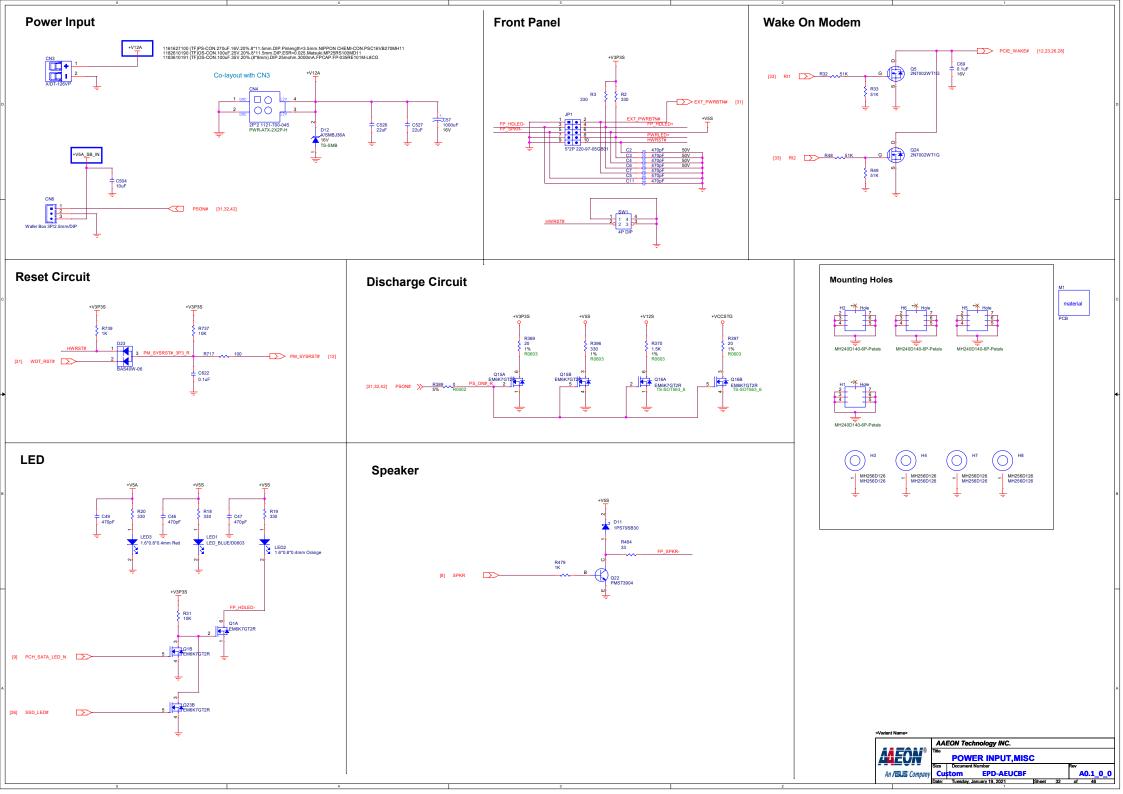
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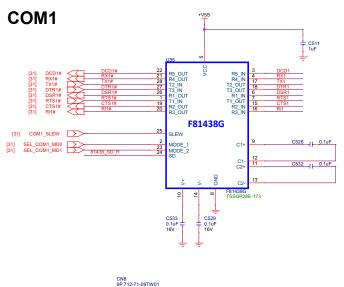
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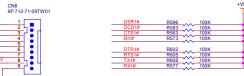
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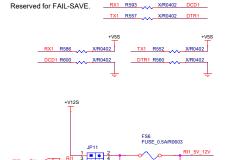
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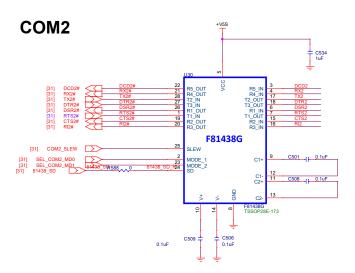


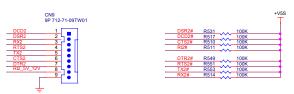




COM2	Pin9 Functio	n Selection
1-2	+12V	
3-4	Ring	Default
5-6	+5V	

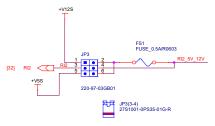
JP11(3-4) 27S1001-0PS35-01G-R











COM2	Pin9 Function	Selection
1-2	+12V	
3-4	Ring	Default
5-6	+5V	

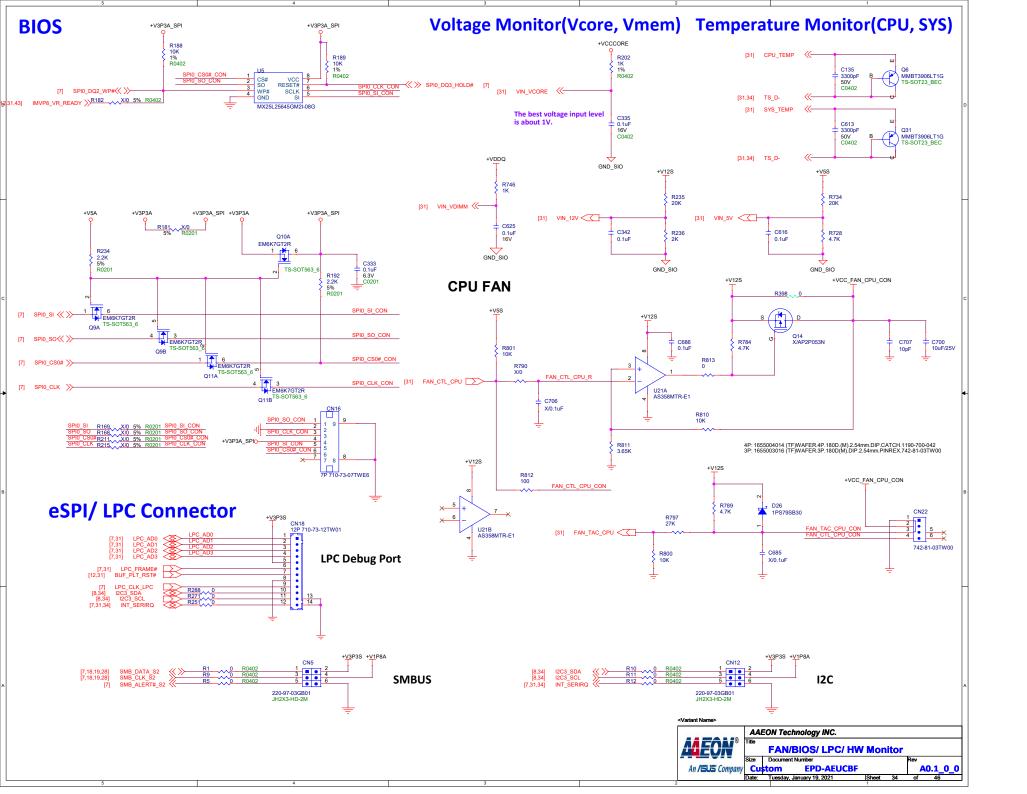
Maximum Slew rate control					
SLEW	RS-232	RS-485/RS-422			
0	1Mbps	10Mbps			
1	250Kbps	250Kbps			

	Pin Mapping					
Ī	RS-232	RS-485	RS-422			
R1_IN	DSR					
T1_OUT	RTS					
T2_OUT	TX		RS422_RX+ (A)			
T3_OUT	DTR		RS422_RX- (B)			
R2_IN	CTS					
R3_IN	RI					
R4_IN	RX	RS485_D+ (A)	RS422_TX+ (A)			
R5_IN	DCD	RS485_D- (B)	RS422_TX- (B)			

Serial	erial Port 2 Mode Selection							
SD	MODE_1	MODE_2	MODE					
0	0	0	RS-422					
0	0	1	RS-232					
0	1	0	RS-485 (Driver Half Duplex)					
0	1	1	RS-485 (Receiver Half Duplex)					
1	Х	Х	Shutdown MODE					

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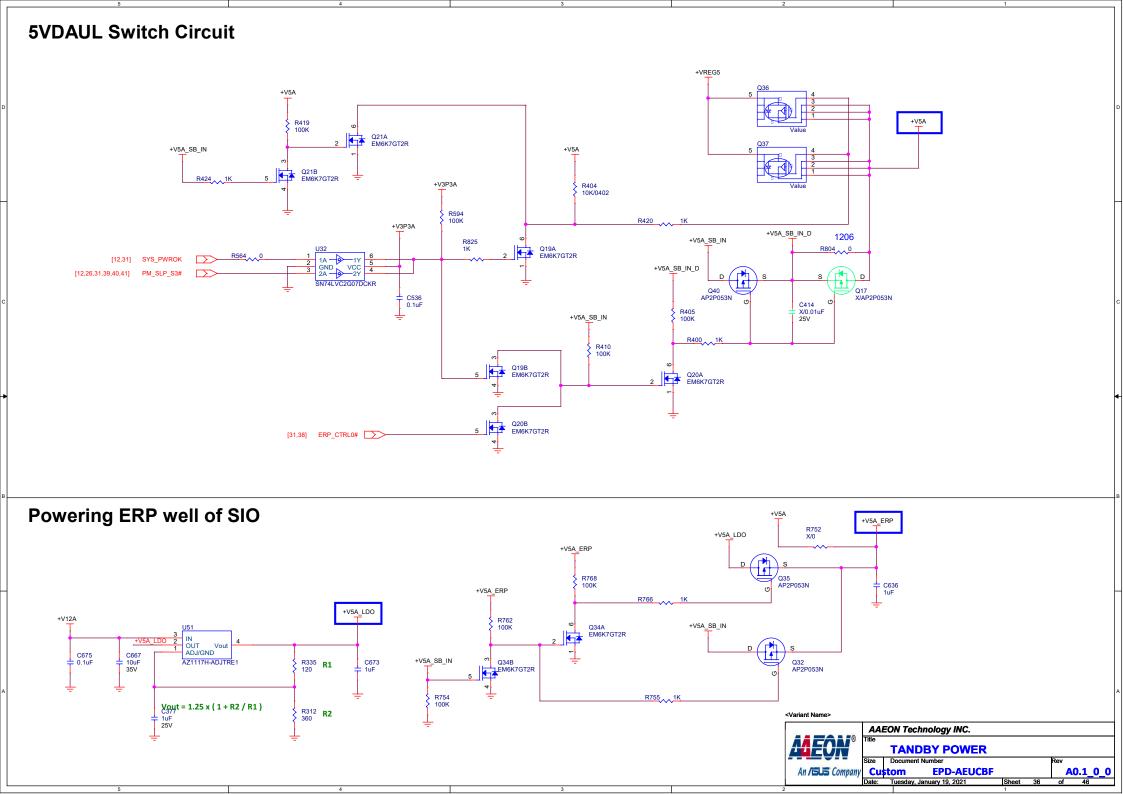


Remove Audio

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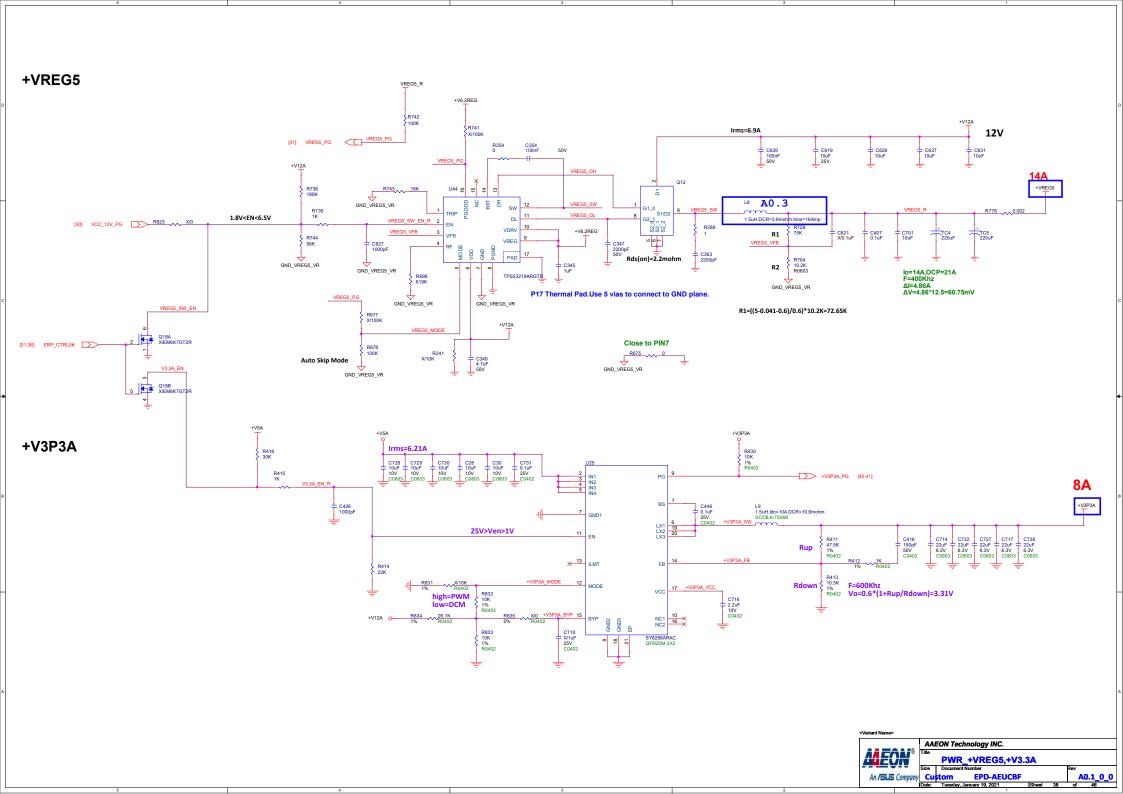


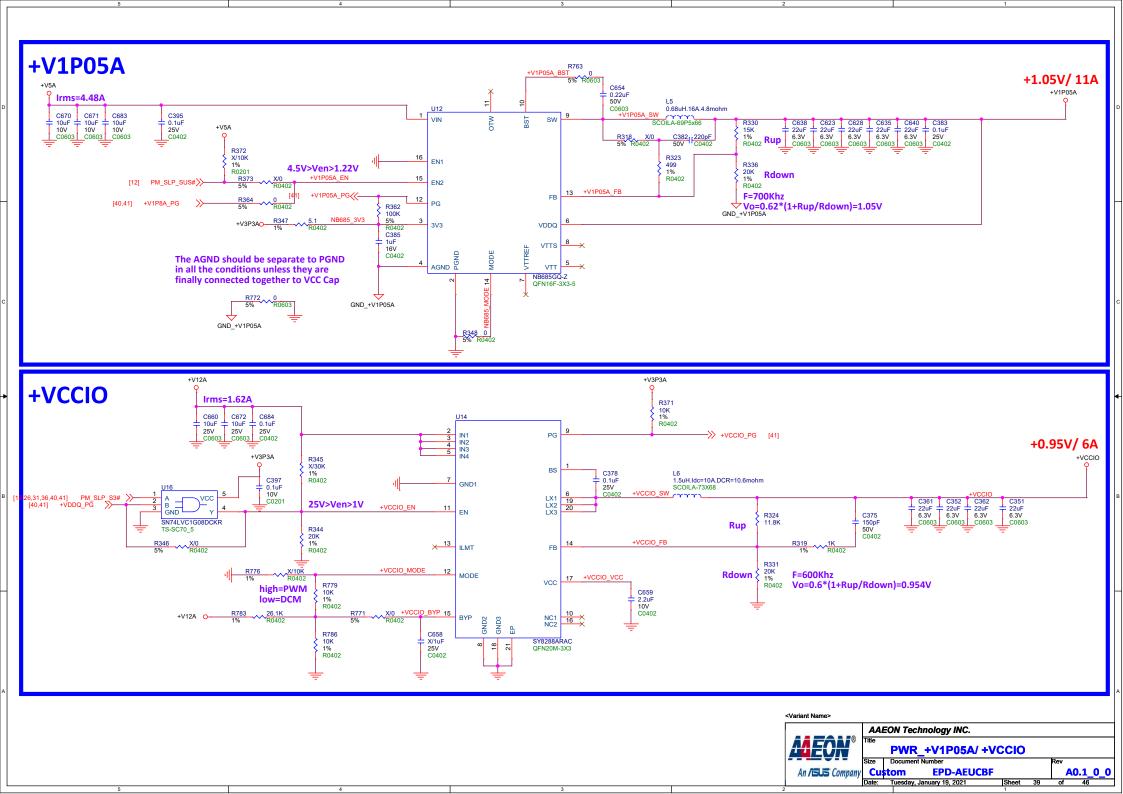
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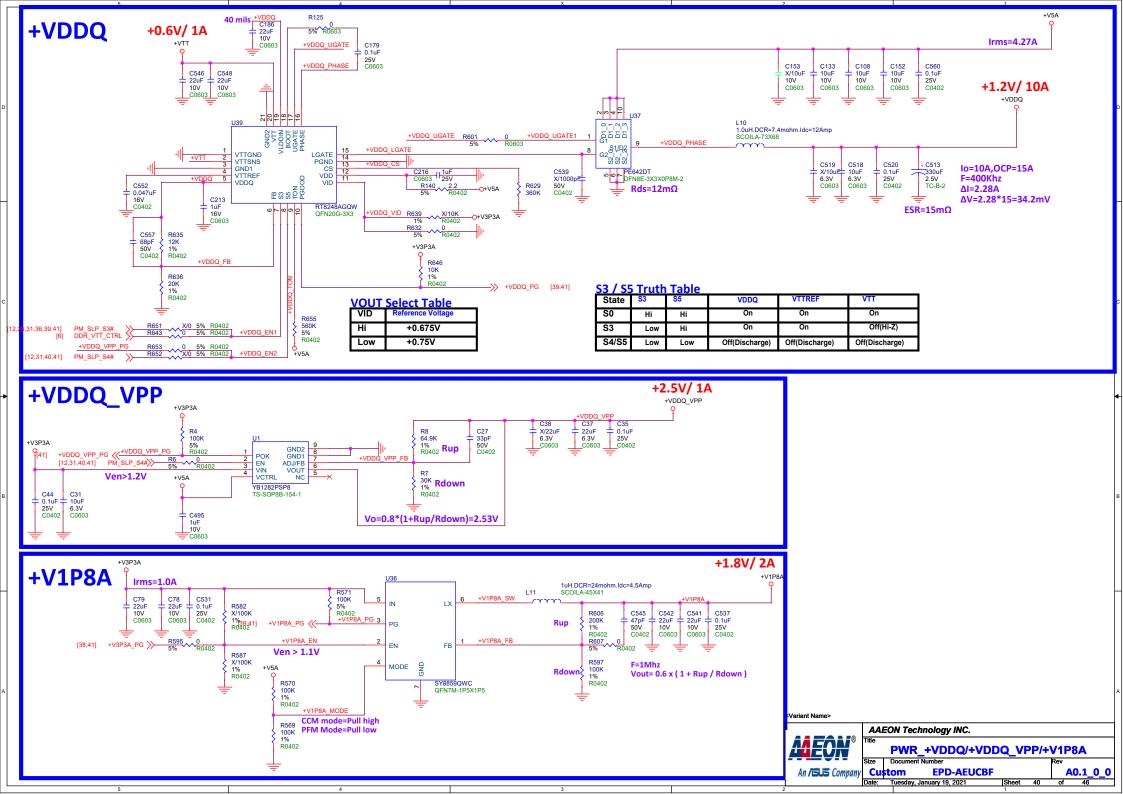


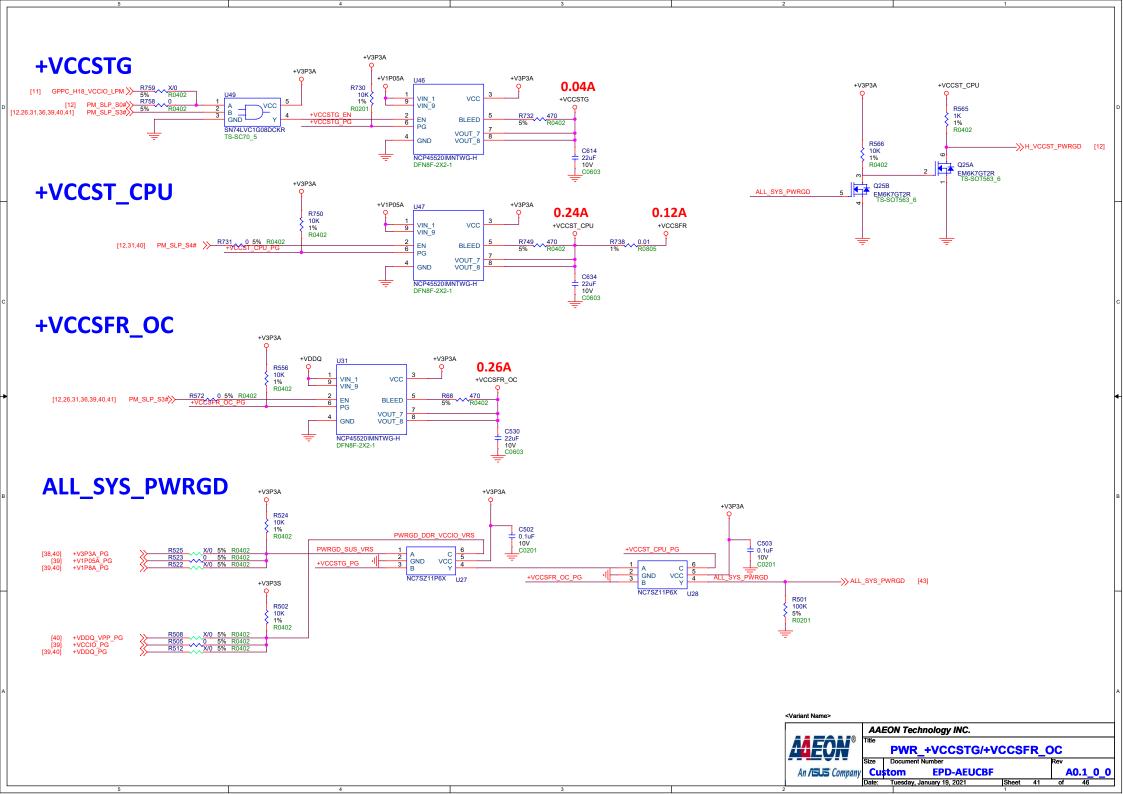
Remove 9~36V input

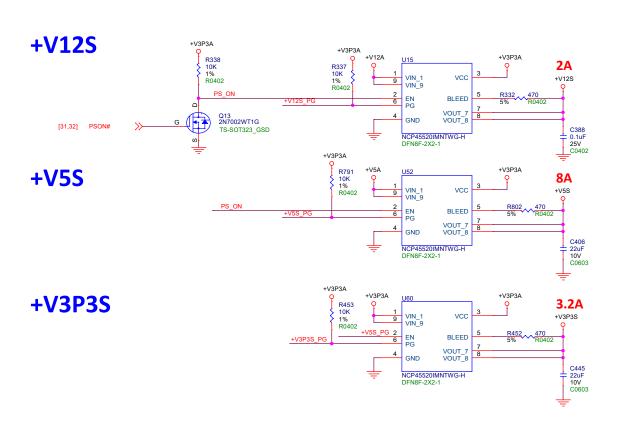
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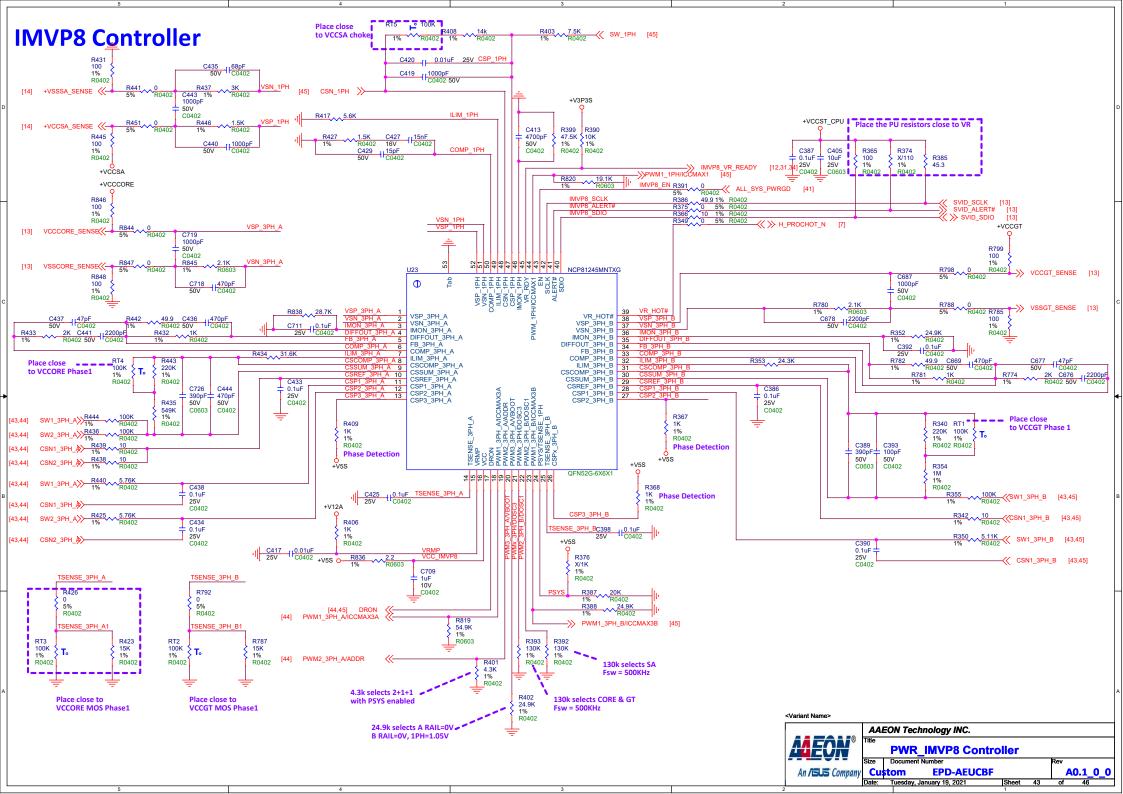


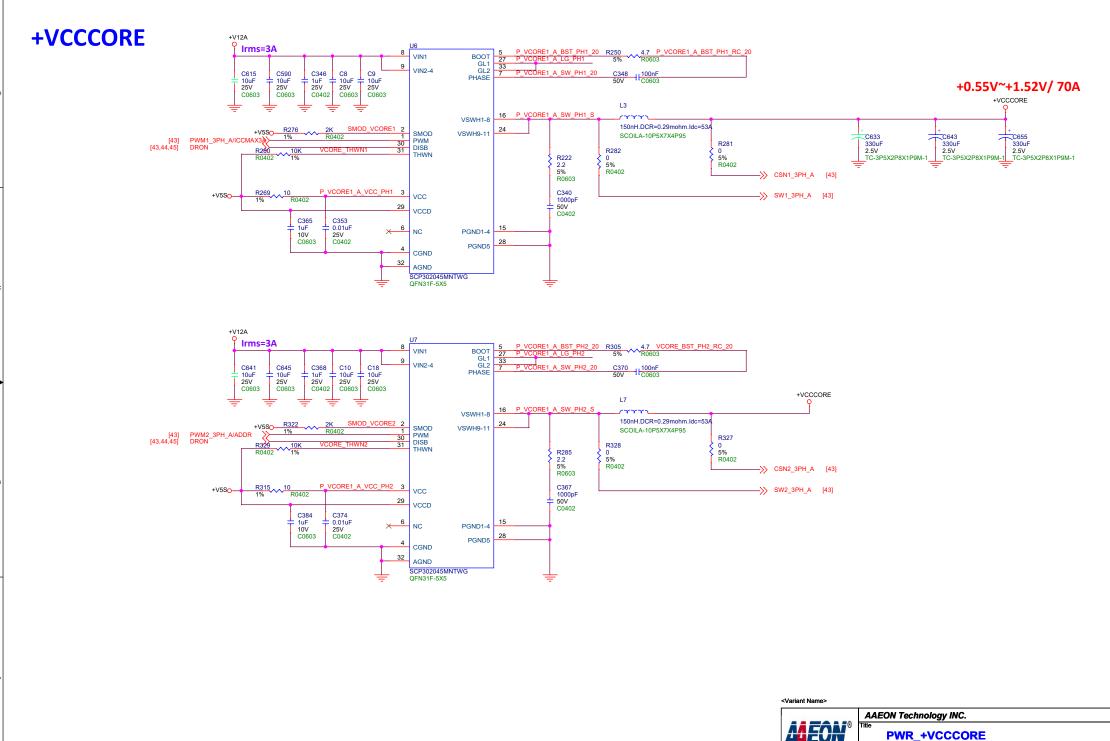






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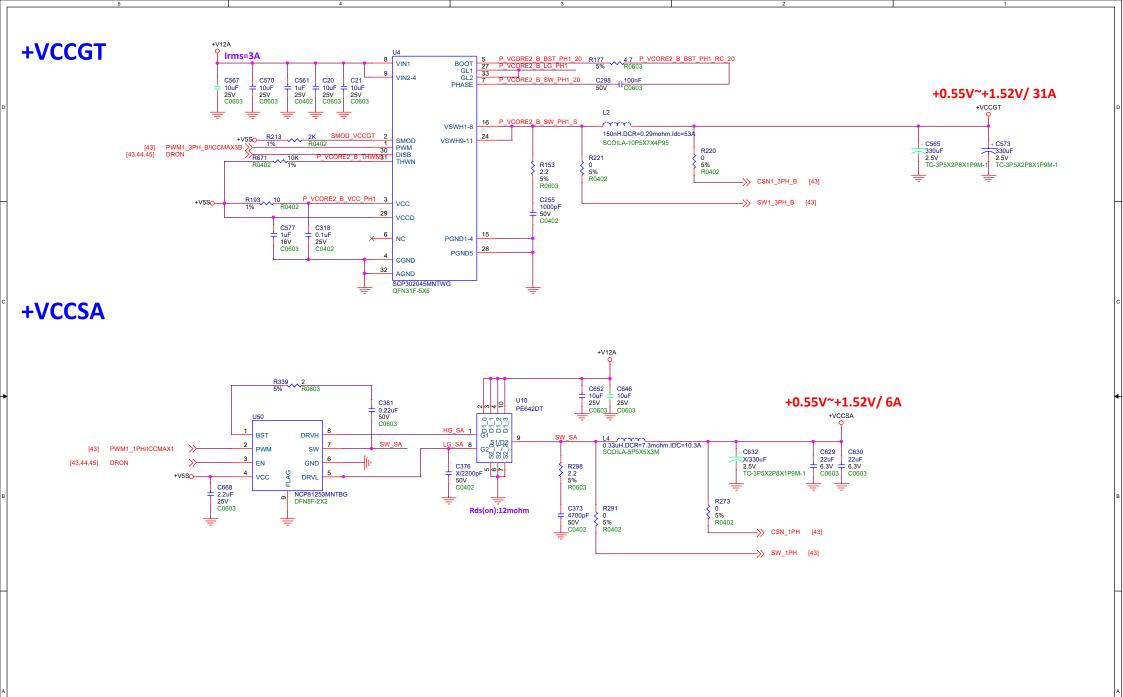


AAEON Technology INC.

Title

PWR_+VCCCORE

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<Variant Name> AAEON Technology INC. PWR_+VCCGT/ +VCCSA Size Document Number **EPD-AEUCBF** An /SUS Company Custom A0.1 0 0 Tuesday, January 19, 2021

HISTORY

Date	Revision	Page	Modification list	Reason
2020/11/10	A0.1		First Release.	

AN /SJS Company

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