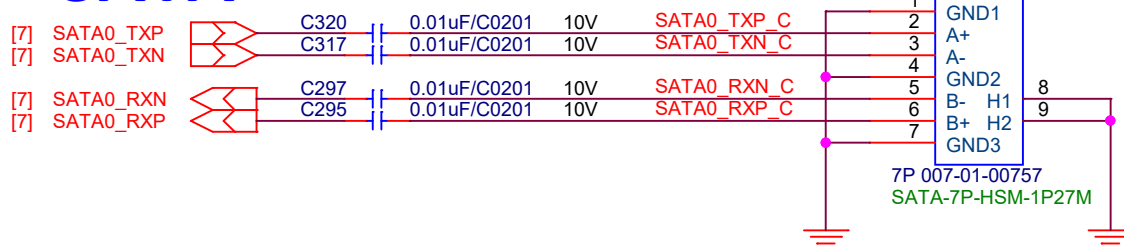
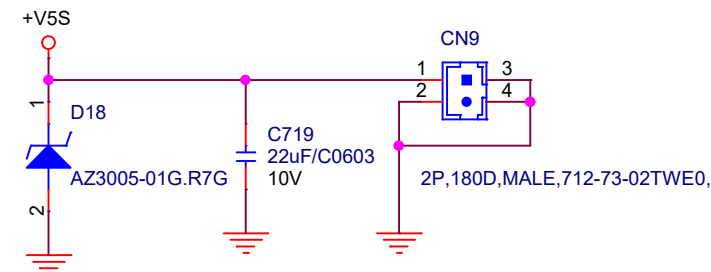




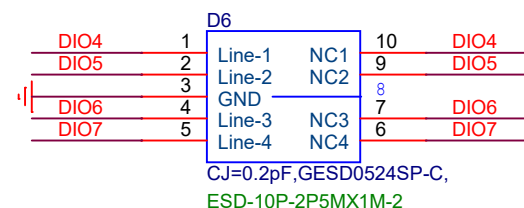
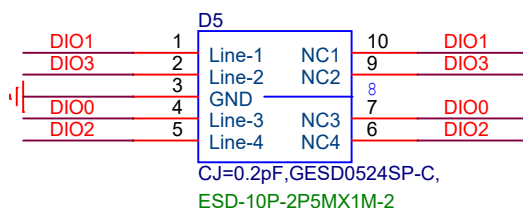
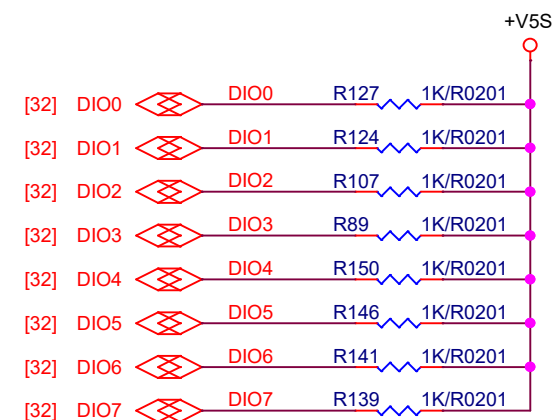
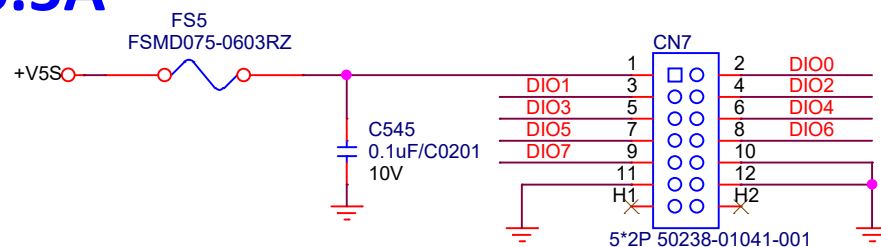
# SATA



# 1A



# 0.5A



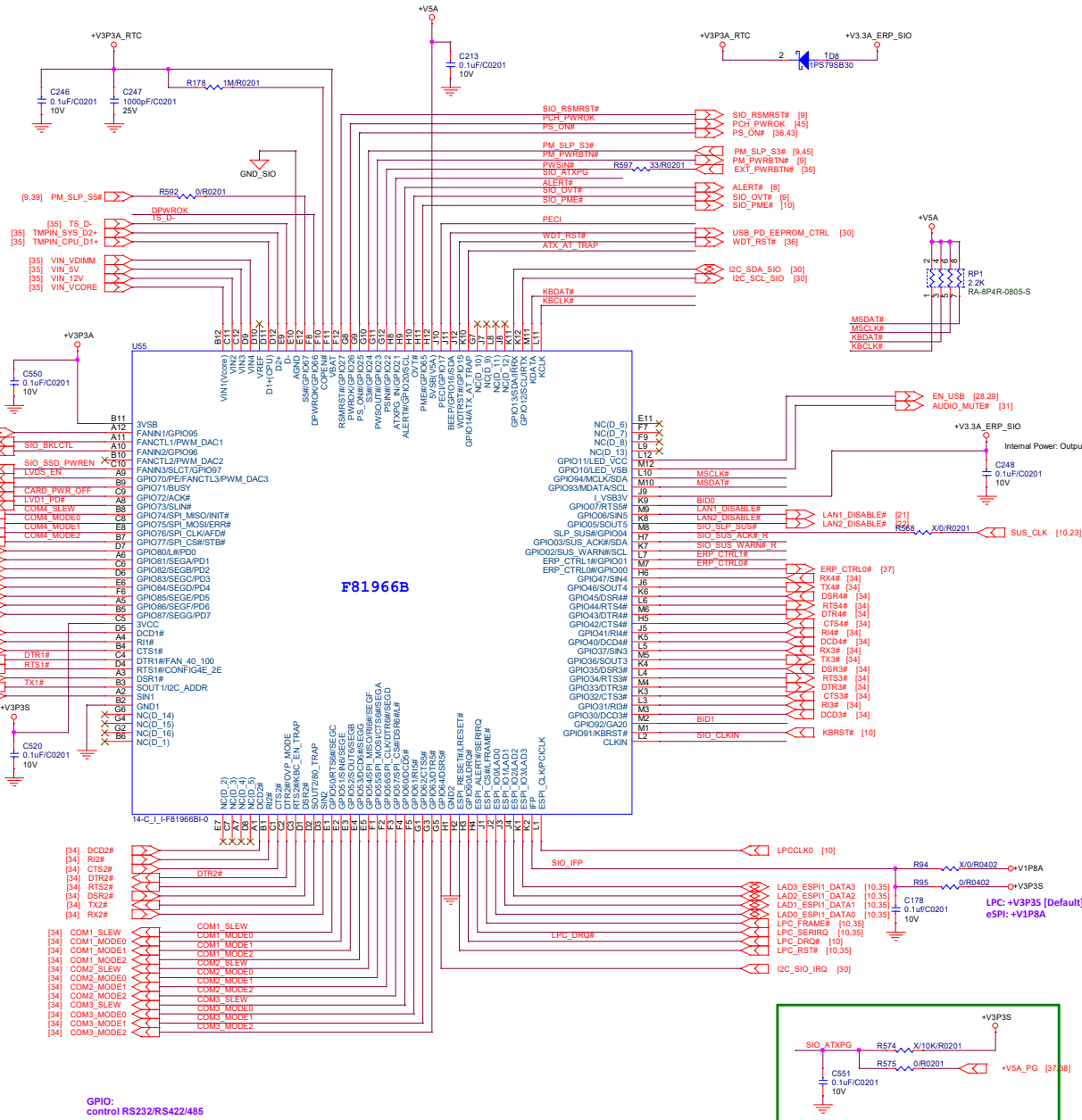
Title <b>SATA_DIO</b>		
Size A	Document Number <b>PICO-V2K4/EPICOV2K4A02</b>	Rev: <b>A0.2_0_0</b>
Date: <i>Thursday, November 25, 2021</i>		Sheet: <b>27</b> of <b>46</b>

**Board ID Mode Selection**

Mode	BID0	BID1
Functions1	0	0
Functions2	0	1
Functions3	1	0
Functions4	1	1

**Component Values:**

- R96, R125, R97, R142: 10K/R0402
- R521, R468, R469, R522, R467, R523, R466, R102, R103, R524, R465, R525, R104, R105, R526, R464, R560, R140, R139, R138: 10K/R0201
- R153: 0/R0201
- R148: 0/R0402
- R59: 10K/R0201
- C180: 22uF/C0402
- C179: 0.1uF/C0201
- C143: 0.01uF/C0201
- Y3: 24MHz/15pF/3.3V/4P
- C41: X0.1uF/C0201



JP2

1 2 3

ATX\_5V TRAP

R740 470/R0402

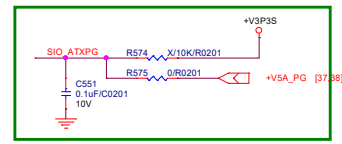
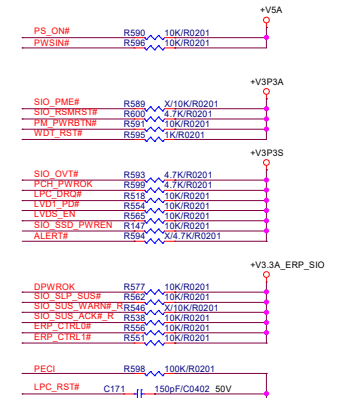
1.3P\*, 180D, MALE, 27-4101-103-1G-R

[JP2(2-3)]  
1.27mm. 820F002-0084T-R

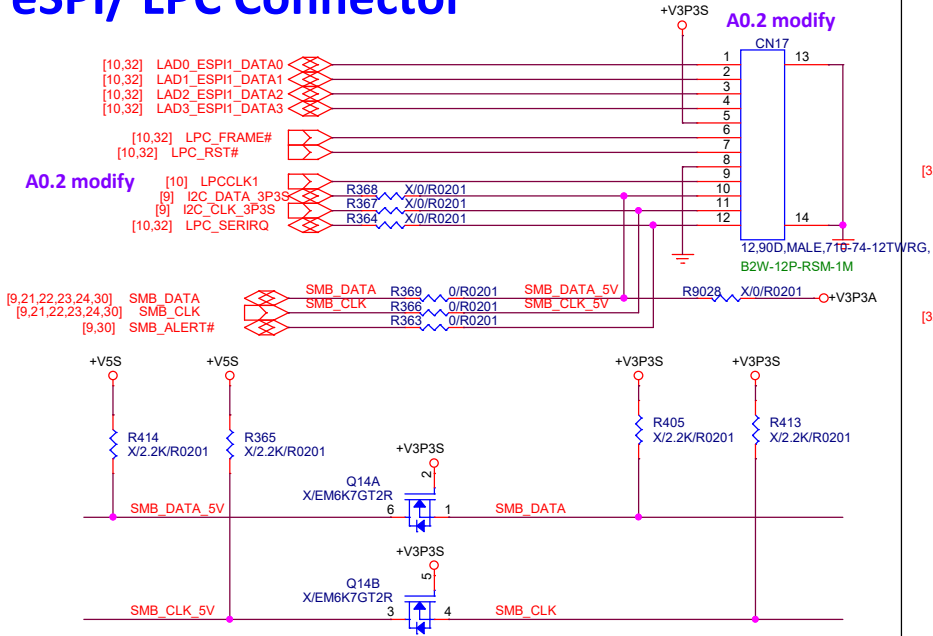
### Power On Strapping Options pin



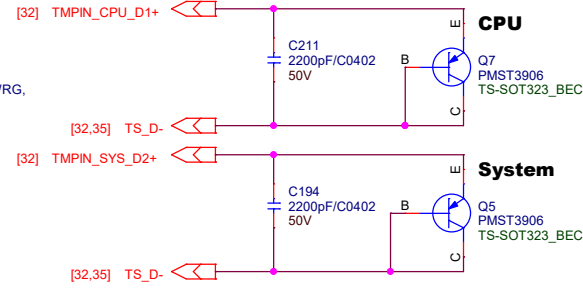
JP4, B3	I2C_ADDR
HIGH	The I2C slave address is 7h2E. (Default)
LOW	The I2C slave address is 7h2D.
JP3, D4	Config4E_2E
HIGH	Configuration Register I/O port is 4E/4F.
LOW	Configuration Register I/O port is 2E/2F(Default).
JP2, C4	FAN40_100
HIGH	Power on fan speed default duty is 40% (Default)
LOW	Power on fan speed default duty is 100%
JP1, C2	OVP_Mode
HIGH	(ALARM mode) Disable OVP function
LOW	(FORCE mode) Enable OVP function



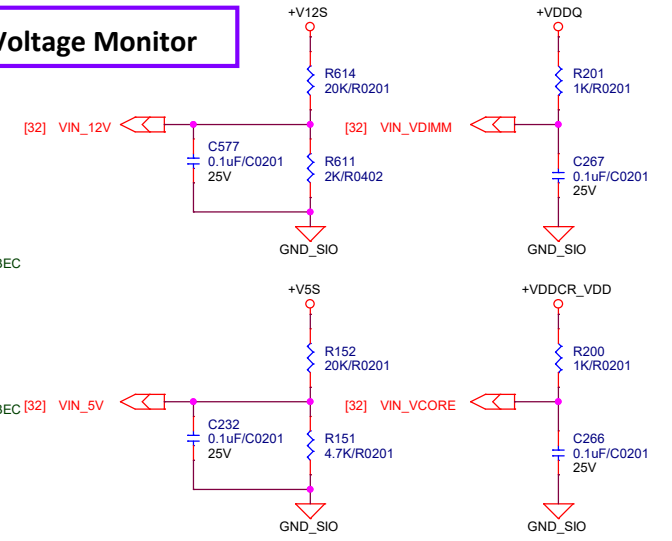
# eSPI/ LPC Connector



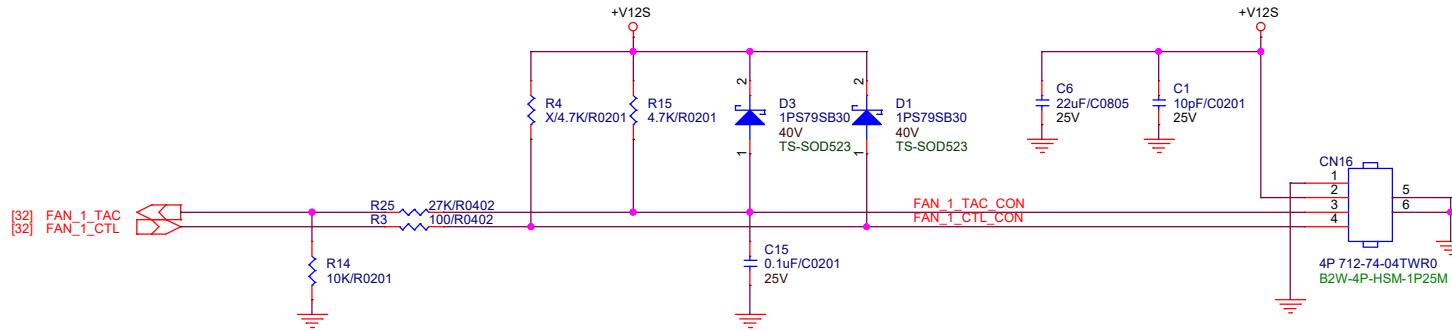
## Temperature Monitor



## Voltage Monitor

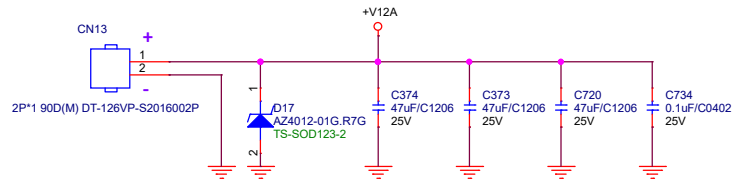


## CPU FAN

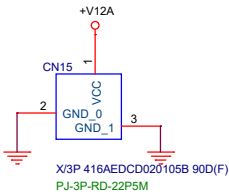


Title		
FAN/ LPC/ HW Monitor		
Size B	Document Number PICO-V2K4/EPICOV2K4A02	Rev: A0.2_0_0
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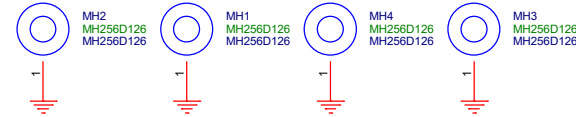
## Power Input



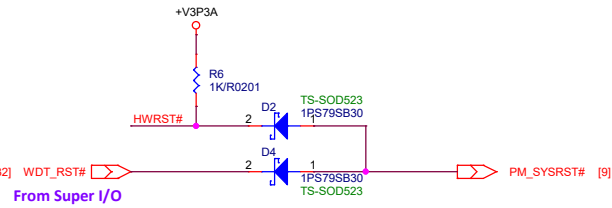
## DC Jack



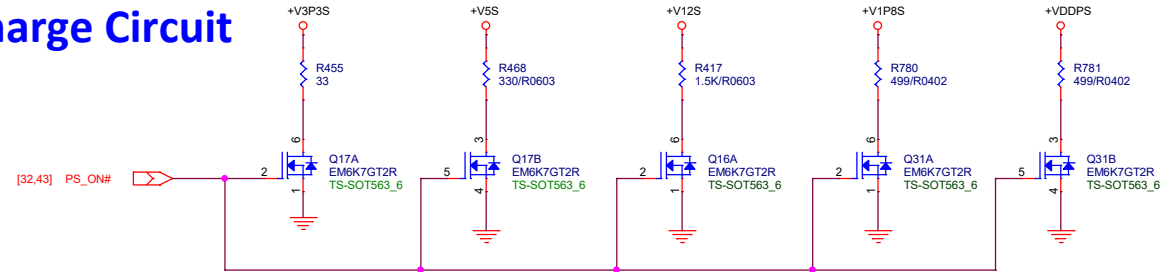
## Mounting Holes / Non-PTH



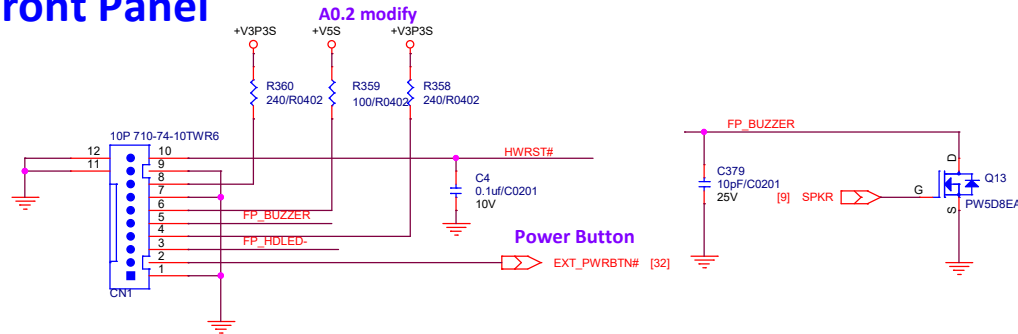
## System Reset



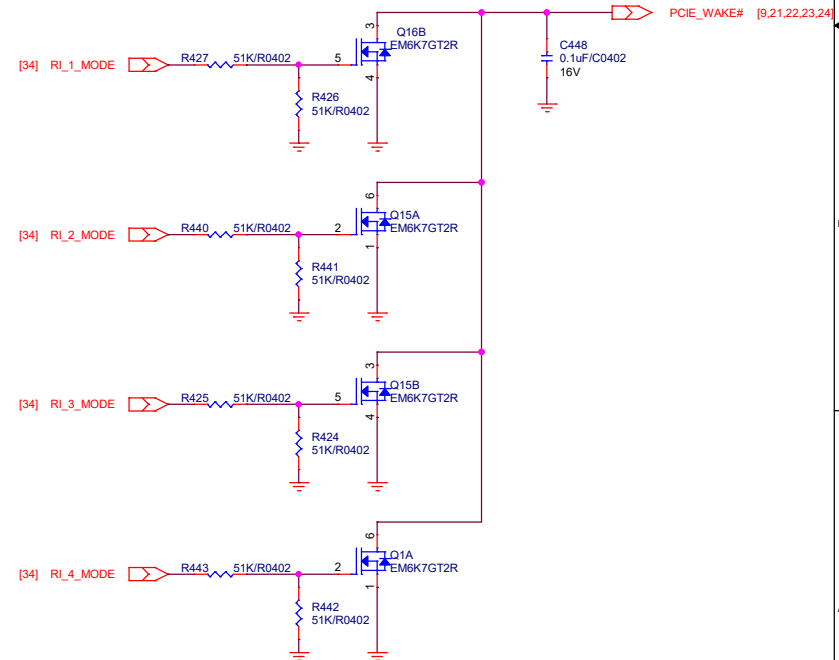
## Discharge Circuit



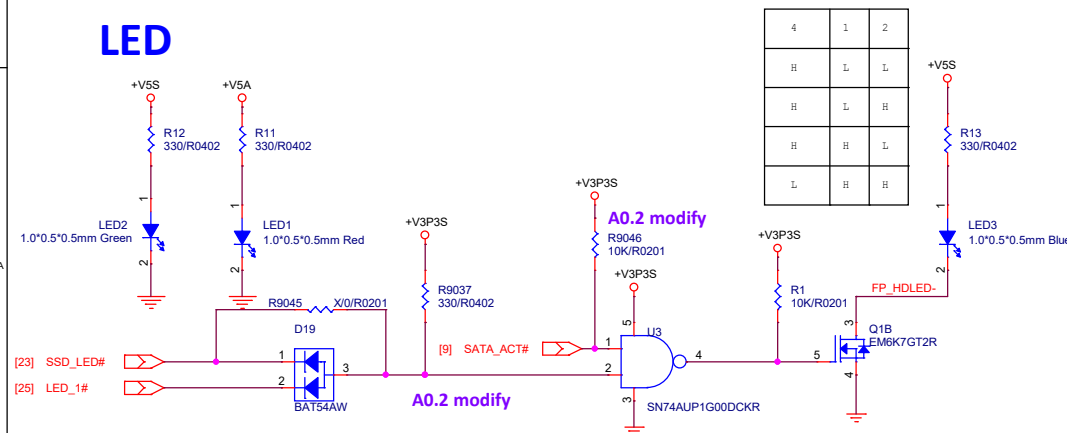
## Front Panel



## Wake On Modem



## LED



Title		
Power IN/ Front Panel/ MISC		
Size	Document Number	Rev:
Custom	PICO-V2K4/EPICOV2K4A02	A0.2_0_0
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