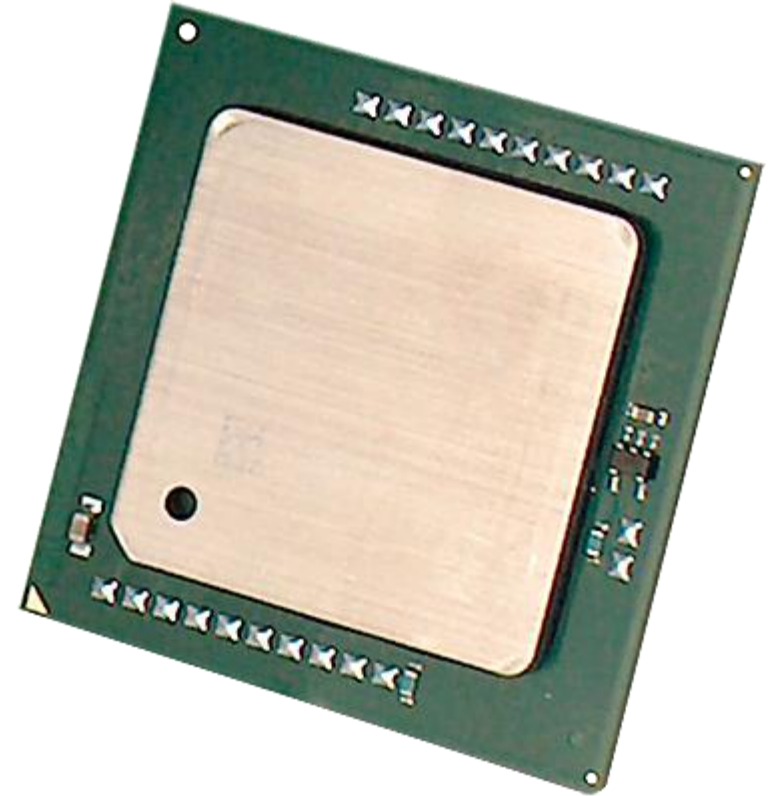


PROCESSADOR 8-BITS

ALUNO: LUCAS DANIEL MIRANDA FREITAS

RISC

UNICICLO



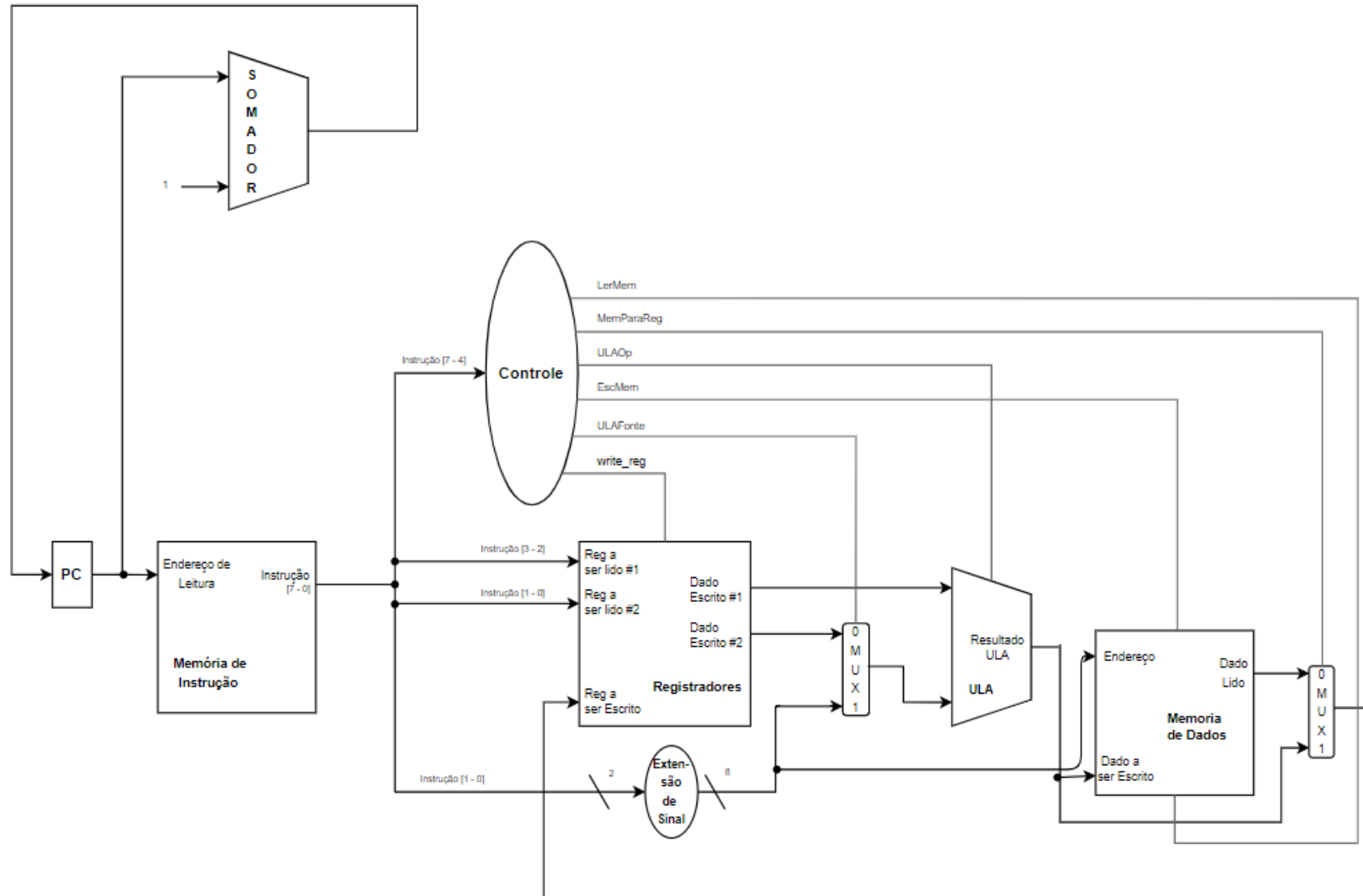
DIVISÃO DE BITS

Opcode	Registrador rs	Registrador rt
4 Bits	2 Bits	2 Bits
7-4	3-2	1-0

OPERAÇÃO TIPO R		
Opcode	rs	rt
4 Bits	2 Bits	2 Bits
7-4	3-2	1-0

OPERAÇÃO TIPO I		
Opcode	rs	Endereço
4 Bits	2 Bits	2 Bits
7-4	3-2	1-0

DATAPATH 8-BITS



Opcode	Nome	Formato	Exemplo
0000	ADD	R	add \$S0, \$S1
0001	ADDI	I	addi \$S0, 7
0010	SUB	R	sub \$S0, \$S1
0011	SUBI	I	subi \$S0, 7
0100	MULT	R	mult \$S0, \$S1
0101	MULTI	I	multi \$S0, 7
0110	MOVE	R	move \$S0, \$S1
0111	MOVI	I	movi \$S0, 7
1000	LW	I	lw \$S0, memoria (00)
1001	SW	I	sw \$S0, memoria (00)