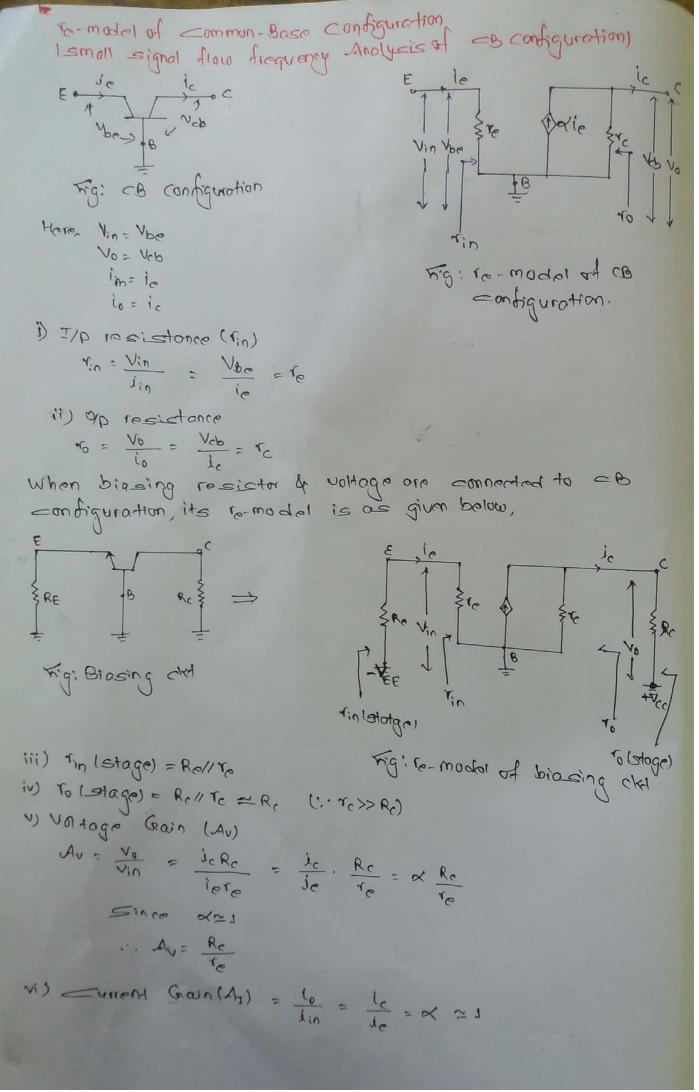
Chapter 5 Small Signal Low Frequency Analysis model of BIT Small Signal Parameters # The small signal eviront goins of BJT are; B= ic | ver= const It is the resistance offered by the base-emitter junction to # Emitter recietance (re): the small signal ip signal. E = 1c | Ver scond. The small signal Emiliter base restations of But also collect also collect emitter reststance. re = Vec Was = conf. Since BE junton of BJT resembles to a forward biased en junction diode, we can advulate re in the similar way that we calcutated dynamic reststance (re) of an junction taking mes & VT = 0.026 at room temps 75 = 0.026 T .. Te = 0.026 # Small signal collector-bose resistance of BJT, also called collector resistance (Te) is given by, Since. collector-base junction is covered biased the value of to is very high (MSZ).



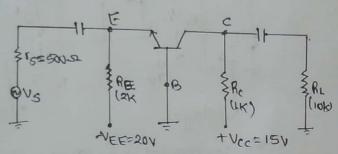
When a transistor is driven by source ustage us with resistories is of load resistance Rr, then its re-model is, AND THE PRICE OF T Fig: CB omplifier det rin(stage) Fig: 10-model of 18 amplier The transister amplifier can be represented by the transister block as shown bolow, Ser Ar rin (Stage) Overall Gain (voltage) (VL/Vs)  $\frac{V_L}{V_S} = \frac{V_L}{V_0} \times \frac{V_0}{V_{in}} \times \frac{V_{in}}{V_S} = \frac{V_L}{V_0} \times A_V \times \frac{V_{in}}{V_S} = A_V \times \left(\frac{V_L}{V_0}\right) \times \left(\frac{V_{in}}{V_S}\right)$ . VL = Av × (RL rolstage) + RL) × (rin(stage) + rs) Overall current Gain (il/is) we have, i = V/RL & is = Vs - il = VL/RL = VL rs+ rin(Btage)

Vs rs+ rin(Stage) .: it = VL / 1s + fint stage!

# for the transister amplifier and shown below, drow E-model & find

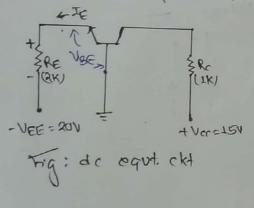
i) fin ii) fin (stage), iii) to (stage) iv) Av

v) VL/Vs vi) iv/is. Assume x=1.



Soln: We have, Te = 0.026

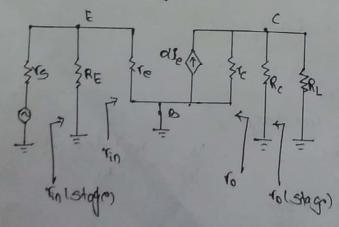
To find IE, need to perform de analysis of given ckt.
For this, drow de equivalent ckt.



Applying KVL at its loop,  $V_{EE} + I_{ERE} + V_{GE} = 0$   $V_{CE} + I_{ERE} + V_{GE} = 0$   $V_{C$ 

Thuse, 
$$r_e = \frac{0.026}{9.65 \times 10^{-3}} = 2.69.52$$

Now re-model of given ckt is,



ii) 
$$r_{in}$$
 (stage) =  $RE//r_{e}$  =  $RE//r_{in}$  = 2000// 2.69  
=  $\frac{2000 \times 2.63}{2000 + 2.69}$  =  $2.68 \times 12$   
iii)  $r_{0}$  (stage) =  $R_{c}$  //  $r_{c}$   $\propto R_{c}$  = 1 K.2  
iv)  $N_{v}$  =  $N_{v}$  ( $\frac{R_{c}}{r_{e}}$ ) =  $1 \times \left(\frac{1000}{2.69}\right)$  =  $371.74$   
v)  $\frac{V_{L}}{V_{S}}$  =  $N_{v}$  ( $\frac{r_{in}}{r_{S}}$  +  $r_{in}$  (stage)) ( $\frac{R_{L}}{R_{L}}$  +  $r_{in}$  (stage)) =  $371.74 \times \left(\frac{2.68}{500 + 2.68}\right) \times \left(\frac{10}{10 + 1}\right)$   
=  $1.798$  ( $\frac{1}{N_{S}}$  =  $\frac{N_{L}}{N_{S}}$  =  $\frac{N_{L}}{N_{S}}$ 

Small signal Analysis of Common Emitter Configuration (re-model of CE configuration)

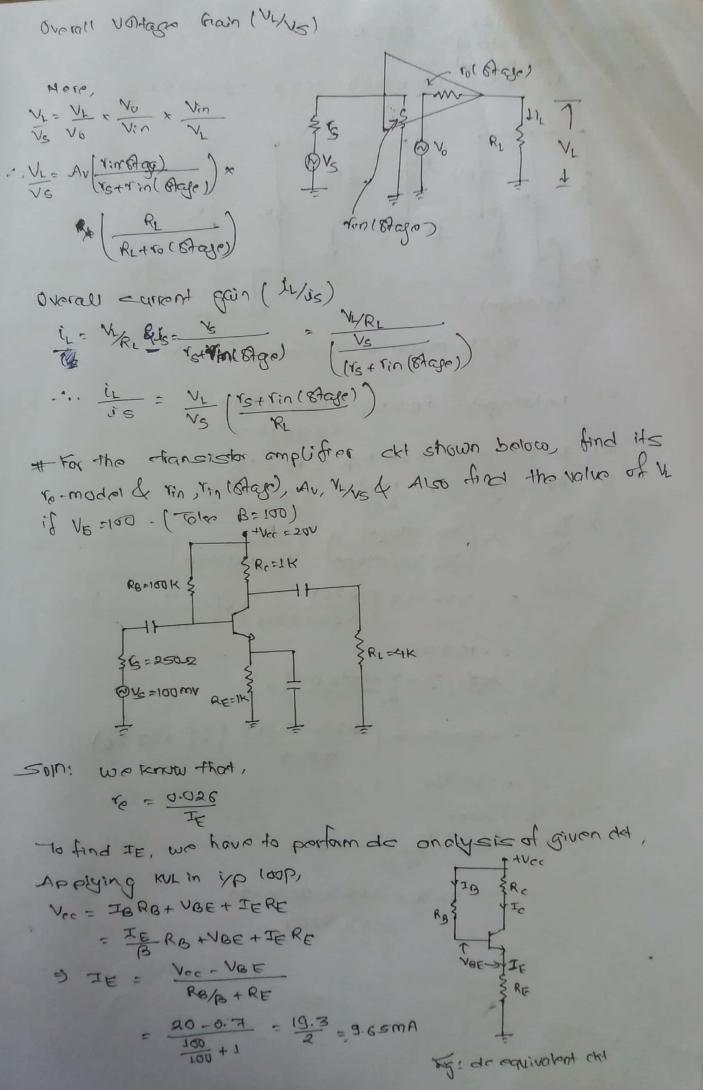
1) Emitter bypossed with byposs copacitor)

2) Emitter unbypossed (without byposs eapocitor)

Input resistance, 
$$r_{in} = \frac{V_{in}}{din} = \frac{V_{be}}{db} = \frac{V_{be}}{de} = \frac{1}{16} \frac{V_{be}}$$

Fig: To-model of CE ampiguration

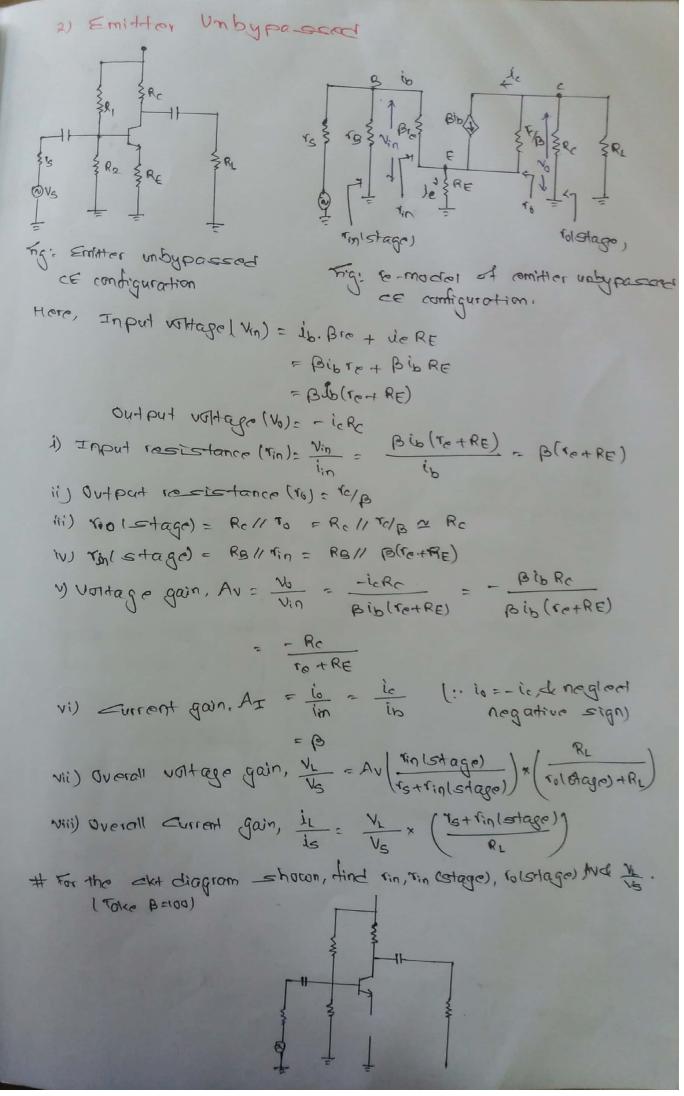
Output resistance, ro = Vo = Vce = Vce ptb 6 = 6 If the CE tronsister is biased by do wittage than about akt becomes, ERE Capacida Tinistages Fin rolstage, Fig: Emiliter bypassered Fig: re-model of emiliter biasing eks by passed CE configuration. Here, rin ( Agge) = RB//rin = RB// Bre ro (Stage) = Relline = Re (10>> Re) Voltage gain (Av) =  $\frac{V_0}{V_{in}} = \frac{-icRc}{ibBre} = \frac{-BibRc}{Breib} = \frac{-Rc}{re}$ -. Av = - Rc (Here -ve sign indicates is dup and out of phase)  $= \frac{i_0}{i_0} = \frac{i_0}{i_0}$ 1. AT =B When the source voltage us & load restation RL is connected to the CE amplifier cikt then the old and its re-model is as shown below. p+Vcc 3 RB Tint Hage) Tig: To-model of CE ampliator (Emitter bypassed) Scanned by CamScanner

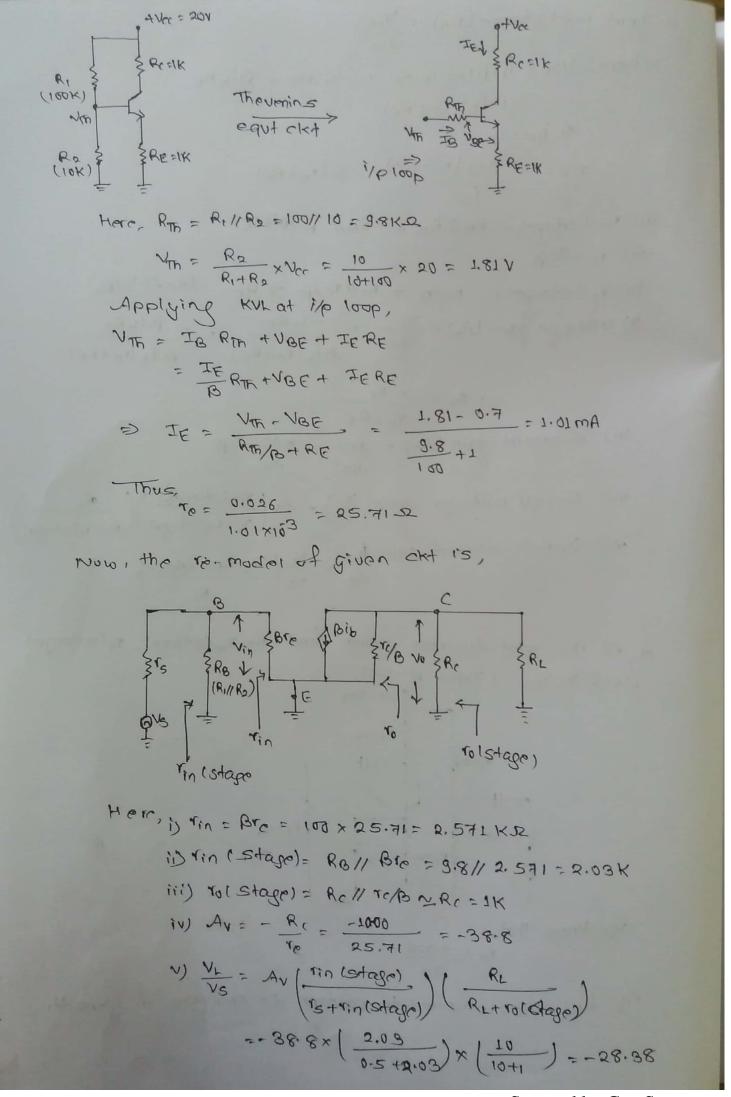


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D V=-153.26 × 100

=-15.326V





# Draw re-model & find rin, rin (stage), rolstage), Av, Vulus & julis. Take B=100. 15:500 ST STONE STIKE RL SAN: Here, re= 0.026 = 0.026 = 0.026 = 25.7152 Re Bib D Re RE RE "in stapp) i) Tin = B(Te+ RE) = 100 ( 25.71 + 1000) = 102.57 KD ii) rin(stage) = RB// fin = (A1// A2) // Tin = 9.8 K// 102.57 iii) rol stage = Rell (c/B = Re = 1K iv) Ay = - Ro = 1000 = -0.97 V)  $\frac{V_L}{V_S} = A_V \left( \frac{fin(stage)}{r_S + r_{in} + stage} \right) \times \left( \frac{R_L}{r_{olstage} + R_L} \right)$  $= -0.97 \times \left( \frac{8.94}{0.5 + 8.94} \right) \times \left( \frac{10}{10 + 1} \right)$ vi) 1/2 = VL ( 1/5 + find stage) =-0.83 ( 0.5+8.9) = -0.78

# Draw re-model & find rin, rin(stage), rolstage), Au. take B:100. RB=10K\$

RC=1KR

RE=2K Son: We know that, re = 0.026 To find IE, we have to perform do ambysis of the det. Now, applying KVL in i/p loop

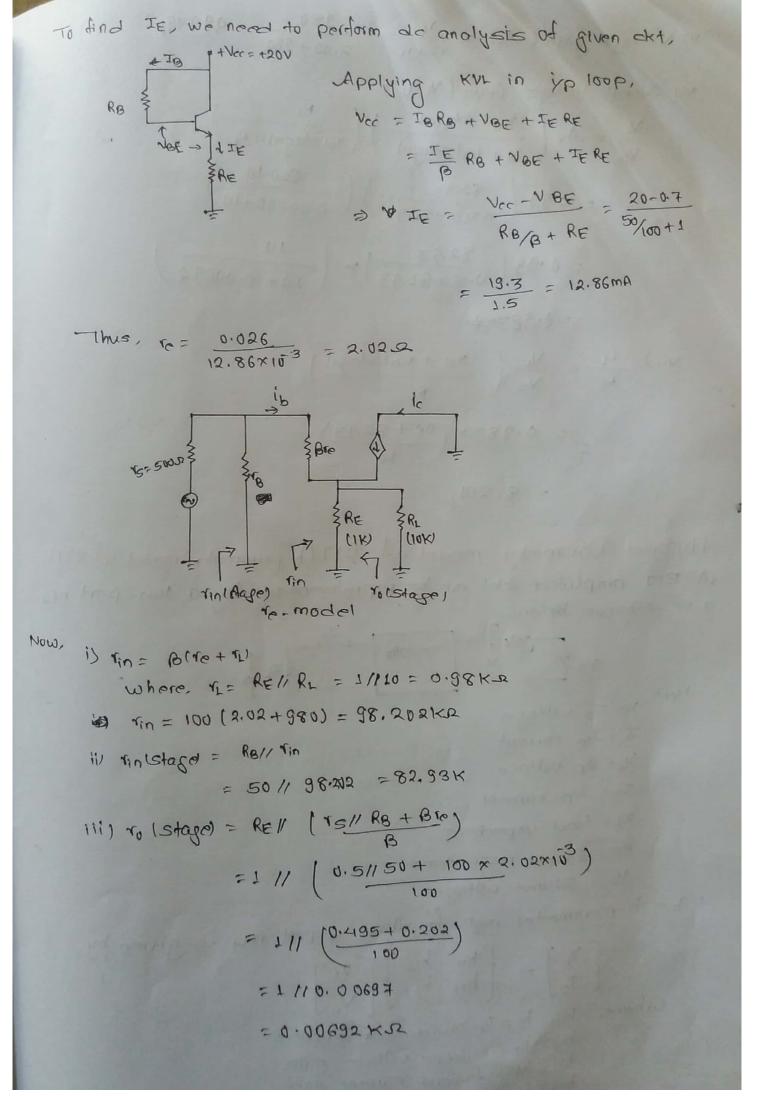
Vec = IB RB + VBE + IERE

Type | Or, Vec = IE RB + VBE + IERE or, IE = 20-0.7 -. Te = 0.026 = 4.04 sz Ro Bre Bie A RE Tin Istage 10 (Stage) i) fin = B(fe+ RE)

= 100 x (4.04 + 2000)

= 200.404 Ks

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V) Voltage gain (AV) = 
$$\frac{r_L}{r_{e+r_L}} = \frac{980}{2.02 + 980} = 0.9973/l$$

Vi) Overall Voltage (rain (NL/Vs))

 $\frac{V_L}{V_S} = AV \left( \frac{r_{in} L stage}{r_S + r_{in} L stage} \right) \left( \frac{R_L}{r_{ols} + r_{ols} + r_{ols}} \right)$ 
 $= 0.99 \left( \frac{82.93}{500.7 + 82.93} \right) \left( \frac{380 \cdot 10}{0.00692 + 10} \right)$ 
 $= 0.98 \left( \frac{82.93}{0.5 + 82.93} \right) \times \left( \frac{10}{10 + 0.00692} \right)$ 
 $= 0.983 \times \left( \frac{0.5 + 82.93}{10} \right)$ 
 $= 8.201$ 

Hybrid Parameter model of BIT (h-parameter model of BIT)

A BIT amplifier ext an be represented by a two-port no
as shown below,

Here, VIS I/p voltage

I = I/p current

V2 = up voltage

In = orp current

ZL = load improduce

As = Source resistance

Vs = source voltage

The h-parameter eqn for above 2-port n/w is given by,

$$\begin{bmatrix} V_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ V_2 \end{bmatrix}$$

where, his is imadamo tresistance)

his = hr > reverse voltage gain

has = hf -> forward current gain

has = ho -> orp admittance

Thus, 
$$V_1 = \begin{cases} h_1 & h_1 \\ h_2 & h_1 \\ h_1 & h_2 \end{cases} = \begin{cases} h_1 & h_1 \\ h_2 & h_1 \\ h_1 & h_2 \end{cases} = \begin{cases} h_1 & h_1 \\ h_2 & h_2 \\ h_1 & h_2 \end{cases} = \begin{cases} h_1 & h_2 \\ h_2 & h_1 \\ h_2 & h_2 \\ h_$$

4 
$$z_0 = \frac{1}{1}y_0$$

V) Overall Valtages Grain,  $|Av_s| = \frac{10}{15} = \frac{10}{15} \times \frac{1}{15}$ 

To find Value, let us doow The vaning equt clei of in side.

Res

Or.  $\frac{V_1}{V_2} = \frac{Z_1}{X_1 + R_2}$ 

Vi) Overall current  $\frac{Z_1}{Z_1 + R_2}$ 

Thus,  $Av_0 = Av \times \left(\frac{Z_1}{Z_1 + R_2}\right)$ 

Vi) Overall current  $\frac{Z_1}{Z_2} \times \frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3}$ 

To find  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3}$ 

Here,  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3}$ 

The first  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3}$ 

To find  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3}$ 

The first  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3} \times \frac{Z_3}{Z_3}$ 

The first  $\frac{Z_1}{Z_3} \times \frac{Z_2}{Z_3} \times \frac{Z_3}{Z_3} \times \frac{Z$ 

# For the given transistor amplifier det, the h-parameters are, hie= 160000, he= 80, hie= 20x10 & hoe= 2000. Find Zi, Zi (stage), zo, Zu (stage), AI, Av, Ava & AIS. hre, har, hoe RS=BOODS (RE) 500: Given, hie = 1600-2 hge = 80 h to = 20 × 104 hoe = 2045 the h-parameter model of given ext is, To Zo Zo LST Here, ZL = Rell RL = 3.3/110 = 2.48KD RB= R,11R2= 501/12 = 3.8KR Where,  $\chi = \frac{h_{1}e - h_{2}e * h_{1}e}{h_{0}e + \gamma_{L}}$ where,  $\chi = \frac{1}{2.48 \times 6.8} + 0.3 \times 10^{-4}$  ser - Zi = 1600 - 80x 20x 10-4 = 1221.955 20x56+ 408×104 ii) Zilstage) = RB11 Zi = 9800// 1221-95 =1086.472

Where, 
$$Y_0 = h_{00} - \frac{h_{10} \cdot h_{10}}{h_{10} + R_0 // R_0}$$

=  $20 \times 10^{-6} - \left( \frac{80 \times 20 \times 10^{-4}}{1600 + (500 // 3900)} \right)$ 

=  $-5.7 \times 10^{-5} \text{S}$ 

=  $5.7 \times 10^{-5} \text{S}$ 

=  $5.7 \times 10^{-5} \text{S}$ 

(  $1.7 \times 10^{-5} \text{S}$ 

=  $1.7 \times 10^{-5} \text{S}$ 

(  $1.7 \times 10^{-5} \text{S}$ 

W)  $1.7 \times 10^{-5} \text{S}$ 

=  $1.7 \times 10^{-5} \text{S}$ 

=  $1.7 \times 10^{-5} \text{S}$ 

W)  $1.7 \times 10^{-5} \text{S}$ 

=  $1.7 \times 10^{$