

Luchian Mihai

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Electronics Engineer

DESCRIPTION

Enthusiastic young engineer. Second year Master of Technology student, lecturing in Electronic Systems and Integrated Communications field. Research assistant at Transilvania University of Brasov's research institute.

EDUCATION

Bachelor's degree in Applied Electronics

Department of Electronics and Computers,
School of Electrical Engineering and Computer Science,
Transilvania University of Brasov
(2014 - 2018)

Master's degree in Integrated Electronics and Communications Systems

Department of Electronics and Computers,
Transilvania University of Brasov
(2018 - Present)

PROFESSIONAL SKILLS

Computer Experience

Strong knowledge of UNIX like operating systems, with good experience using Arch, Void and Debian based distributions, as well as FreeBSD and OpenBSD. Good experience using UNIX tools, such as "GNU coreutils", Moderate experience in "BSD userland."

Daily user of CLI tools such as Vim editor, Makefile build system, GNU C Compiler, IPython.

Strong emphasis on Command Line Interface over Graphical User Interface

Software Skills

Moderate experience using C programming language for embedded systems developement and Intel x86 architecture.

Moderate experience with Python scripting language

Moderate experience with UNIX shell scripting languages like "*Bash*", "*Zsh*", "*Dash*", "*Mksh*".

Hardware skills

Moderate experience using NGSpice simulation tool in both GUI and CLI modes.

Experienced user with Altium Designer, KiCAD, OrCAD and Proteus tools, for both Schematic and Layout design.

Solid experience using open source iverilog simulator, moderate experience using Vivado, Xilinx ISE and Modelsim suite simulators, in GUI and batch, Tcl/Tk modes

Moderate experience with Vivado design flow (simulation, synthesis, implementation)

Moderate experience with Verilog hardware description language

EMPLOYMENT

Internship at Siemens Industry Software, Siemens Curious Minds 2016.

Strong focus on C++ programming language, Design patterns and Software architecture.
Ranked 4th at the final assessment.

(2016 - 2017)

Internship at PREH ROMANIA S.R.L., PCB Layout Designer.

Worked on trainee projects using Altium Designer. Focus on Printed Circuit Boards manufacturing process, and layout techniques. Introduction in WCCA calculus and hardware architecture.

(2017 - 2018)

Research Assistant, RO-CERN collaboration, project "ATLAS experiment at LHC".

(2018 - Present)

PUBLICATIONS

"The Quality-Assurance Test of the ATLAS New Small Wheel Read-Out Controller ASIC,"

S. Popa, R. Coliban, M. Luchian, M. Ivanovici,

Proceeding of Science (PoS), 081, 2019

"Clock and data signals synchronization for an FPGA-based ASIC testing setup,"

S. Popa, M. Luchian, M. Ivanovici,

2019 International Symposium on Signals, Circuits and Systems (ISSCS), 1-4, 2019

PROJECTS

Blue Streamline, *"The racing team of Transilvania university"*

Electronics department leader assigned to coordinate, design, manufacture and implement the electronics modules which controls the consumers of an single seater. Attendant as an team member at international competition held at Silverstone (UK) and Catalunya (Spain).

T.I.E. Tehnici de Interconectare in Electronica,

Attendant at international competition during years: 2016 (Suceava), 2017 (Iasi), 2018(Ploiesti)