

2018/662 01708

1. class	A	B	C	D
CPI	1×10^5	2×10^5	5×10^5	2×10^5
P1	1	2	3	3
P2	2	2	2	2

$$\begin{aligned} \text{b. P1 Clock cycles} &= 1 \times 10^5 + 2 \times 10^5 \times 2 + 5 \times 10^5 \times 3 + 2 \times 10^5 \times 3 \\ &= 26 \times 10^5 \end{aligned}$$

$$\begin{aligned} \text{P2 Clock cycles} &= 1 \times 10^5 \times 2 + 2 \times 10^5 \times 2 + 5 \times 10^5 \times 2 + 2 \times 10^5 \times 2 \\ &= 20 \times 10^5 \end{aligned}$$

$$\begin{aligned} \text{a. P1 CPU Time} &= \text{Clock Cycles} / \text{clock Rate} = \text{IC} \times \text{CPI} / \text{Clock Rate} \\ &= 26 \times 10^5 / 2.5 \times 10^9 = 10.4 \times 10^{-4} \text{ s} \end{aligned}$$

$$\text{P2 CPU Time} = 20 \times 10^5 / 3 \times 10^9 = 6.666 \times 10^{-4} \text{ s}$$

$$\text{P1 CPI} = 10.4 \times 10^{-4} \times 2.5 \times 10^9 / 10^6 = 2.6$$

$$\text{P2 CPI} = 6.666 \times 10^{-4} \times 3 \times 10^9 / 10^6 = 2.0$$

$$\frac{\text{P1 CPU time}}{\text{P2 CPU time}} = \frac{10.4}{6.666} = 1.56$$

$\therefore P_2$ is 1.56 times faster than P_1 .

$$2. a. CPI = \frac{CPU \text{ Time}}{\text{Clock Cycle time} \times IC}$$

$$b. \text{Clock cycle time} = \frac{CPU \text{ time}}{CPI \times IC}$$

$$CPI_A = 1.1$$

$$CPI_B = 1.25$$

$$\frac{CCT_A}{CCT_B} = \frac{\frac{CPU \text{ time}}{1.1 \times 1.0 \times 10^9}}{\frac{CPU \text{ time}}{1.25 \times 1.2 \times 10^9}} = 1.364$$

Compiler B is 1.364 times faster than Compiler A

$$c. CPU \text{ time} = CPI \times \text{Clock Cycle time} \times IC$$

$$\frac{T_A}{T_{new}} = \frac{1.1 \times CCT \times 1.0 \times 10^9}{1.1 \times CCT \times 6.0 \times 10^8} = 1.667, \text{ New Compiler is 1.667 times faster than A.}$$

$$\frac{T_B}{T_{new}} = \frac{1.25 \times CCT \times 1.2 \times 10^9}{1.1 \times CCT \times 6.0 \times 10^8} = 2.272, \text{ new Compiler is 2.272 times faster than B.}$$

$$3. a. CPI = \frac{CPU \text{ Time}}{\text{Clock cycle time} \times IC} = \frac{750}{0.333 \times 10^{-9} \times 2.389 \times 10^{12}} = 0.943$$

$$b. SPEC \text{ ratio} = \frac{9650}{750} = 12.867$$

$$c. 10\% \text{ , } CPI = \frac{CPU \text{ time} \times \alpha}{CCT \times IC \times 1.1} \rightarrow \alpha = 1.1, 10\% \text{ increase.}$$

$$d. CPU \text{ time} = \underset{\uparrow 5\%}{CPI} \times \underset{\uparrow 10\%}{CCT} \times IC \rightarrow 1.1 \times 1.05 = 1.155, 15.5\% \text{ increase}$$

$$e. CPU \text{ time}_{(new)} = 750 \times 1.155 = 866.25 s.$$

$$SPEC \text{ ratio} = \frac{9650}{866.25} = 11.14$$

$$f. CPI_{new} = \frac{700 \times 4 \times 10^9}{2.389 \times 10^{12} \times 0.85} = 1.379$$

$$4.a. T_{\text{improved}} = \frac{T_{\text{affected}}}{\text{improv factor}} + T_{\text{unaffected}}$$

$$\Rightarrow T_{\text{FP}} = 705 \times 0.8 = 565.$$

$$T_{\text{improved}} = 250 - 14 = 236s, \quad 5.6\% \text{ reduced}$$

b. Since the total time is reduced by 20%,
the time for INT operations reduced by 20%, which is 44s. (from 55s)

$$c. T_{\text{FP}} + T_{\text{LIS}} + T_{\text{INT}} = 70 + 85 + 55 = 210 > 200 = T_{\text{target}}.$$

\therefore impossible. (T_{branch} should be $< 10s$).