

综合数据 (直接映射)

LINE_ADDR_LEN	2
SET_ADDR_LEN	2
TAG_ADDR_LEN	9

Resource	Estimation	Available	Utilization %
LUT	517	32600	1.59
FF	1123	65200	1.72
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8

Resource	Estimation	Available	Utilization %
LUT	738	32600	2.26
FF	1671	65200	2.56
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7

Resource	Estimation	Available	Utilization %
LUT	1233	32600	3.78
FF	2759	65200	4.23
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	5
TAG_ADDR_LEN	6

Resource	Estimation	Available	Utilization %
LUT	2168	32600	6.65
FF	4919	65200	7.54
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	2
TAG_ADDR_LEN	8

Resource	Estimation	Available	Utilization %
LUT	1154	32600	3.54
FF	2012	65200	3.09
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	3
TAG_ADDR_LEN	7

Resource	Estimation	Available	Utilization %
LUT	1354	32600	4.15
FF	3068	65200	4.71
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6

Resource	Estimation	Available	Utilization %
LUT	1860	32600	5.71
FF	5172	65200	7.93
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	5
TAG_ADDR_LEN	5

Resource	Estimation	Available	Utilization %
LUT	3726	32600	11.43
FF	9364	65200	14.36
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7

Resource	Estimation	Available	Utilization %
LUT	2151	32600	6.60
FF	3797	65200	5.82
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	3
TAG_ADDR_LEN	6

Resource	Estimation	Available	Utilization %
LUT	2332	32600	7.15
FF	5873	65200	9.01
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5

Resource	Estimation	Available	Utilization %
LUT	3477	32600	10.67
FF	10017	65200	15.36
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	5
TAG_ADDR_LEN	4

Resource	Estimation	Available	Utilization %
LUT	7112	32600	21.82
FF	18289	65200	28.05
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	5
SET_ADDR_LEN	2
TAG_ADDR_LEN	6

Resource	Estimation	Available	Utilization %
LUT	4593	32600	14.09
FF	7376	65200	11.31
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	5
SET_ADDR_LEN	3
TAG_ADDR_LEN	5

Resource	Estimation	Available	Utilization %
LUT	4009	32600	12.30
FF	11496	65200	17.63
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	5
SET_ADDR_LEN	4
TAG_ADDR_LEN	4

Resource	Estimation	Available	Utilization %
LUT	6772	32600	20.77
FF	19728	65200	30.26
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	5
SET_ADDR_LEN	5
TAG_ADDR_LEN	3

Resource	Estimation	Available	Utilization %
LUT	12476	32600	38.27
FF	36176	65200	55.48
BRAM	8	75	10.67
IO	82	106	77.36
BUFG	1	32	3.13

综合数据 (FIFO)

LINE_ADDR_LEN	2
SET_ADDR_LEN	2
TAG_ADDR_LEN	9
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	1668	32600	5.12
FF	2357	65200	3.62
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	2303	32600	7.06
FF	2913	65200	4.47
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	2615	32600	8.02
FF	3469	65200	5.32
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3028	32600	9.29
FF	4025	65200	6.17
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3417	32600	10.48
FF	4581	65200	7.03
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3779	32600	11.59
FF	5137	65200	7.88
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	1987	32600	6.10
FF	4129	65200	6.33
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	2685	32600	8.24
FF	5233	65200	8.03
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3052	32600	9.36
FF	6337	65200	9.72
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3542	32600	10.87
FF	7441	65200	11.41
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4070	32600	12.48
FF	8545	65200	13.11
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4277	32600	13.12
FF	9649	65200	14.80
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	3345	32600	10.26
FF	7652	65200	11.74
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4337	32600	13.30
FF	9843	65200	15.10
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5094	32600	15.63
FF	12035	65200	18.46
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5980	32600	18.34
FF	14229	65200	21.82
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6857	32600	21.03
FF	16421	65200	25.19
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8355	32600	25.63
FF	18632	65200	28.58
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	2
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	3397	32600	10.42
FF	4262	65200	6.54
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4217	32600	12.94
FF	5326	65200	8.17
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4490	32600	13.77
FF	6390	65200	9.80
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5669	32600	17.39
FF	7454	65200	11.43
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6168	32600	18.92
FF	8518	65200	13.06
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6893	32600	21.14
FF	9582	65200	14.70
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	3
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	3403	32600	10.44
FF	7558	65200	11.59
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4747	32600	14.56
FF	9678	65200	14.84
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5123	32600	15.71
FF	11798	65200	18.10
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6169	32600	18.92
FF	13918	65200	21.35
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7135	32600	21.89
FF	16038	65200	24.60
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7006	32600	21.49
FF	18158	65200	27.85
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	5720	32600	17.55
FF	14128	65200	21.67
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7730	32600	23.71
FF	18352	65200	28.15
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9888	32600	30.33
FF	22595	65200	34.65
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9948	32600	30.52
FF	26803	65200	41.11
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	10796	32600	33.12
FF	31024	65200	47.58
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	13507	32600	41.43
FF	35252	65200	54.07
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	5434	32600	16.67
FF	8087	65200	12.40
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8264	32600	25.35
FF	10171	65200	15.60
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8777	32600	26.92
FF	12255	65200	18.80
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	11122	32600	34.12
FF	14339	65200	21.99
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	12653	32600	38.81
FF	16423	65200	25.19
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	14099	32600	43.25
FF	18515	65200	28.40
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	6202	32600	19.02
FF	14443	65200	22.15
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8514	32600	26.12
FF	18603	65200	28.53
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	10241	32600	31.41
FF	22768	65200	34.92
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	11374	32600	34.89
FF	26923	65200	41.29
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	12922	32600	39.64
FF	31089	65200	47.68
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	13327	32600	40.88
FF	35243	65200	54.05
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO

Resource	Estimation	Available	Utilization %
LUT	10571	32600	32.43
FF	27133	65200	41.62
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	13815	32600	42.38
FF	35437	65200	54.35
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	16378	32600	50.24
FF	43739	65200	67.08
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	18113	32600	55.56
FF	52049	65200	79.83
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	20677	32600	63.43
FF	60351	65200	92.56
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	23197	32600	71.16
FF	68658	65200	105.30
BRAM	1	75	1.33
IO	82	106	77.36
BUFG	1	32	3.13

综合数据 (LRU)

LINE_ADDR_LEN	2
SET_ADDR_LEN	2
TAG_ADDR_LEN	9
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	2460	47200	5.21
FF	2809	94400	2.98
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3624	47200	7.68
FF	3489	94400	3.70
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4049	47200	8.58
FF	4173	94400	4.42
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4927	47200	10.44
FF	4861	94400	5.15
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5370	47200	11.38
FF	5541	94400	5.87
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6110	47200	12.94
FF	6225	94400	6.59
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	2968	47200	6.29
FF	4969	94400	5.26
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	3952	47200	8.37
FF	6329	94400	6.70
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	4648	47200	9.85
FF	7692	94400	8.15
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5461	47200	11.57
FF	9054	94400	9.59
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6282	47200	13.31
FF	10417	94400	11.03
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6993	47200	14.82
FF	11777	94400	12.48
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	4618	47200	9.78
FF	9264	94400	9.81
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6148	47200	13.03
FF	11973	94400	12.68
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7077	47200	14.99
FF	14676	94400	15.55
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7720	47200	16.36
FF	17384	94400	18.42
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8985	47200	19.04
FF	20096	94400	21.29
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	10436	47200	22.11
FF	22803	94400	24.16
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	2
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	3897	47200	8.26
FF	4714	94400	4.99
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	5409	47200	11.46
FF	5902	94400	6.25
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6156	47200	13.04
FF	7098	94400	7.52
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6854	47200	14.52
FF	8292	94400	8.78
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9011	47200	19.09
FF	9482	94400	10.04
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9466	47200	20.06
FF	10672	94400	11.31
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	3
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	4367	47200	9.25
FF	8398	94400	8.90
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6052	47200	12.82
FF	10774	94400	11.41
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	6519	47200	13.81
FF	13151	94400	13.93
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	7100	47200	15.04
FF	15530	94400	16.45
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9055	47200	19.18
FF	17908	94400	18.97
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	8813	47200	18.67
FF	20287	94400	21.49
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	6659	47200	14.11
FF	15739	94400	16.67
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9083	47200	19.24
FF	20483	94400	21.70
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	11130	47200	23.58
FF	25221	94400	26.72
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	13096	47200	27.75
FF	29959	94400	31.74
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	14257	47200	30.21
FF	34696	94400	36.75
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	15033	47200	31.85
FF	39437	94400	41.78
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	6616	47200	14.02
FF	8541	94400	9.05
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9028	47200	19.13
FF	10747	94400	11.38
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	12248	47200	25.95
FF	12973	94400	13.74
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	14046	47200	29.76
FF	15175	94400	16.08
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	14639	47200	31.01
FF	17383	94400	18.41
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	16985	47200	35.99
FF	19595	94400	20.76
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	6924	47200	14.67
FF	15279	94400	16.19
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	9459	47200	20.04
FF	19704	94400	20.87
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	10187	47200	21.58
FF	24114	94400	25.54
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	12244	47200	25.94
FF	28535	94400	30.23
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	13566	47200	28.74
FF	32956	94400	34.91
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	16800	47200	35.59
FF	37369	94400	39.59
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU

Resource	Estimation	Available	Utilization %
LUT	11462	47200	24.28
FF	28744	94400	30.45
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	15237	47200	32.28
FF	37565	94400	39.79
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	17919	47200	37.96
FF	46386	94400	49.14
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	20904	47200	44.29
FF	55203	94400	58.48
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IO	82	210	39.05
BUFG	1	32	3.13

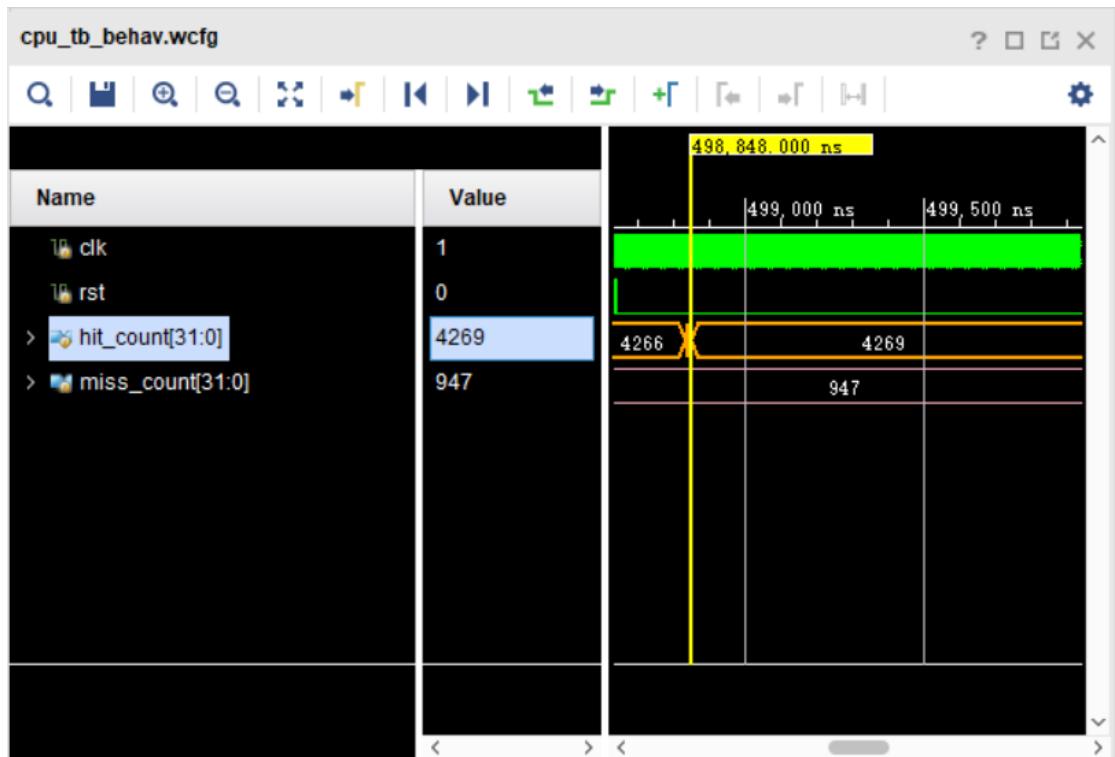
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FF	64024	94400	67.82
BRAM	1	105	0.95
IO	82	210	39.05
BUFG	1	32	3.13

Resource	Estimation	Available	Utilization %
LUT	26353	47200	55.83
FF	72846	94400	77.17
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IO	82	210	39.05
BUFG	1	32	3.13

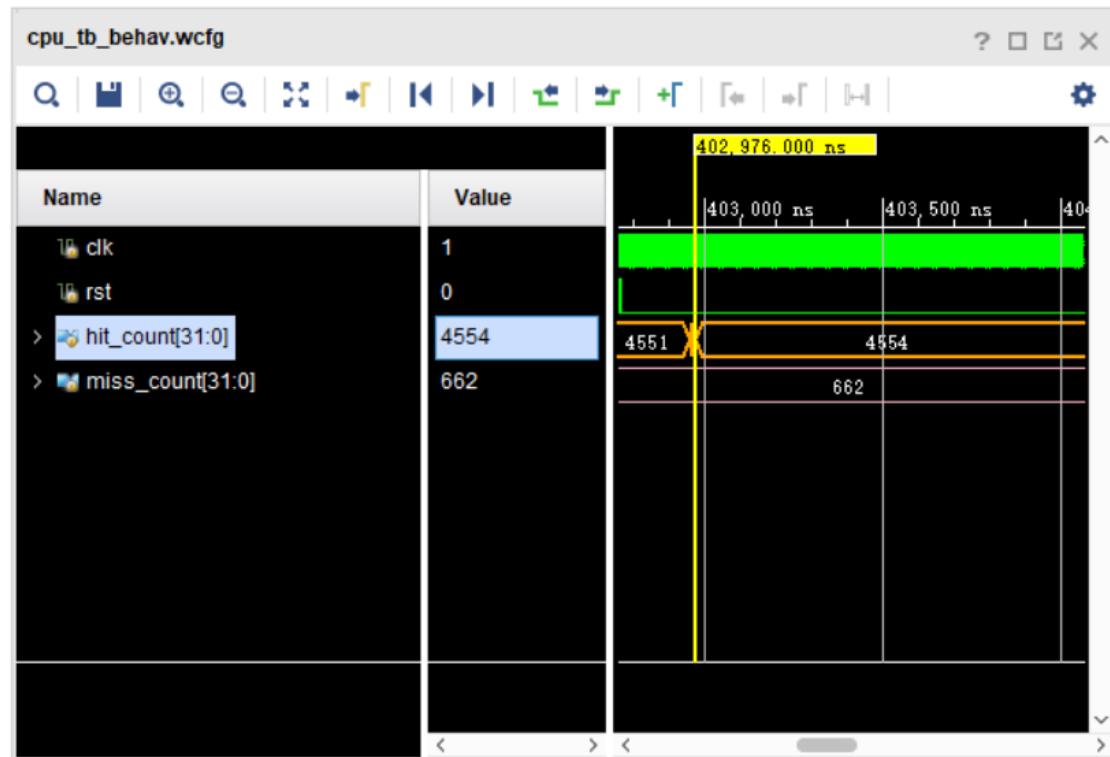
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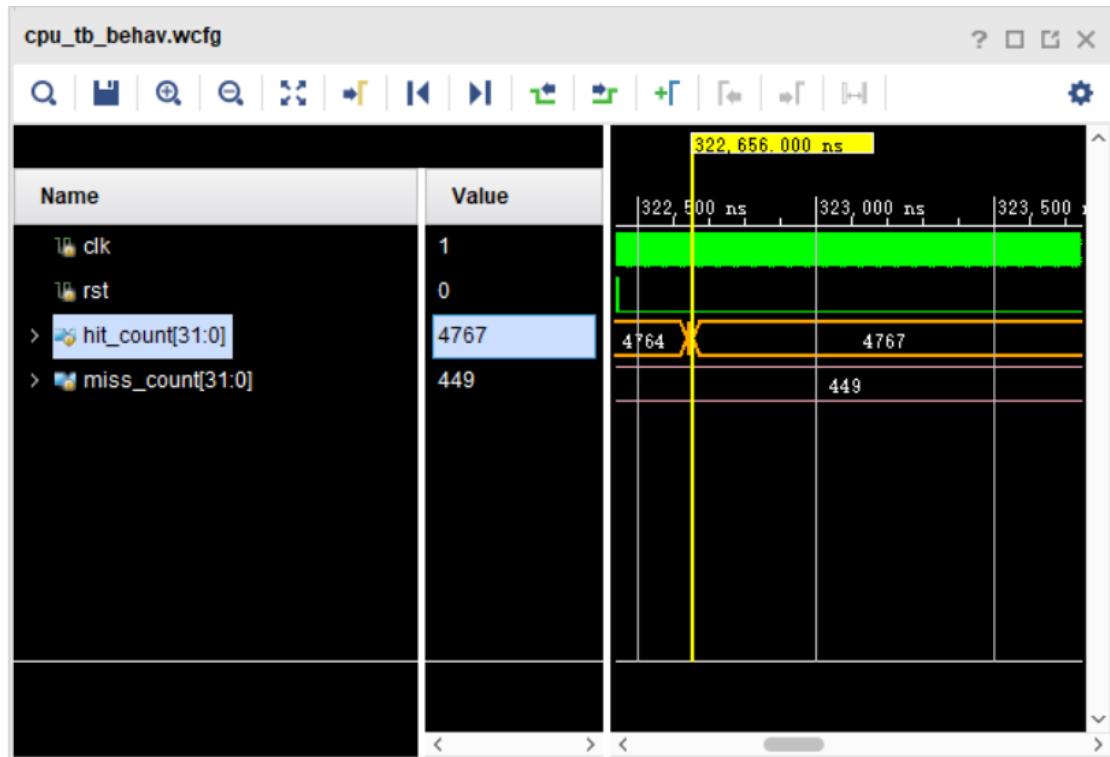
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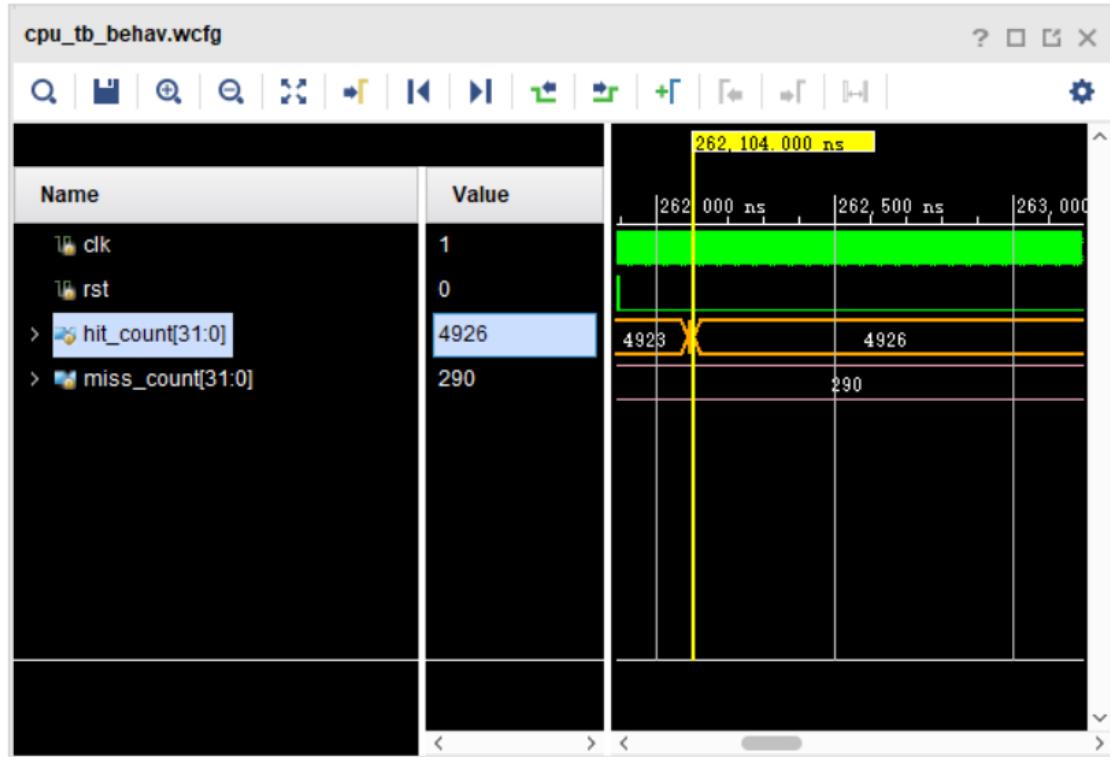
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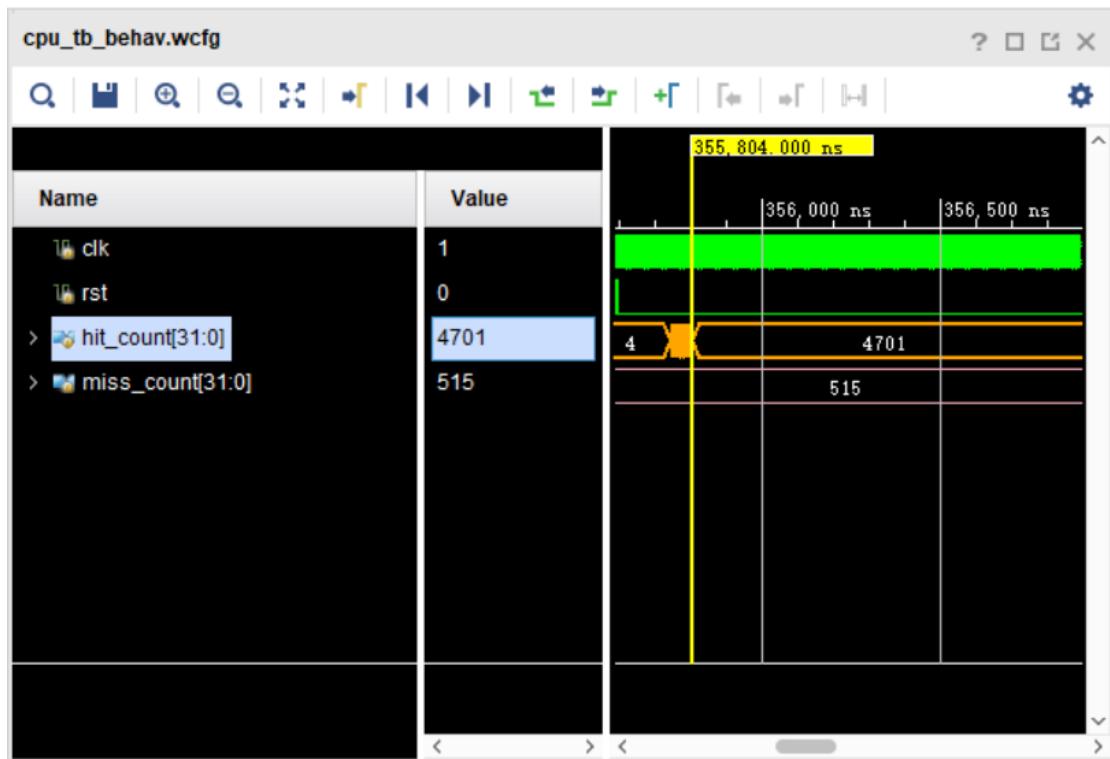
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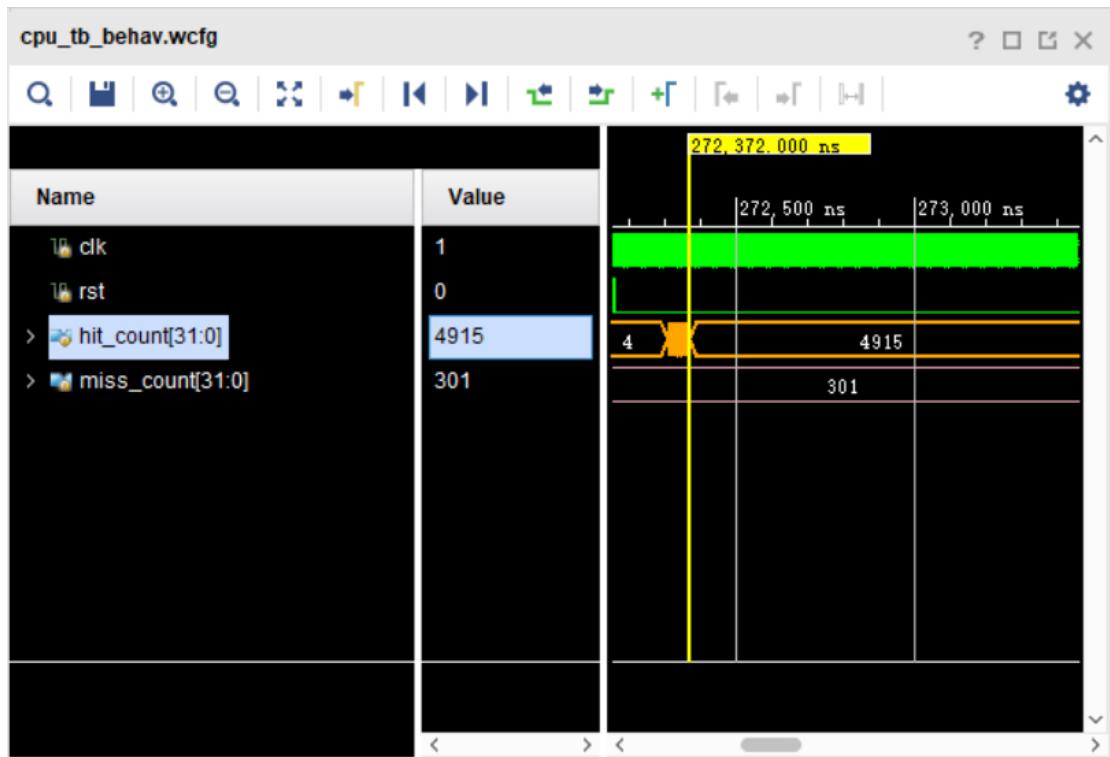
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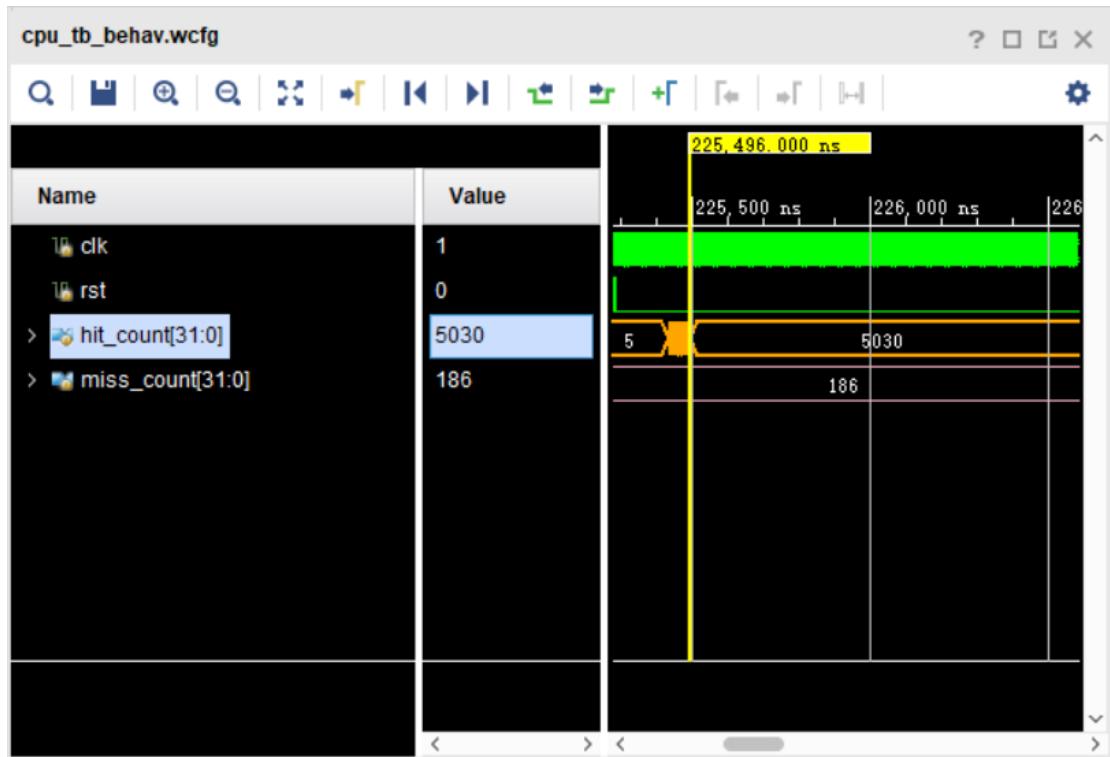
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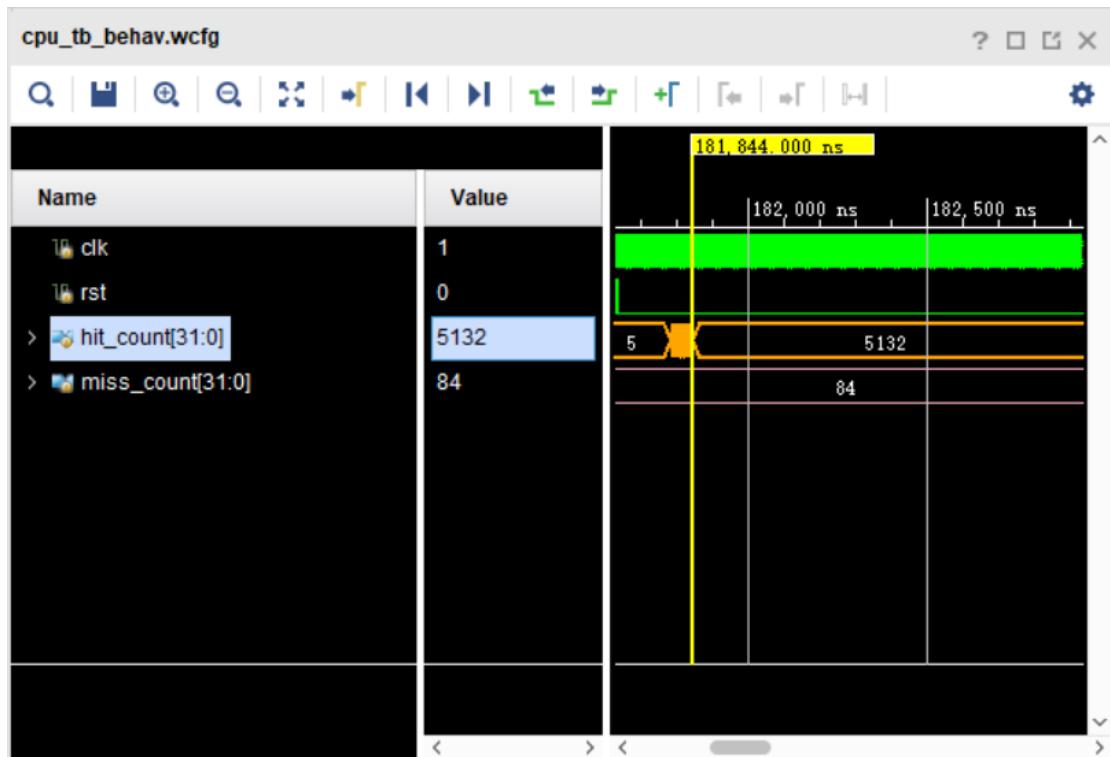
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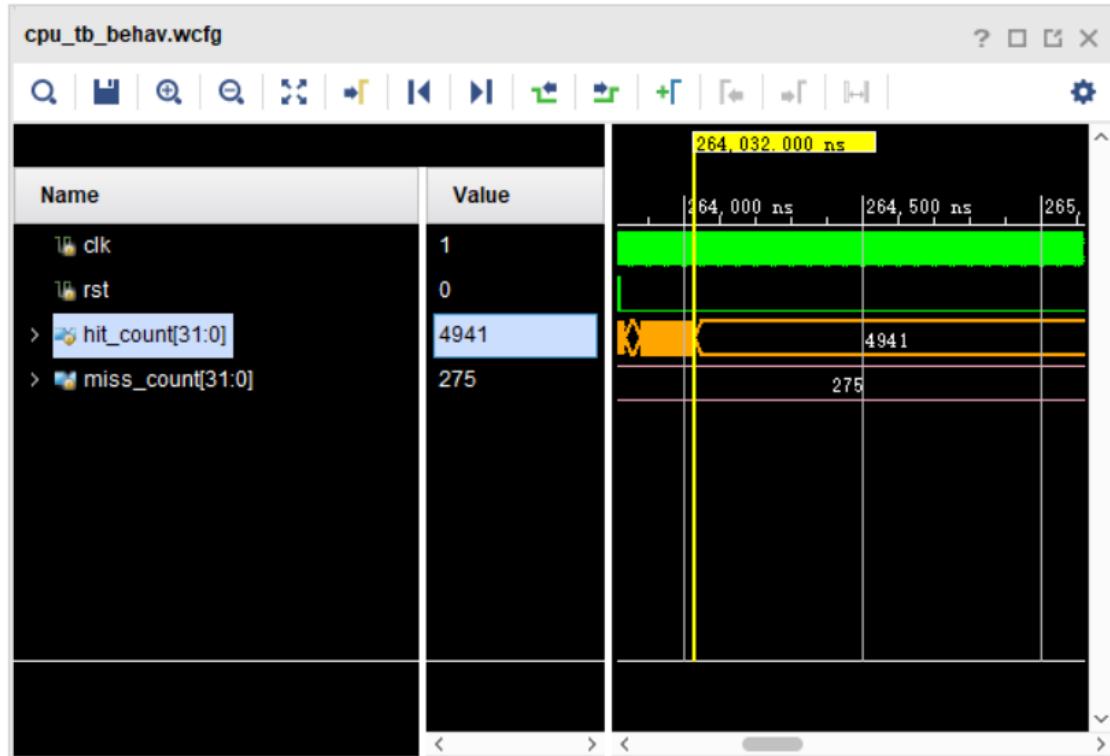
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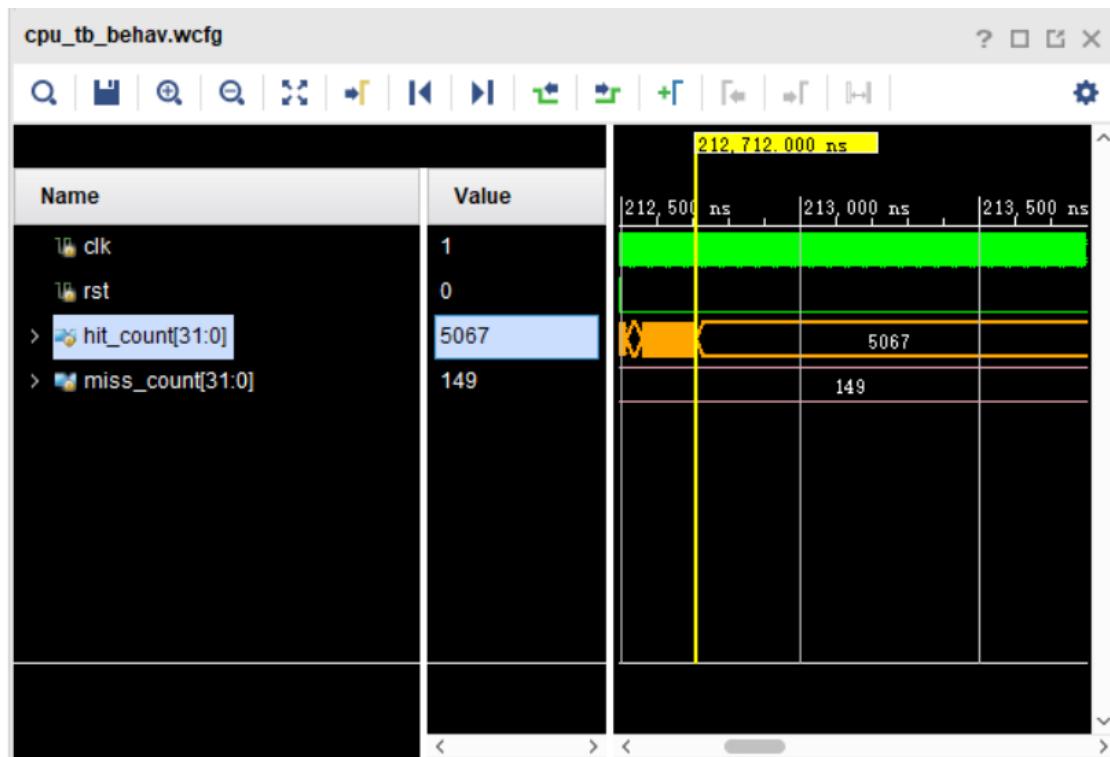
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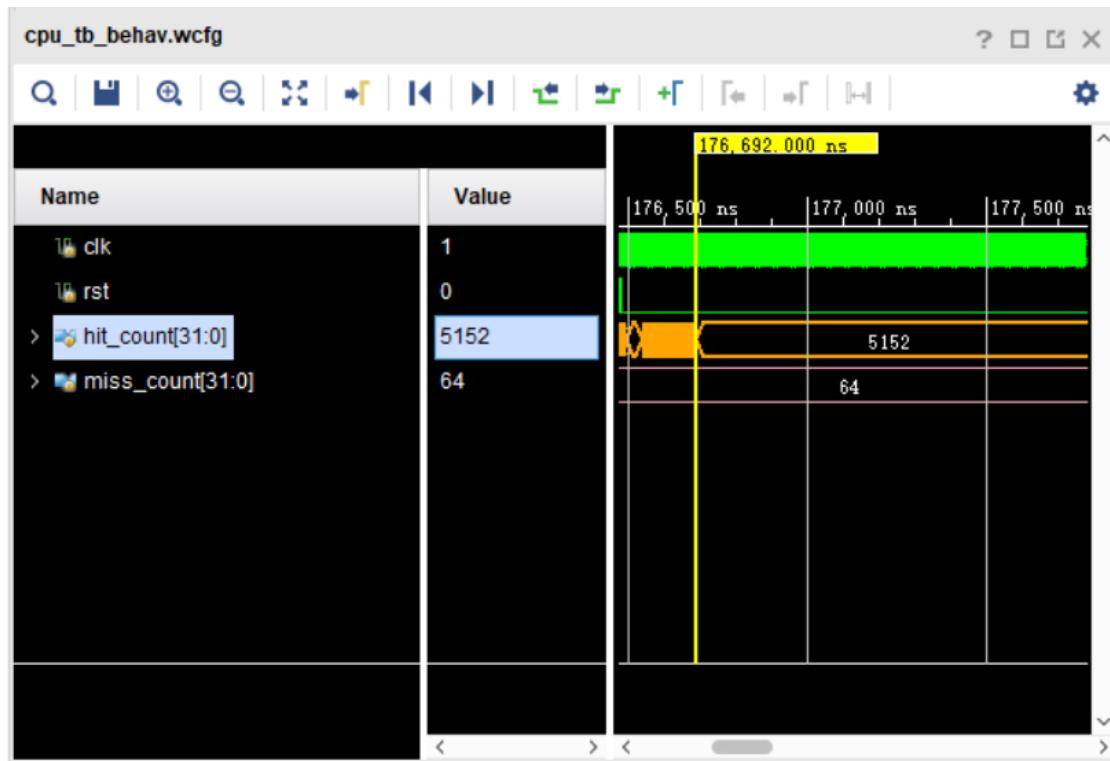
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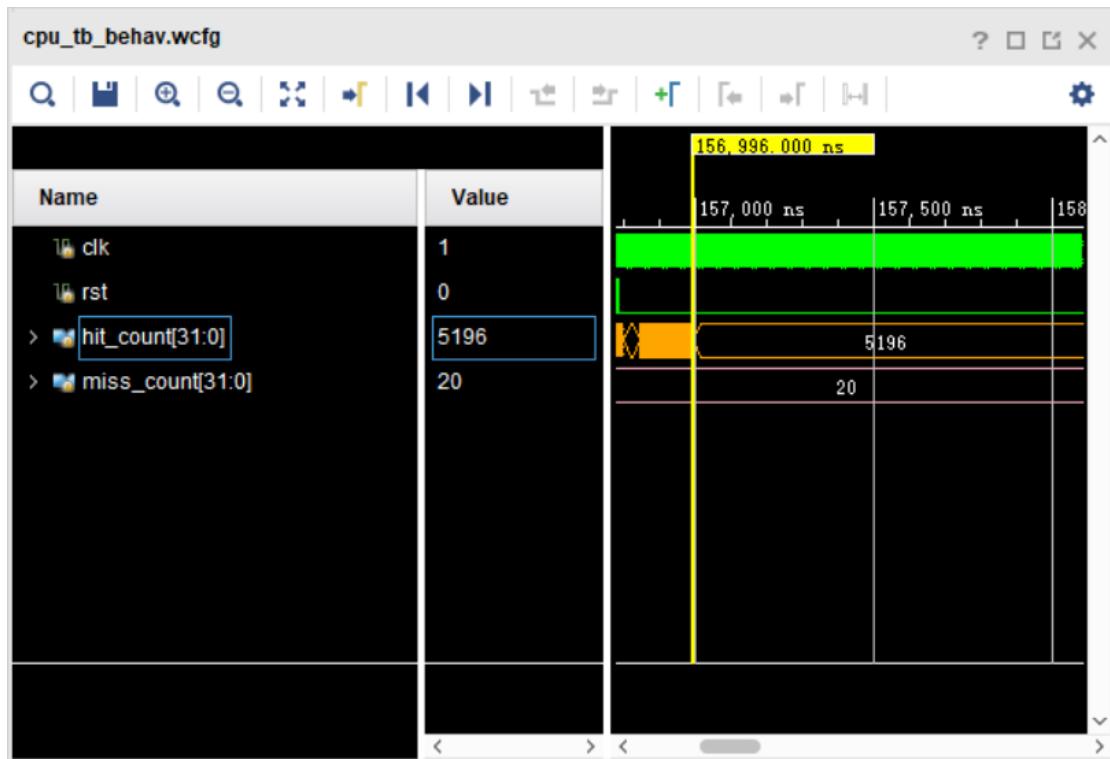
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Test File	QUICKSORT



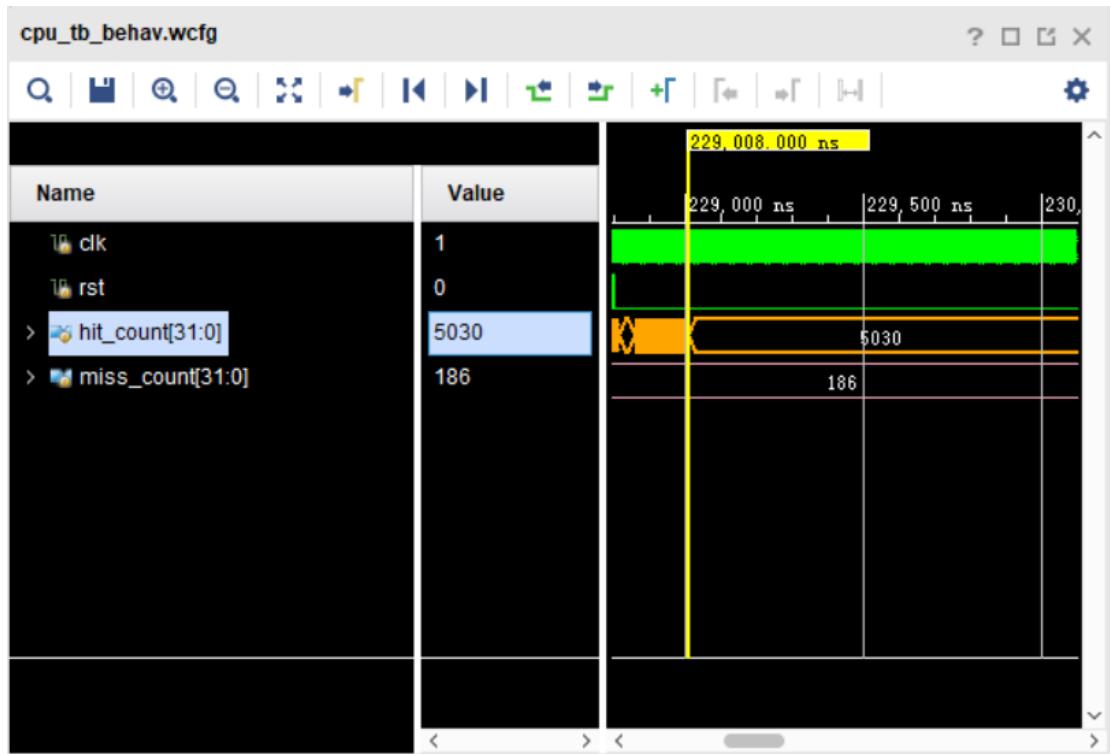
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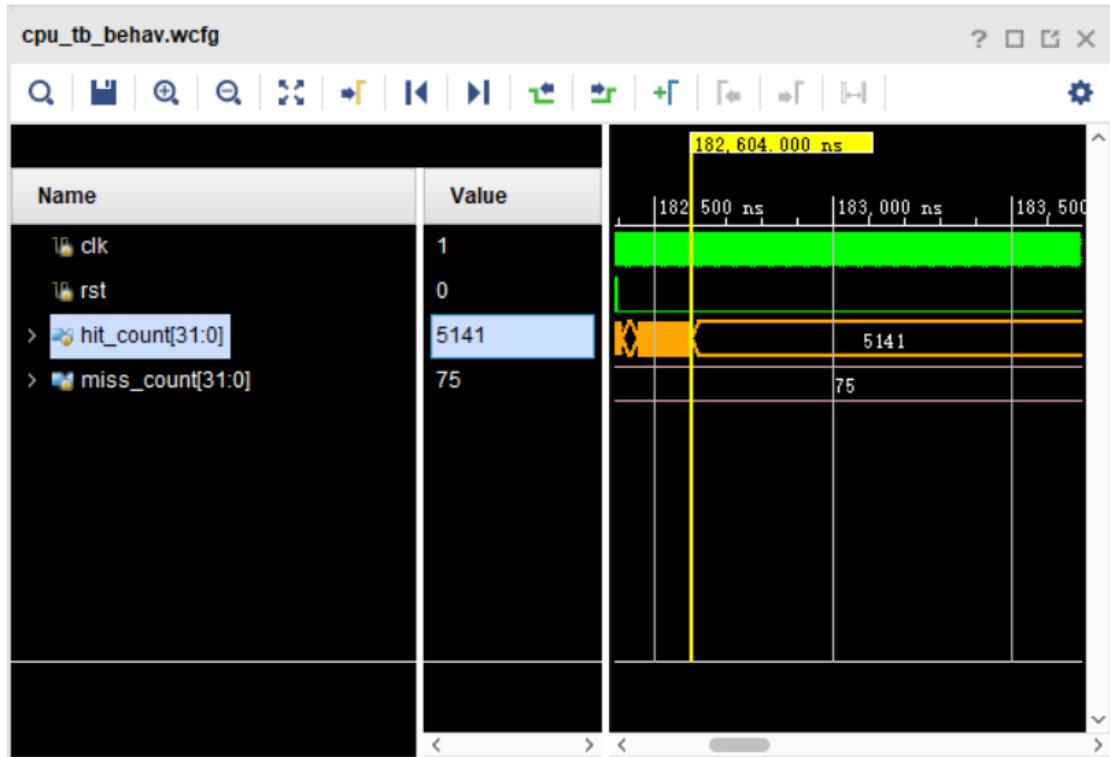
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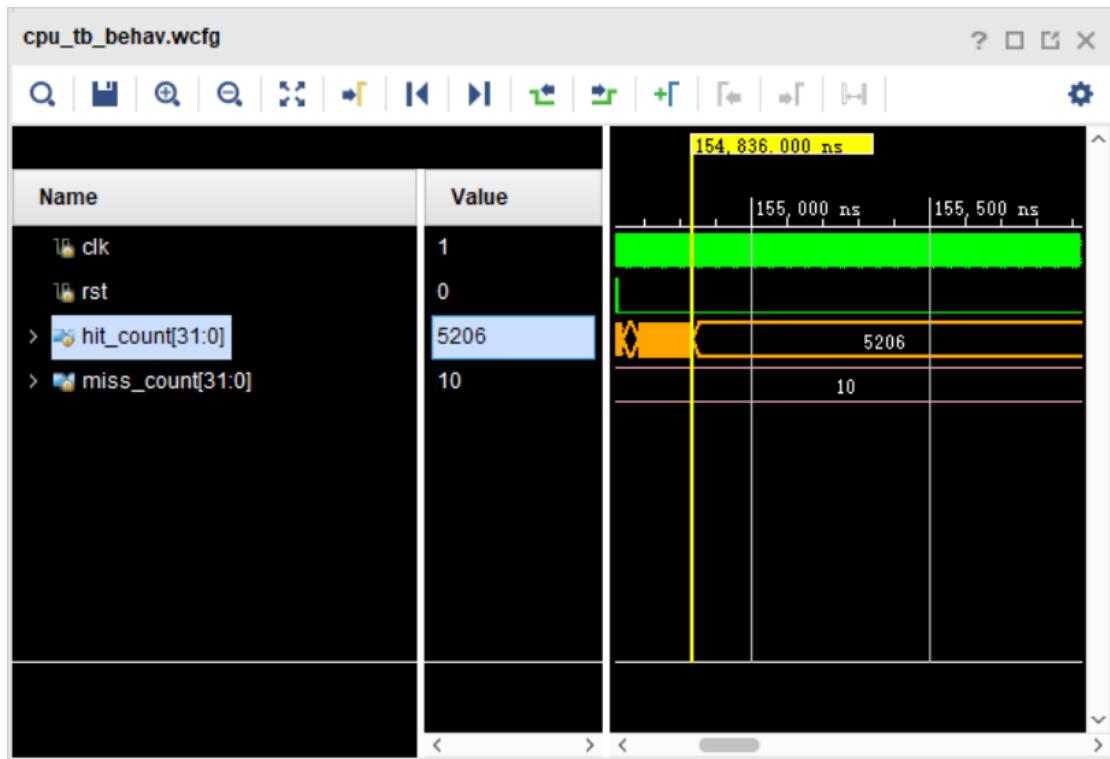
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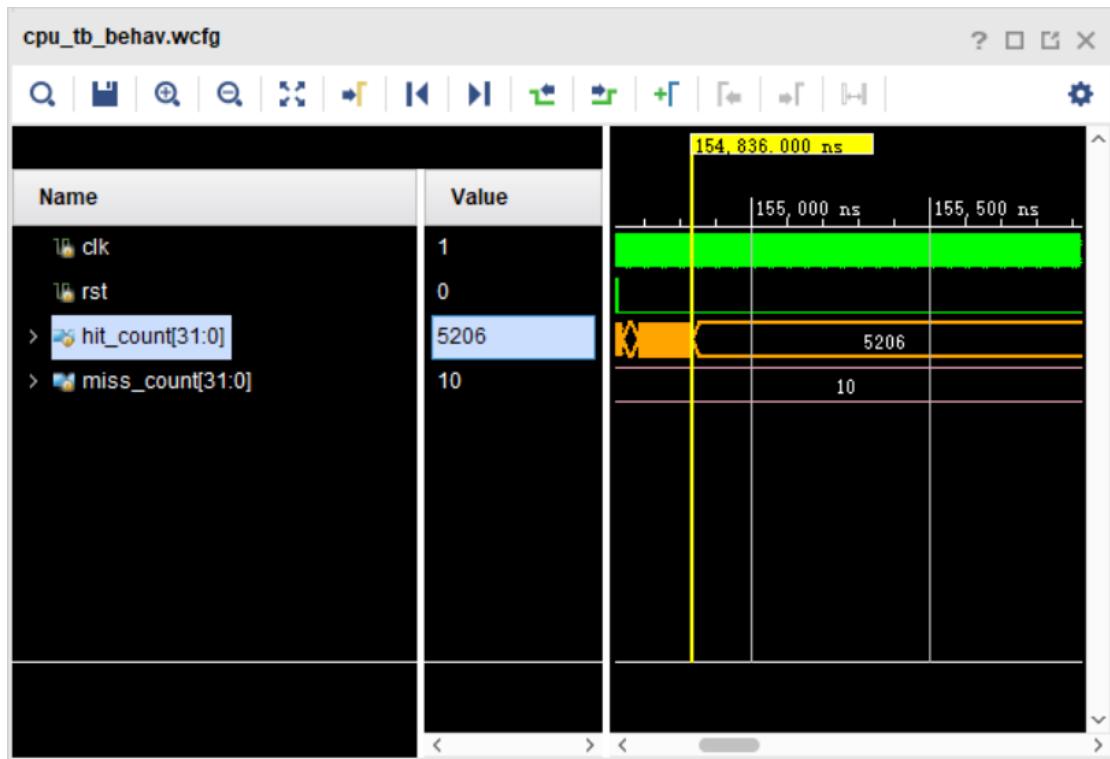
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Test File	QUICKSORT



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Test File	QUICKSORT

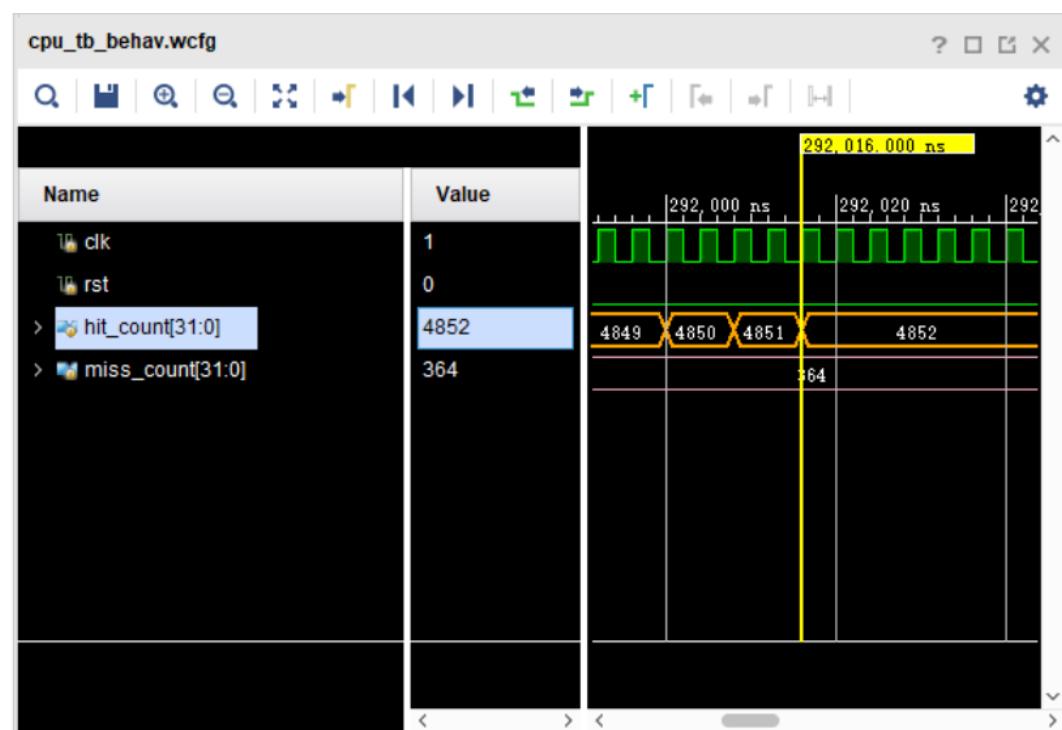
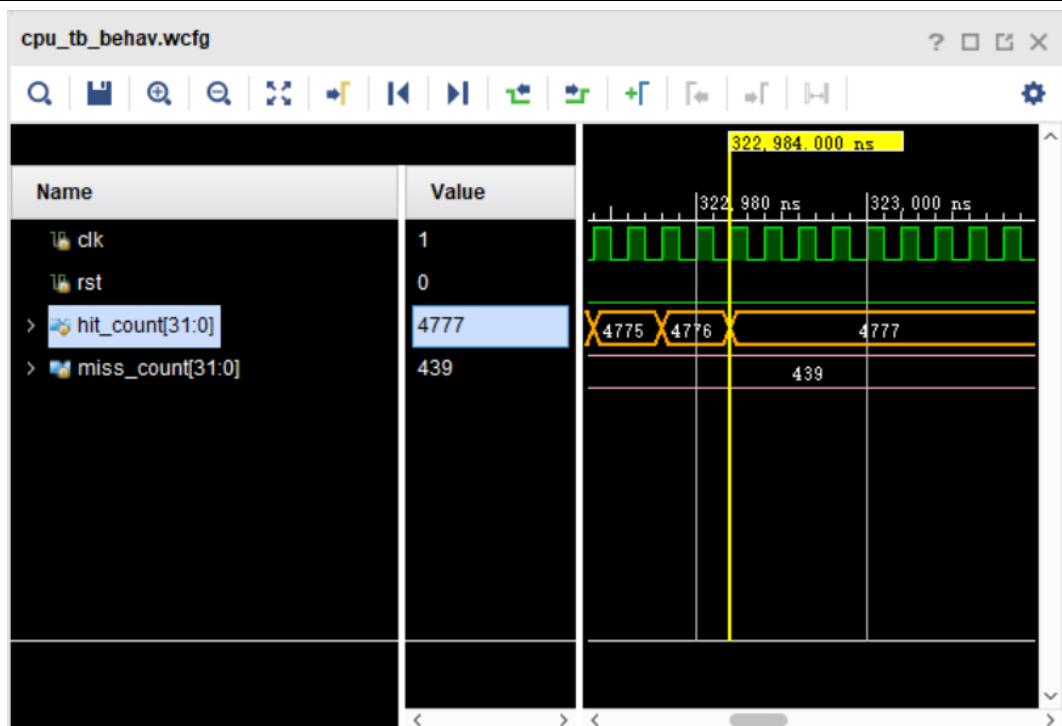


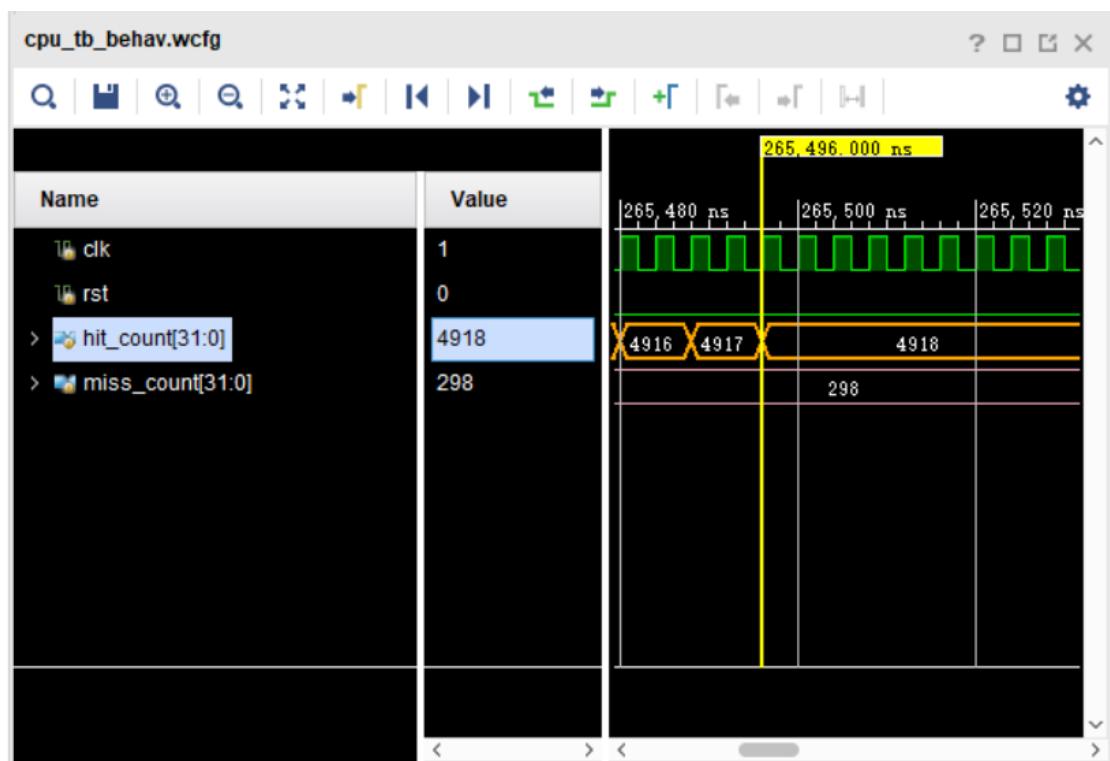
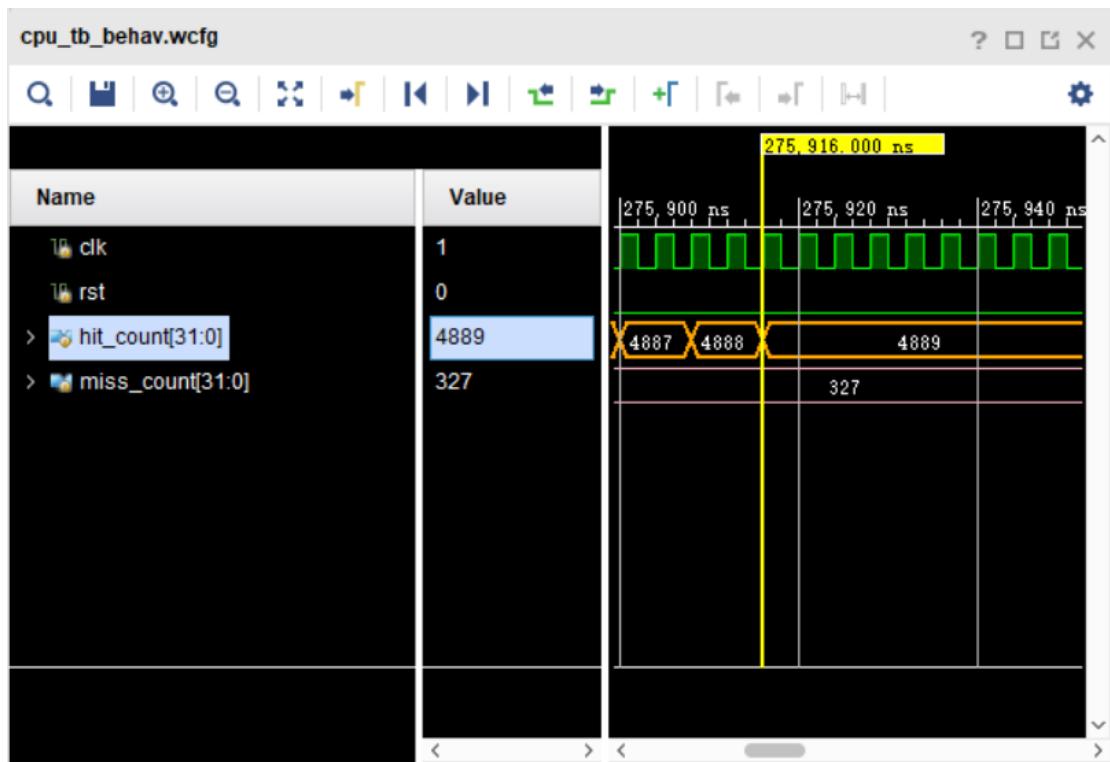
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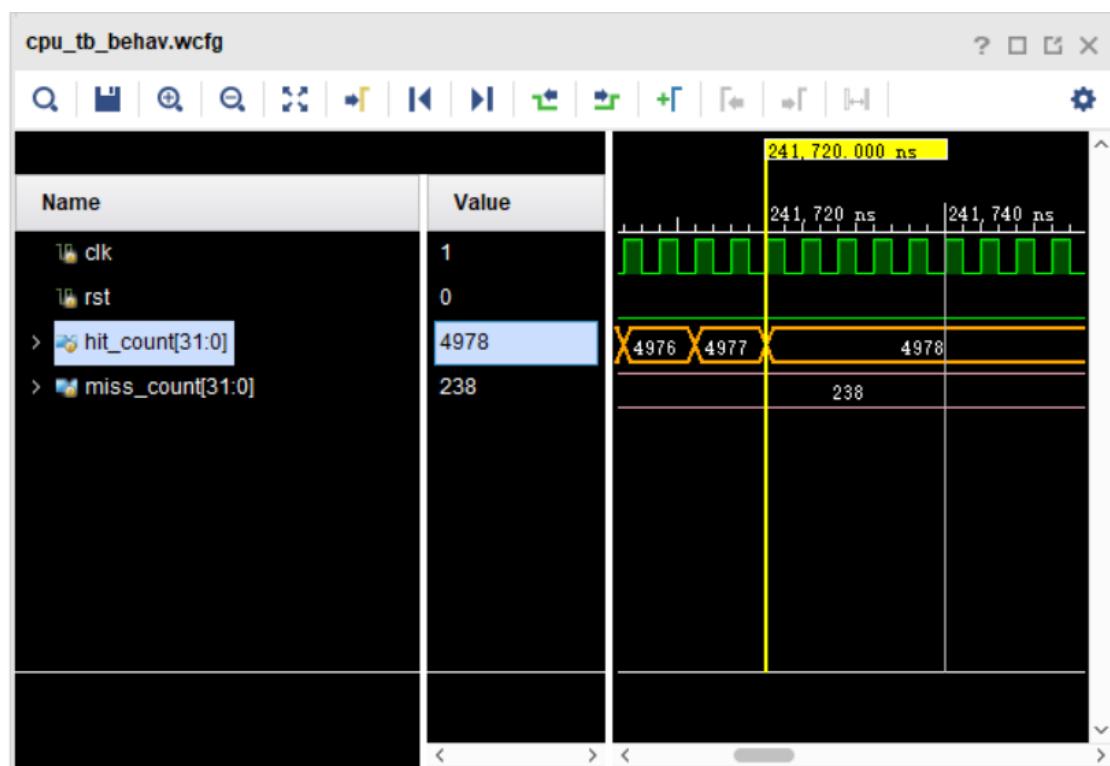
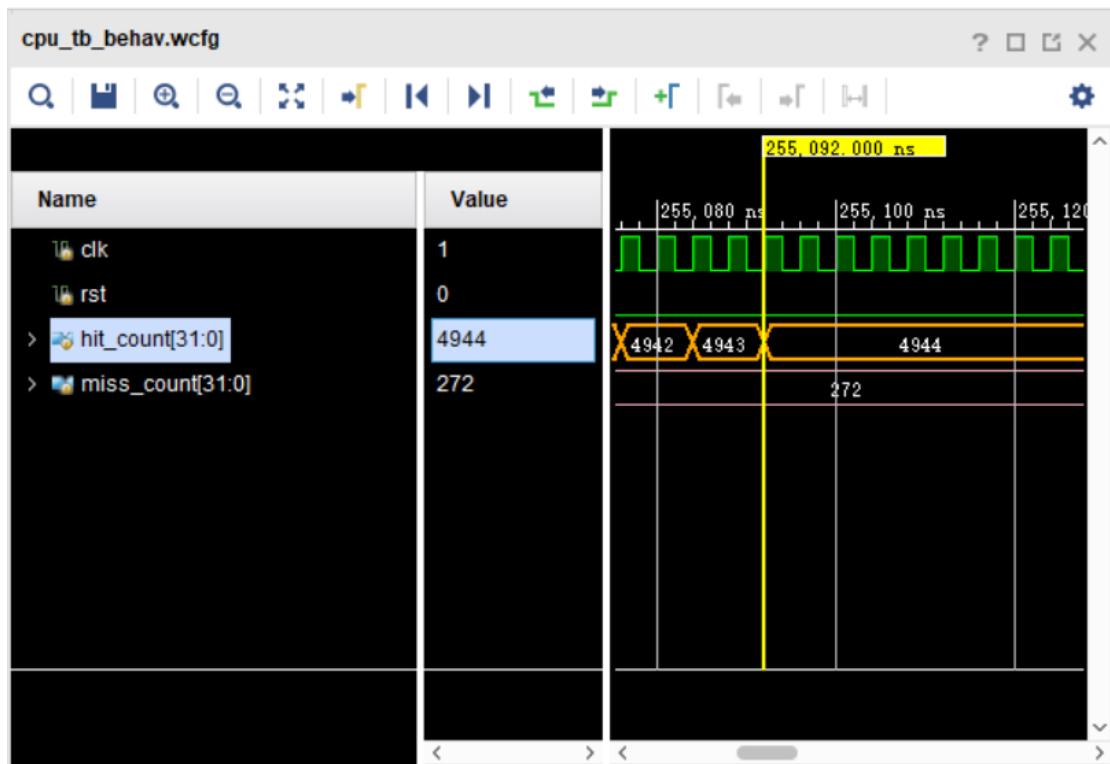


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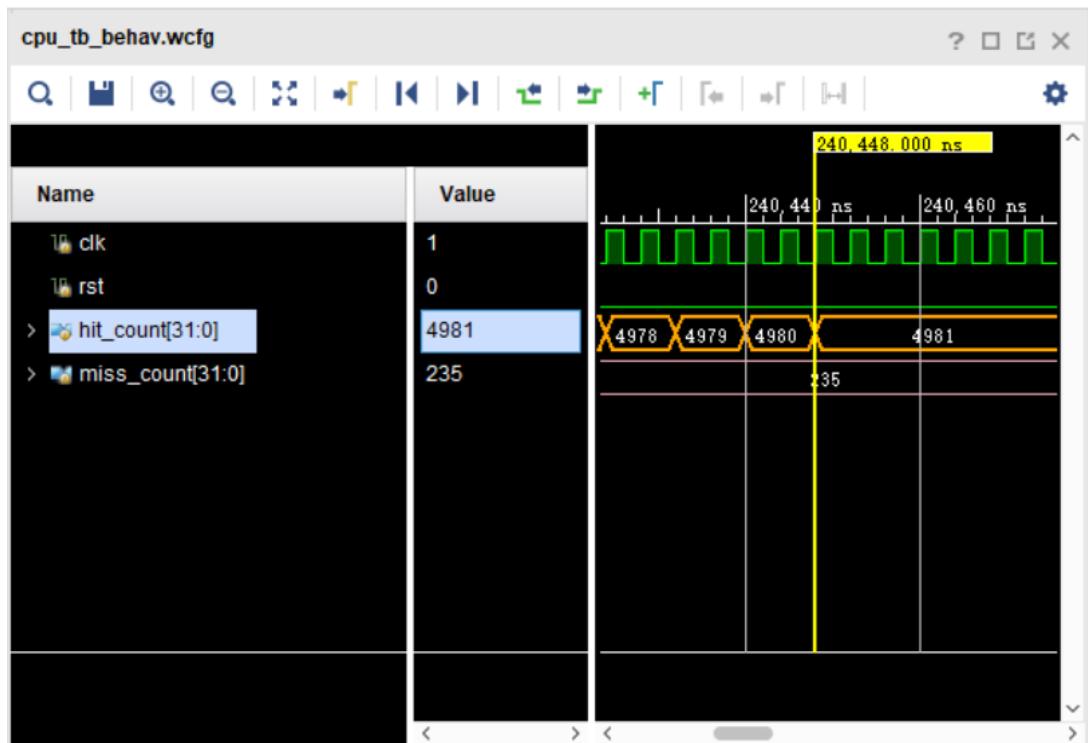
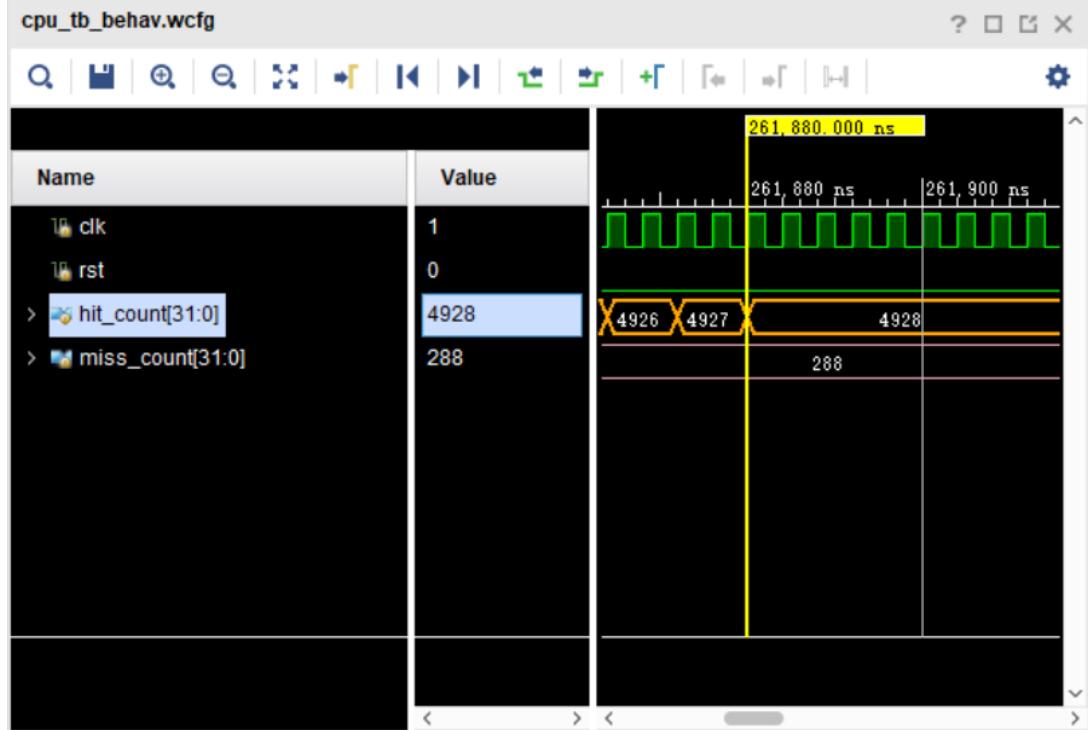
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Policy	FIFO
Test File	QUICKSORT

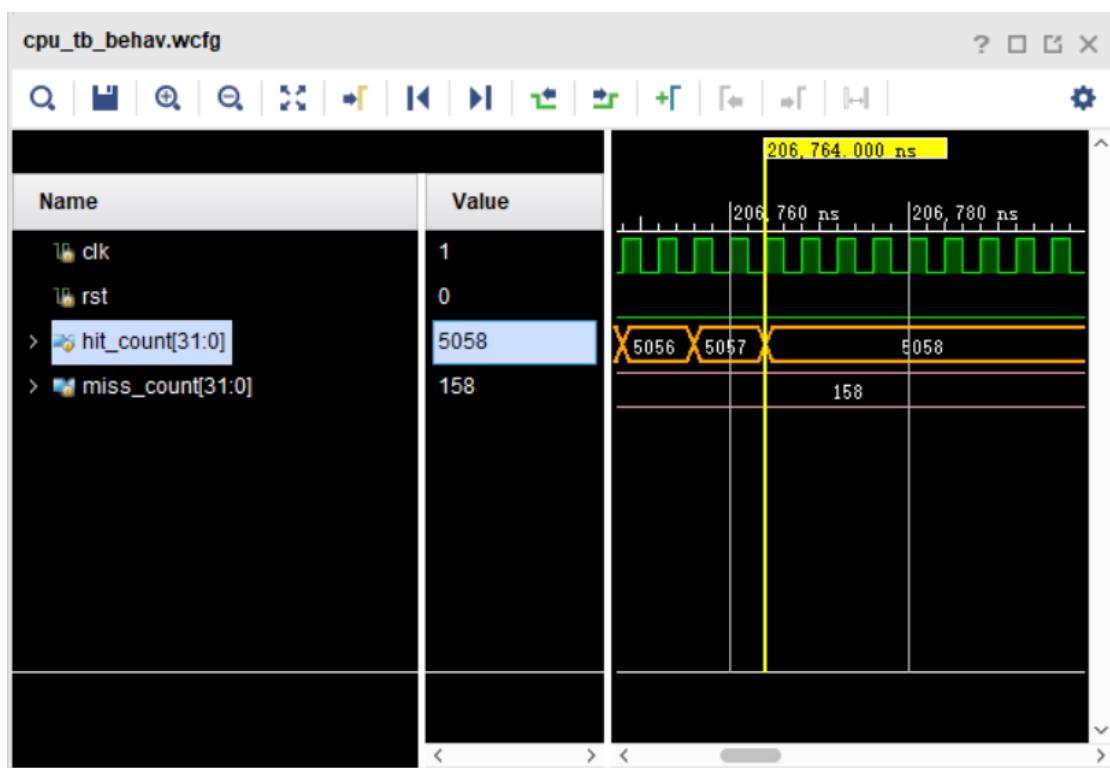
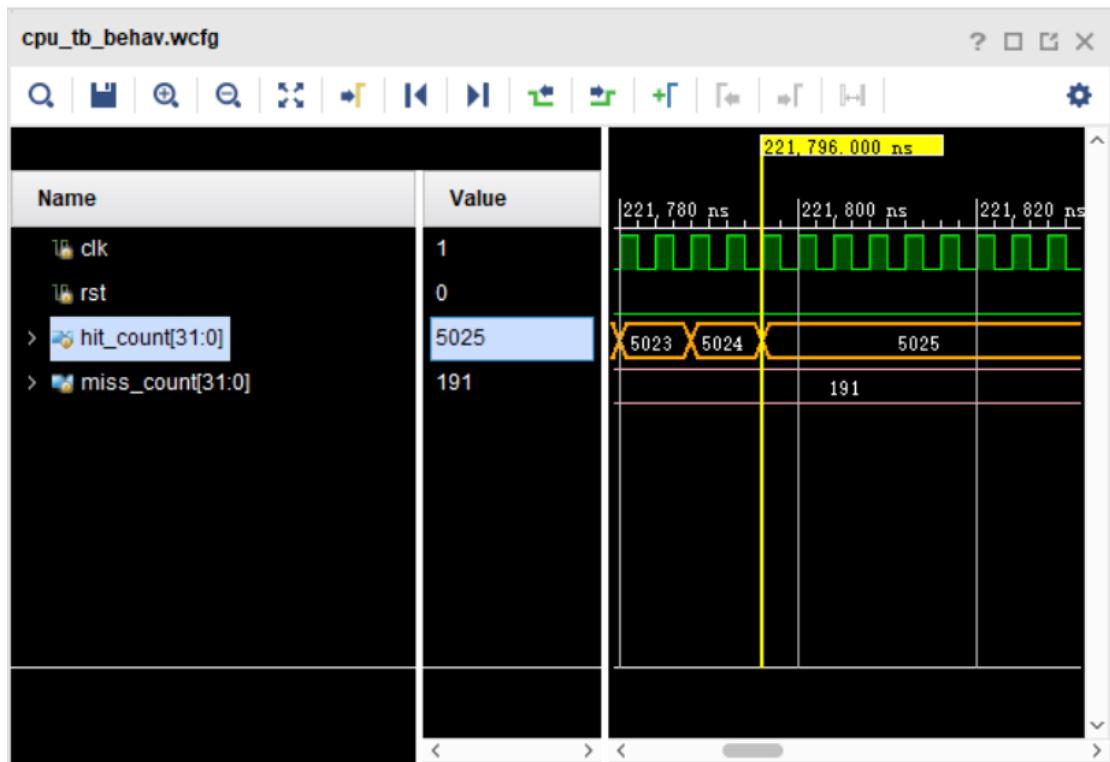


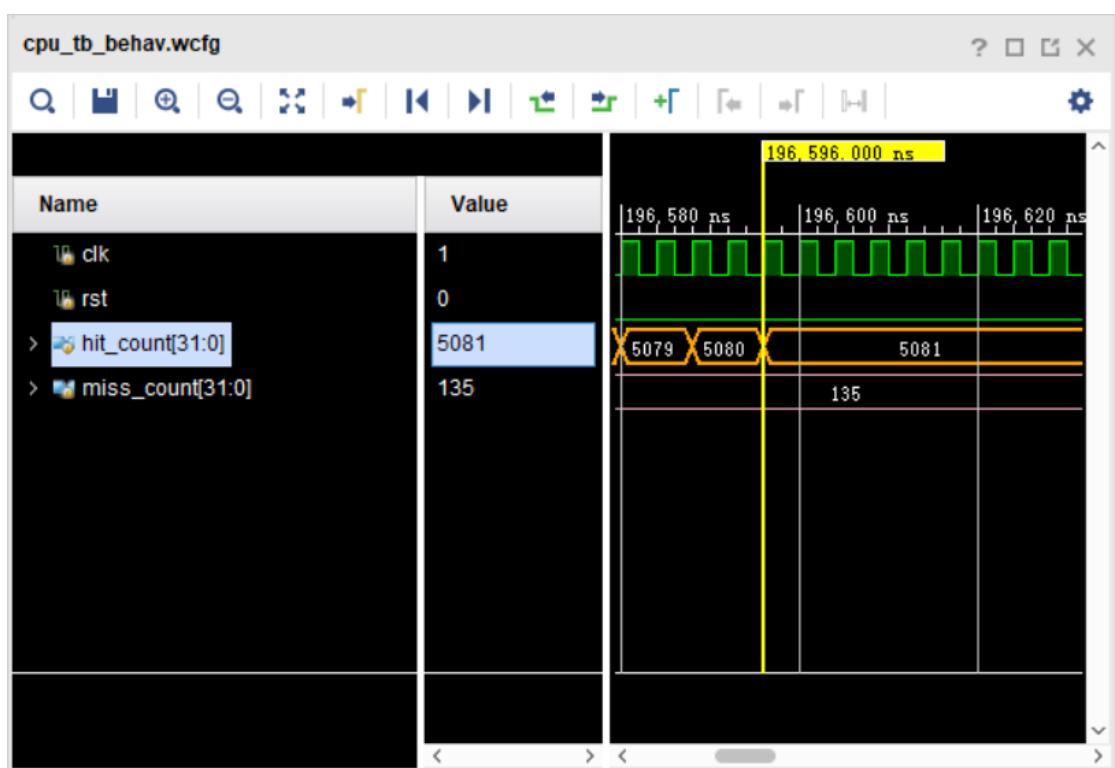
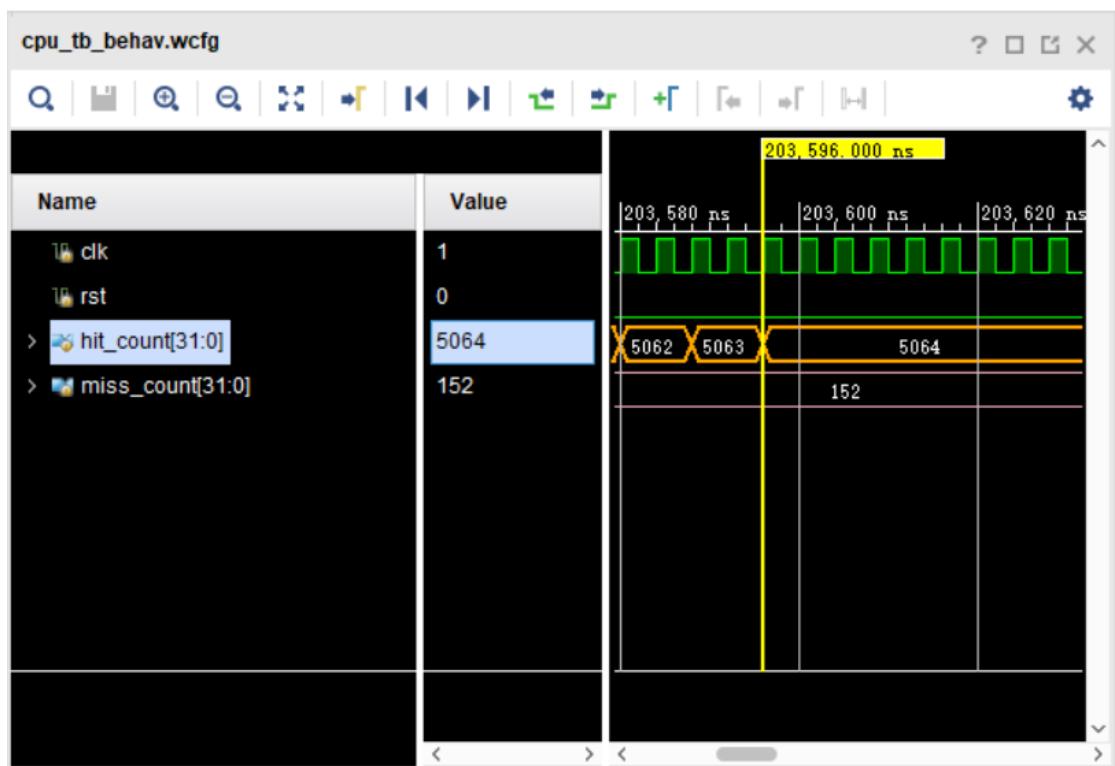




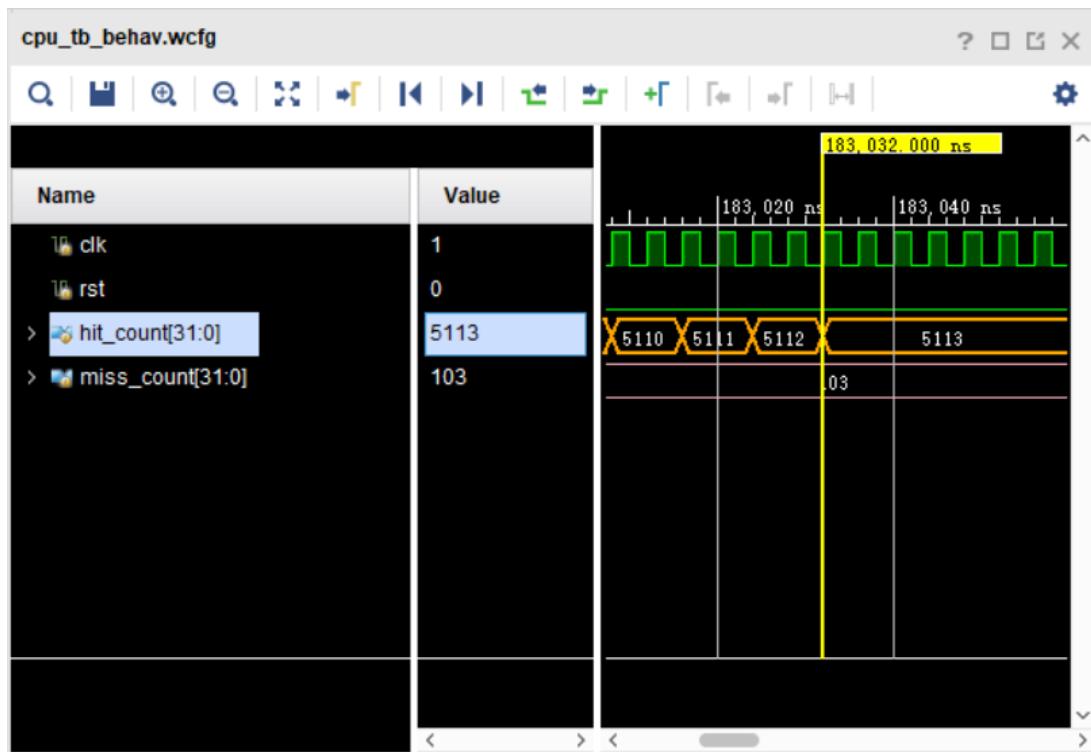
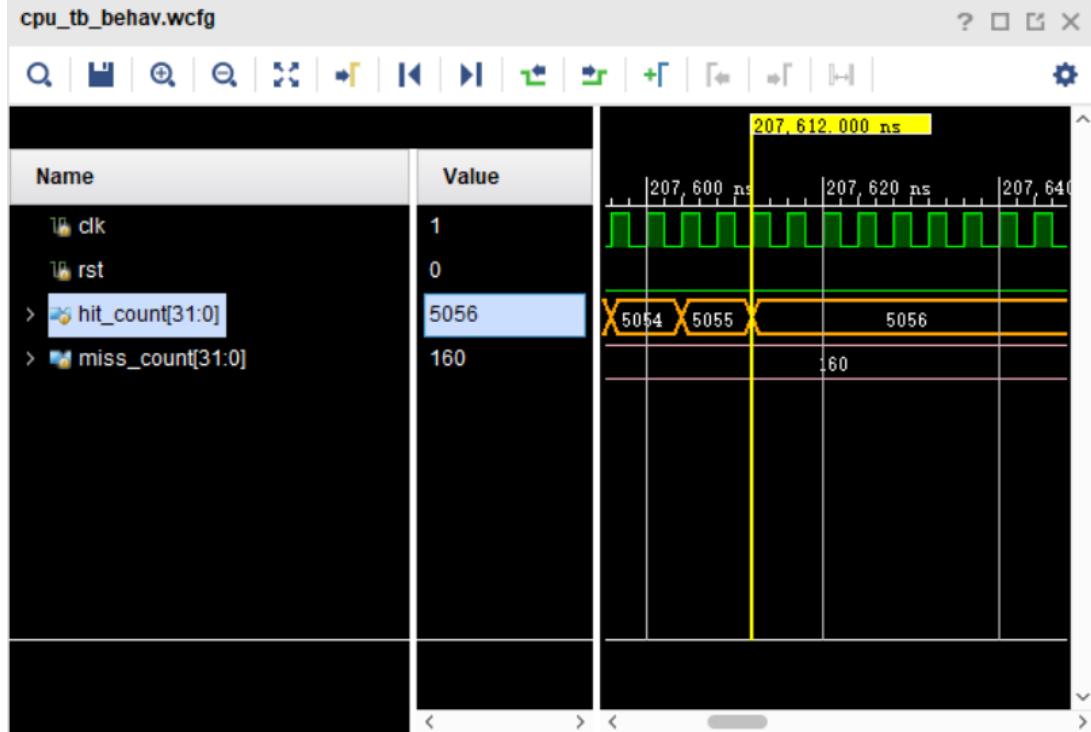
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Test File	QUICKSORT

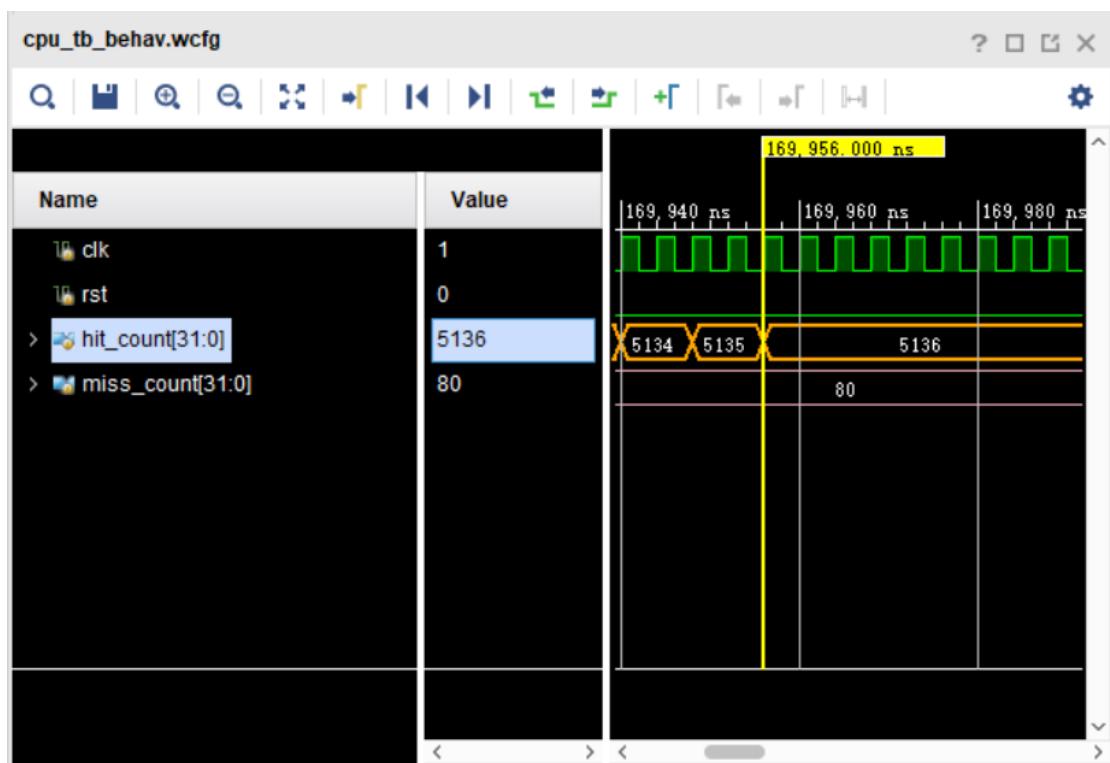
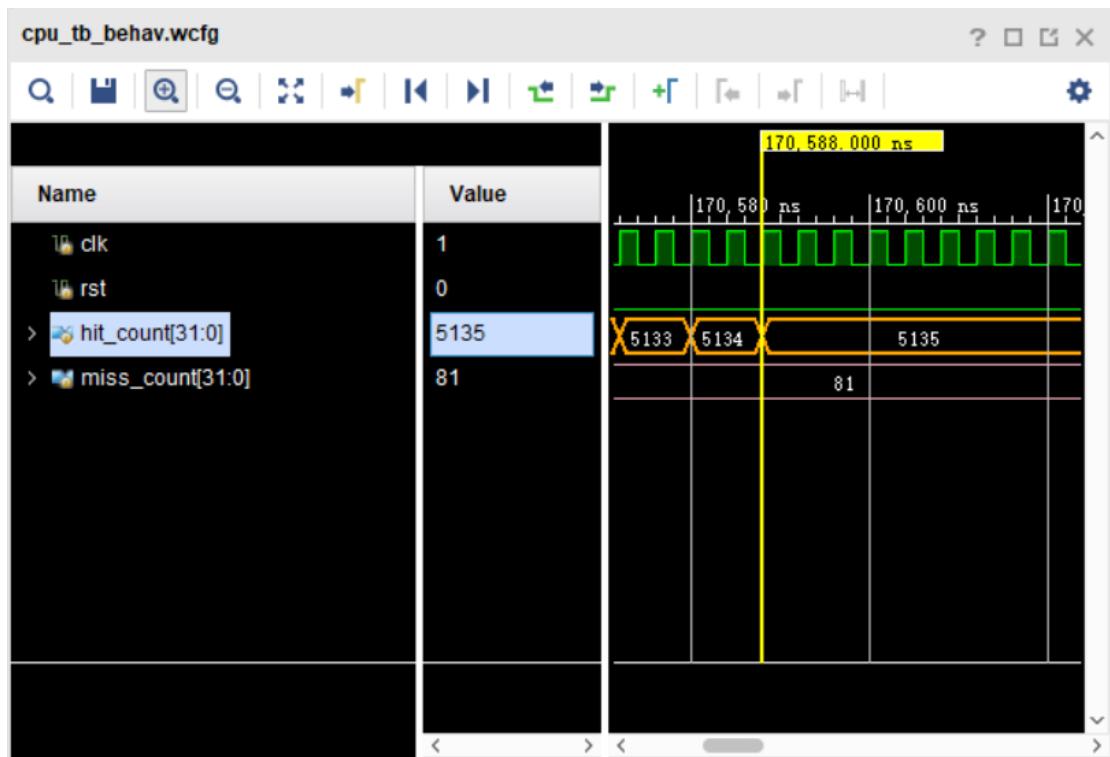


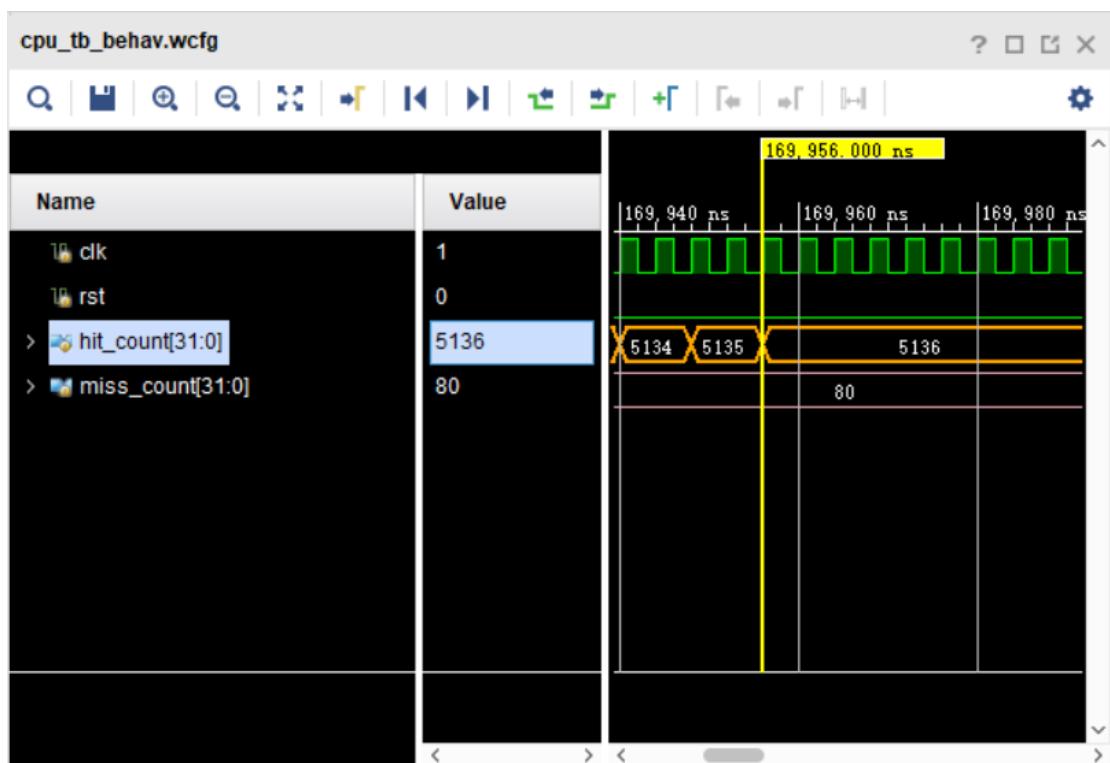
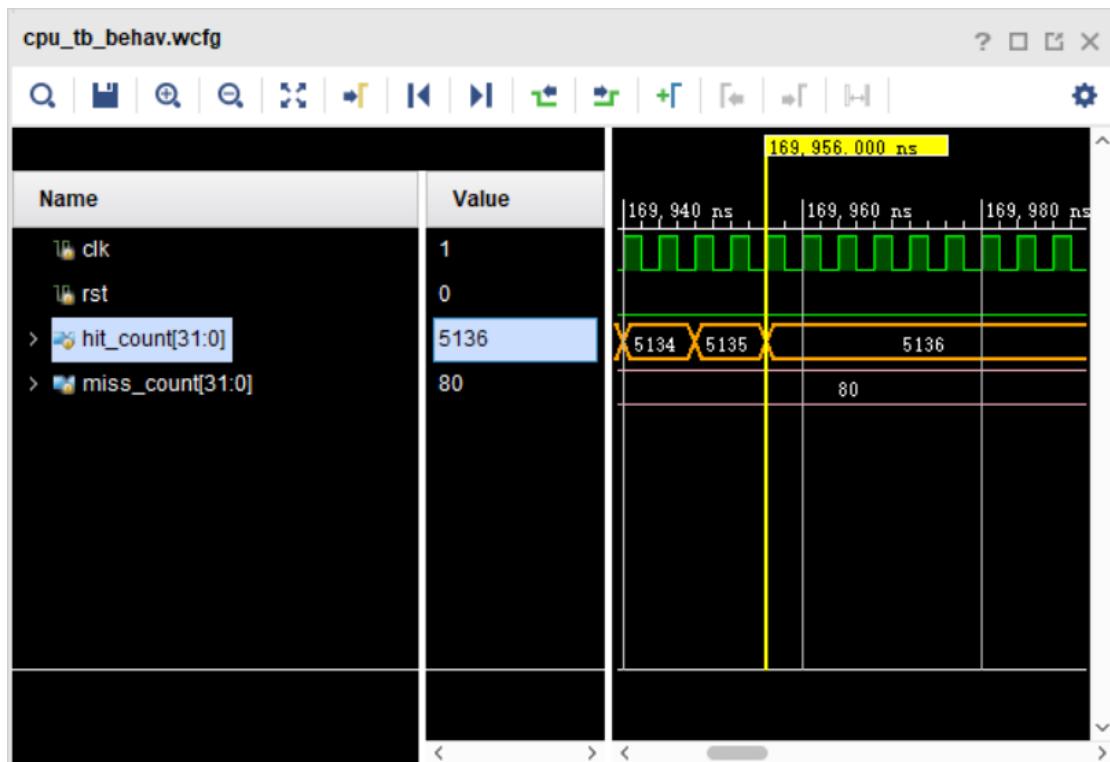




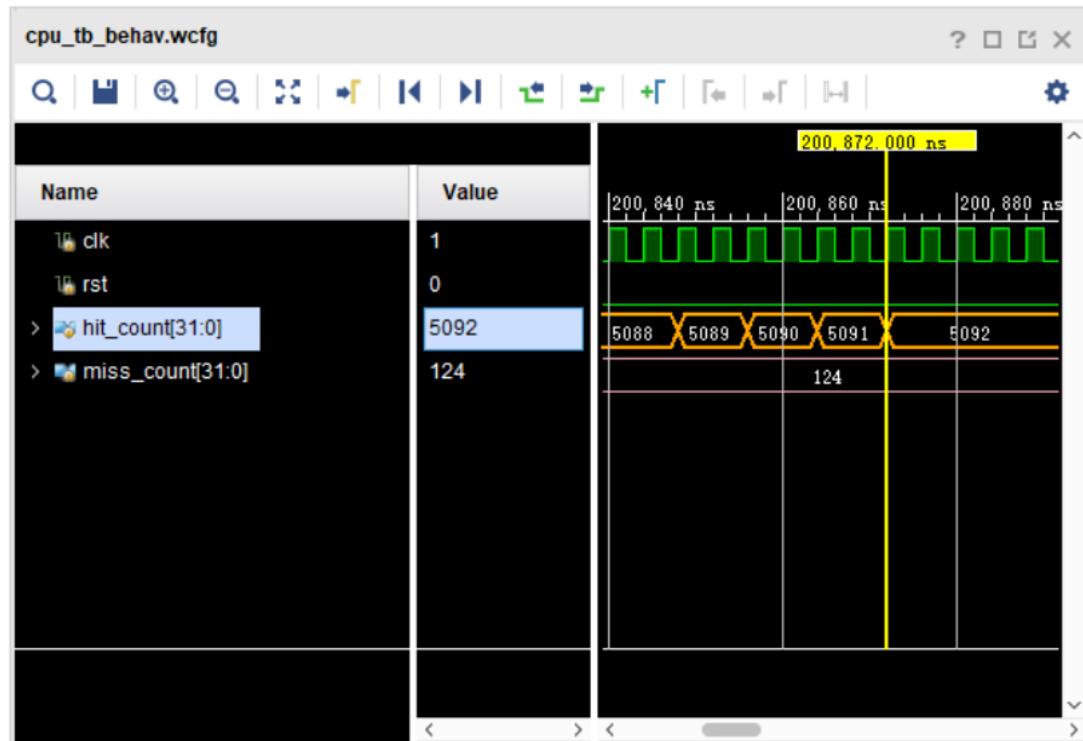
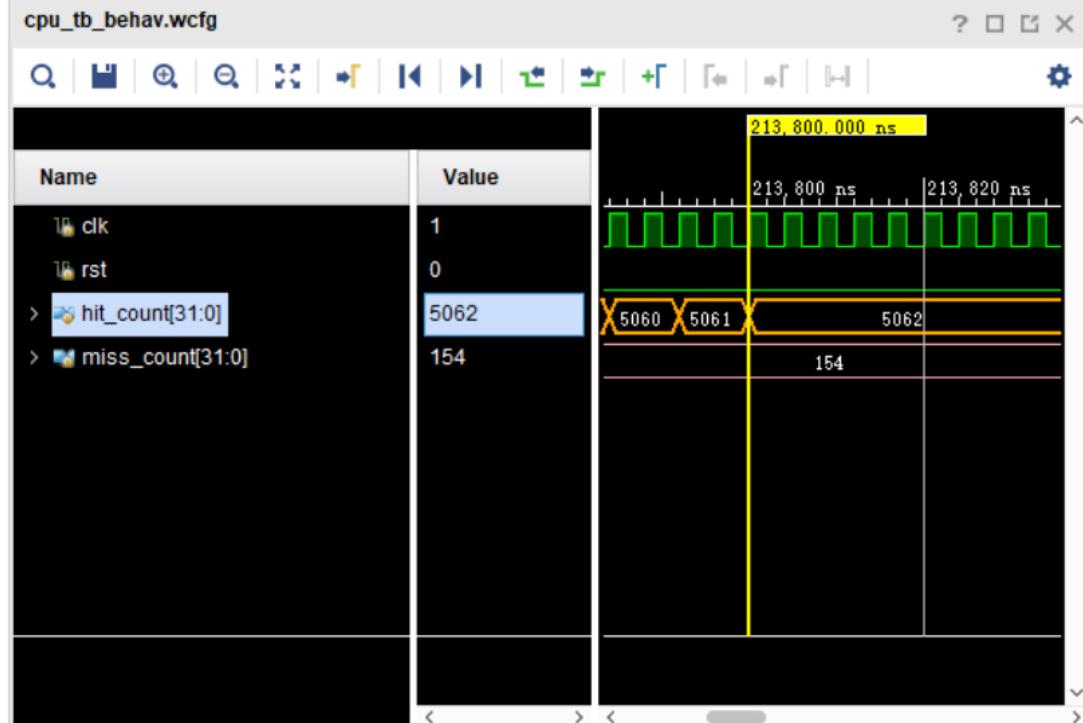
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Policy	FIFO
Test File	QUICKSORT

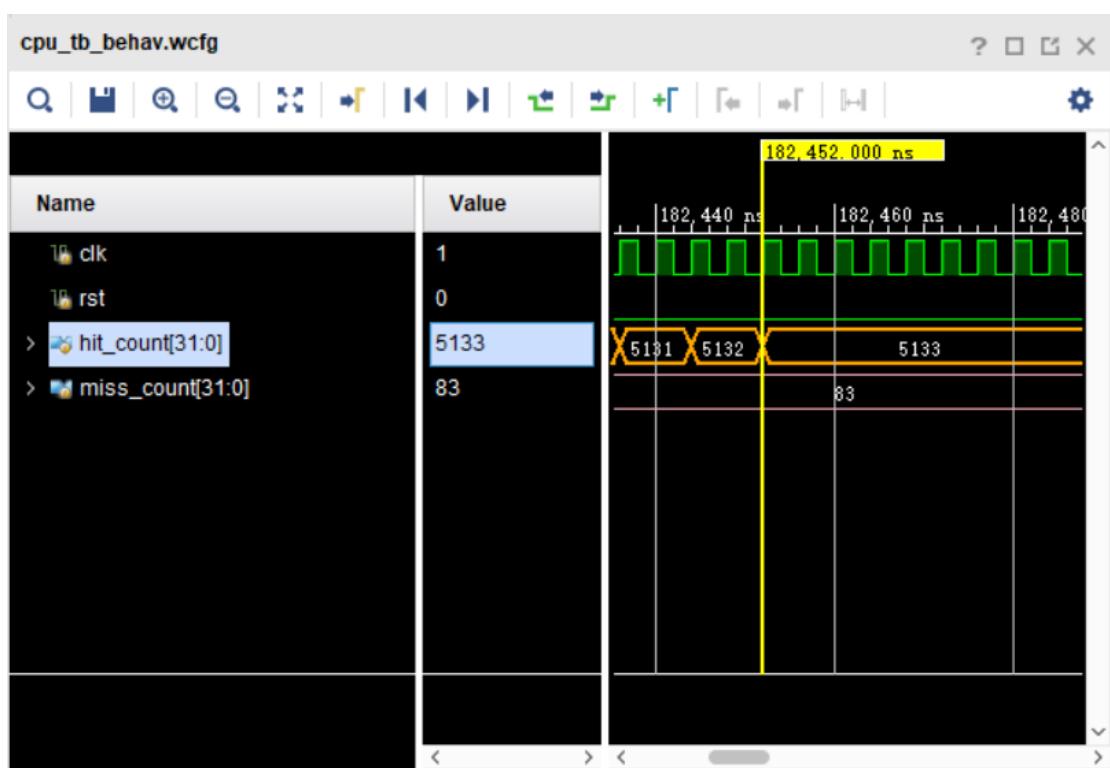
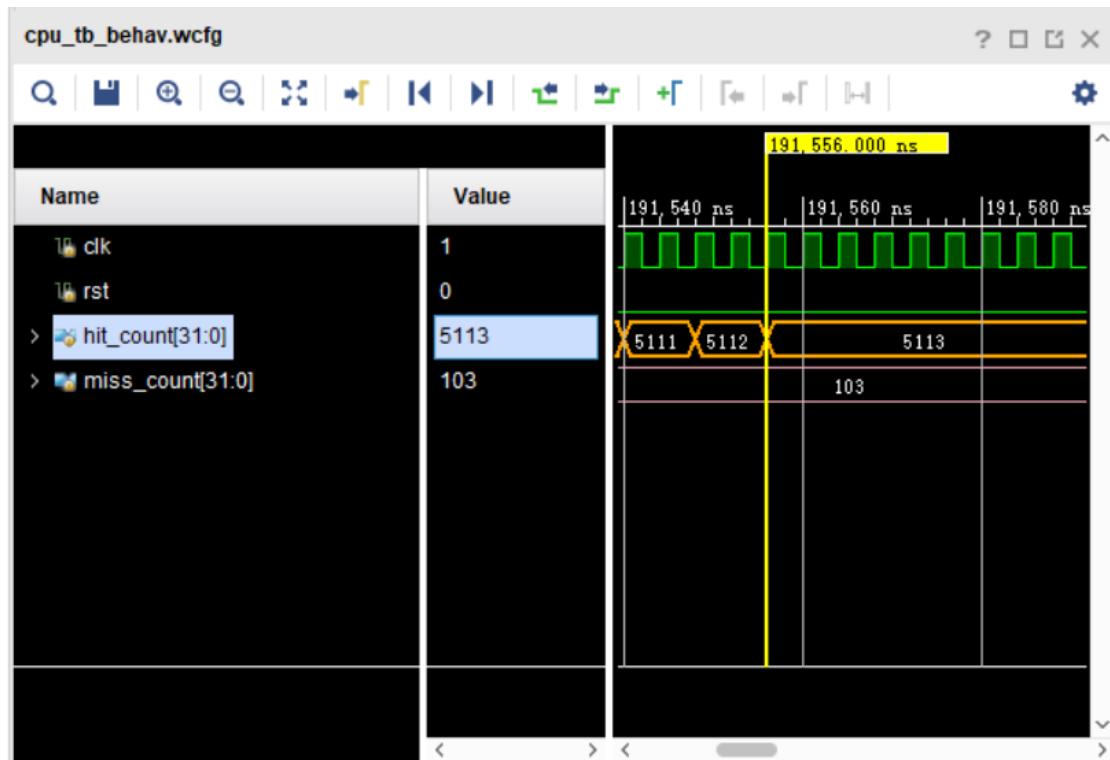


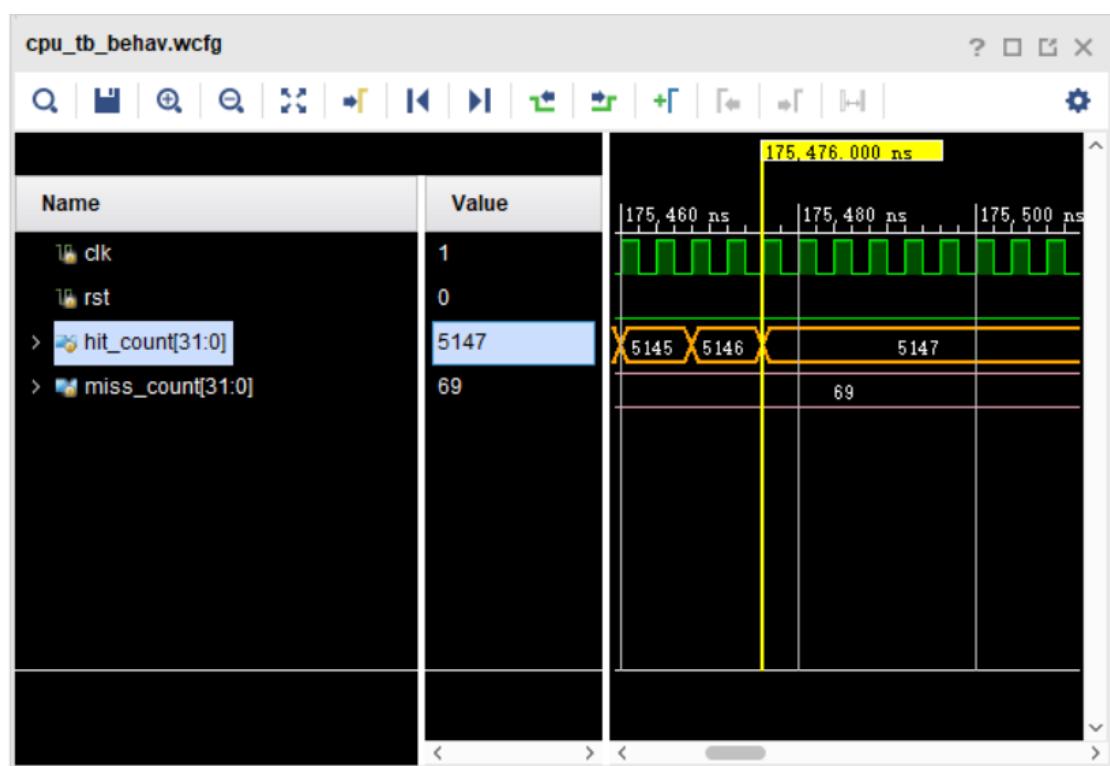
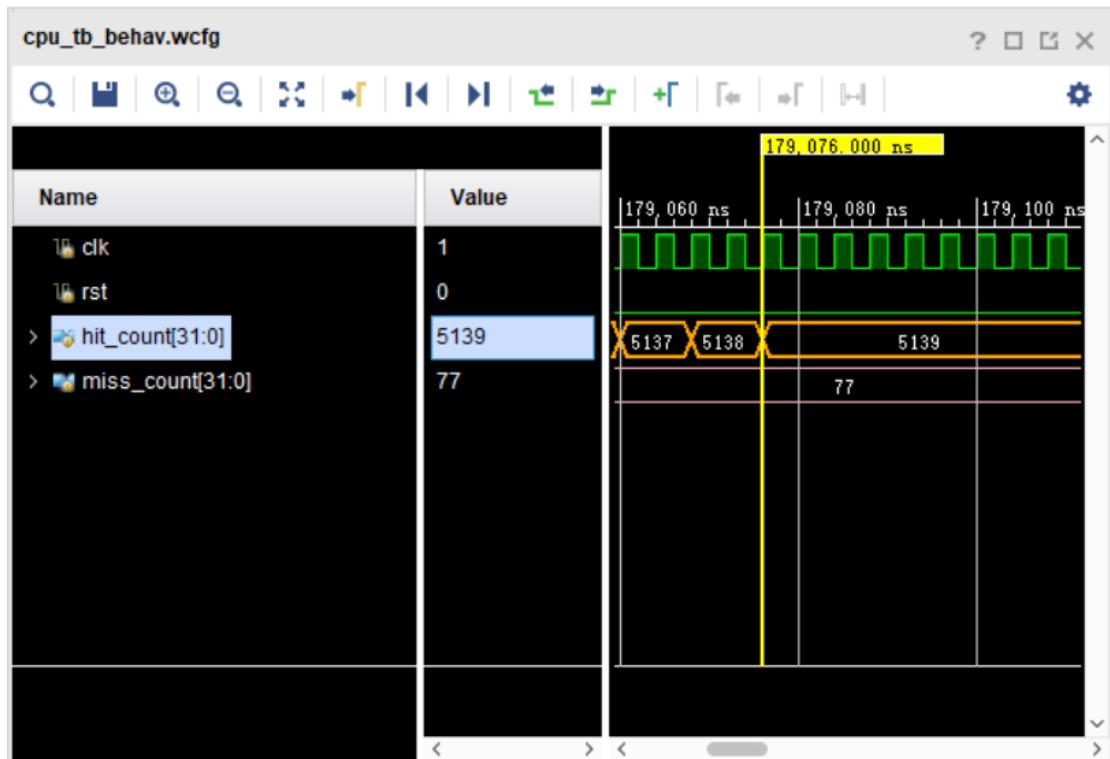




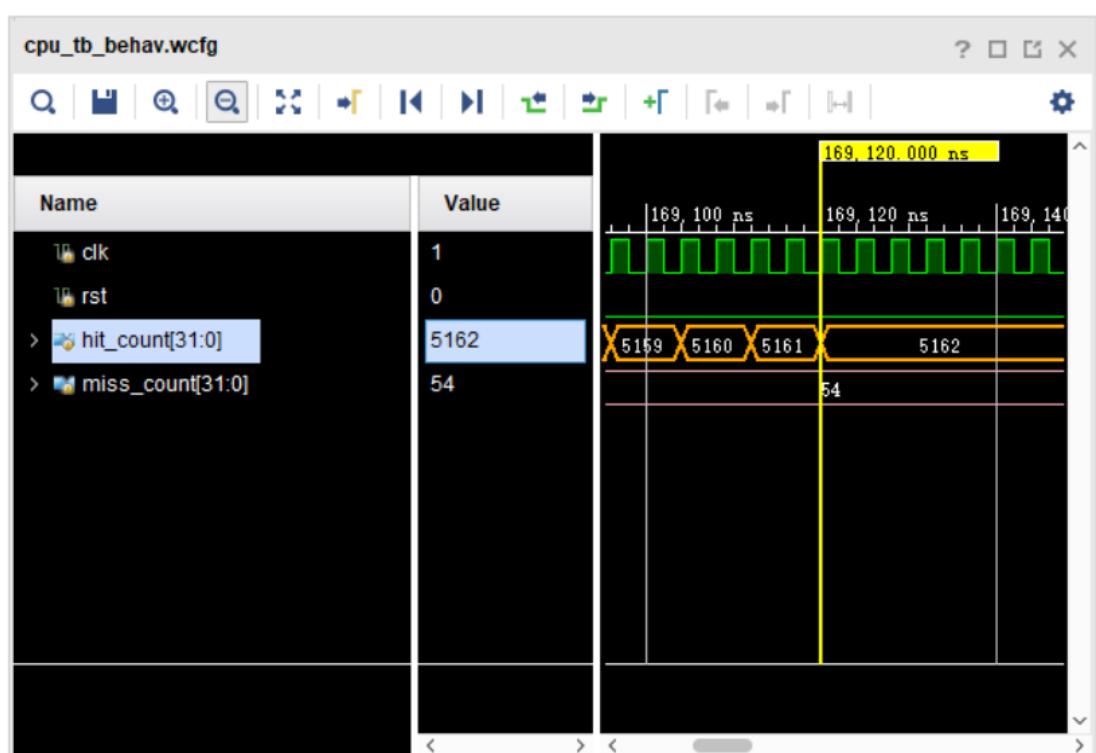
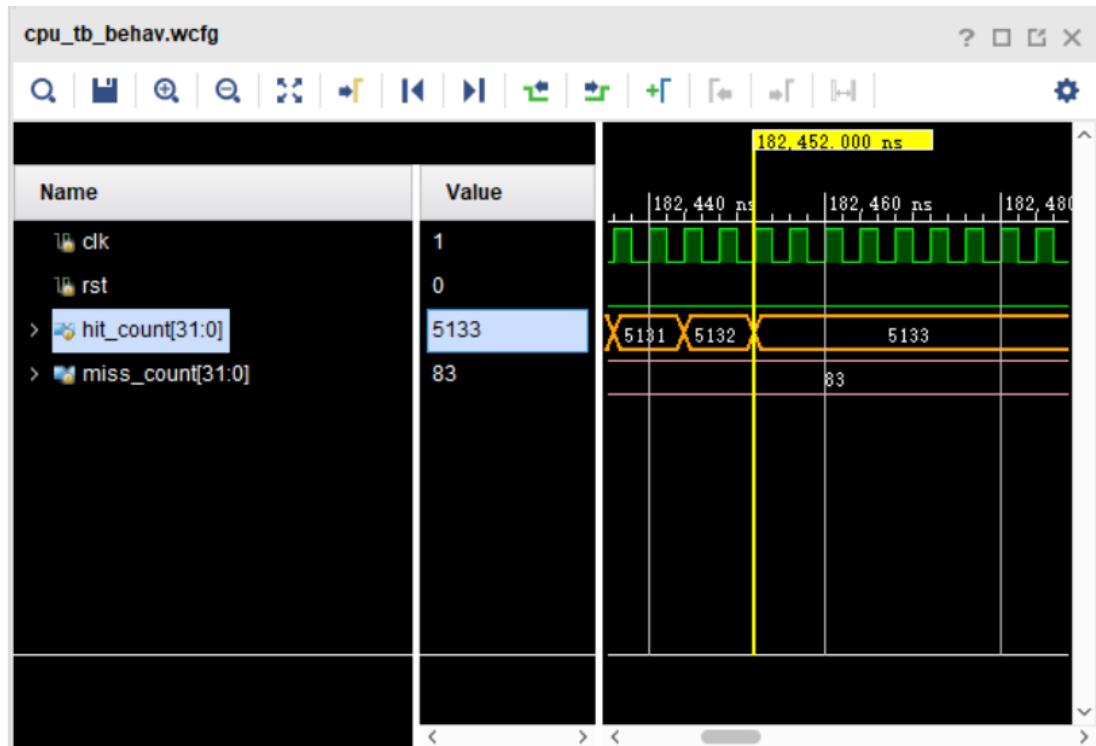
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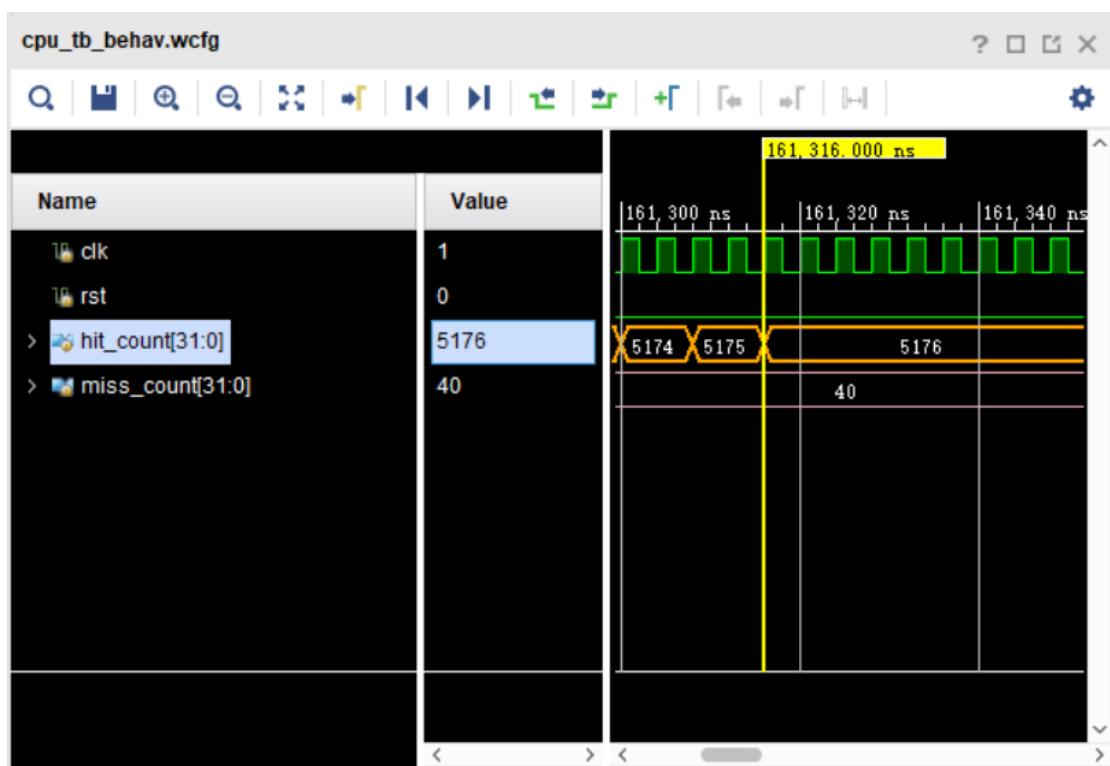
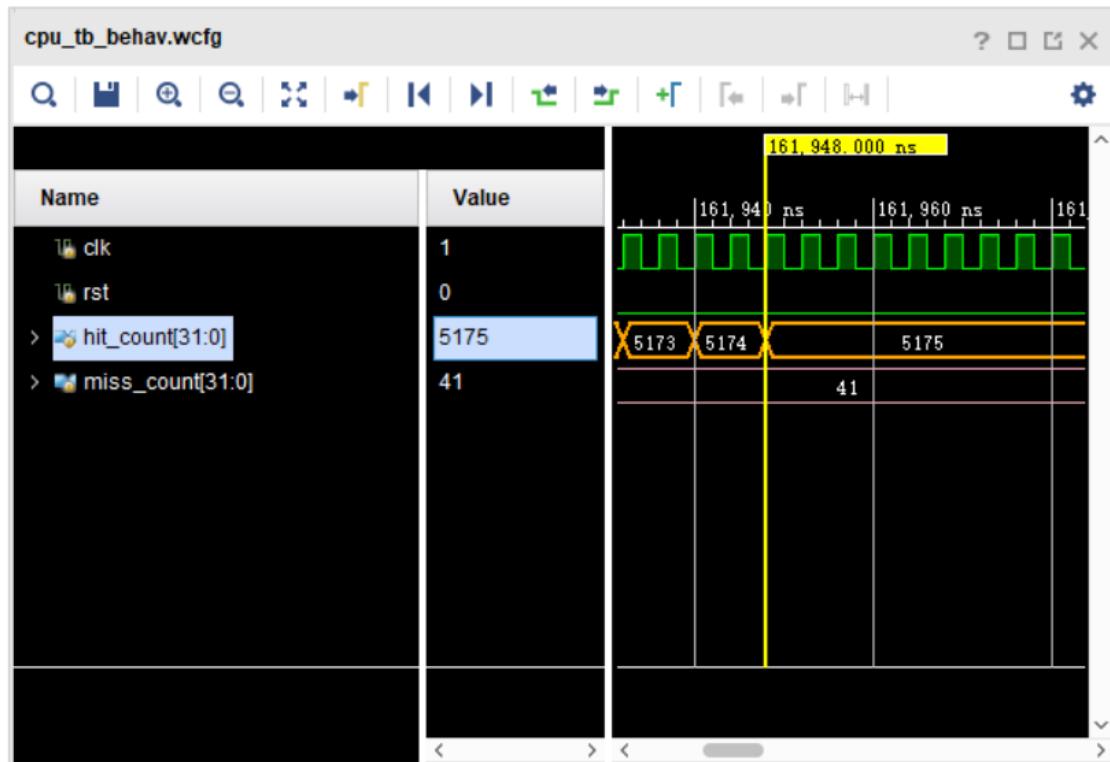


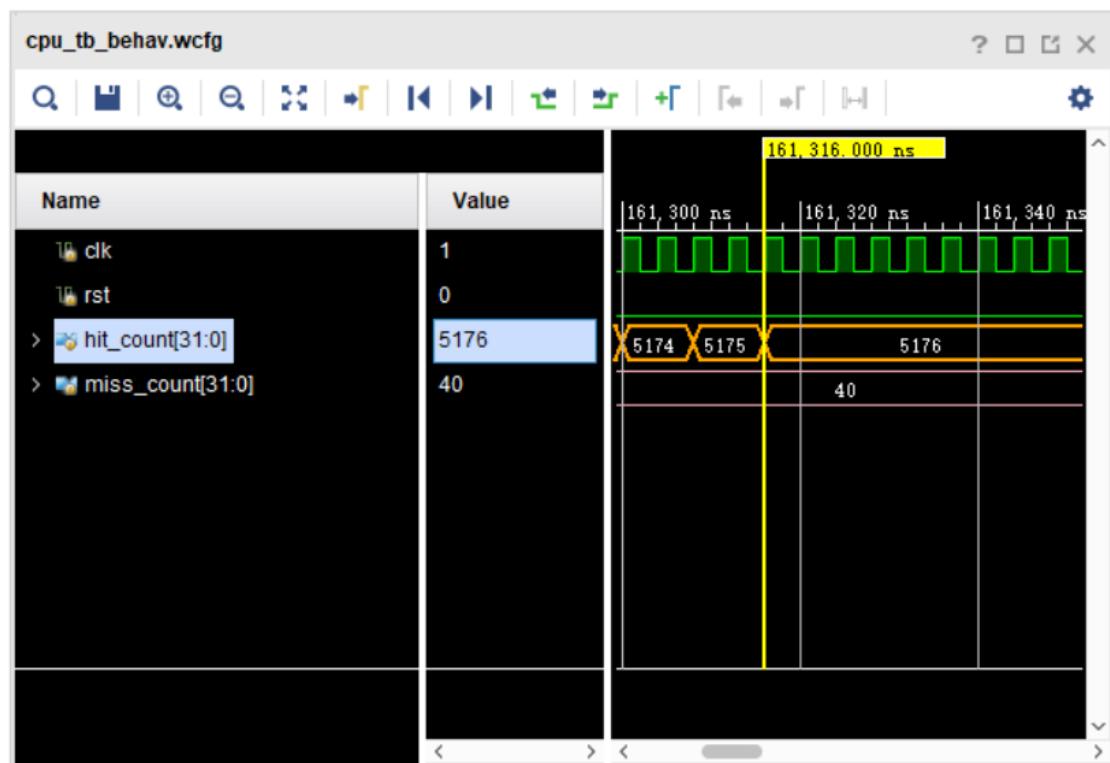
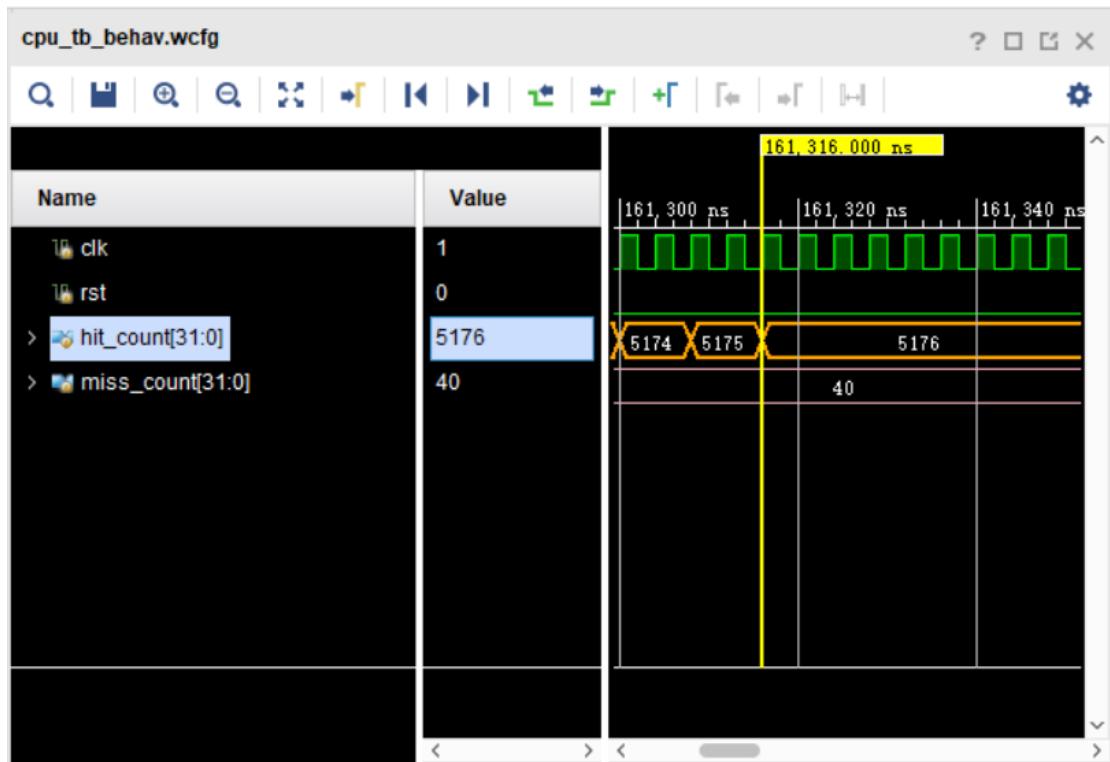




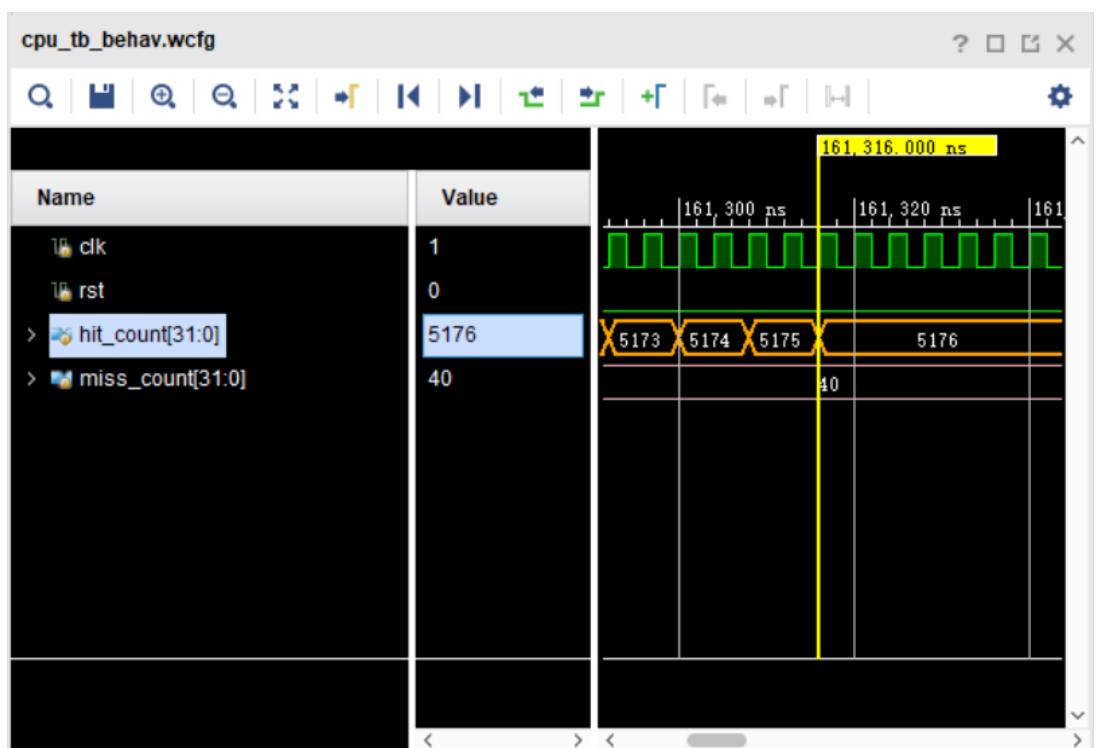
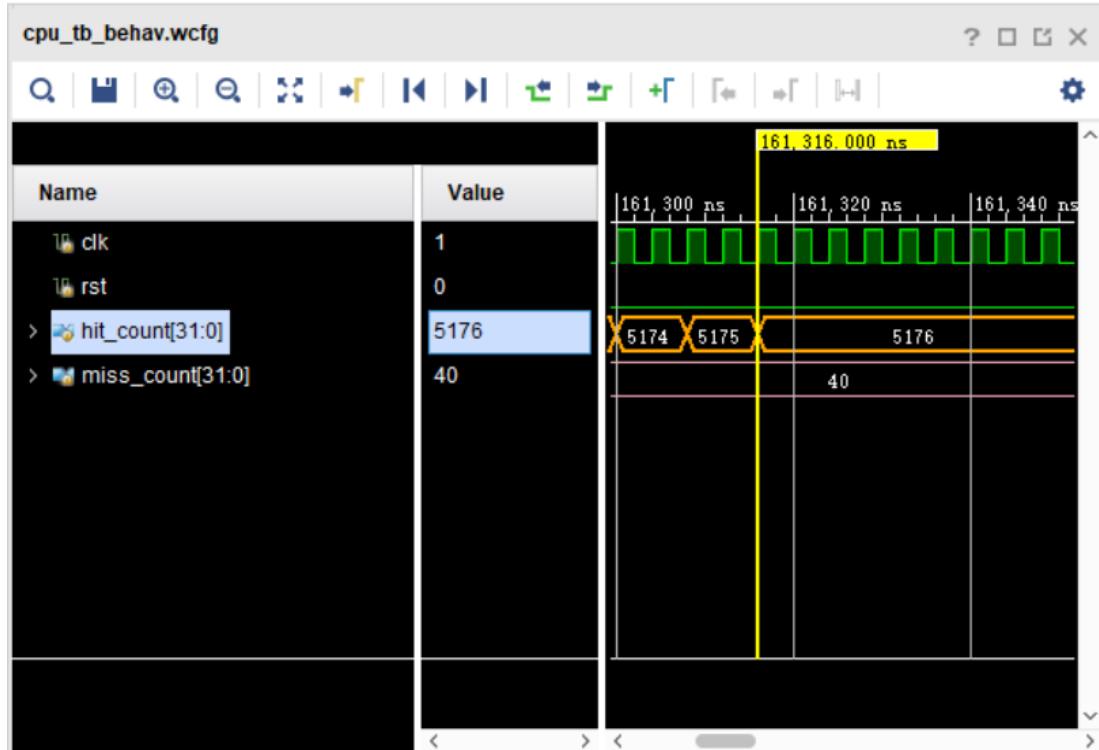
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Policy	FIFO
Test File	QUICKSORT

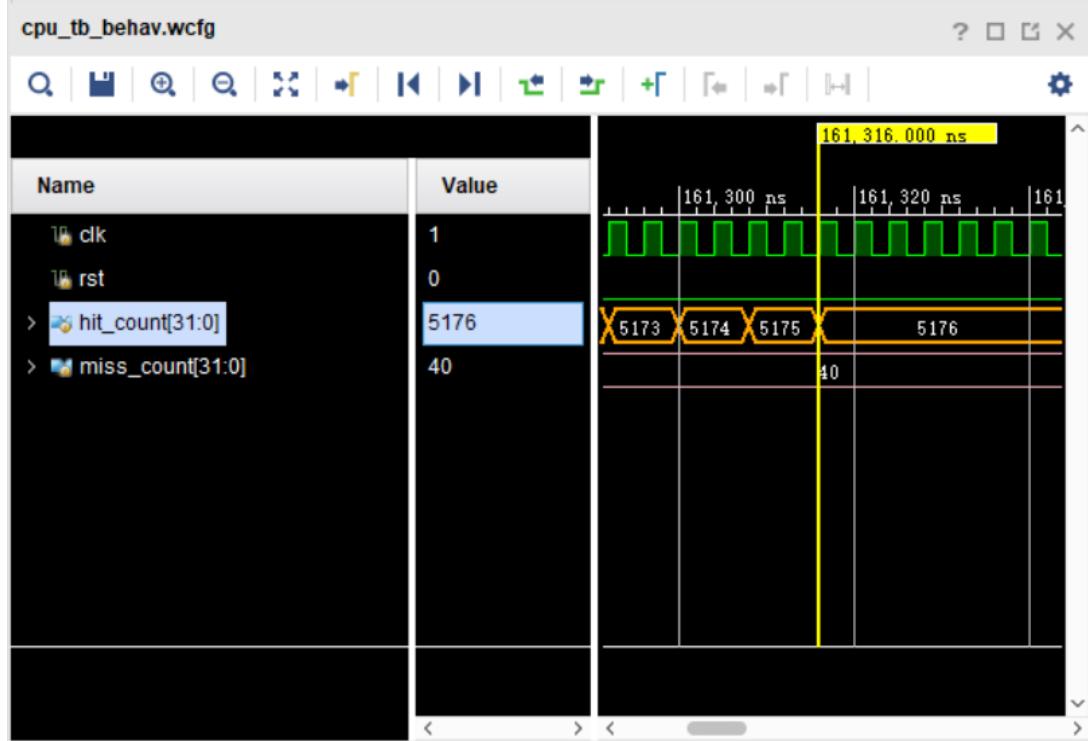
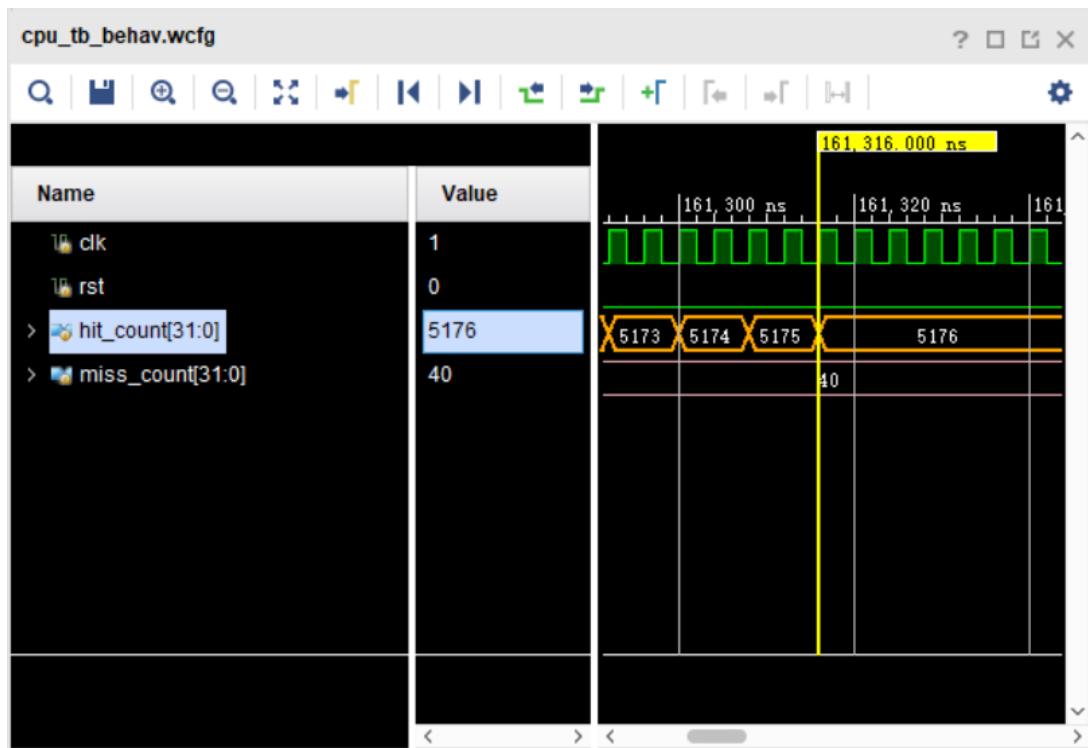


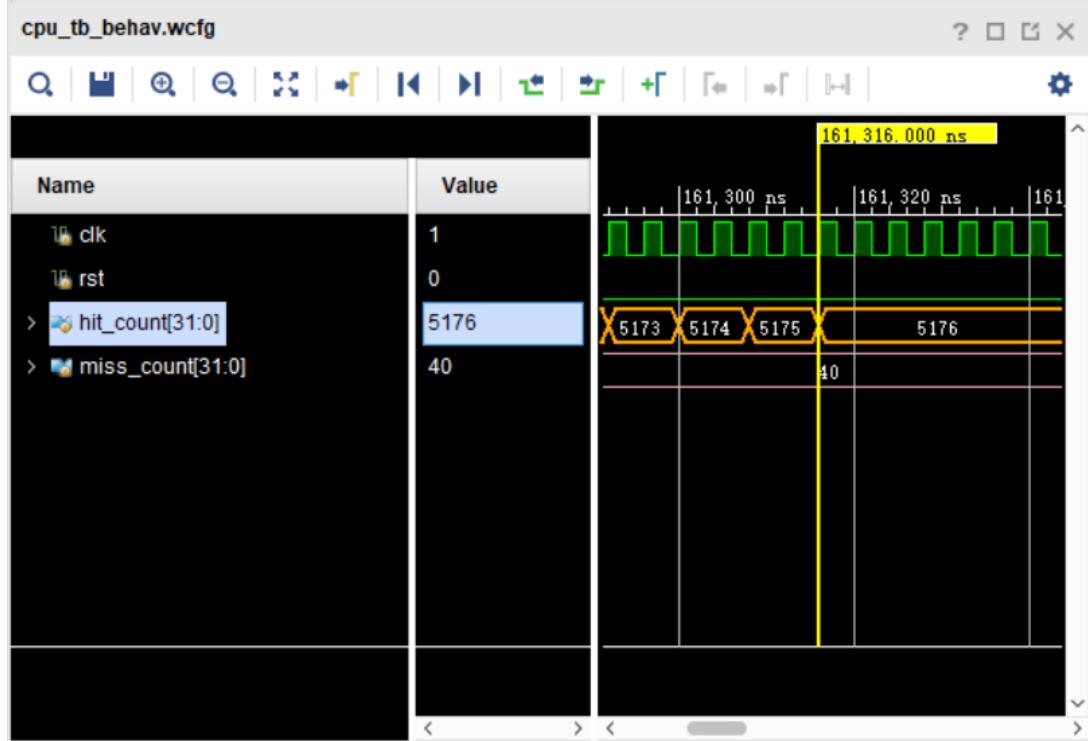
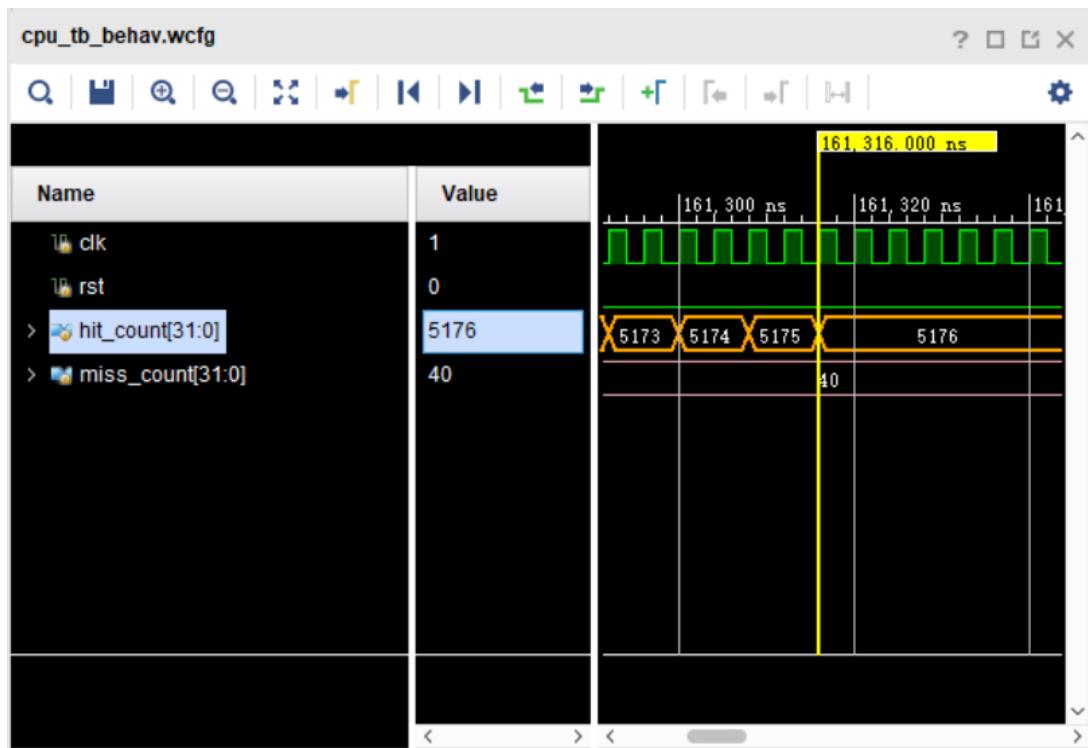




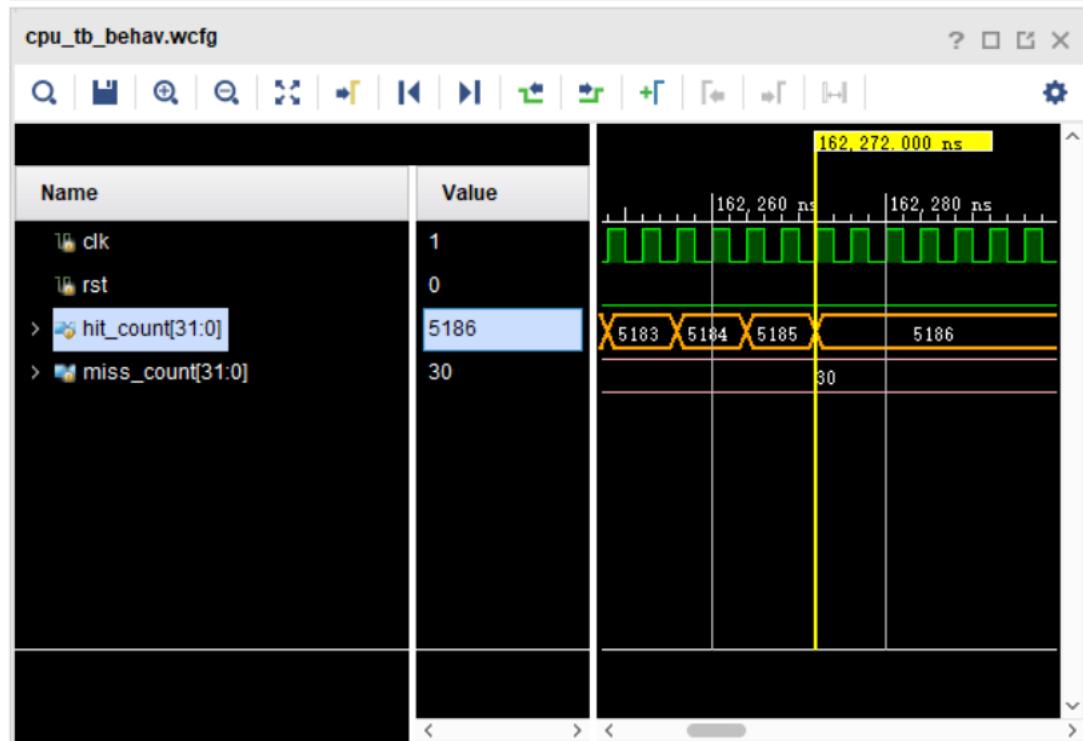
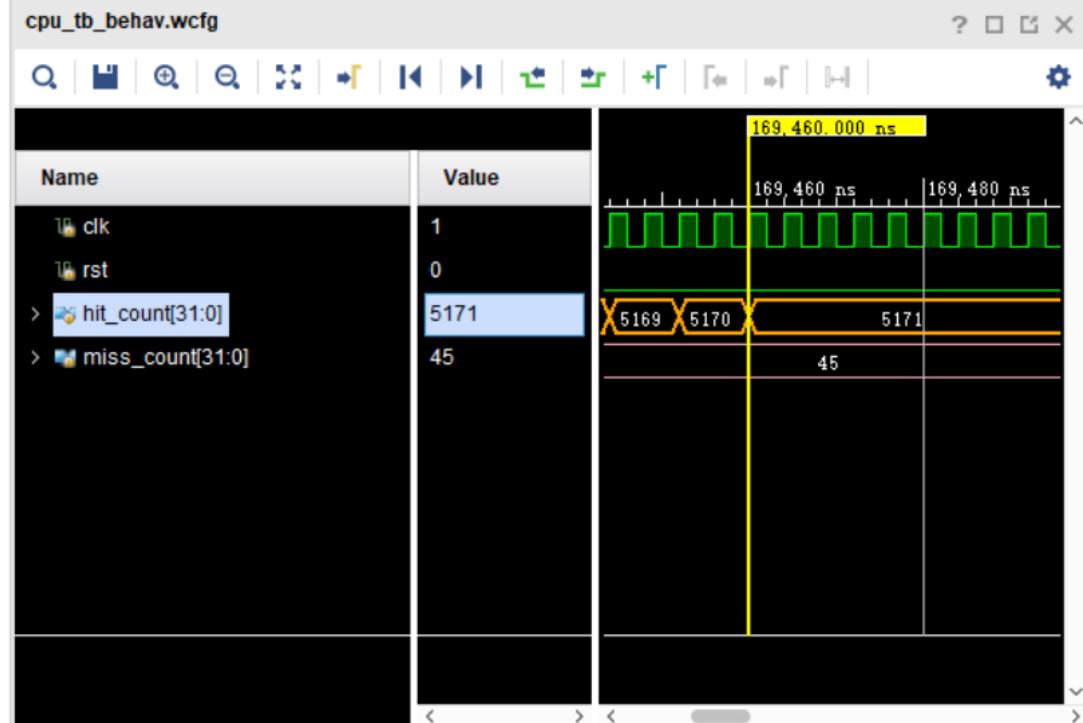
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Test File	QUICKSORT

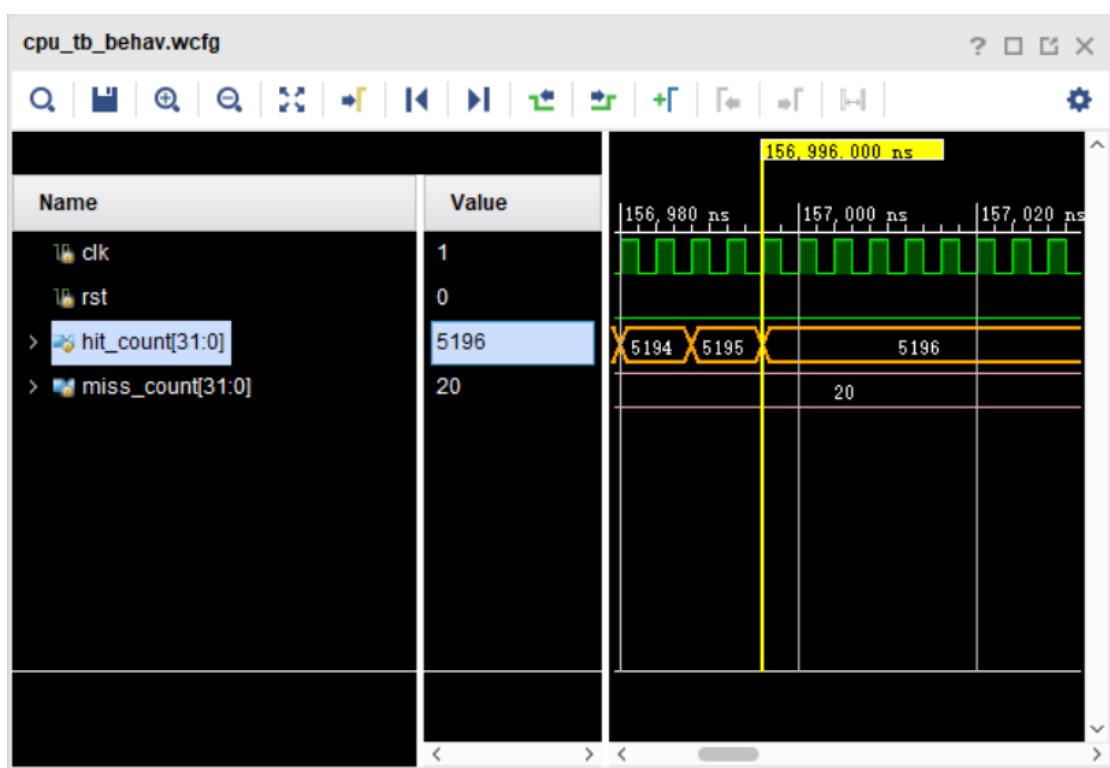
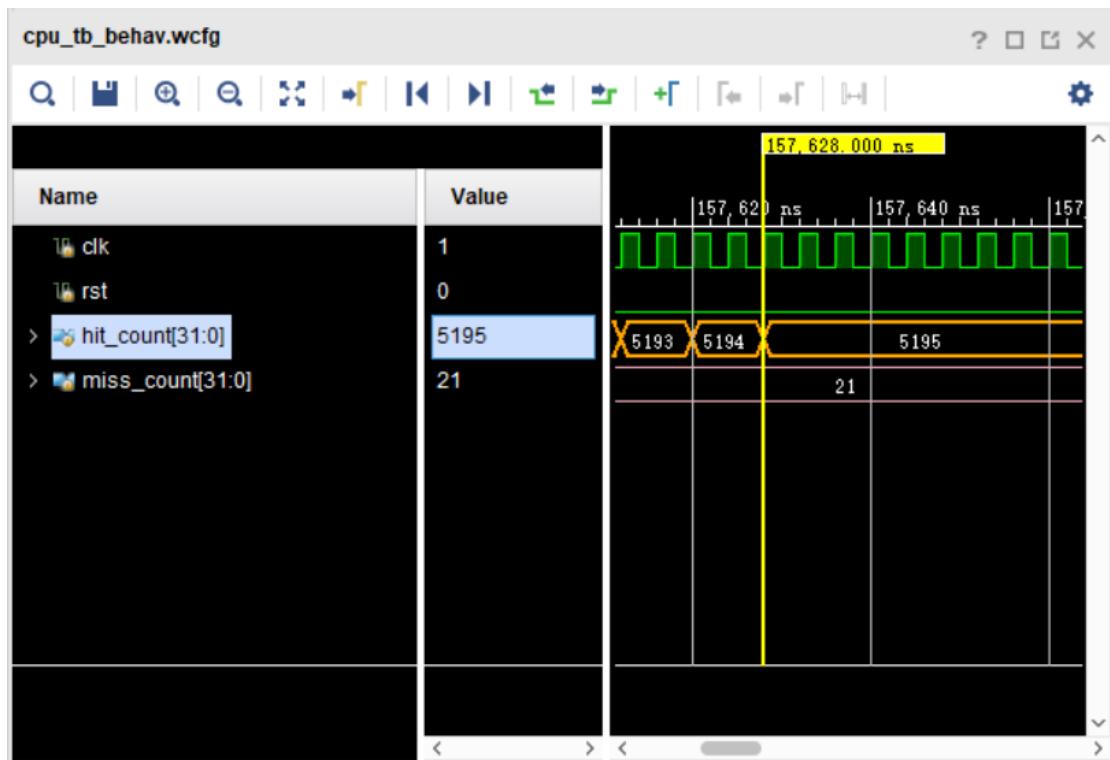


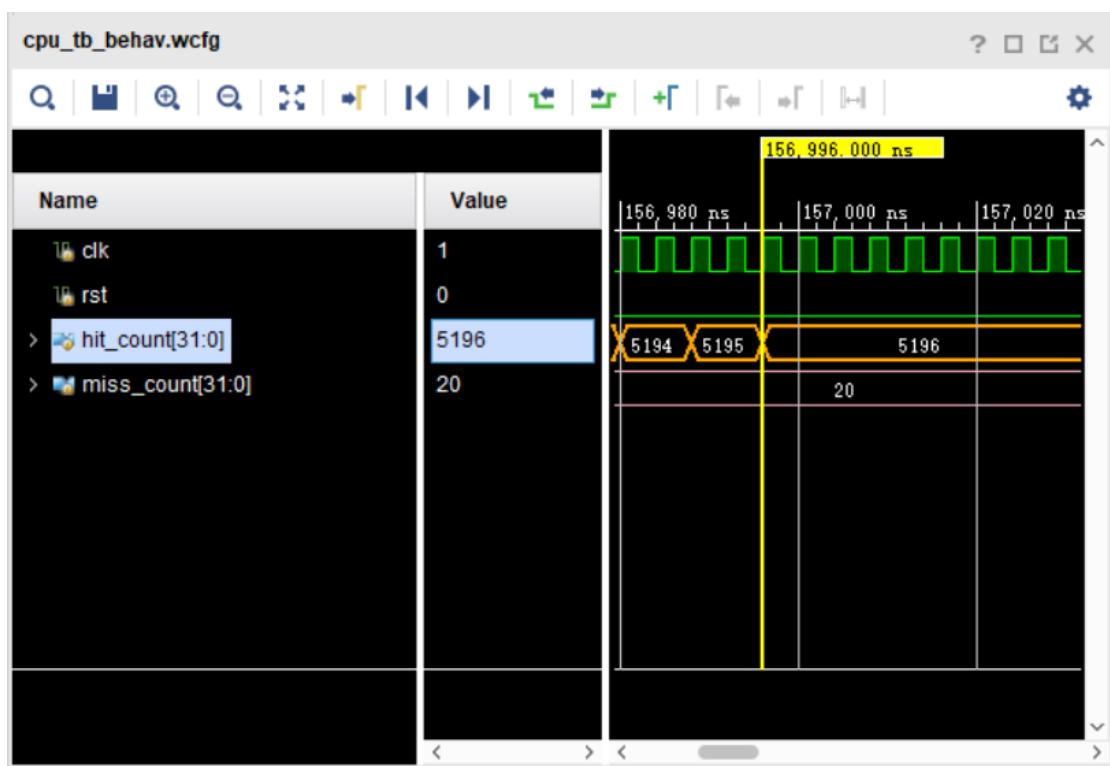
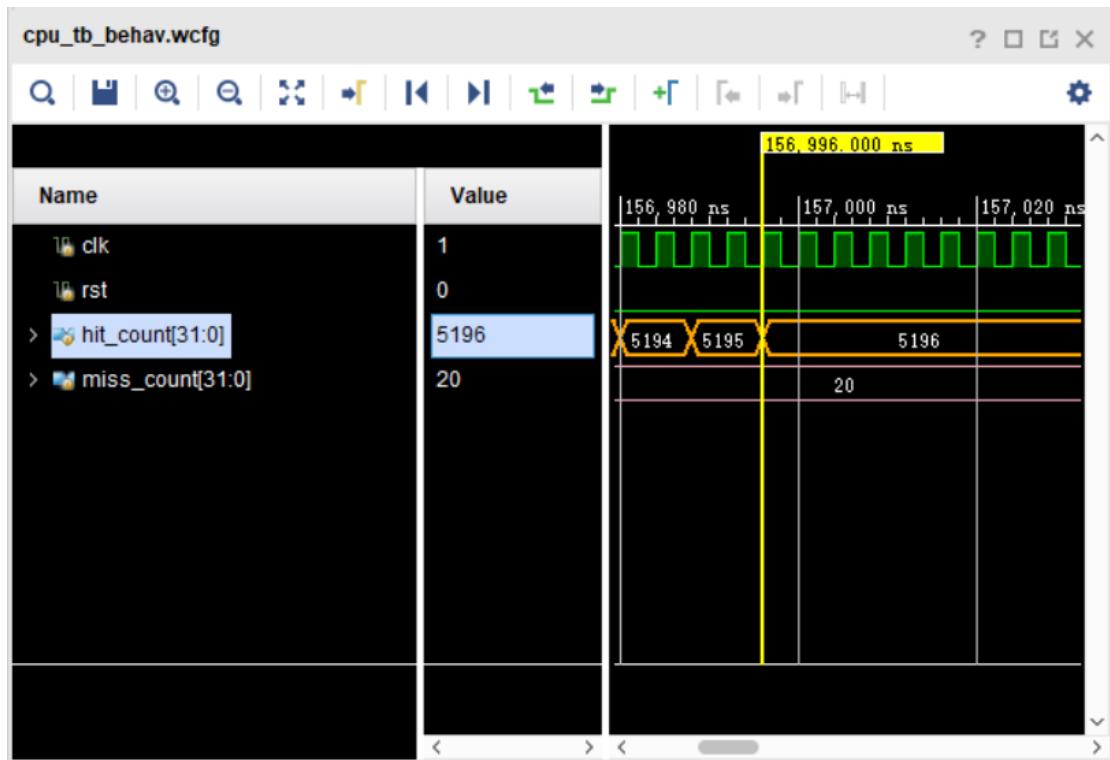




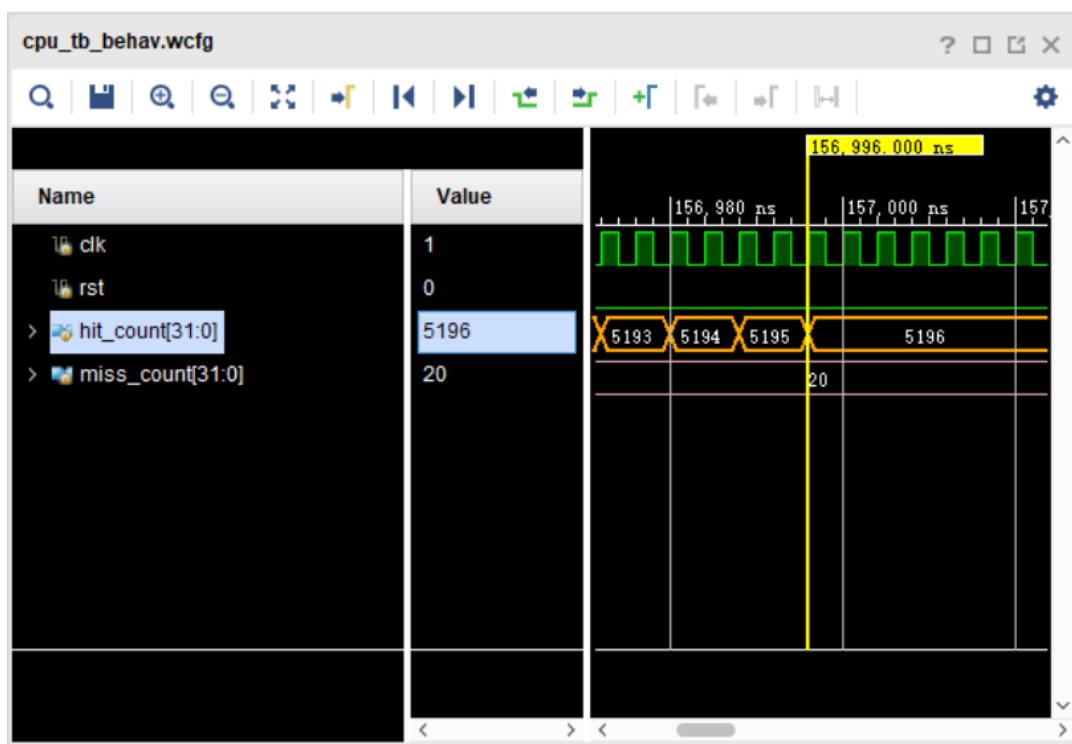
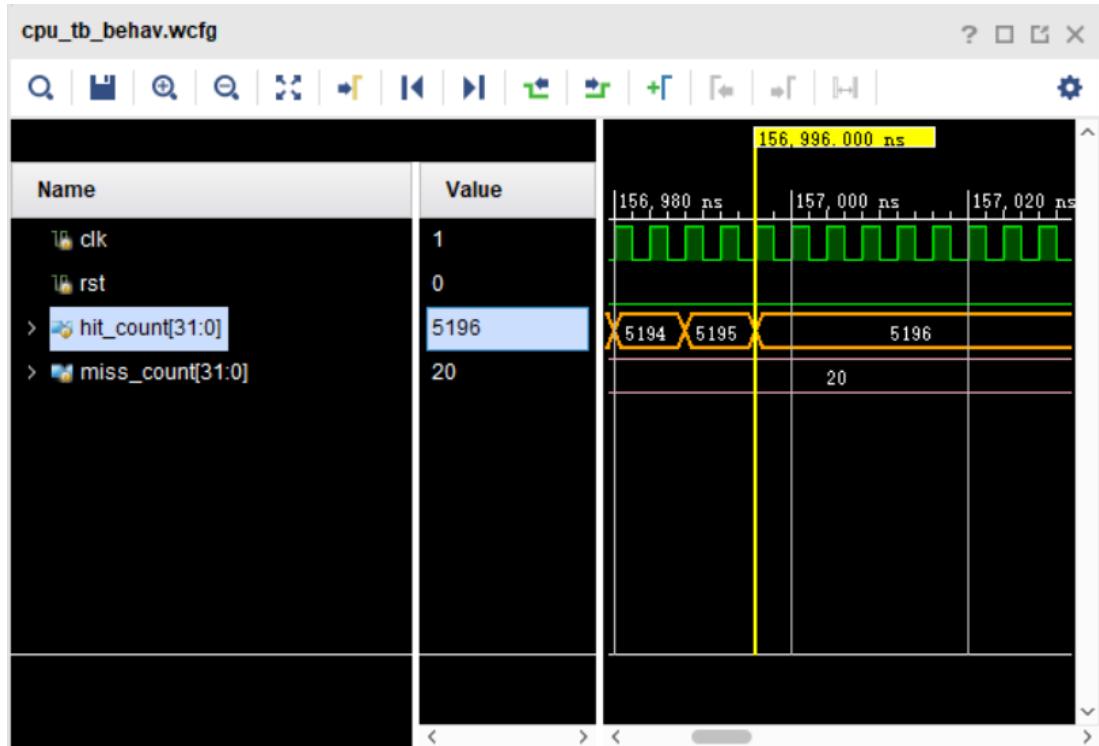
LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	QUICKSORT

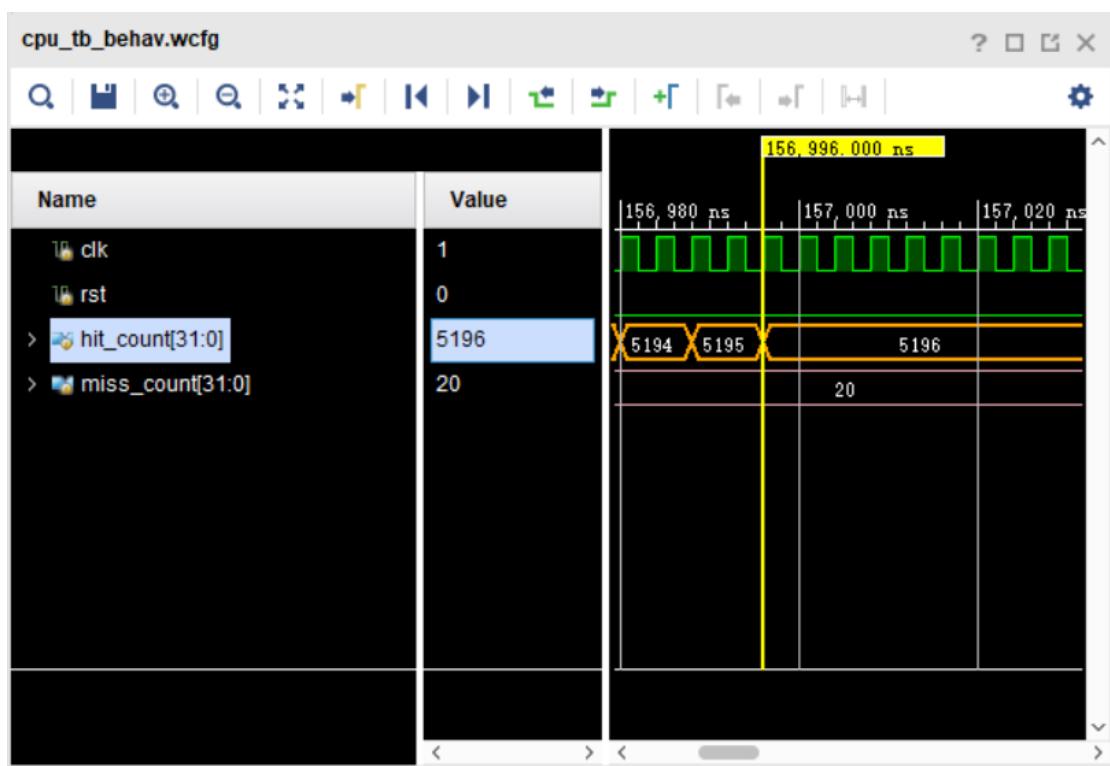
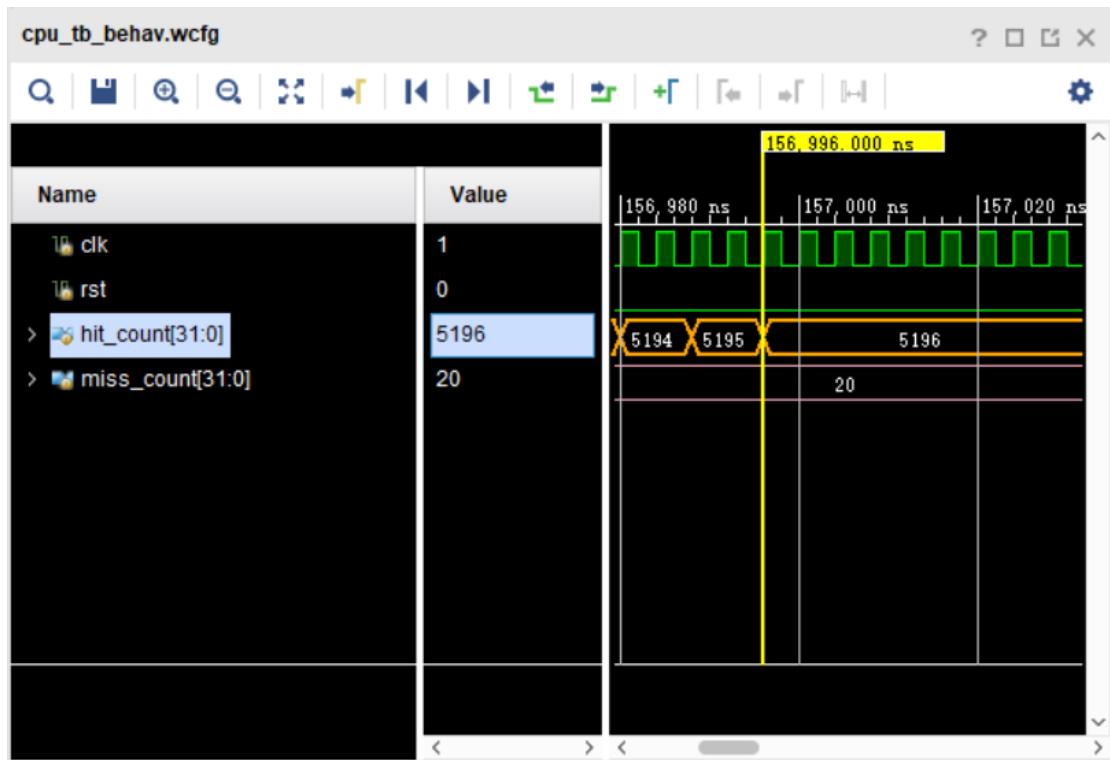


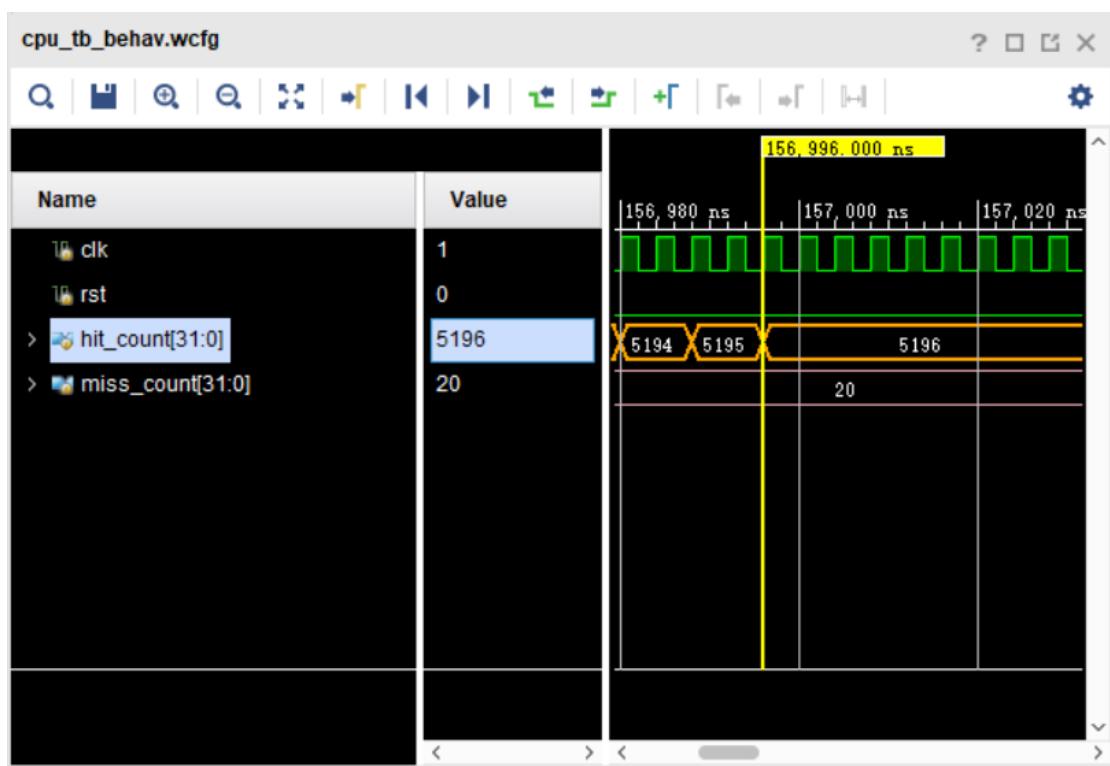
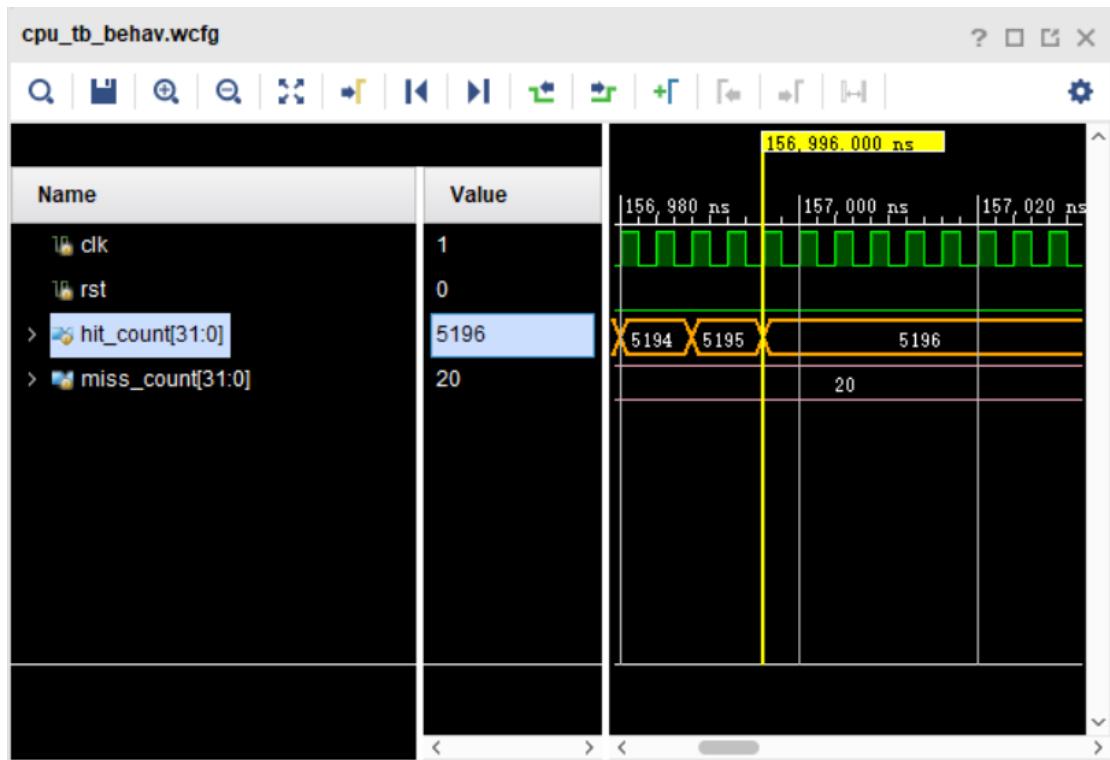




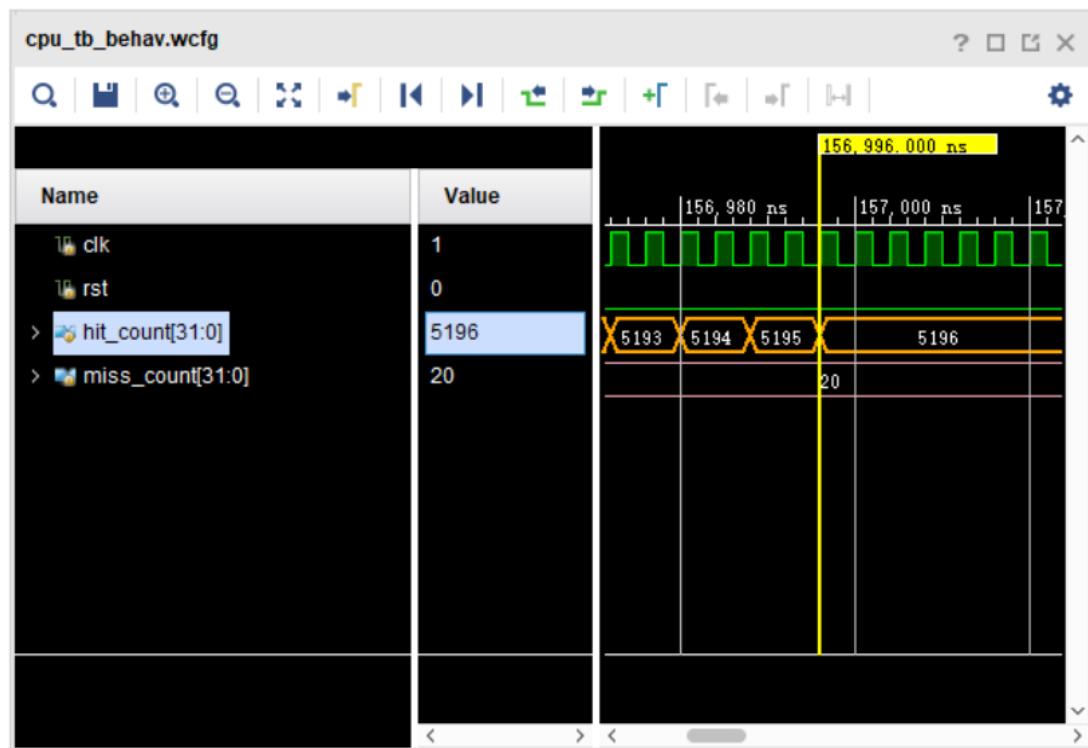
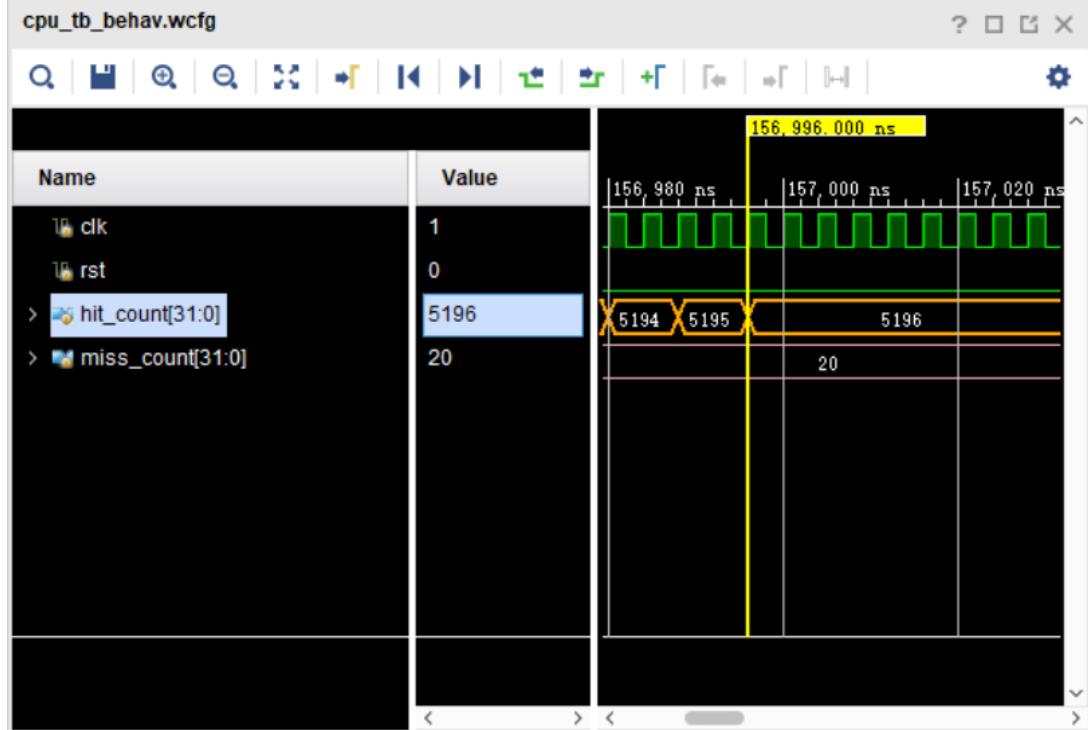
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SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	QUICKSORT

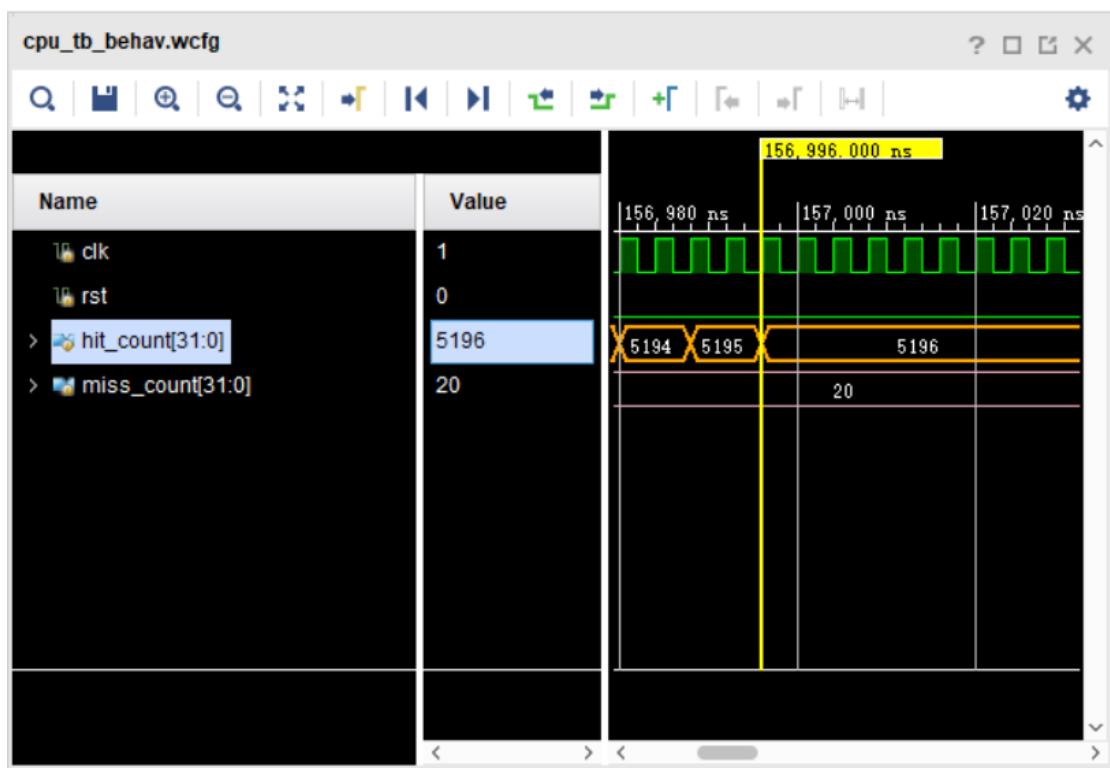
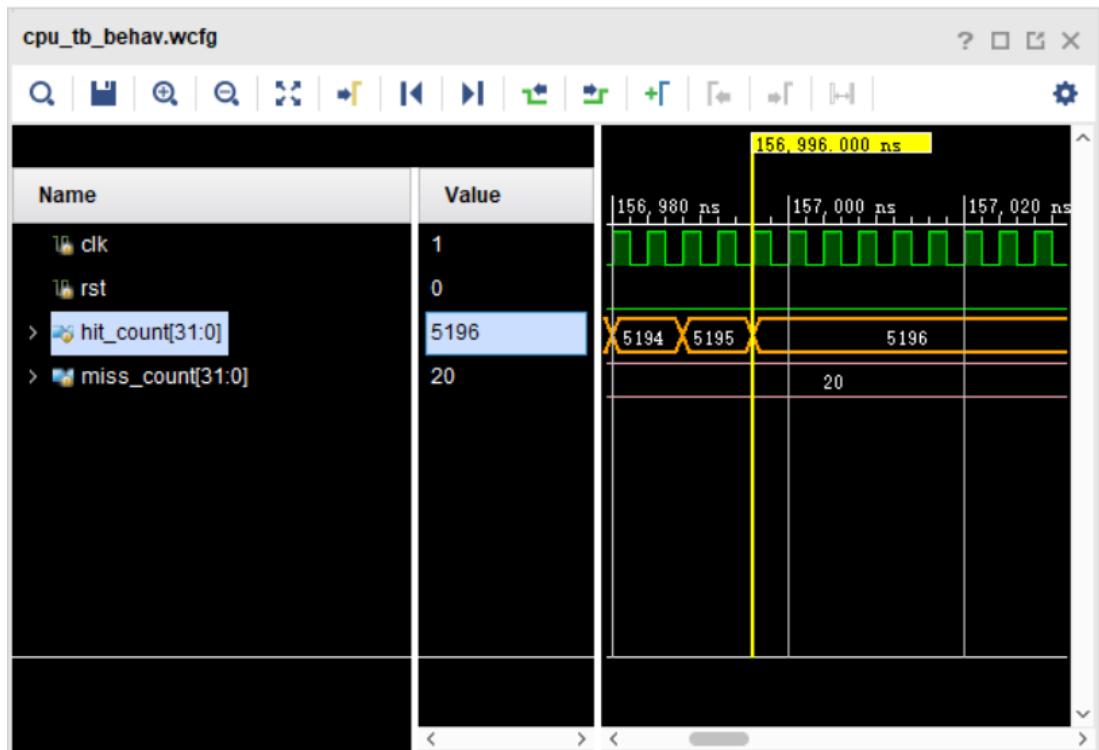


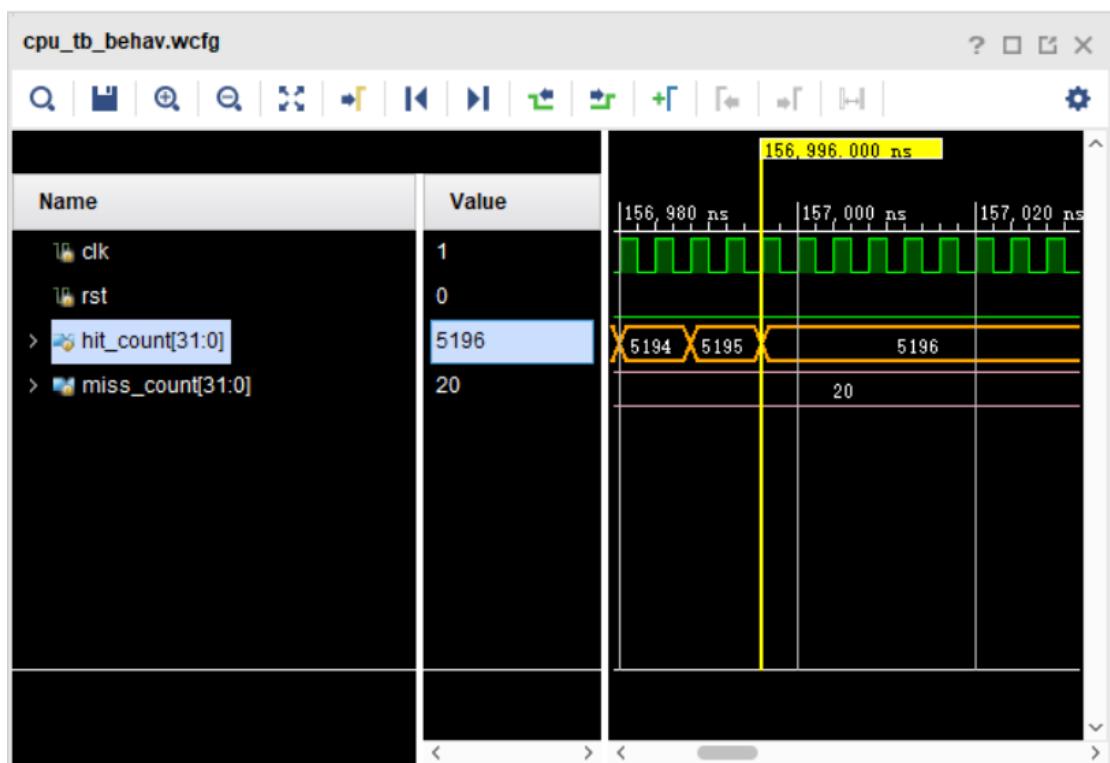
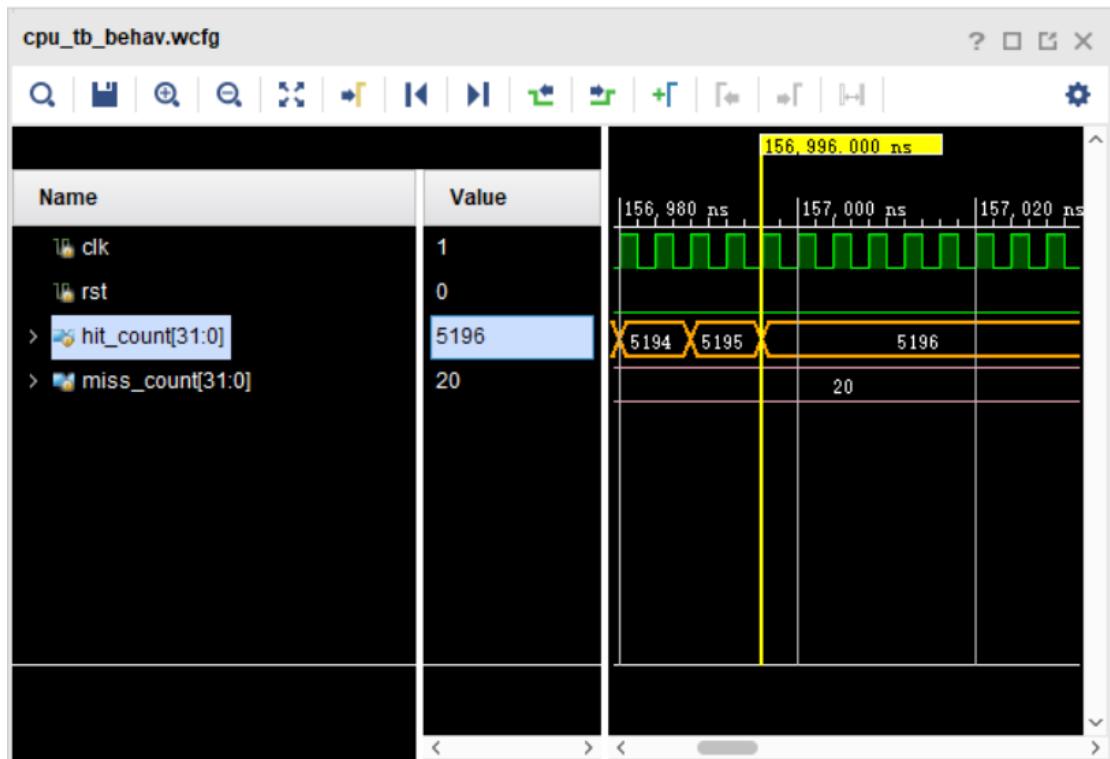




LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	QUICKSORT



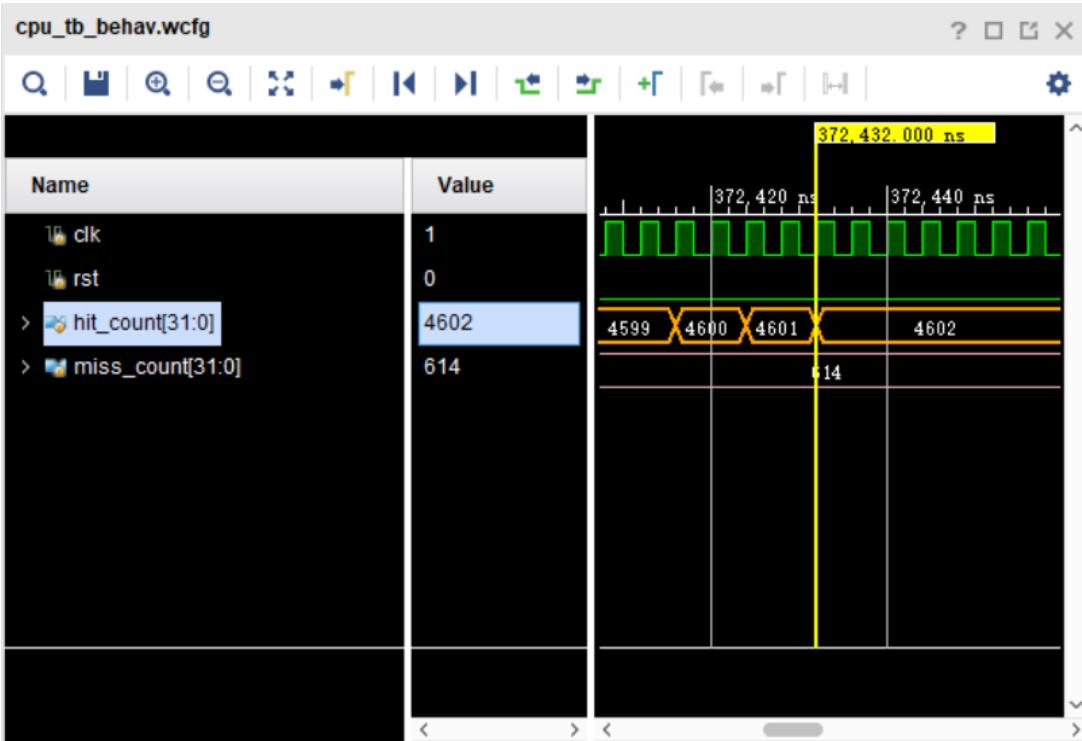
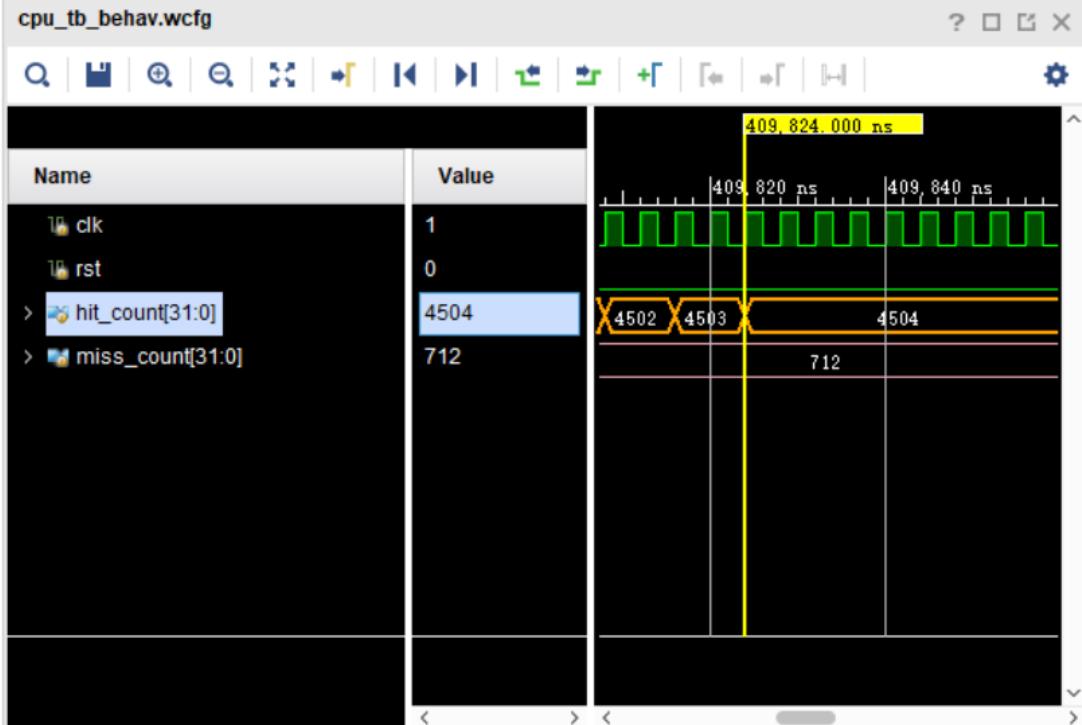


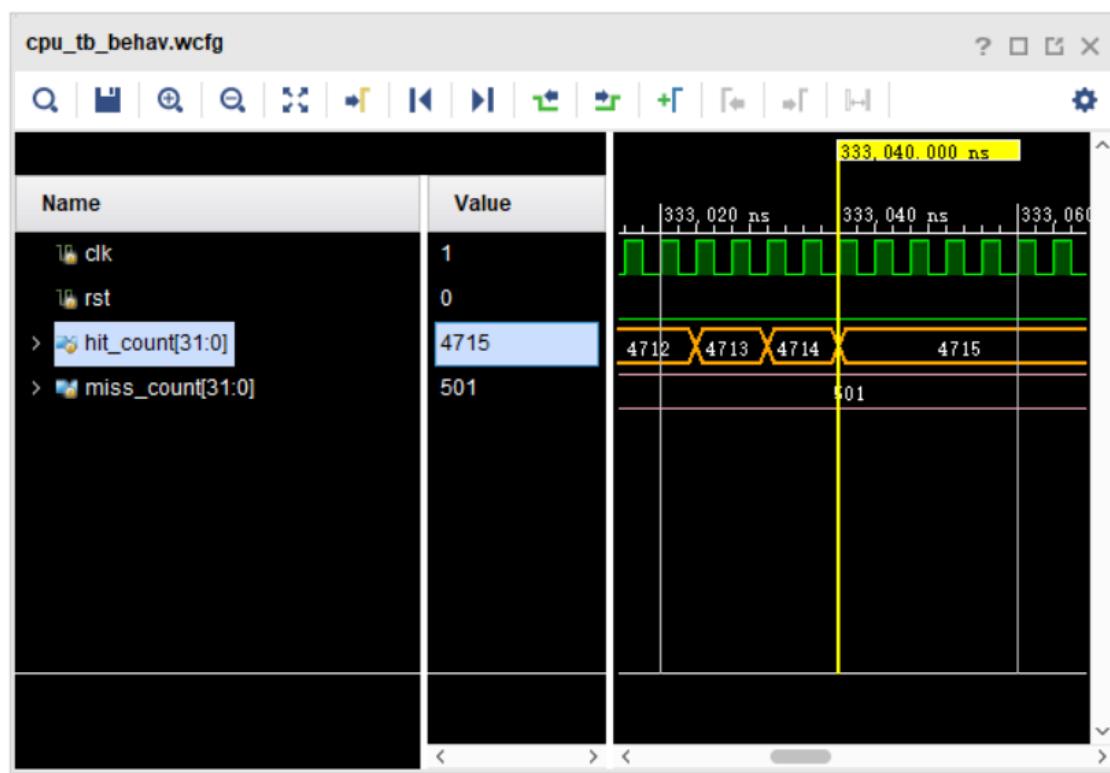
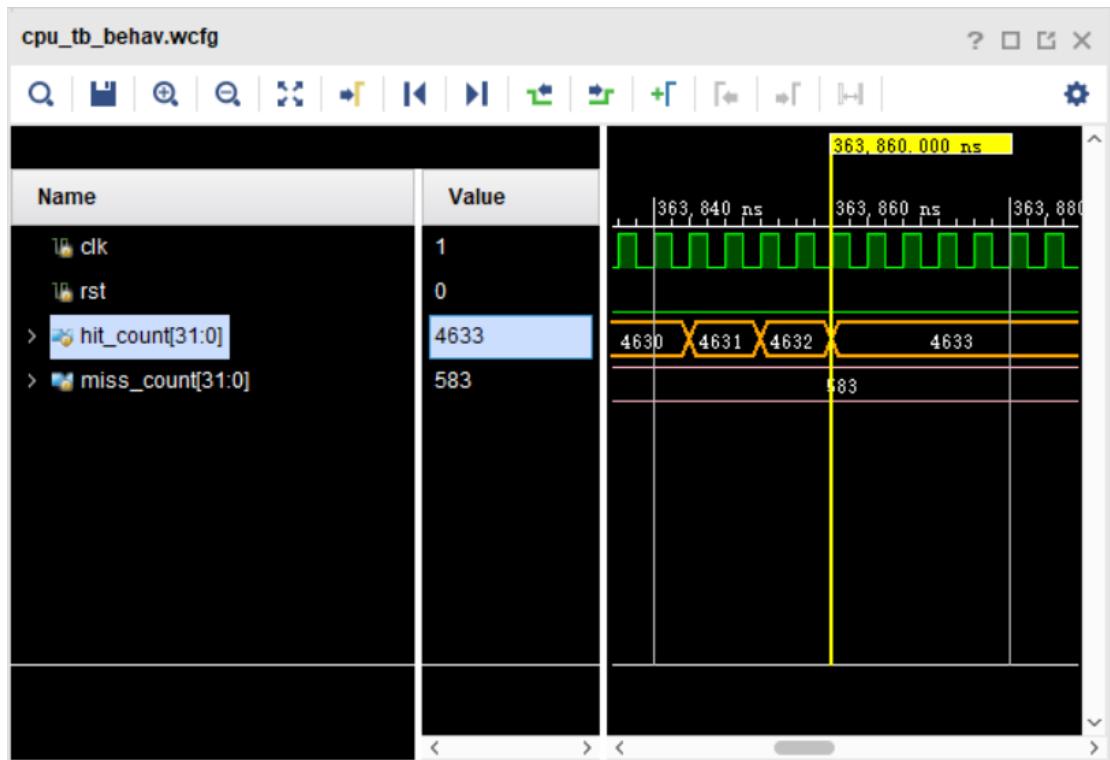


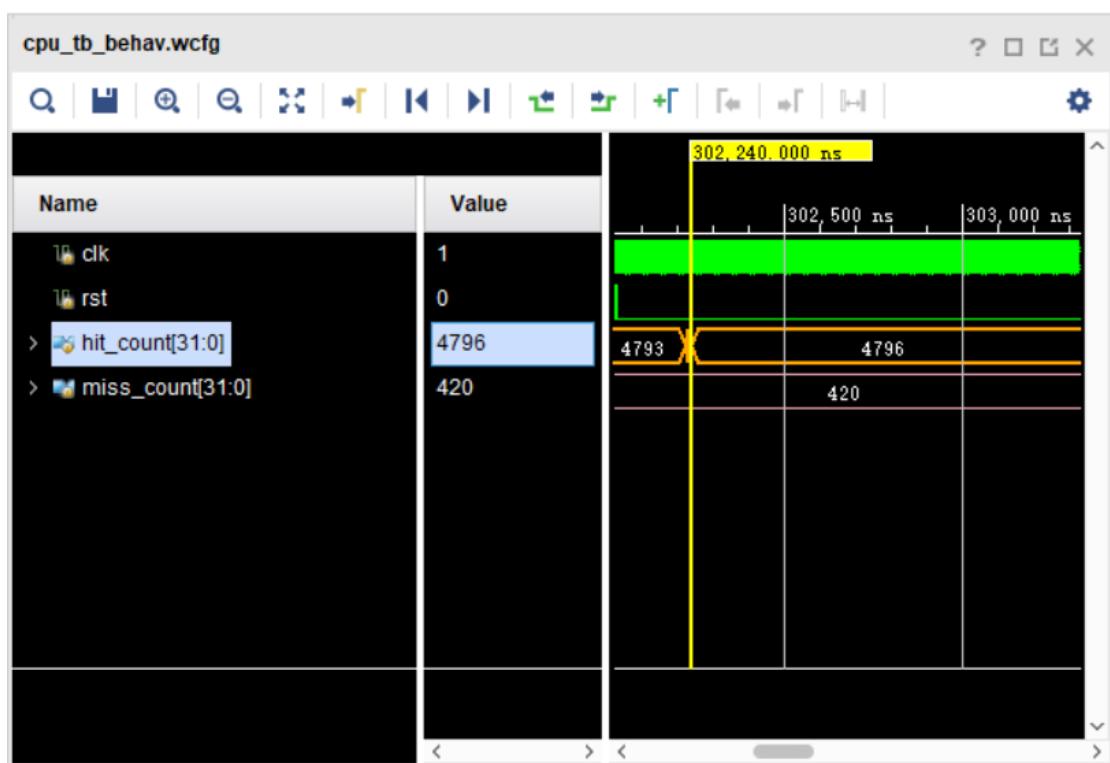
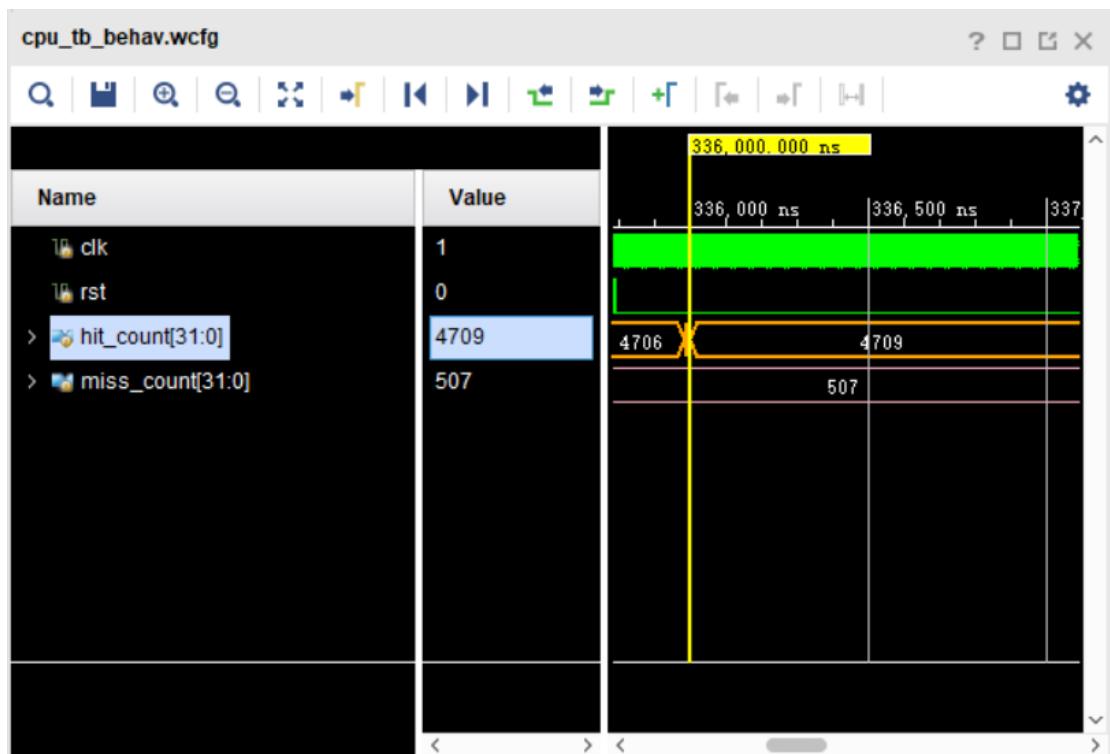
仿真数据

(256 个数 QUICKSORT、LRU)

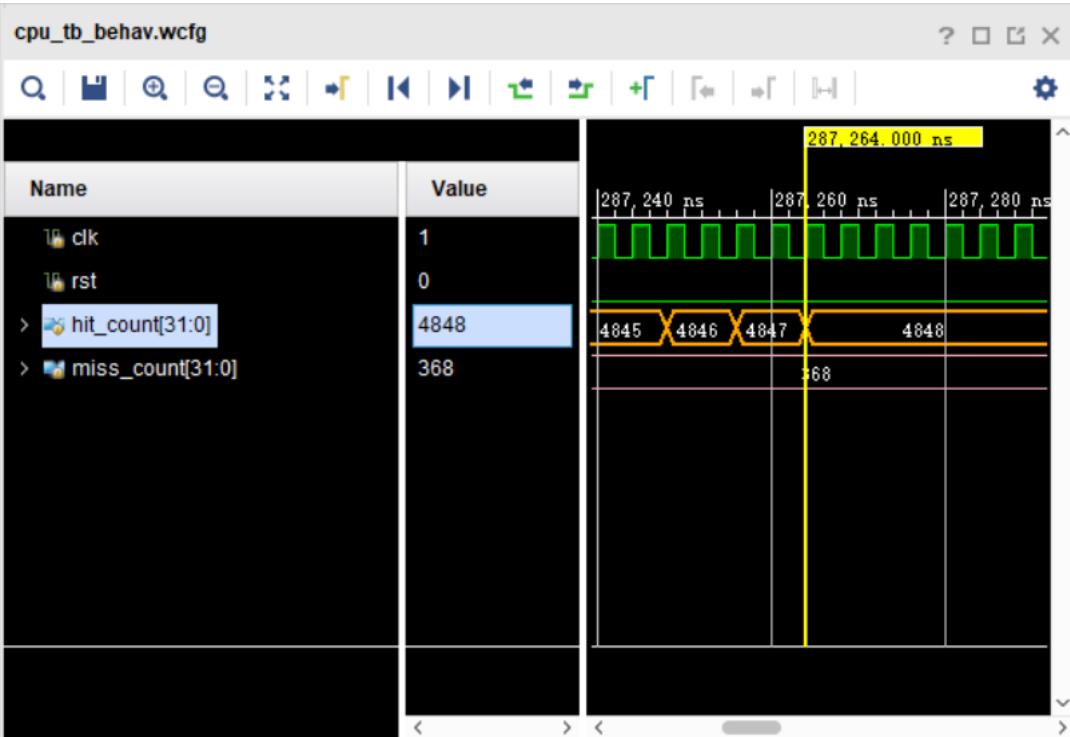
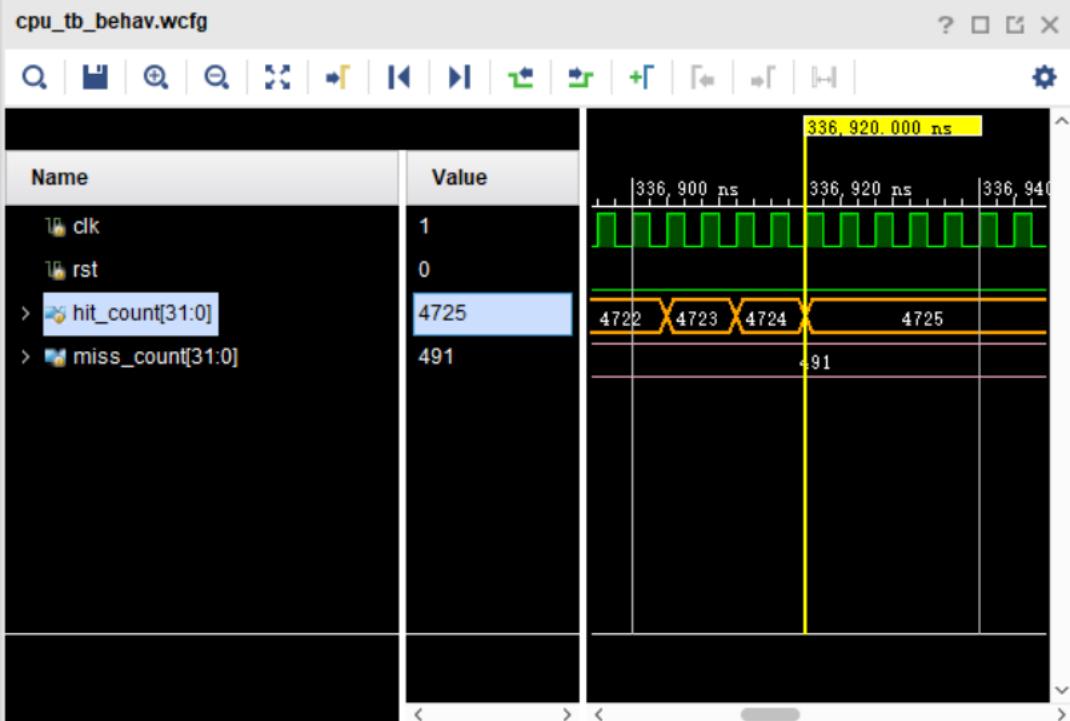
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TAG_ADDR_LEN	9
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

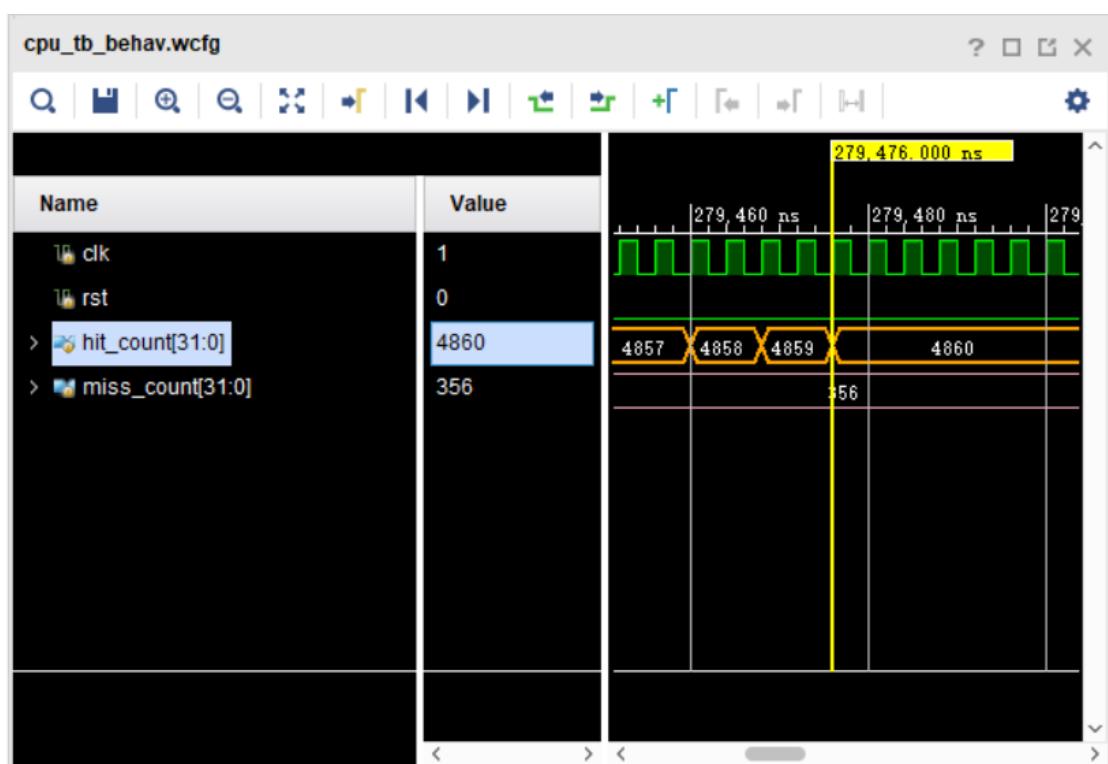
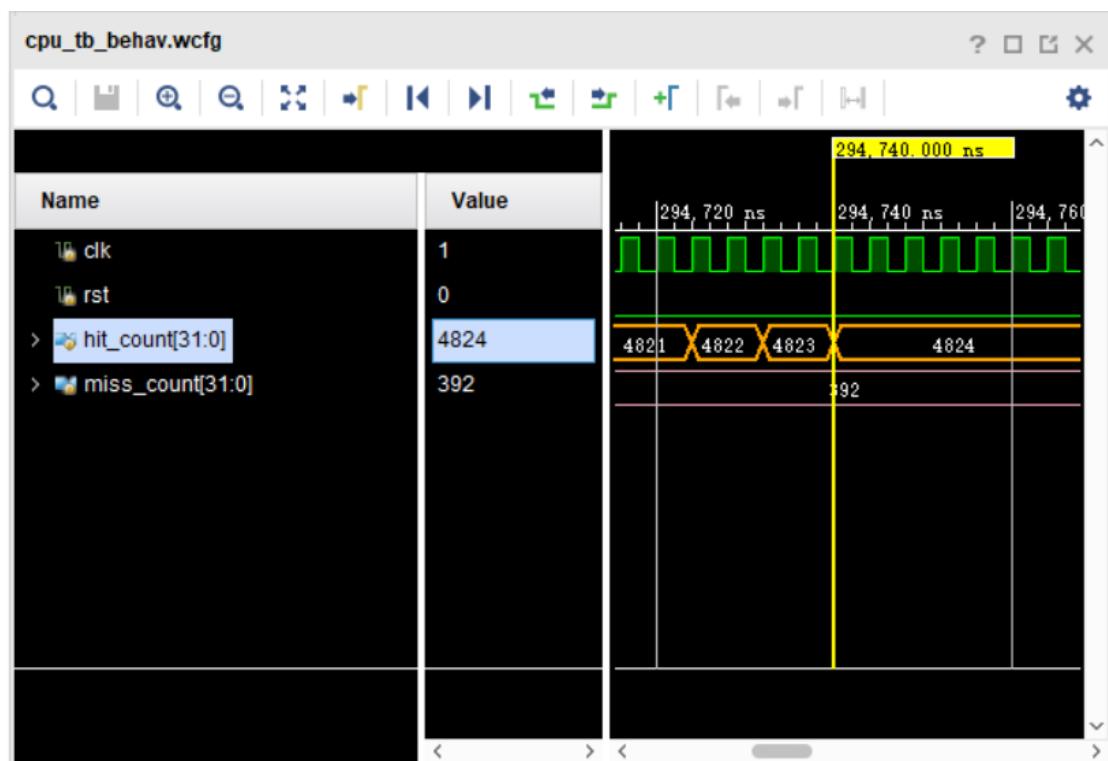


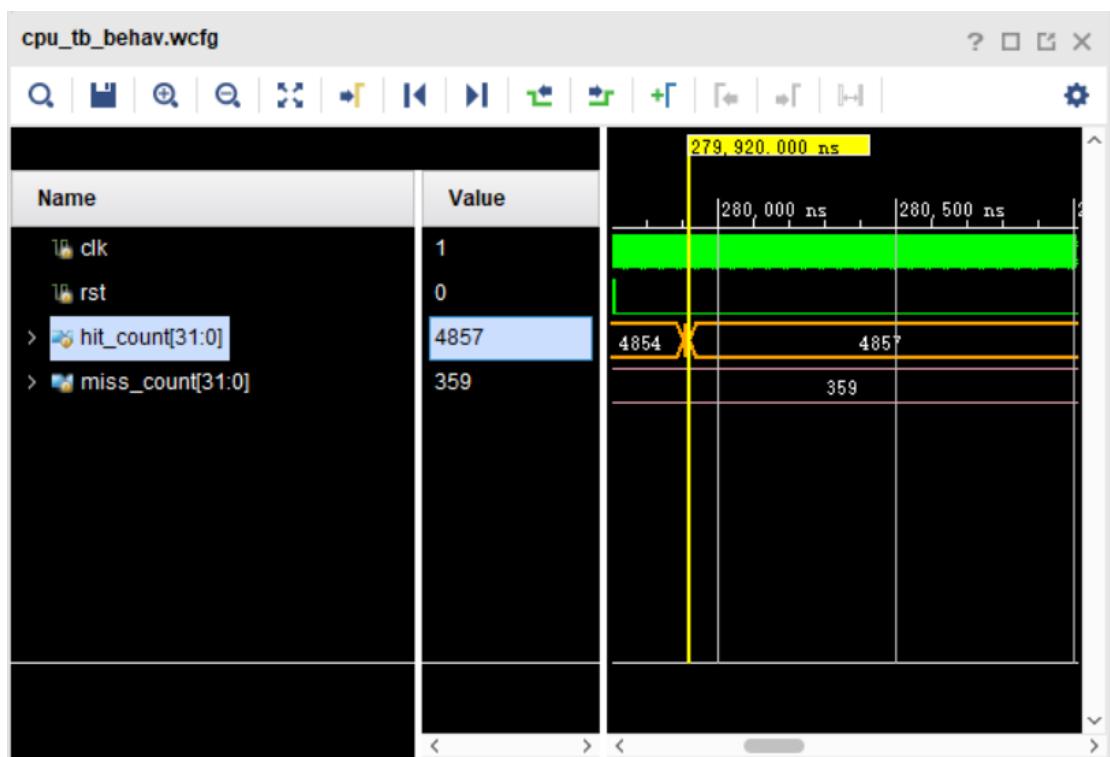
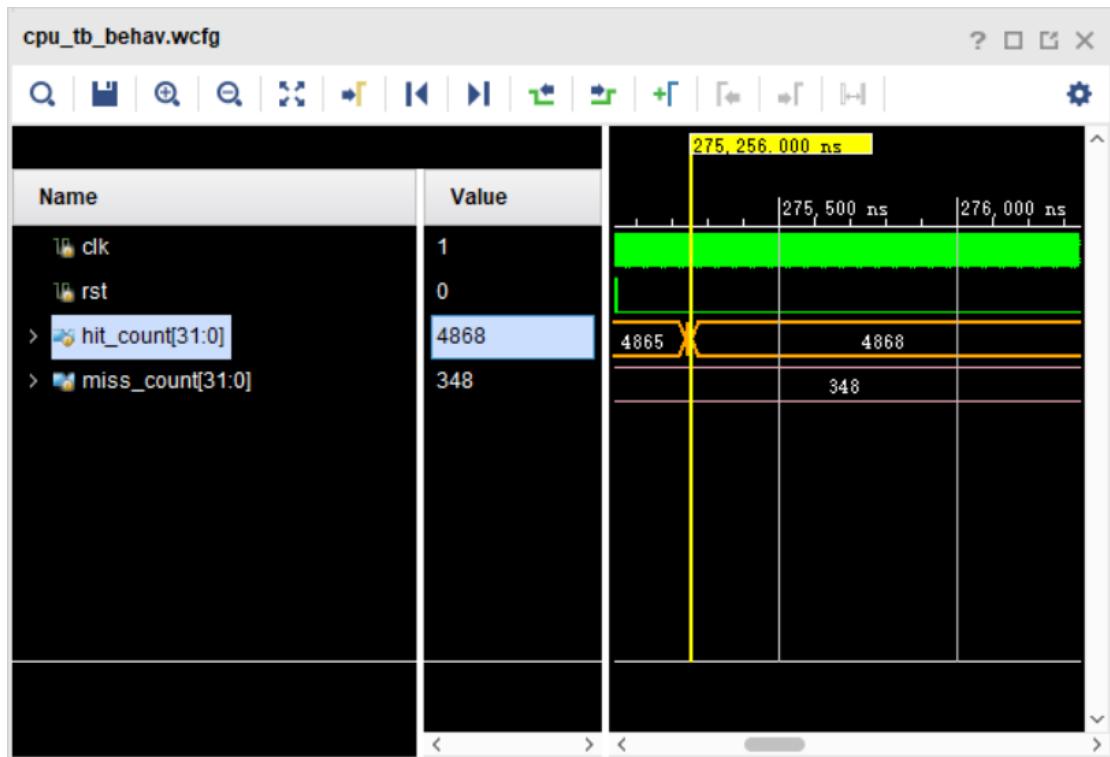




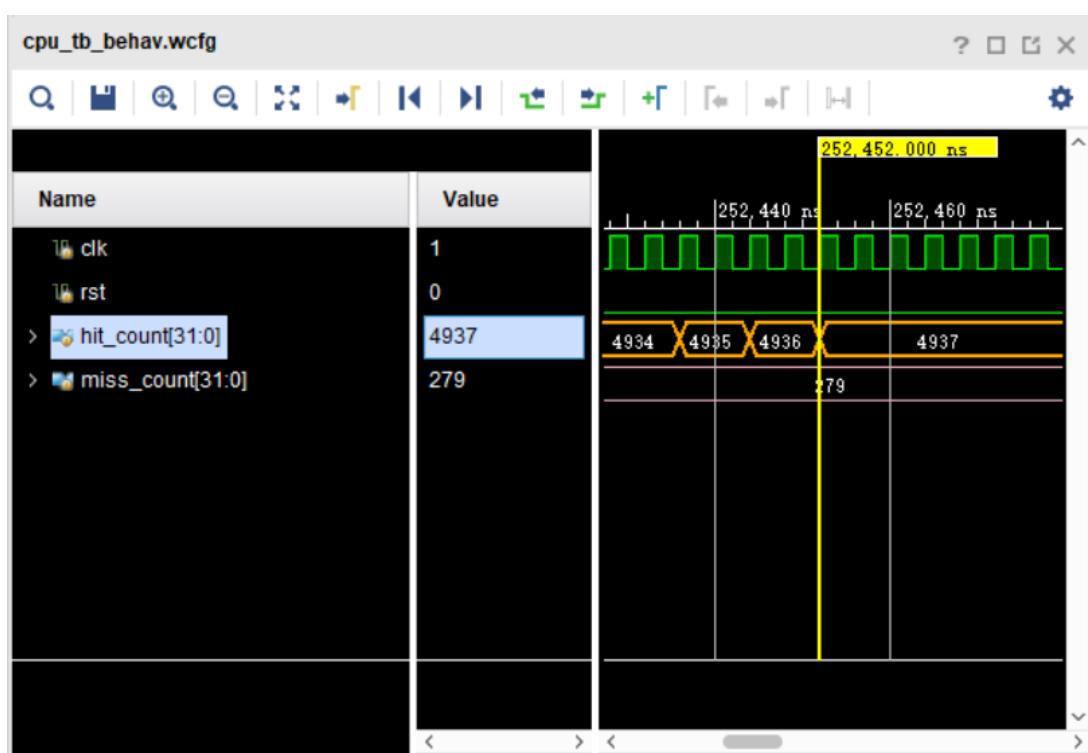
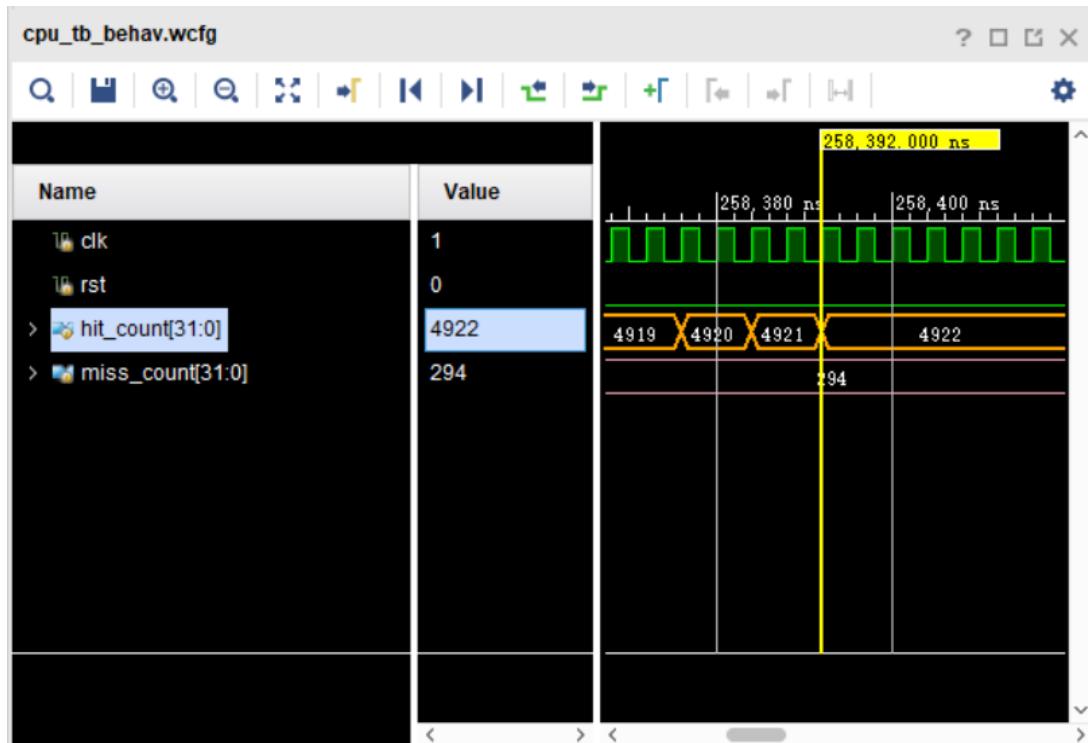
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SET_ADDR_LEN	3
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

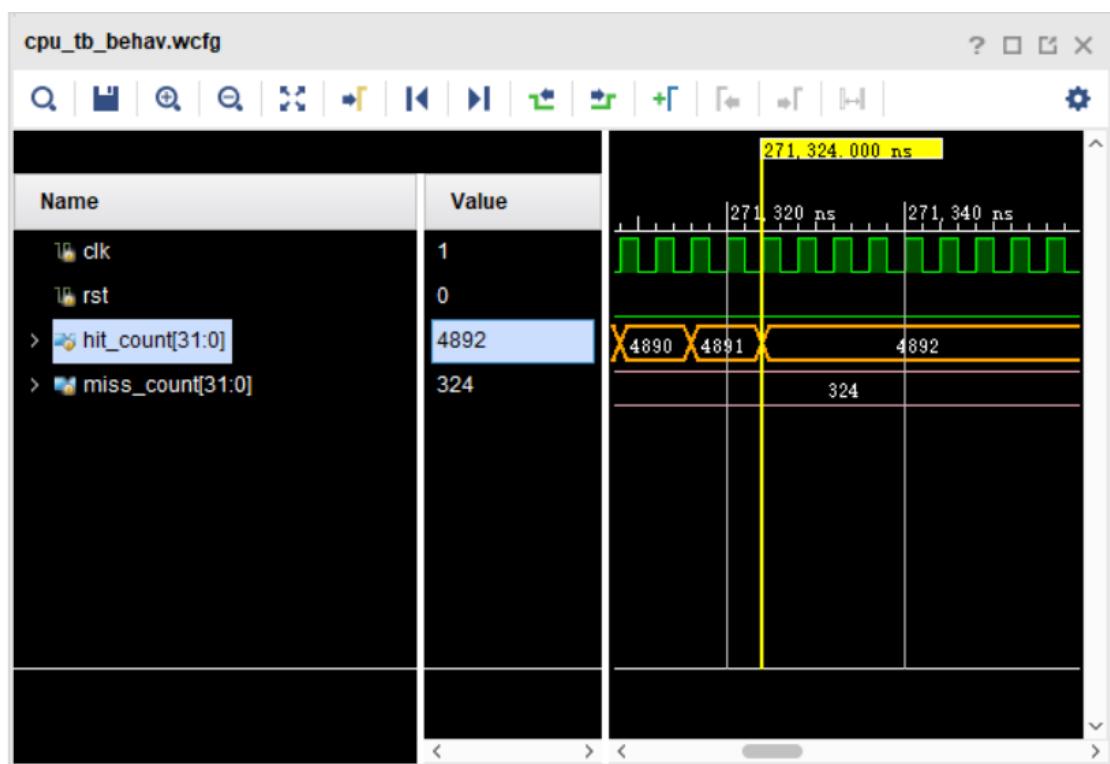
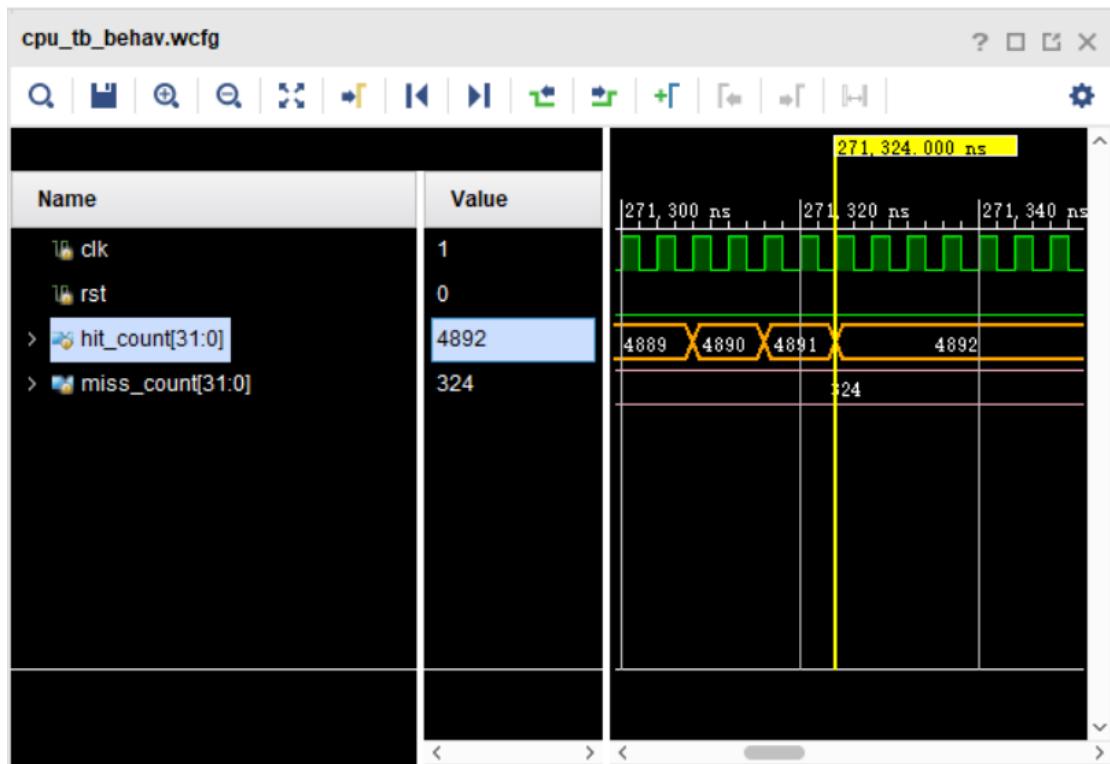


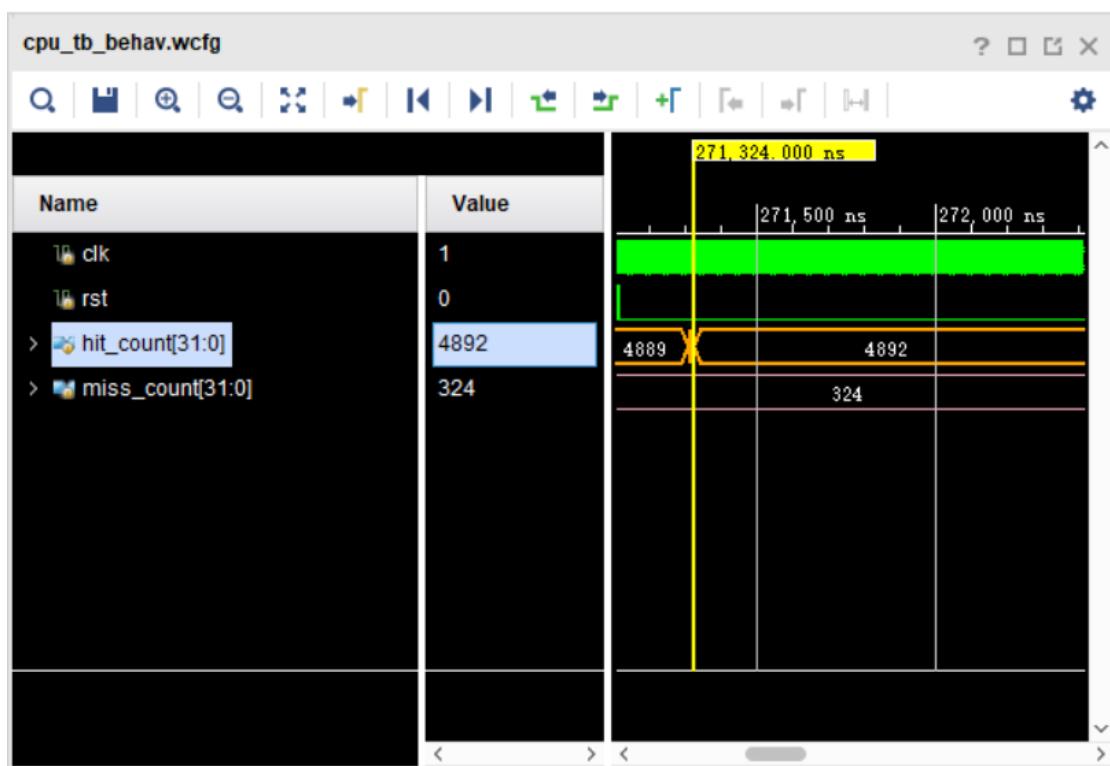
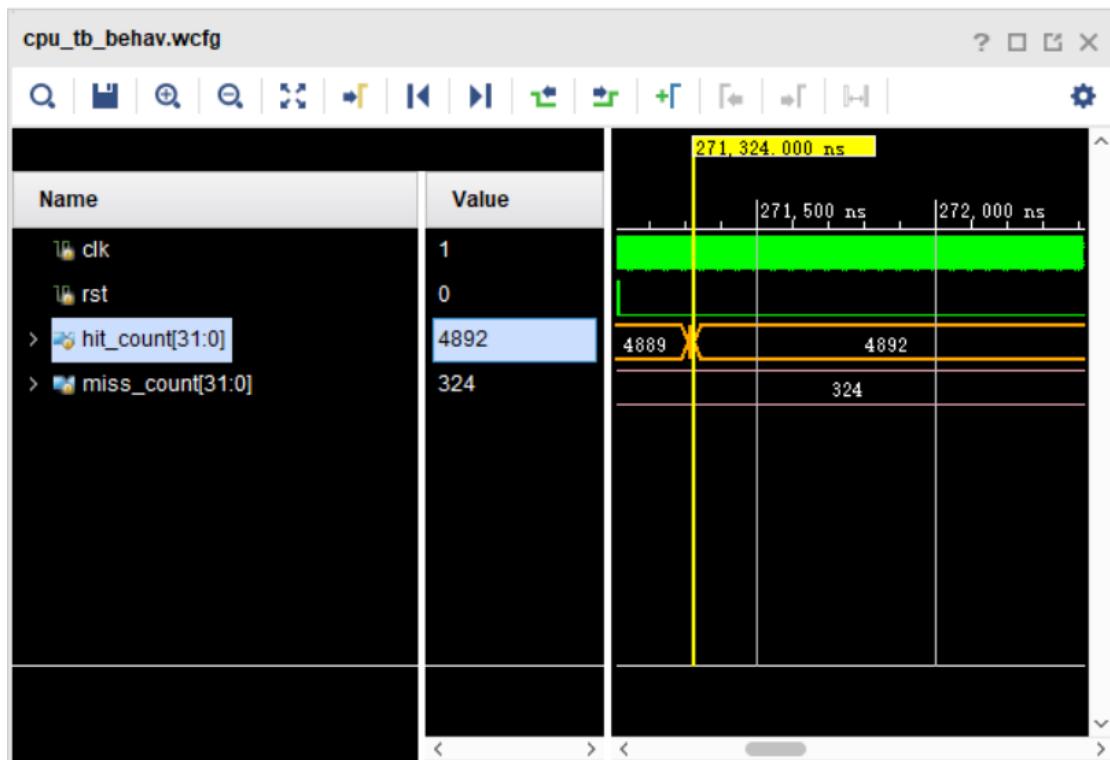




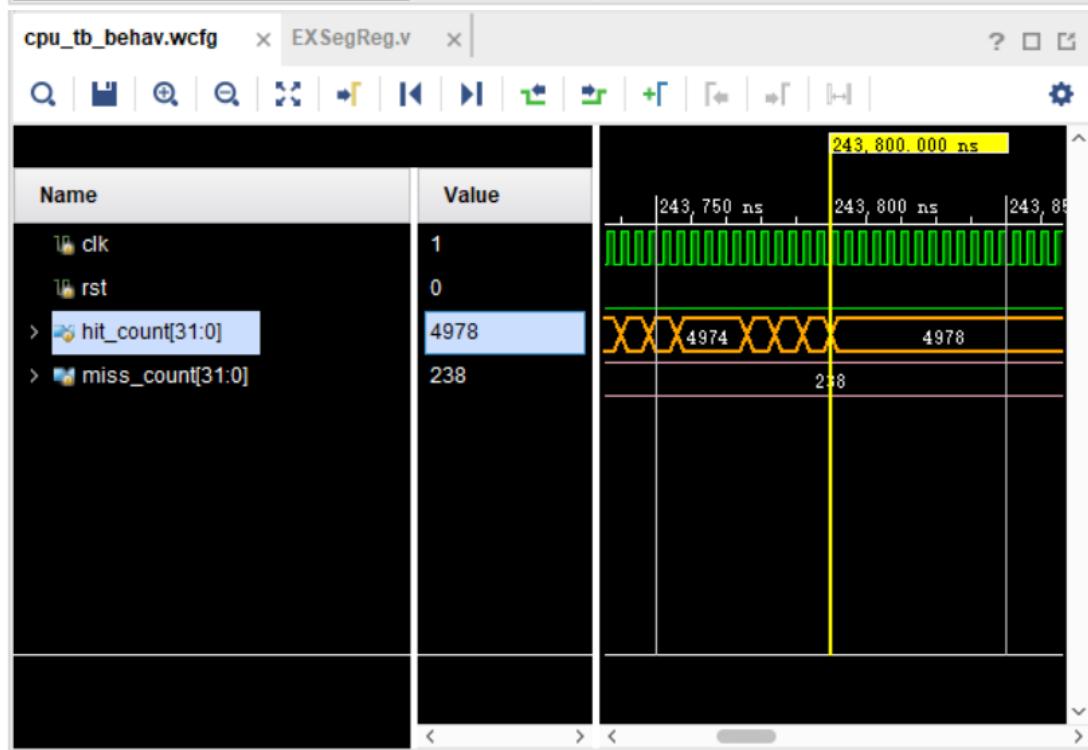
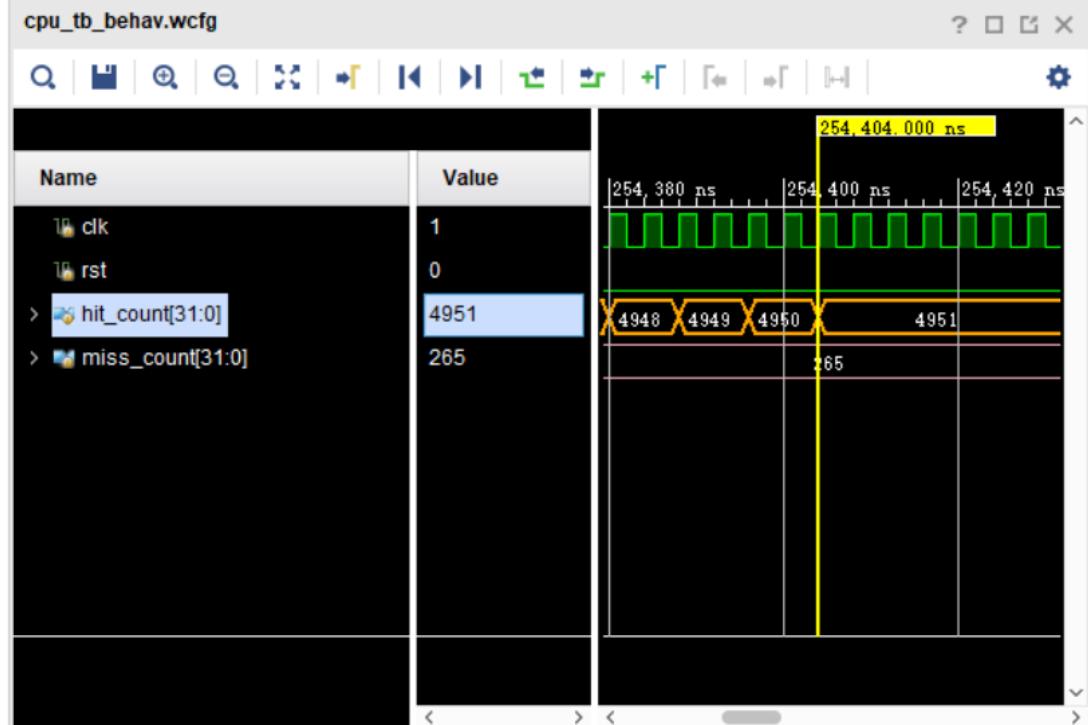
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SET_ADDR_LEN	4
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

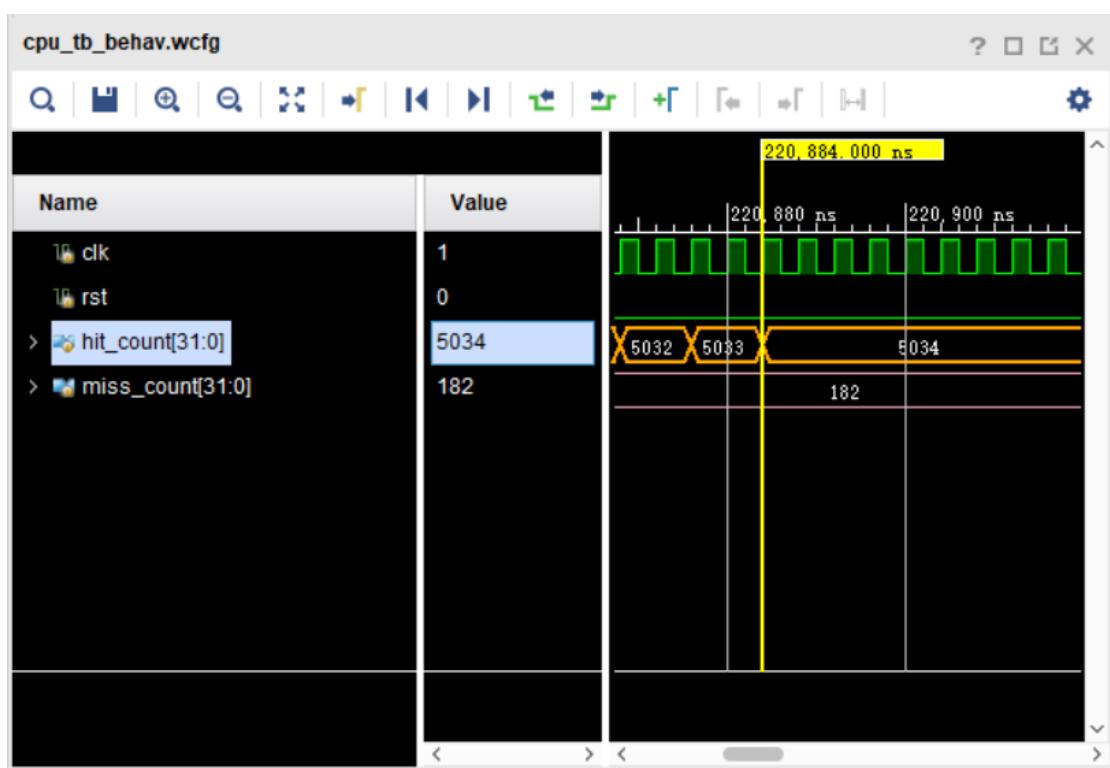
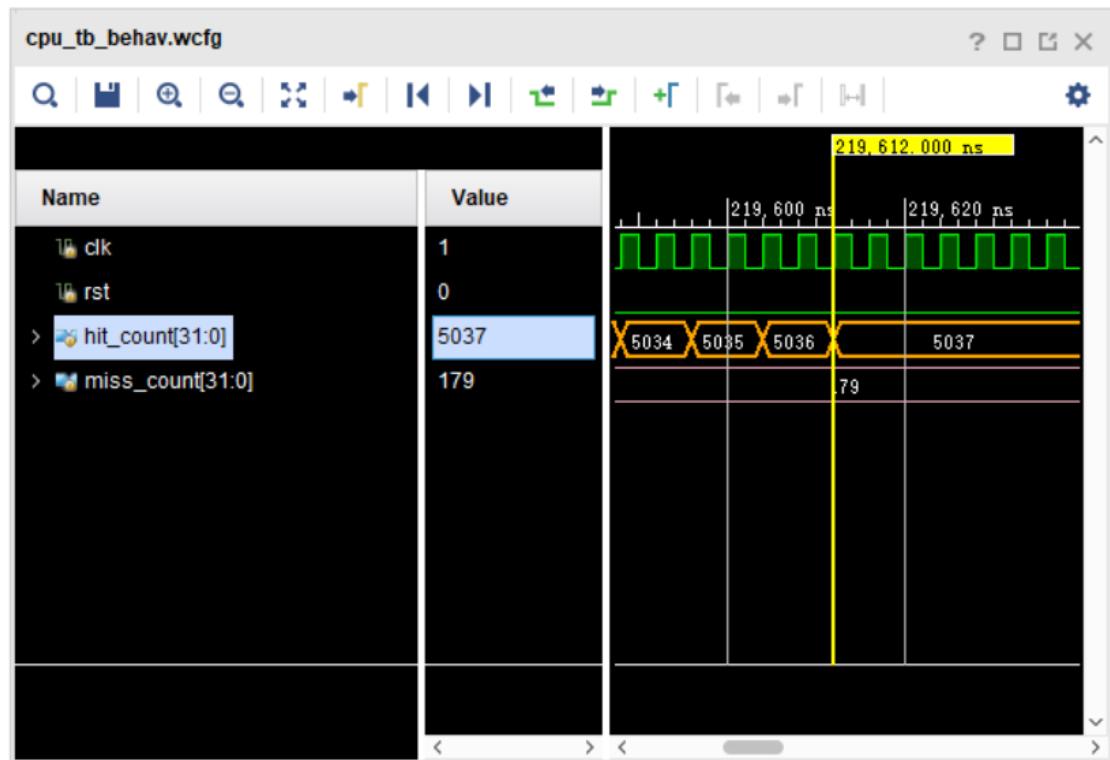


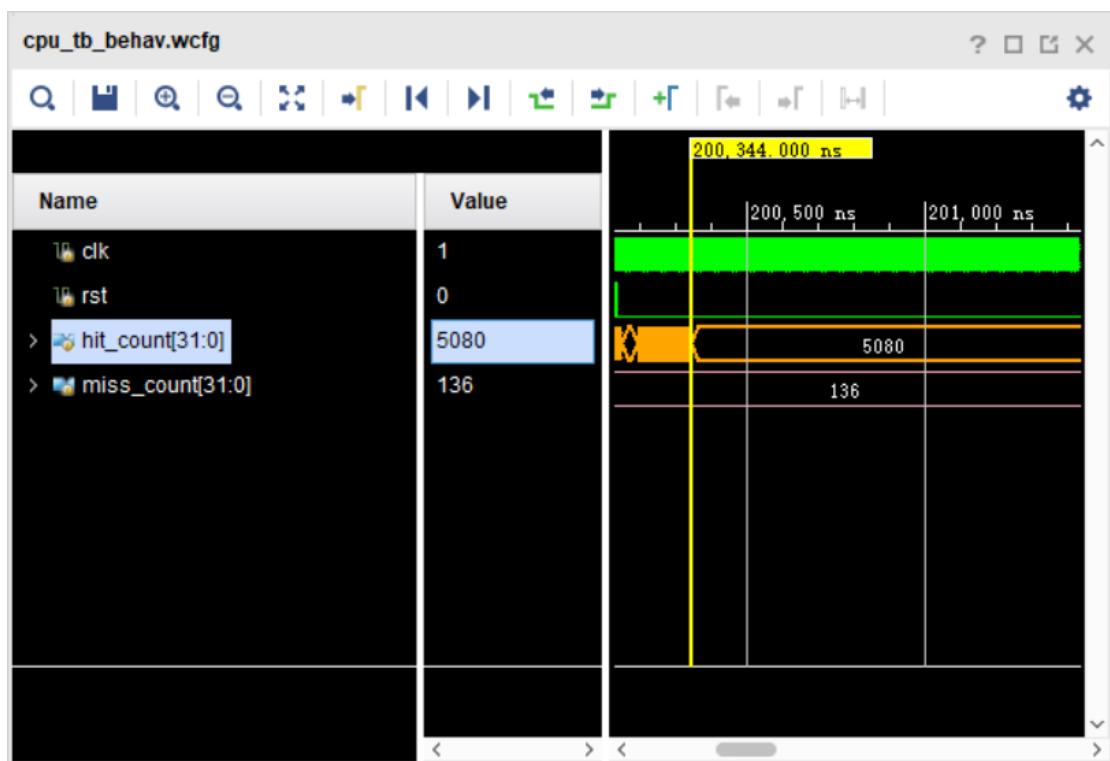
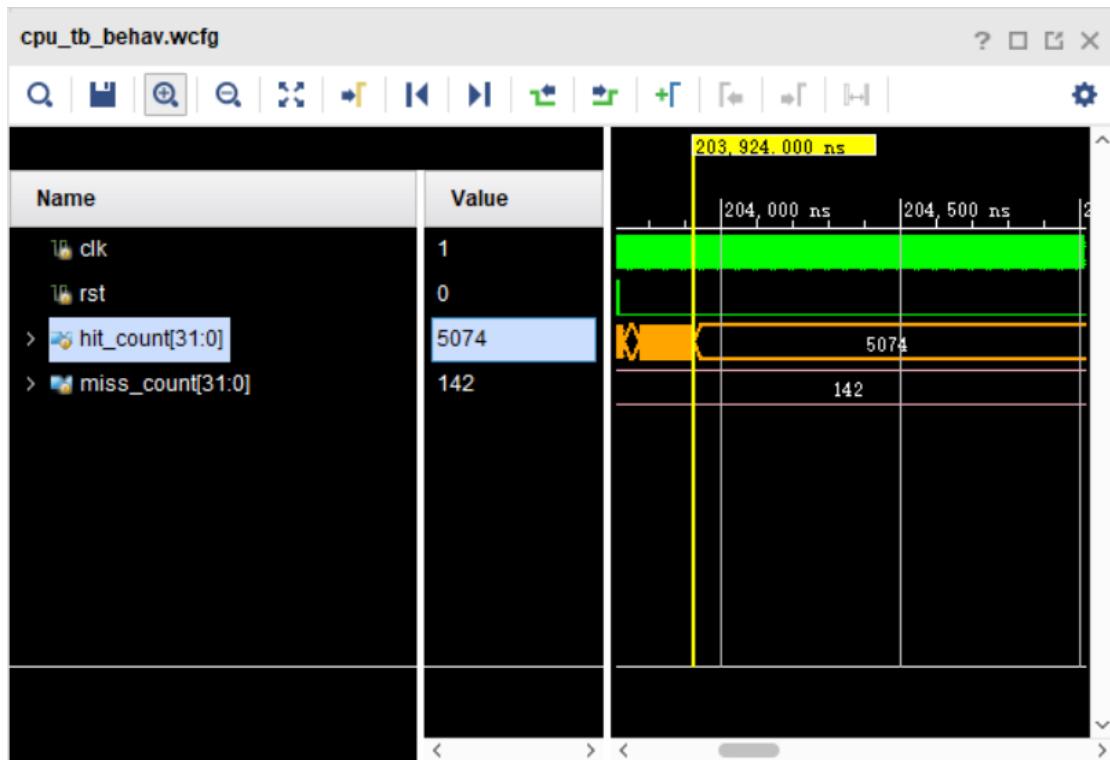




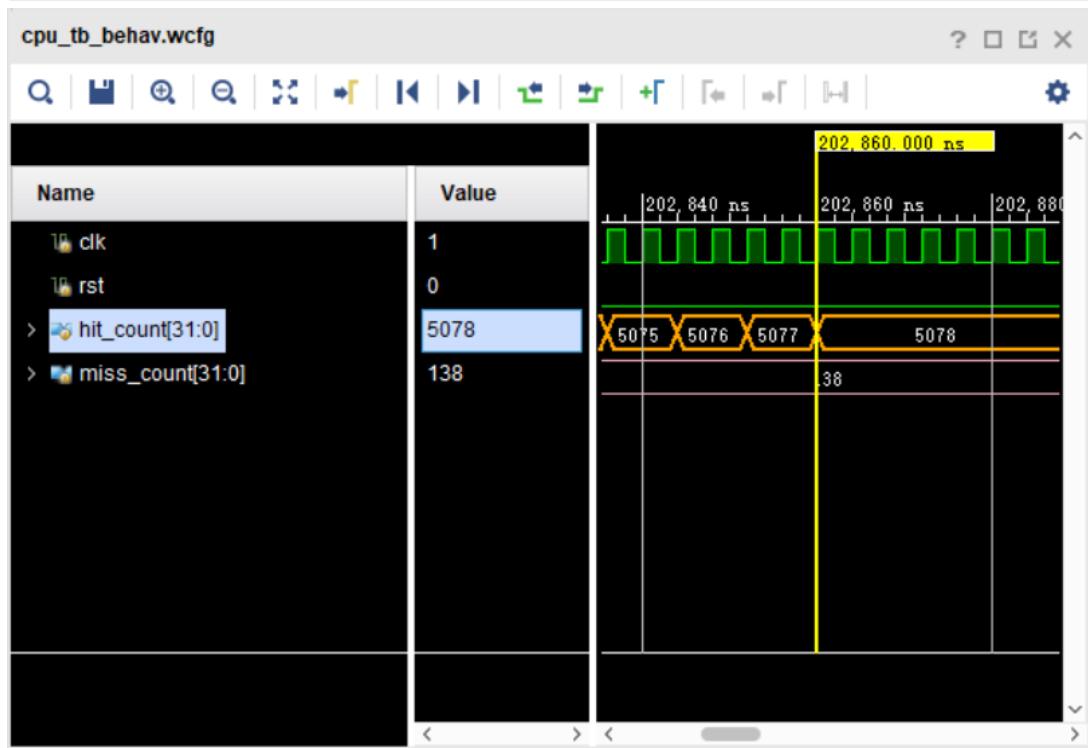
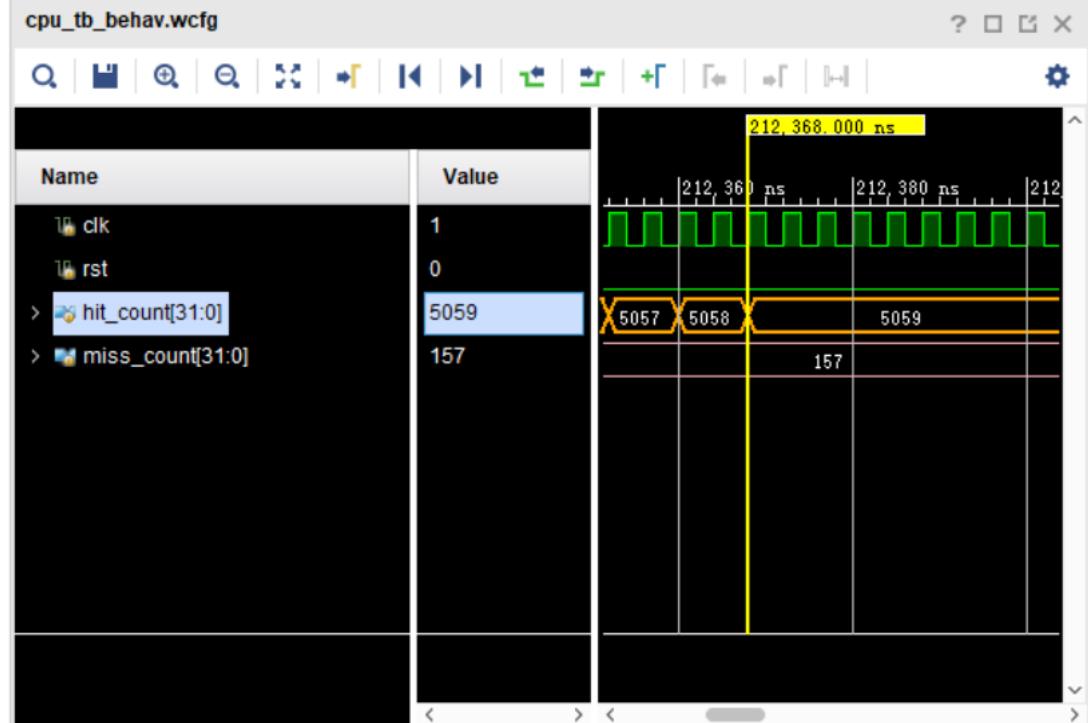
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SET_ADDR_LEN	2
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

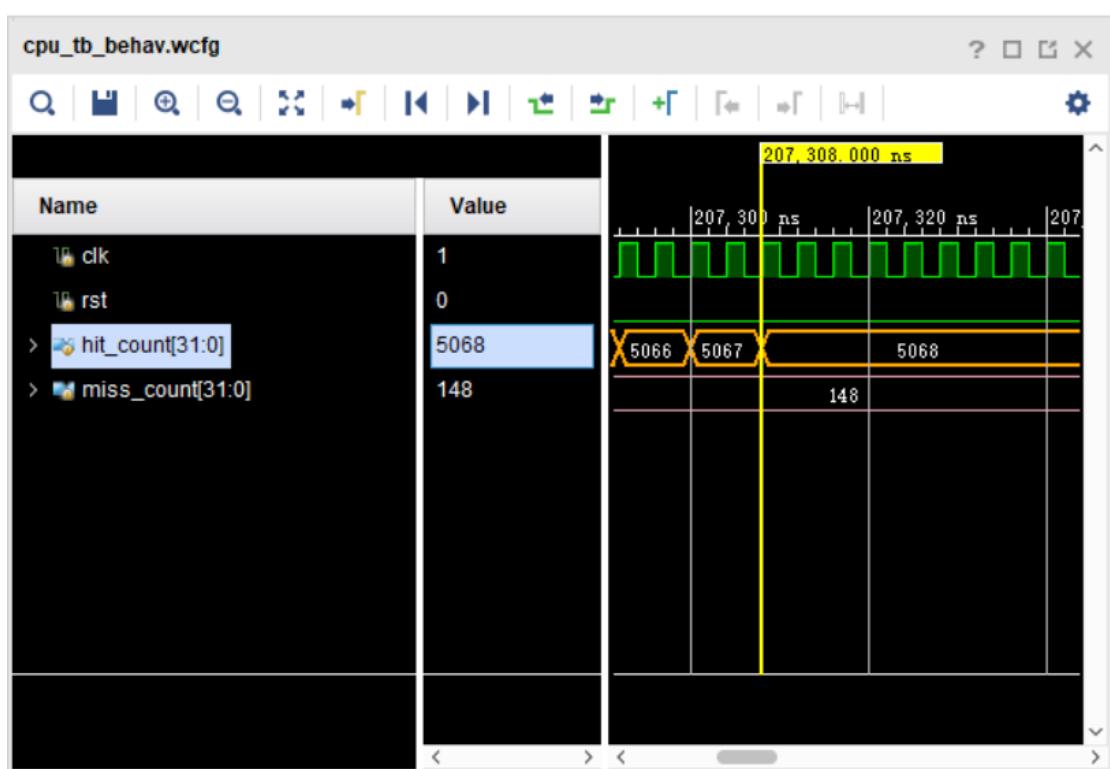
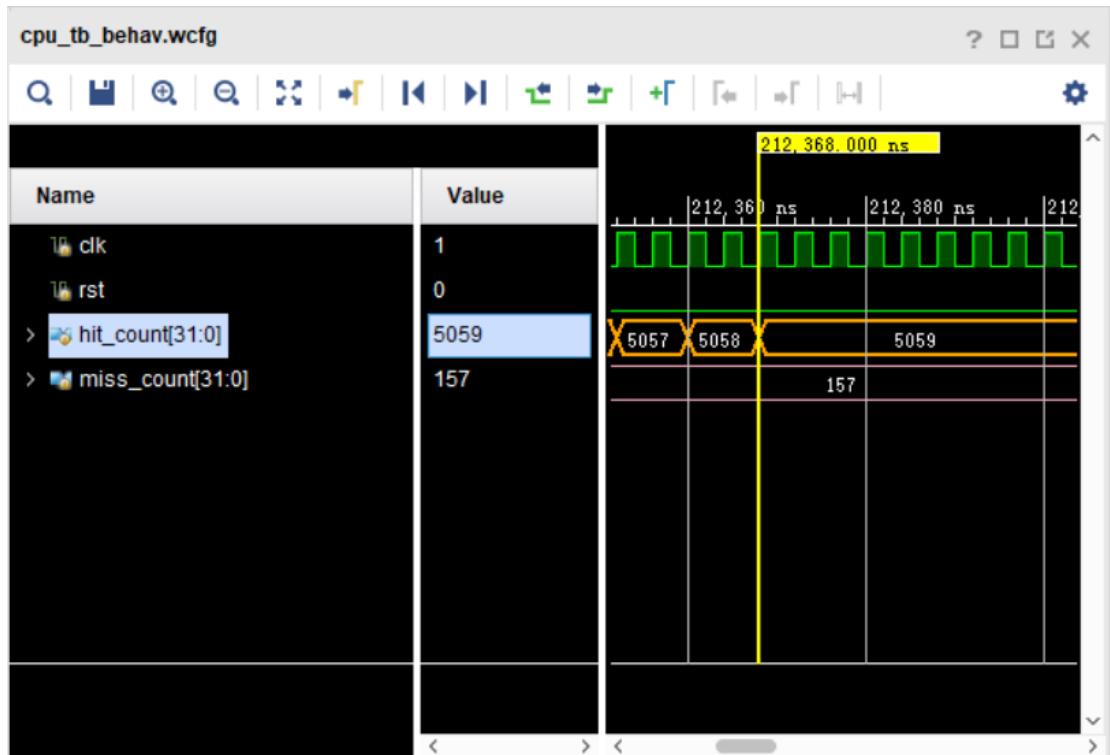


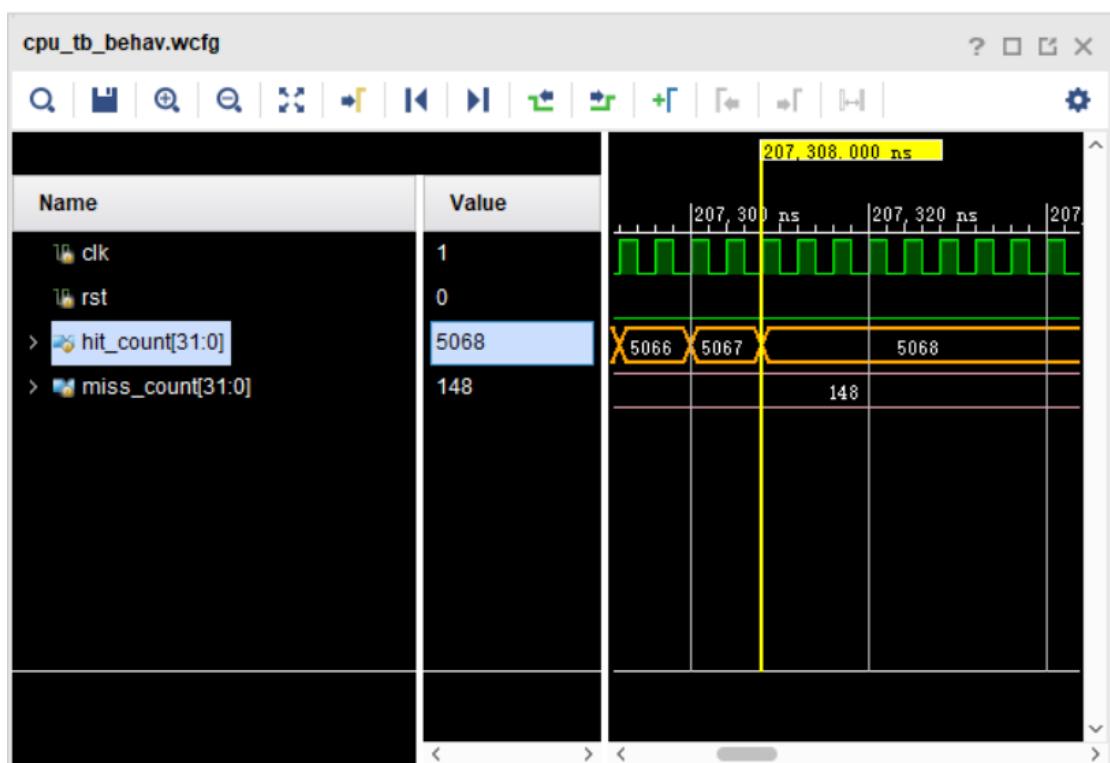
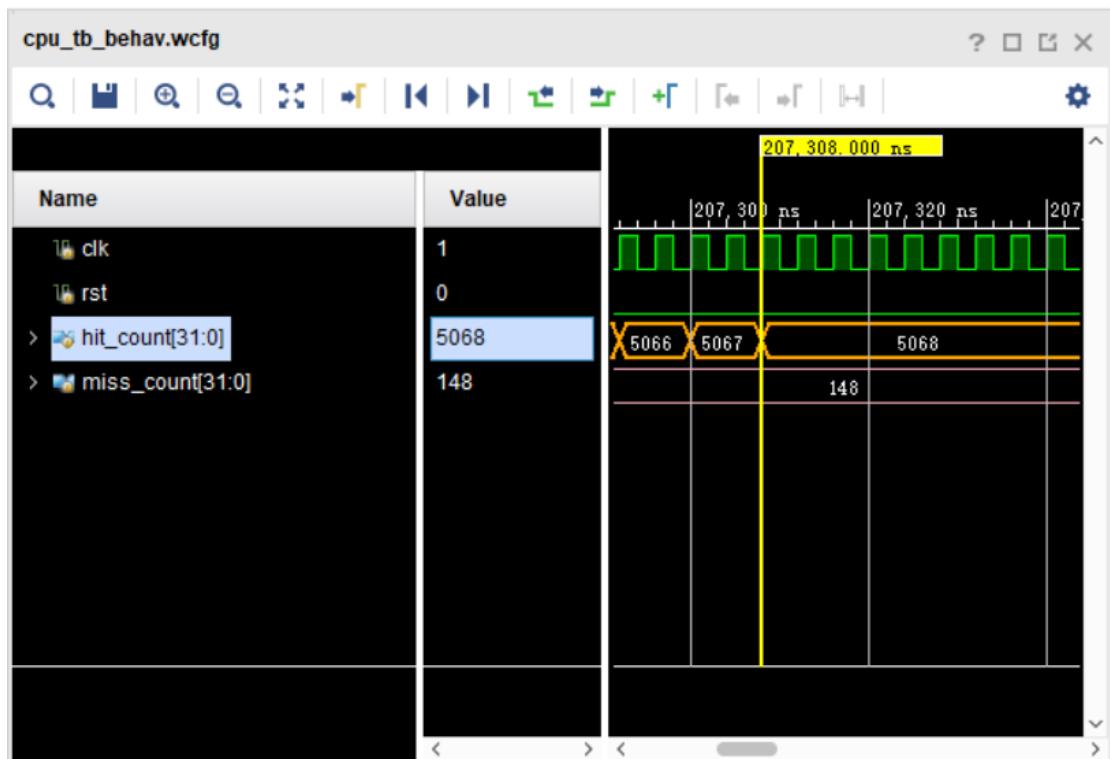




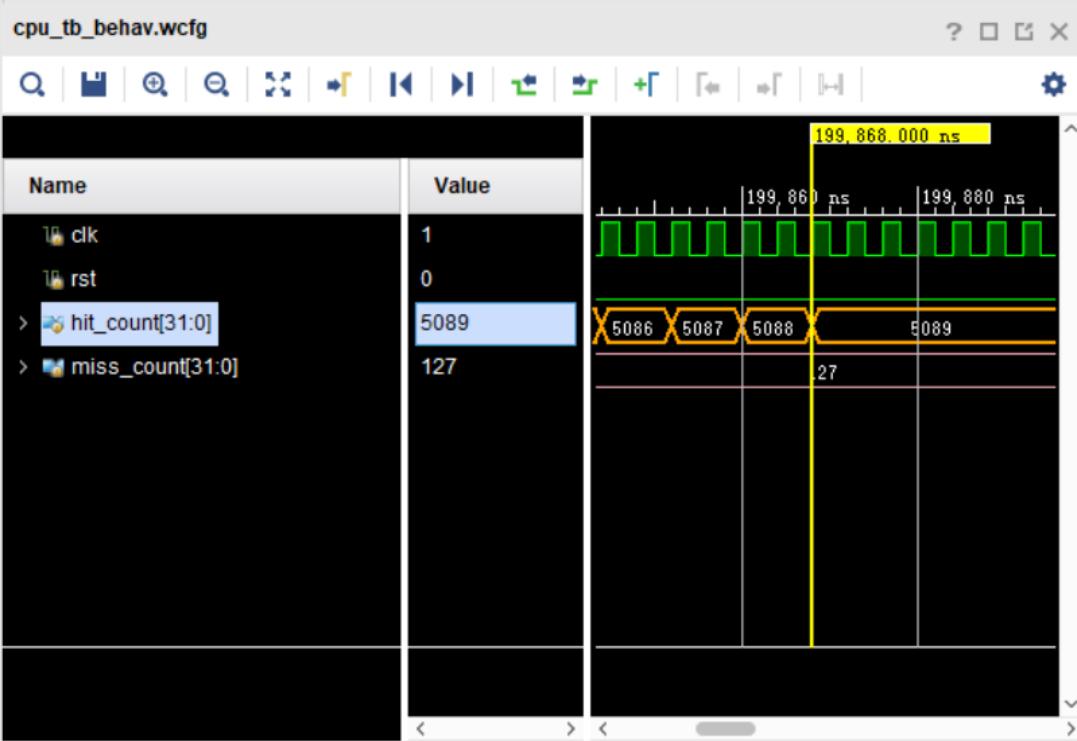
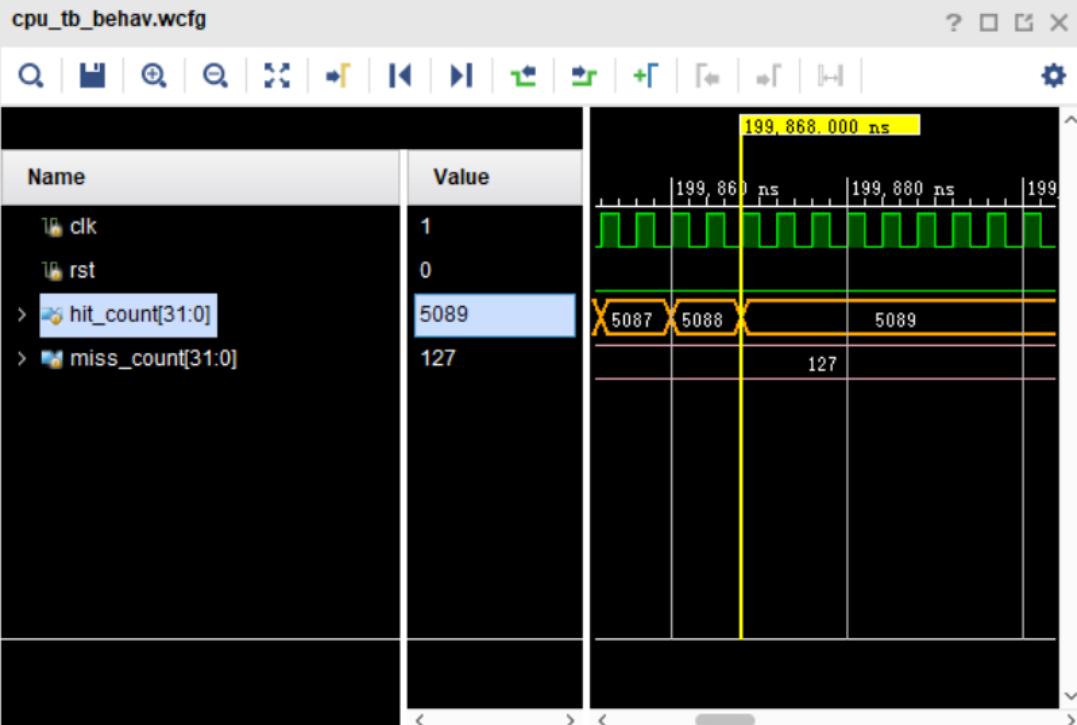
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SET_ADDR_LEN	3
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

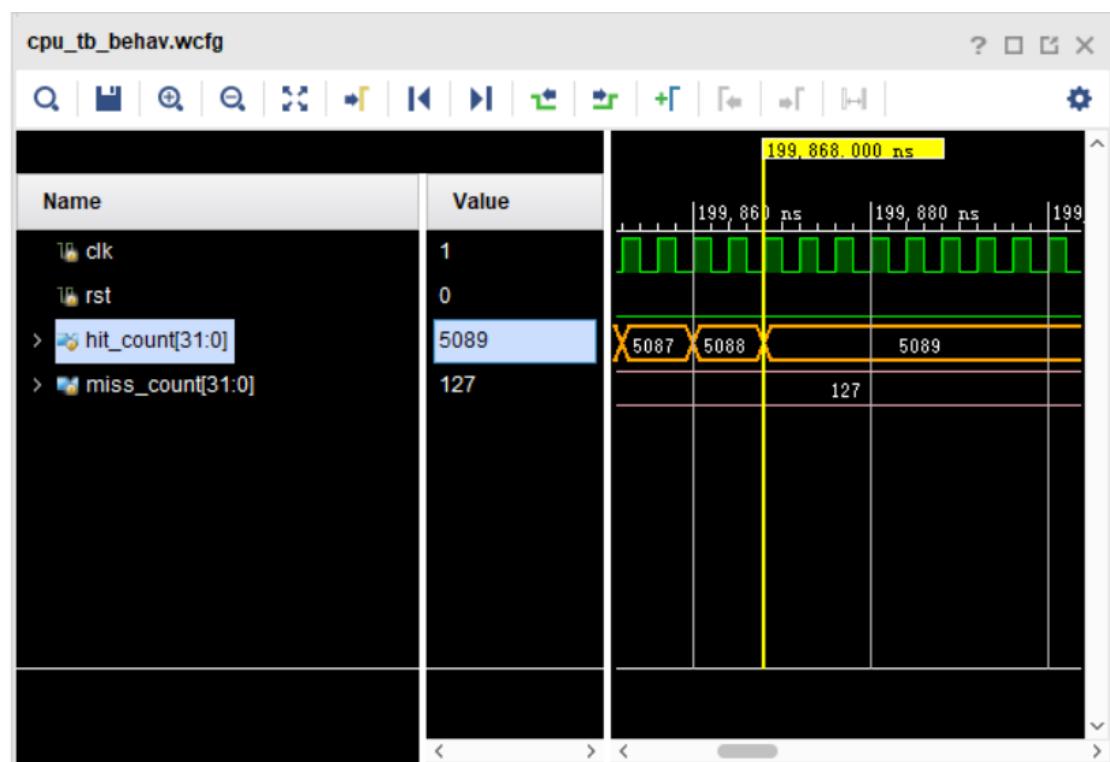
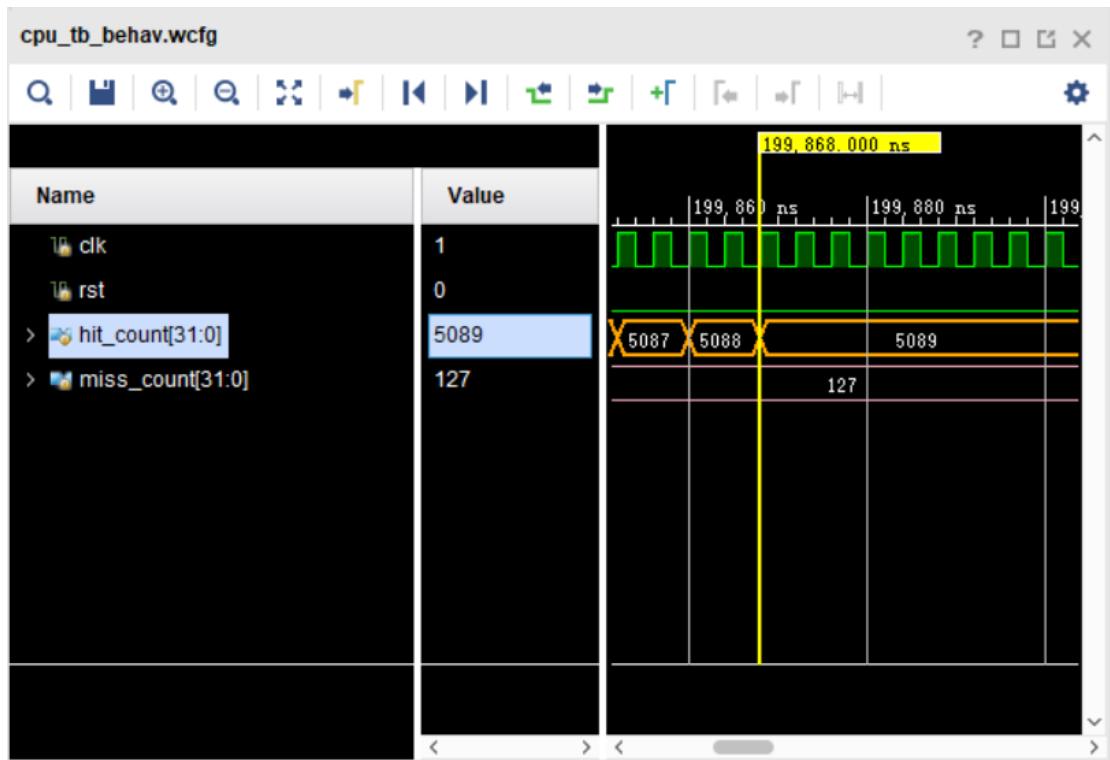


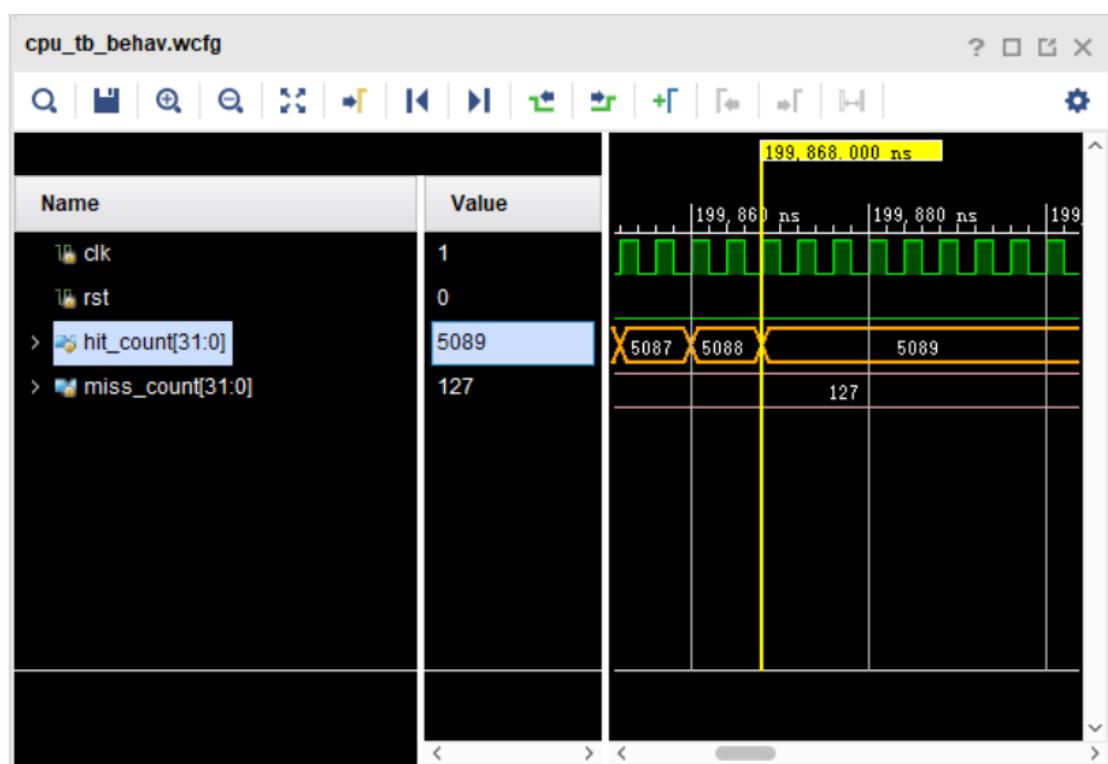
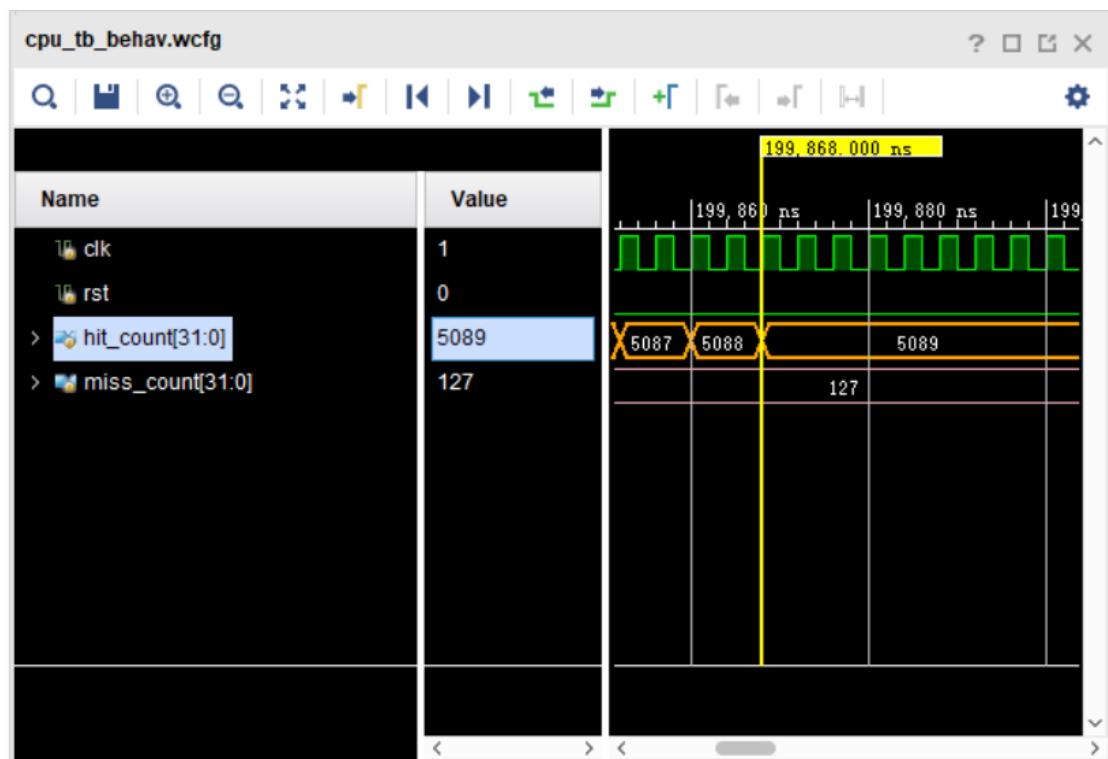




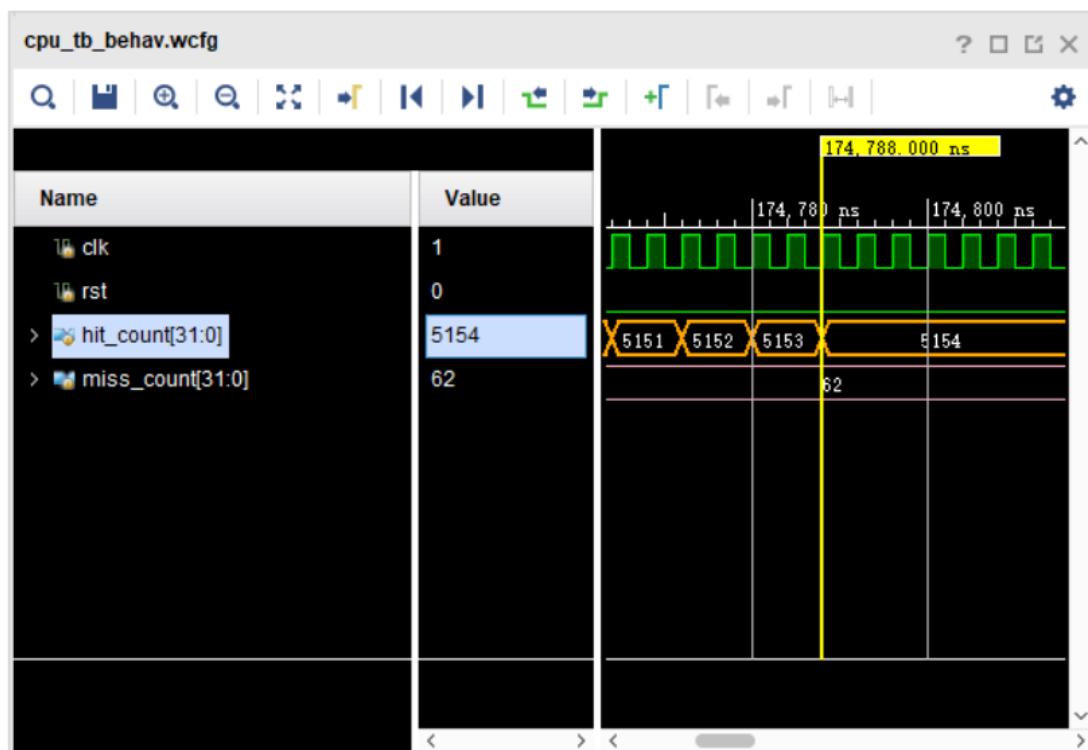
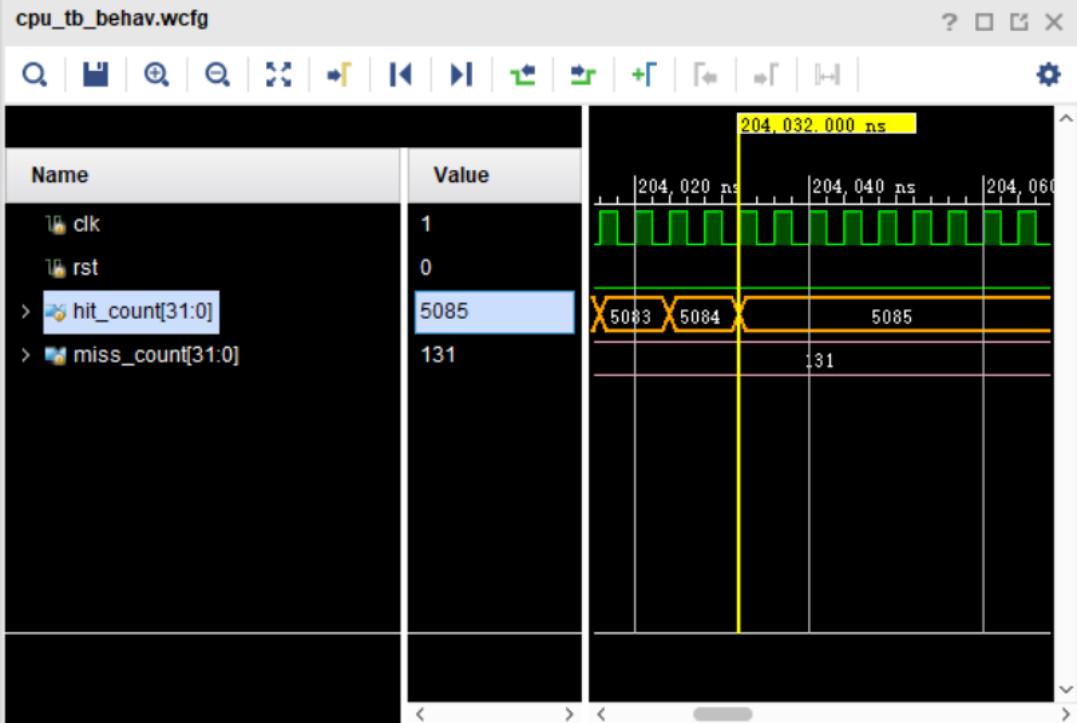
LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

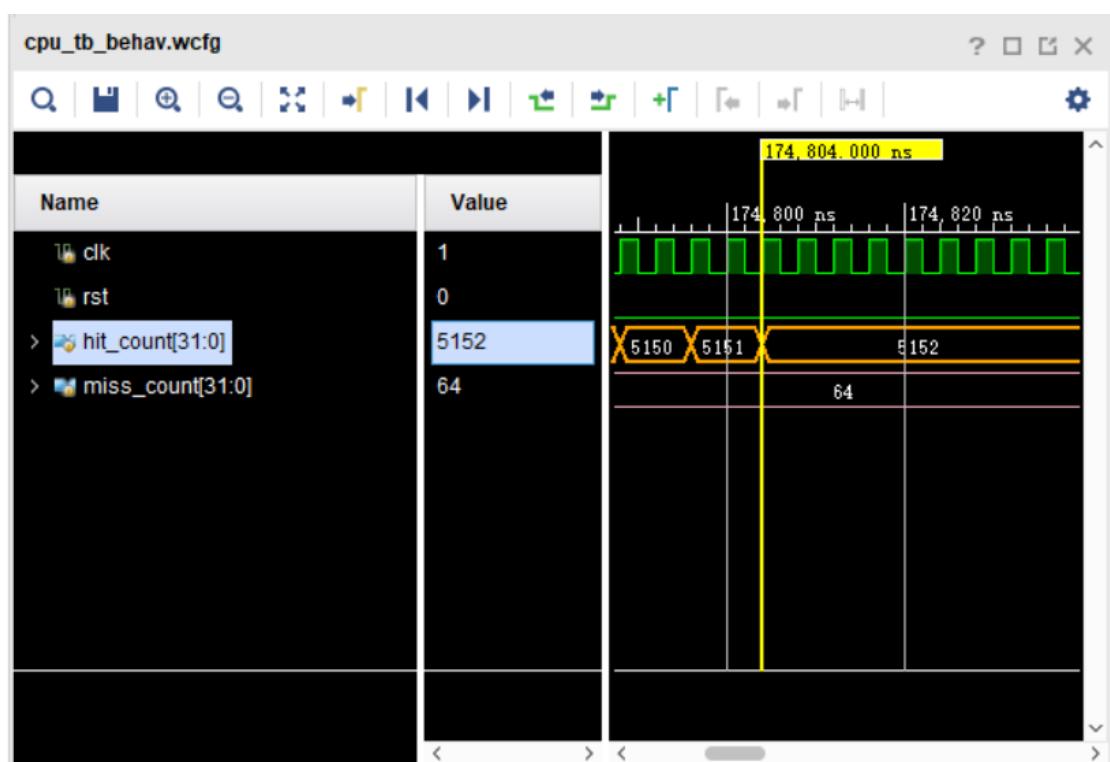
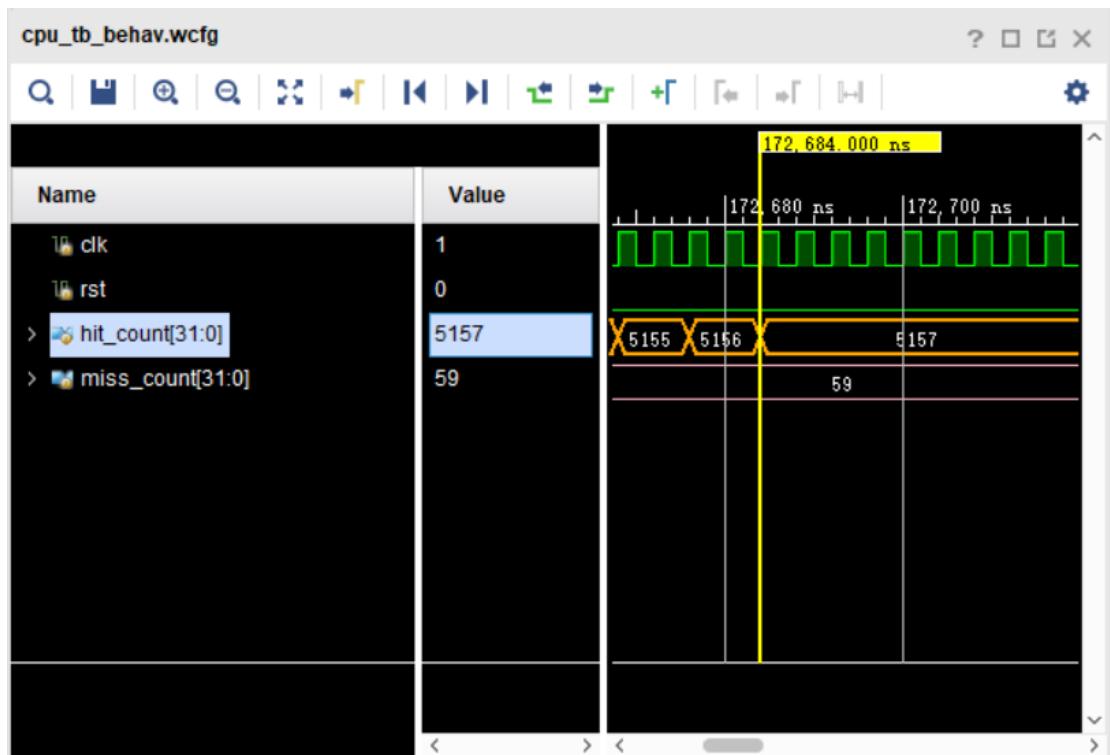


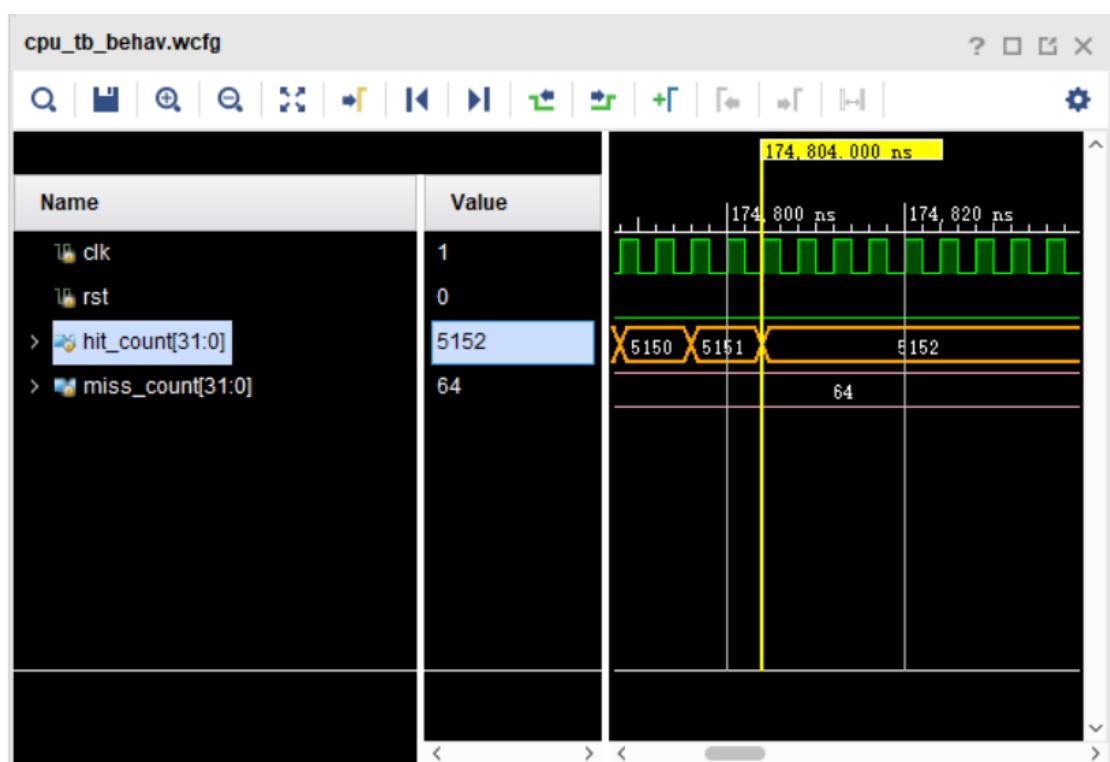
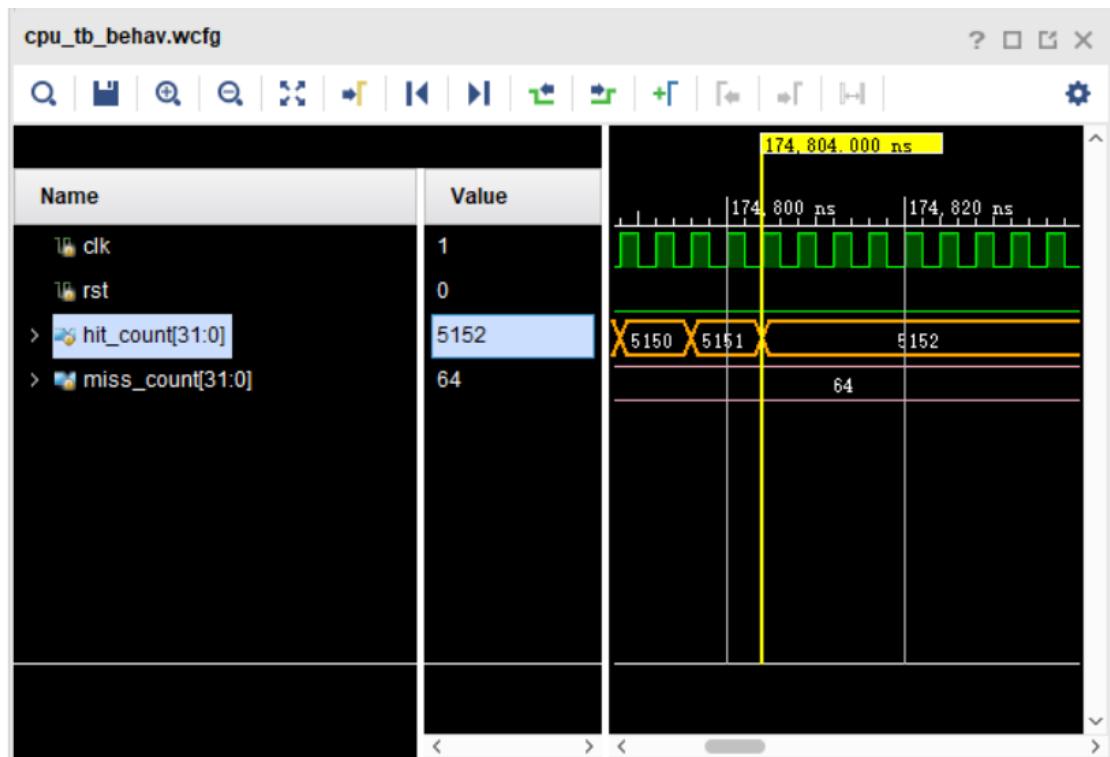




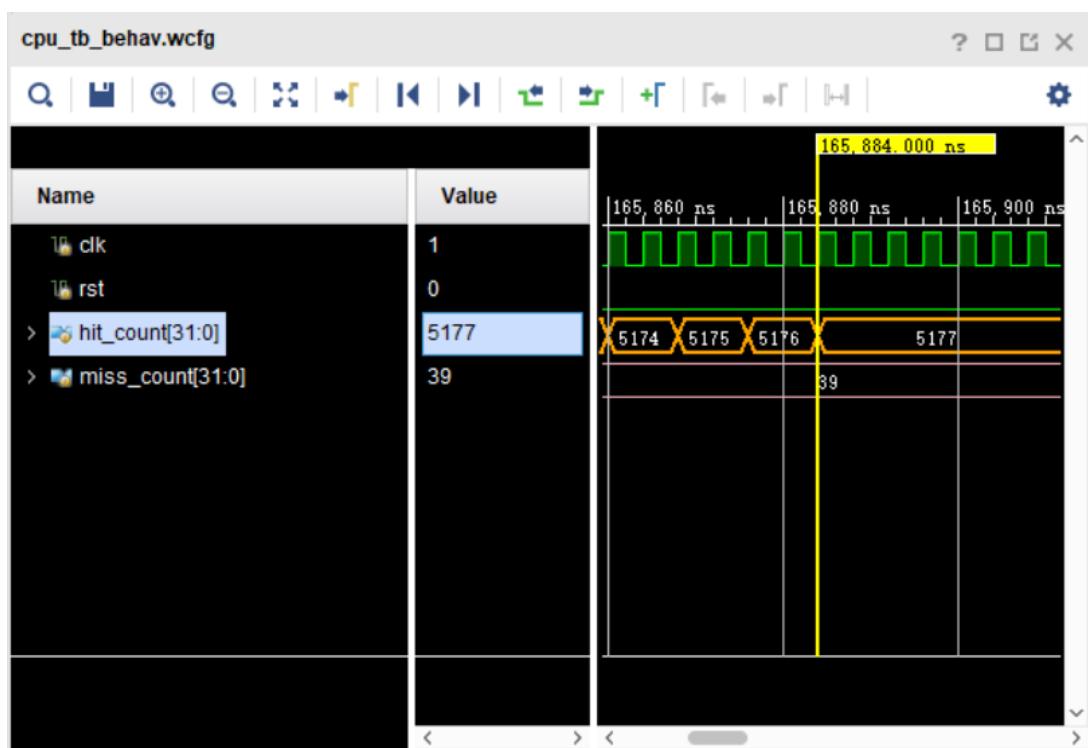
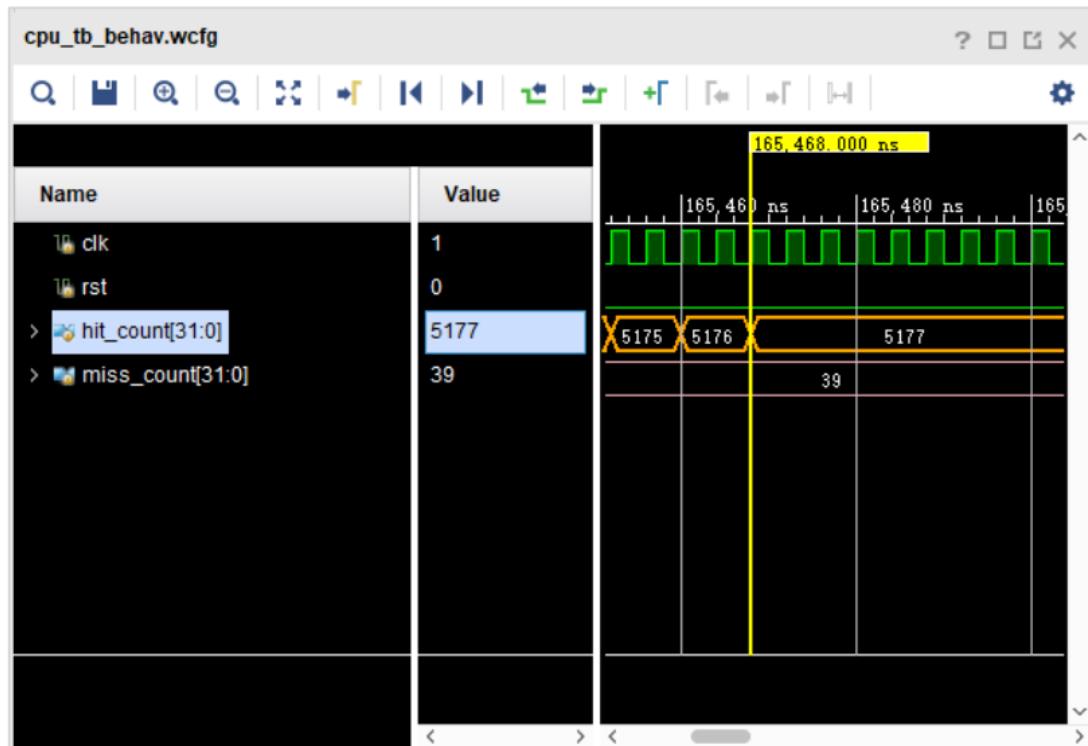
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TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

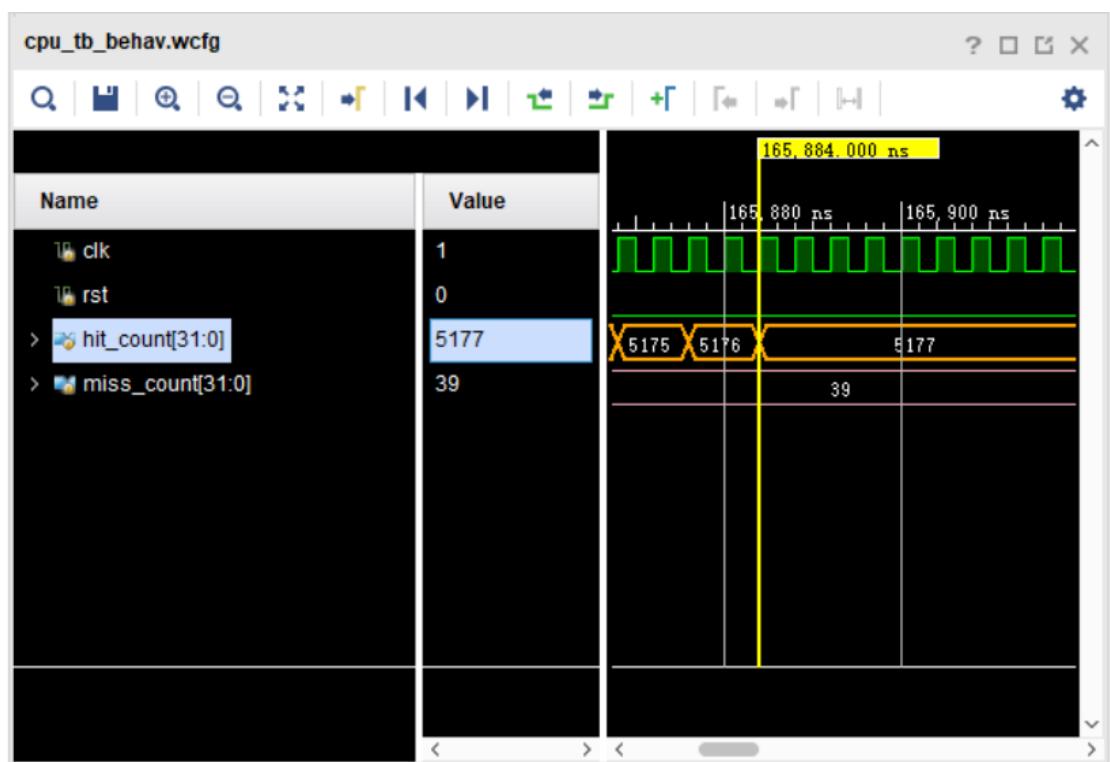
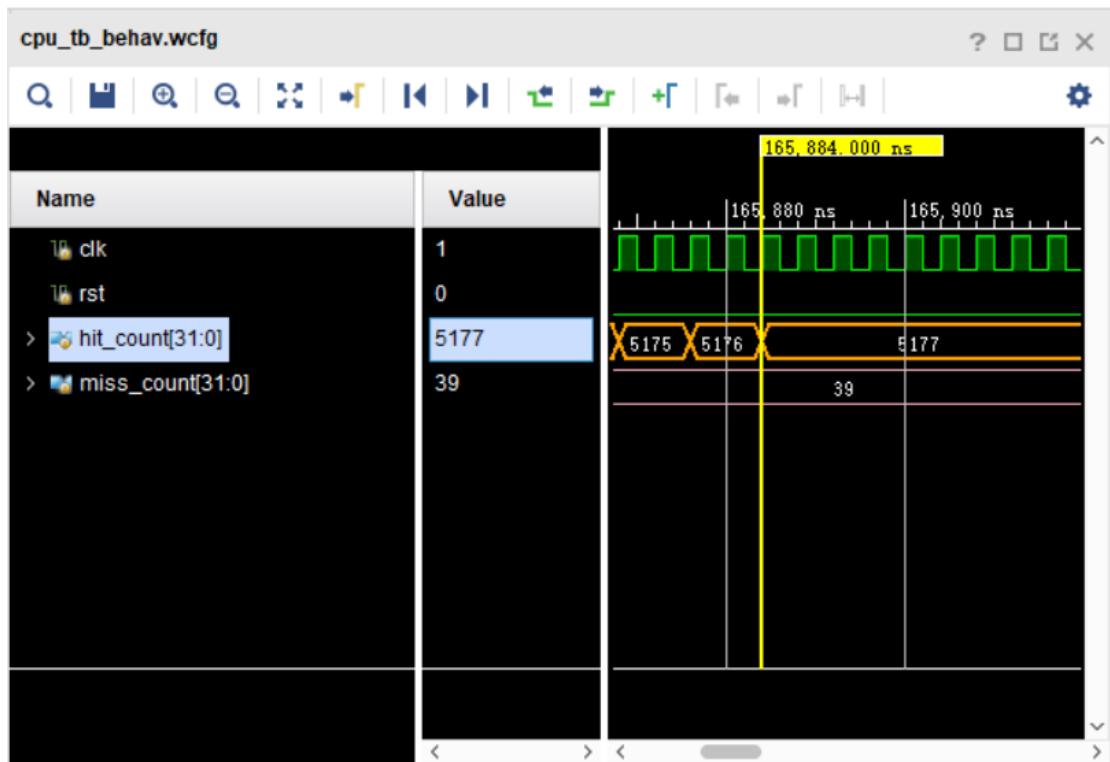


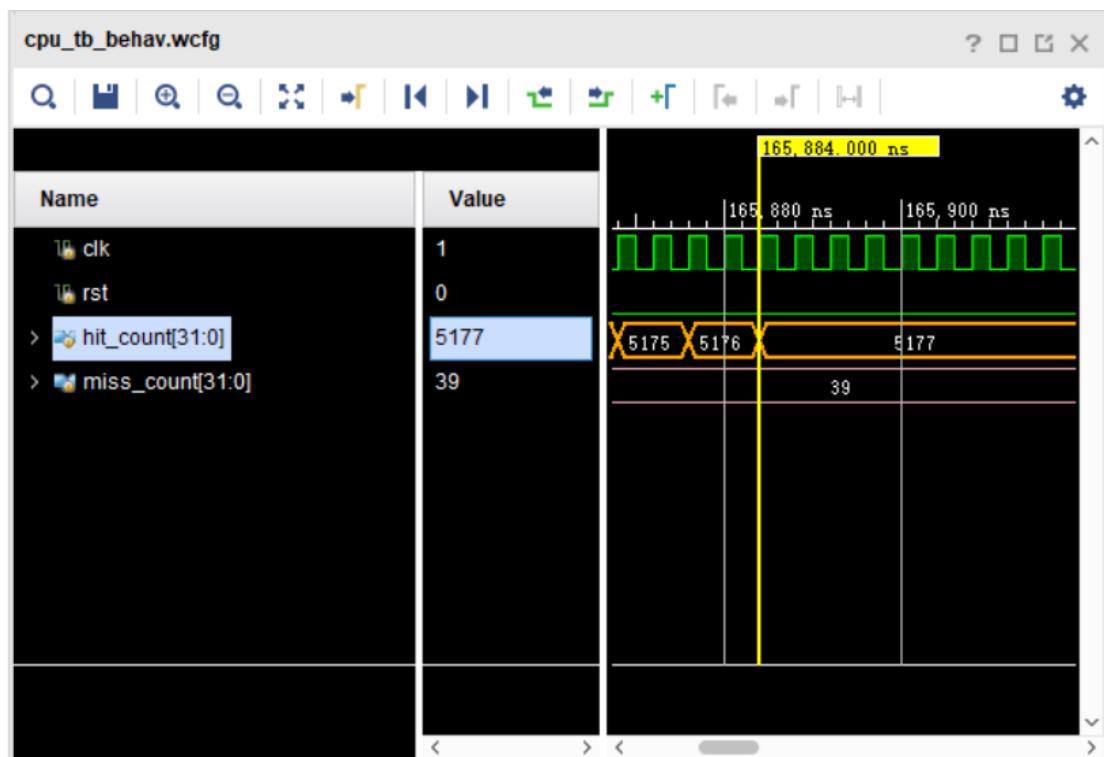
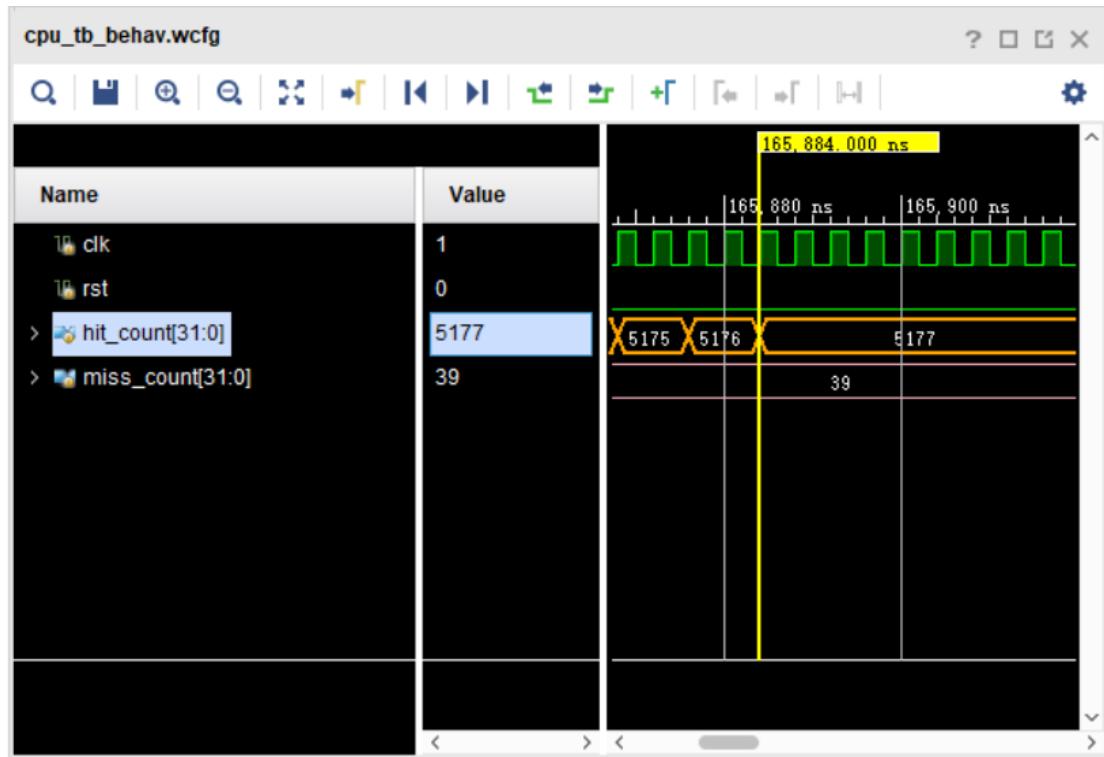




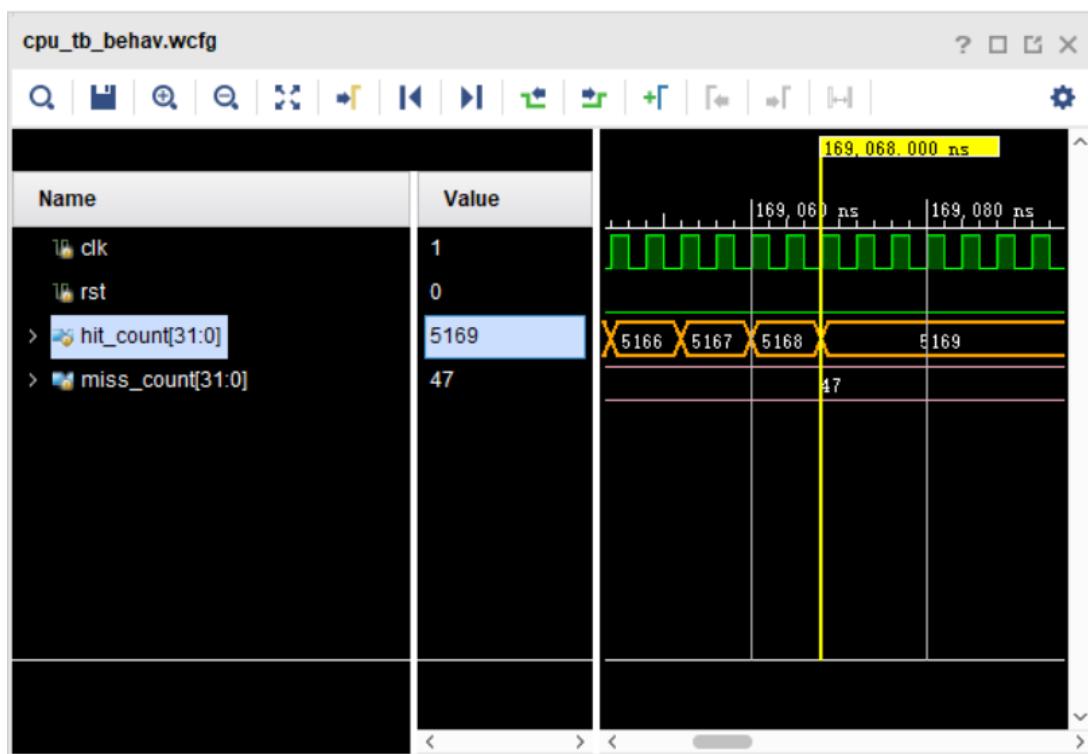
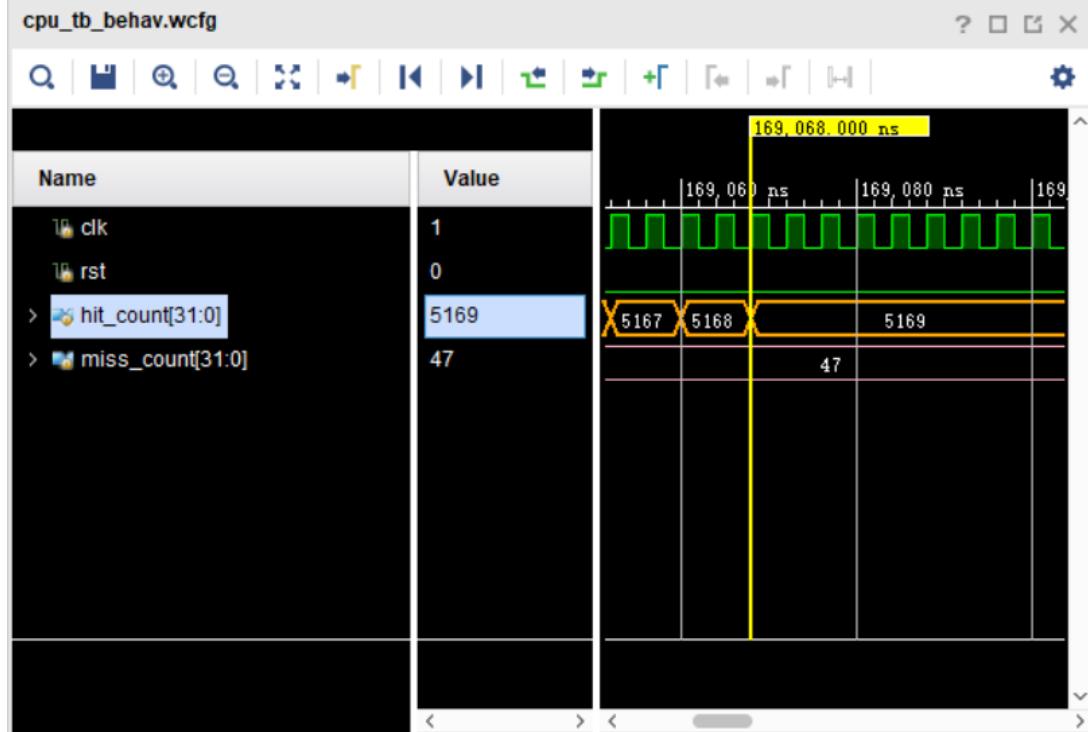
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SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT

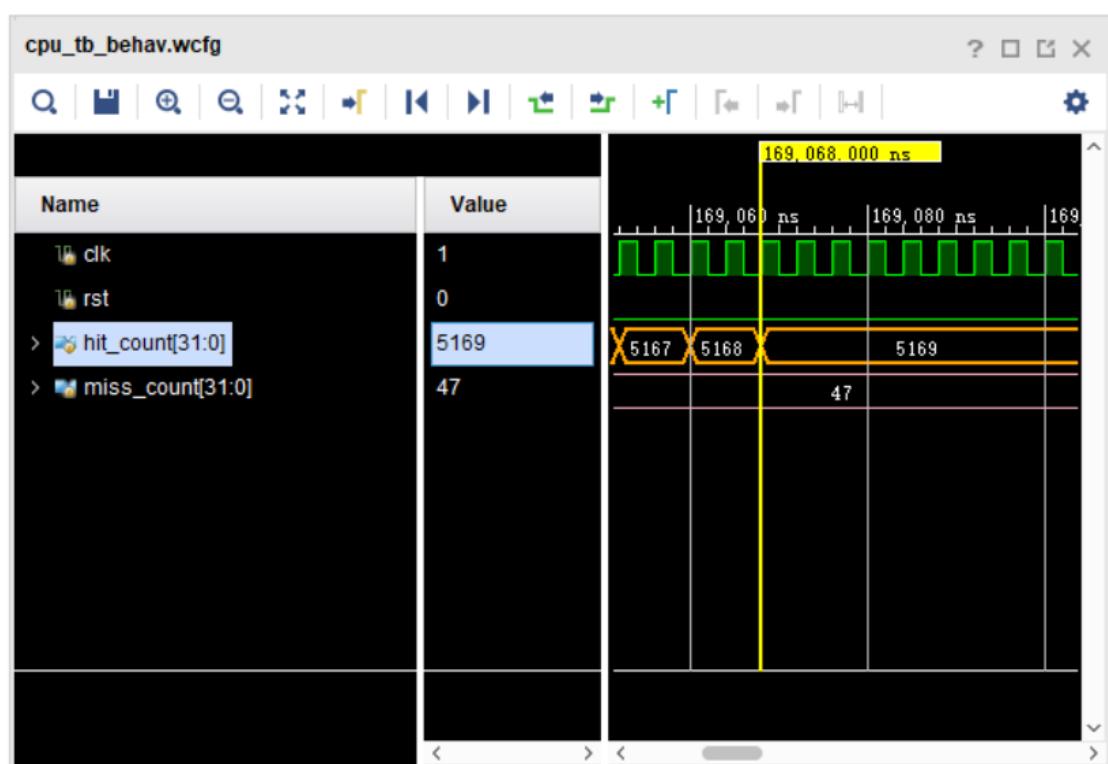
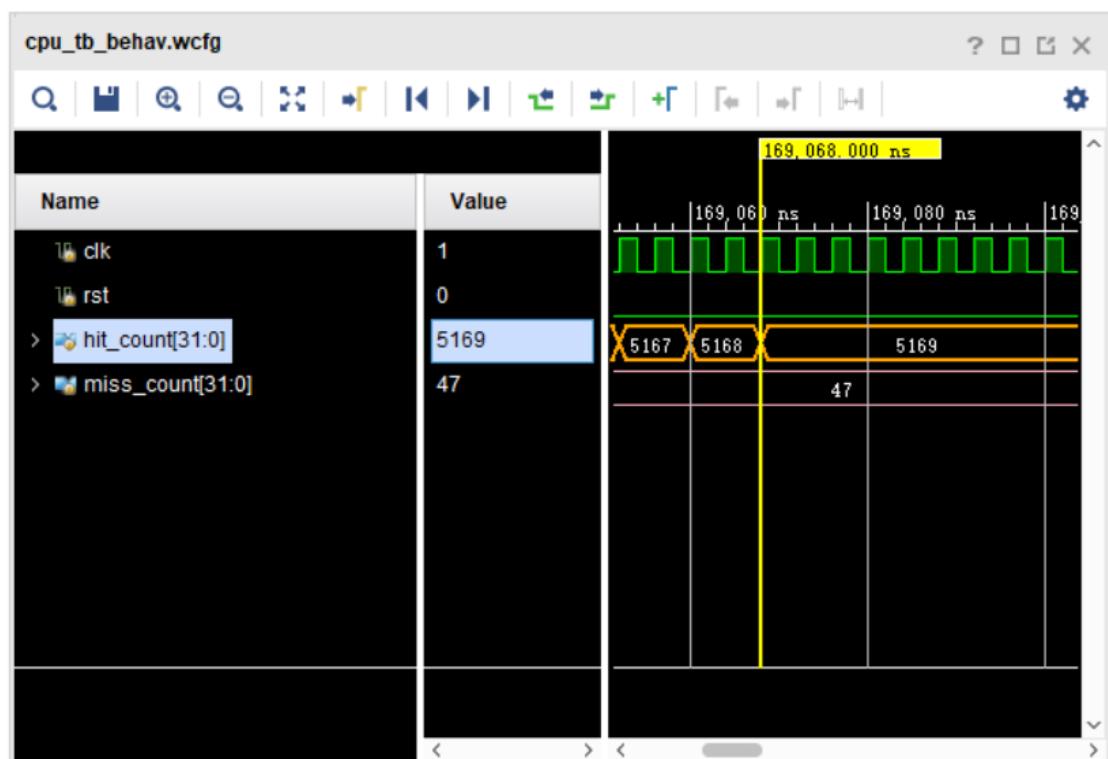


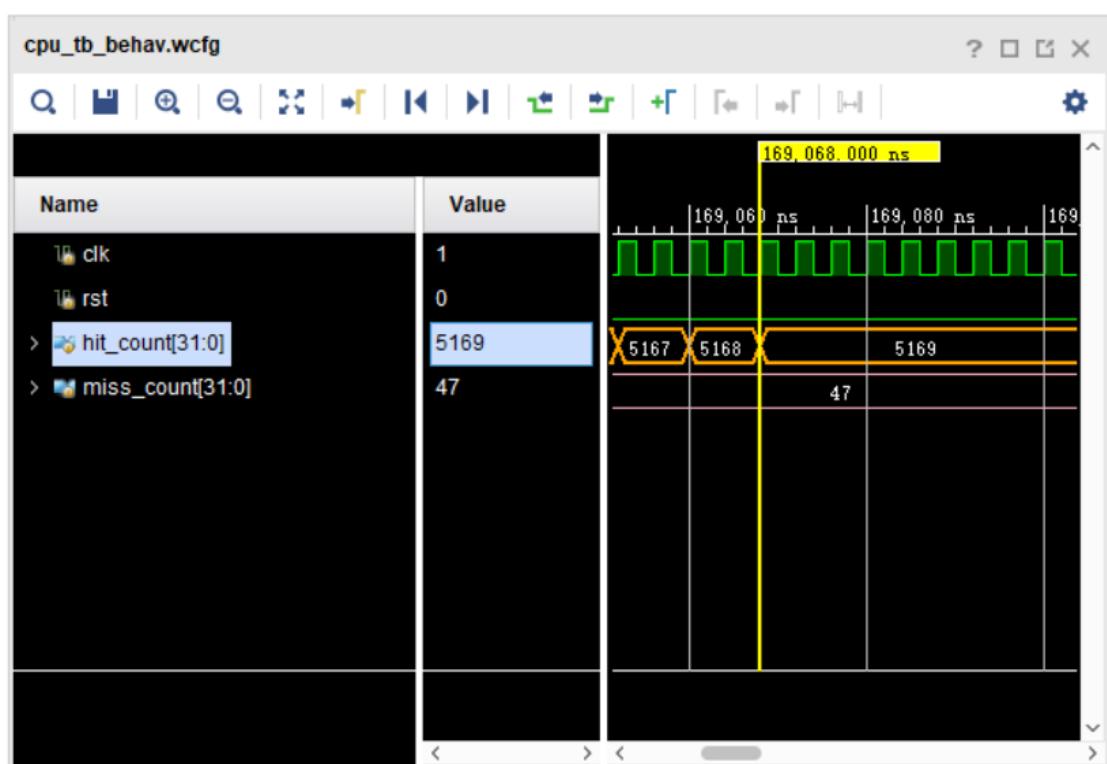
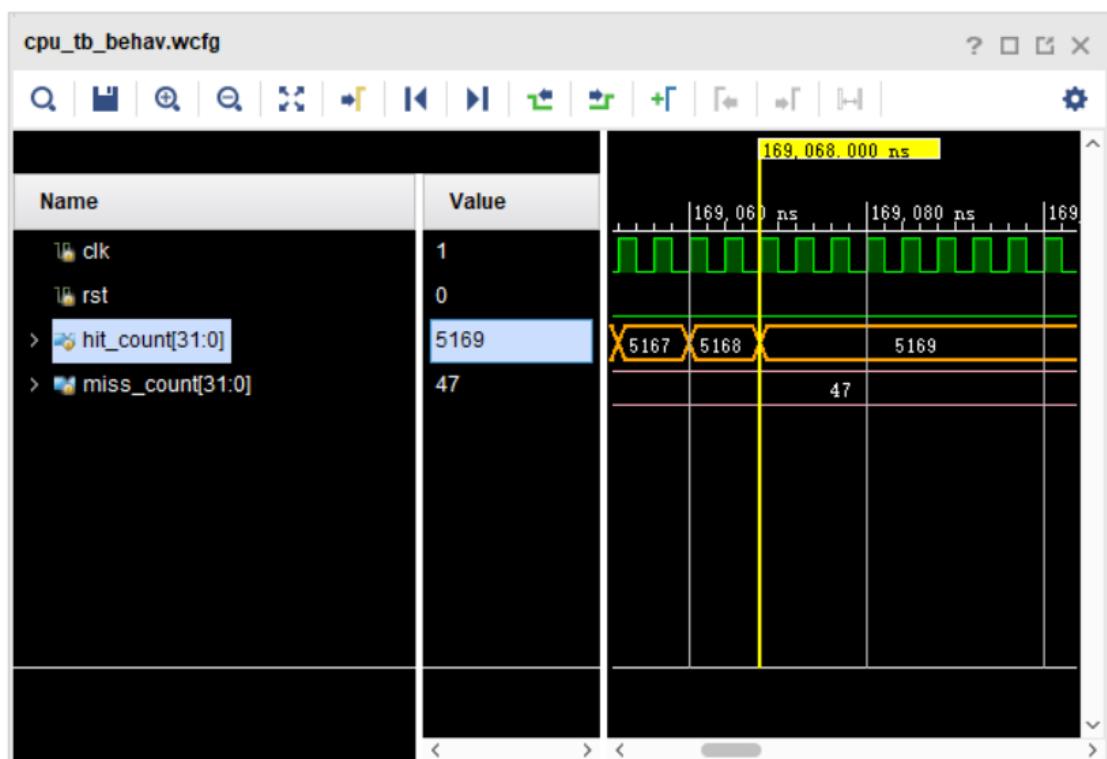




LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	QUICKSORT



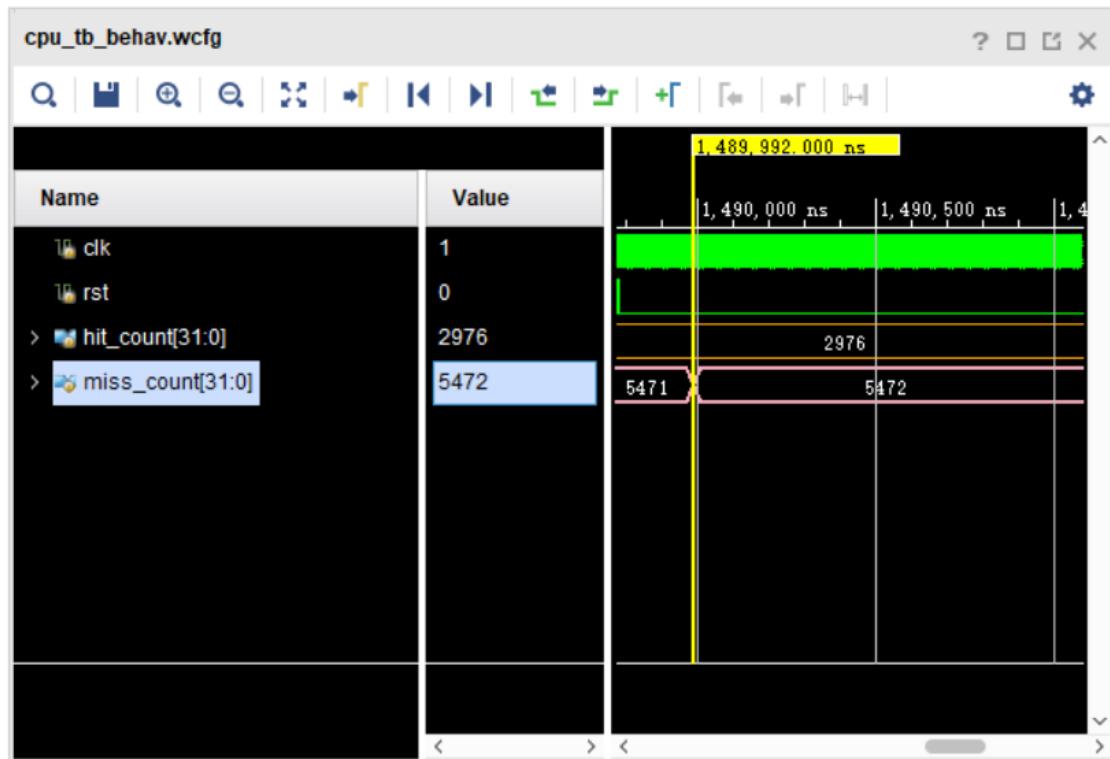




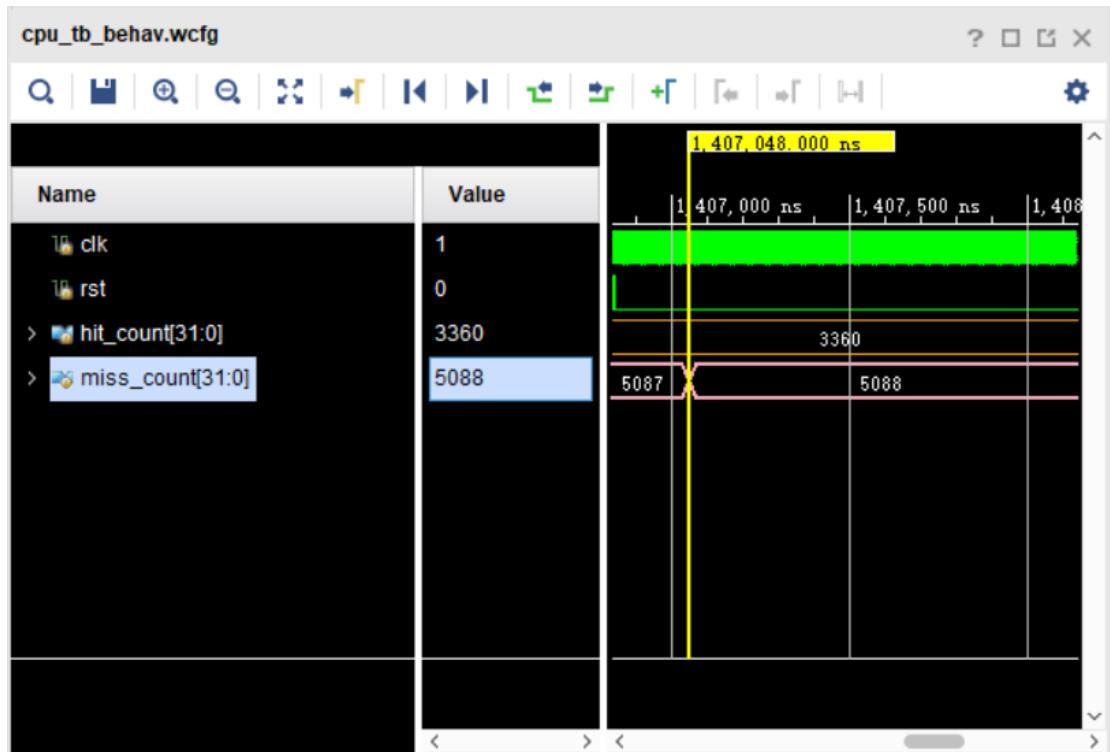
仿真数据

(16X16MATMUL、直接映射)

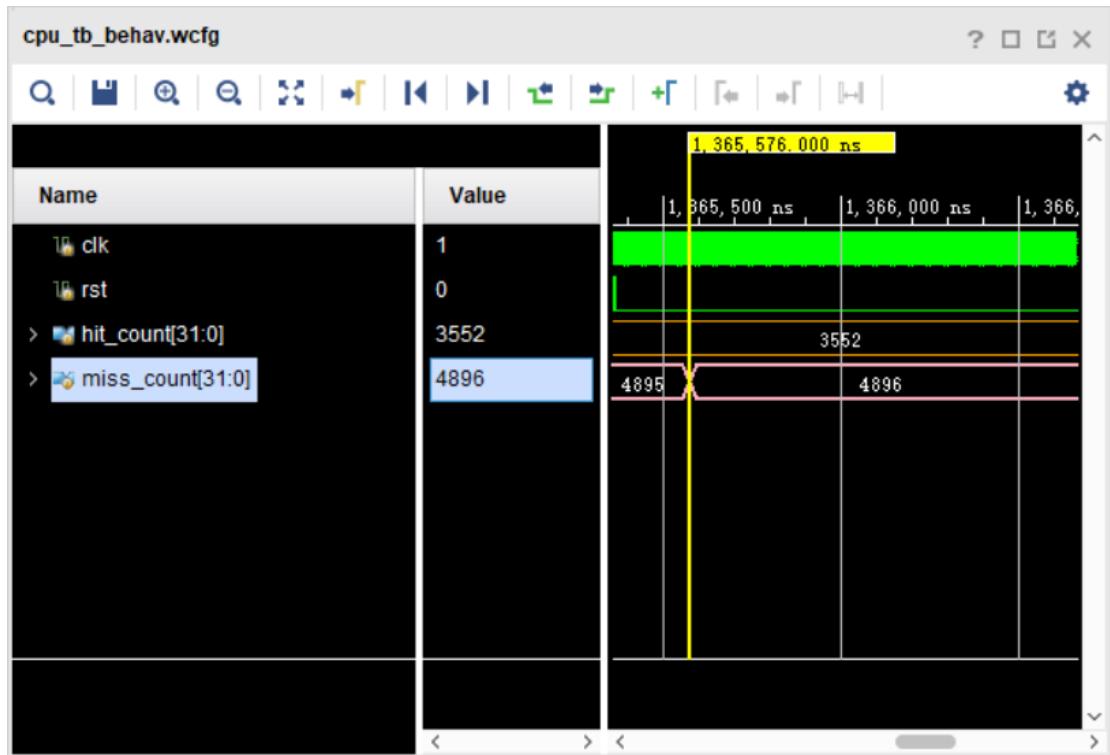
LINE_ADDR_LEN	2
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Test File	MATMUL



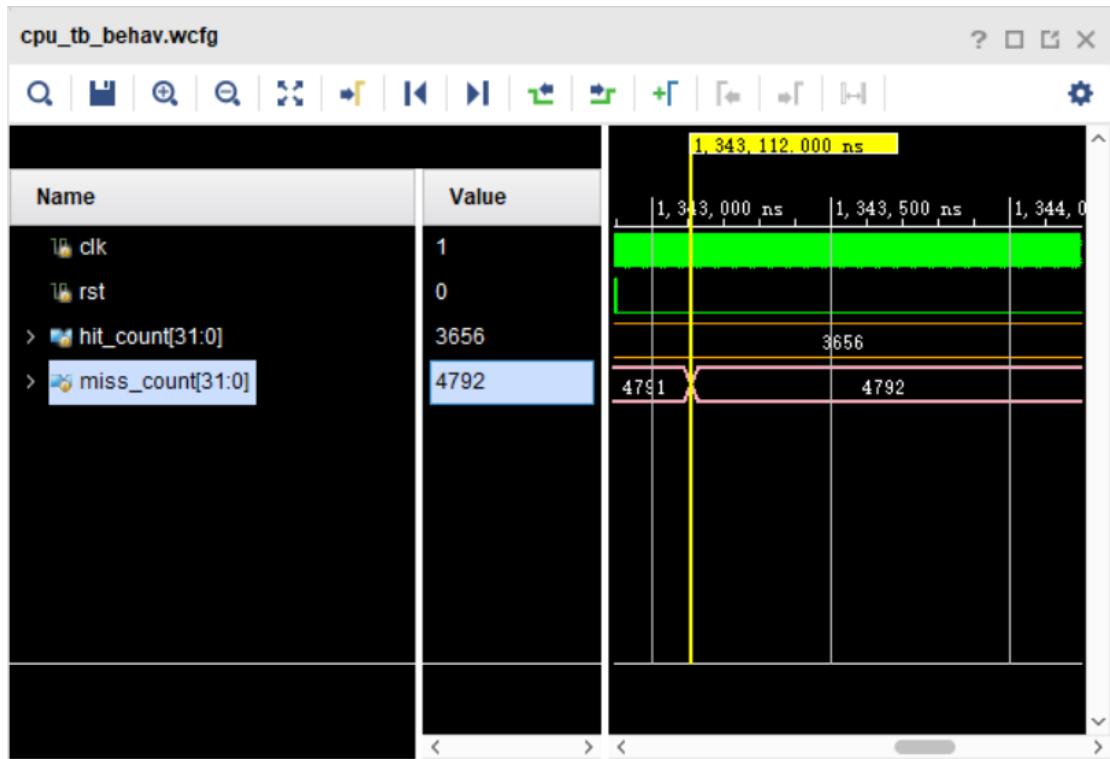
LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8
Test File	MATMUL



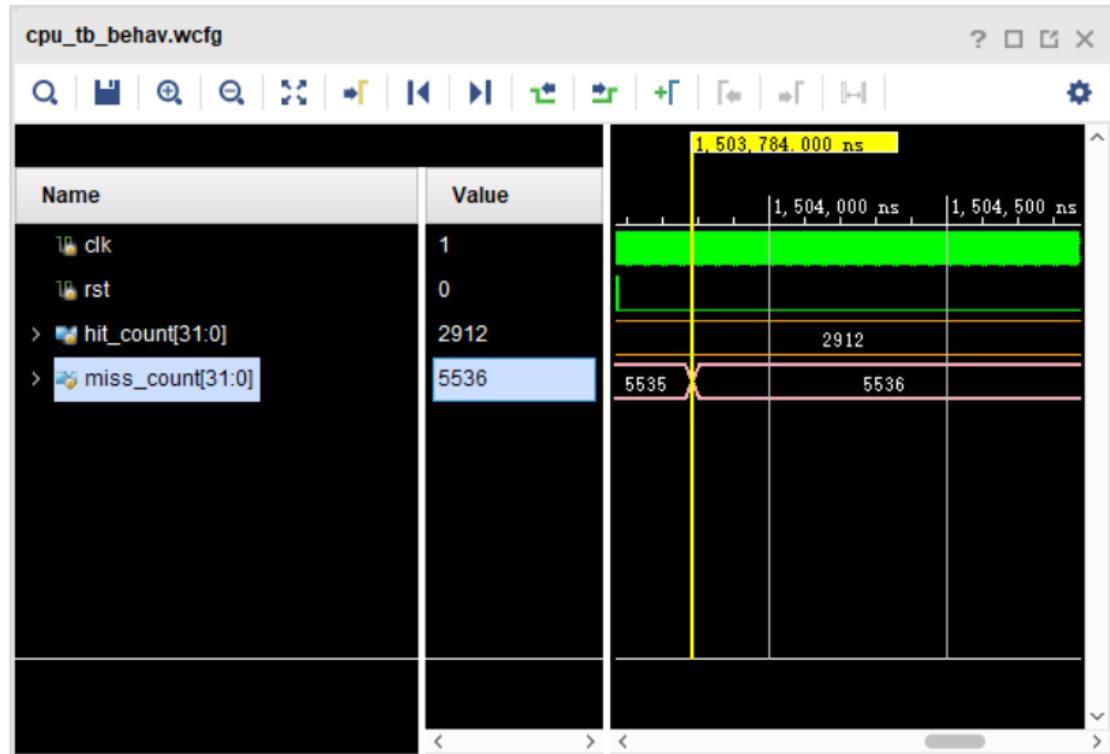
LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7
Test File	MATMUL



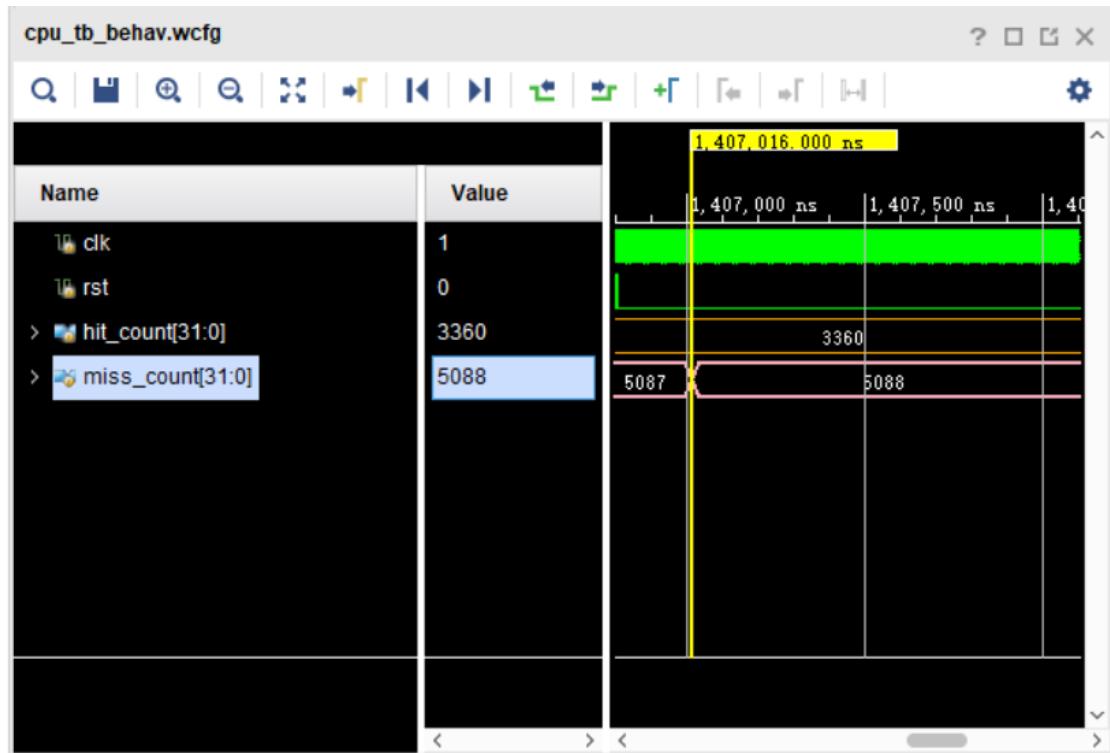
LINE_ADDR_LEN	2
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TAG_ADDR_LEN	6
Test File	MATMUL



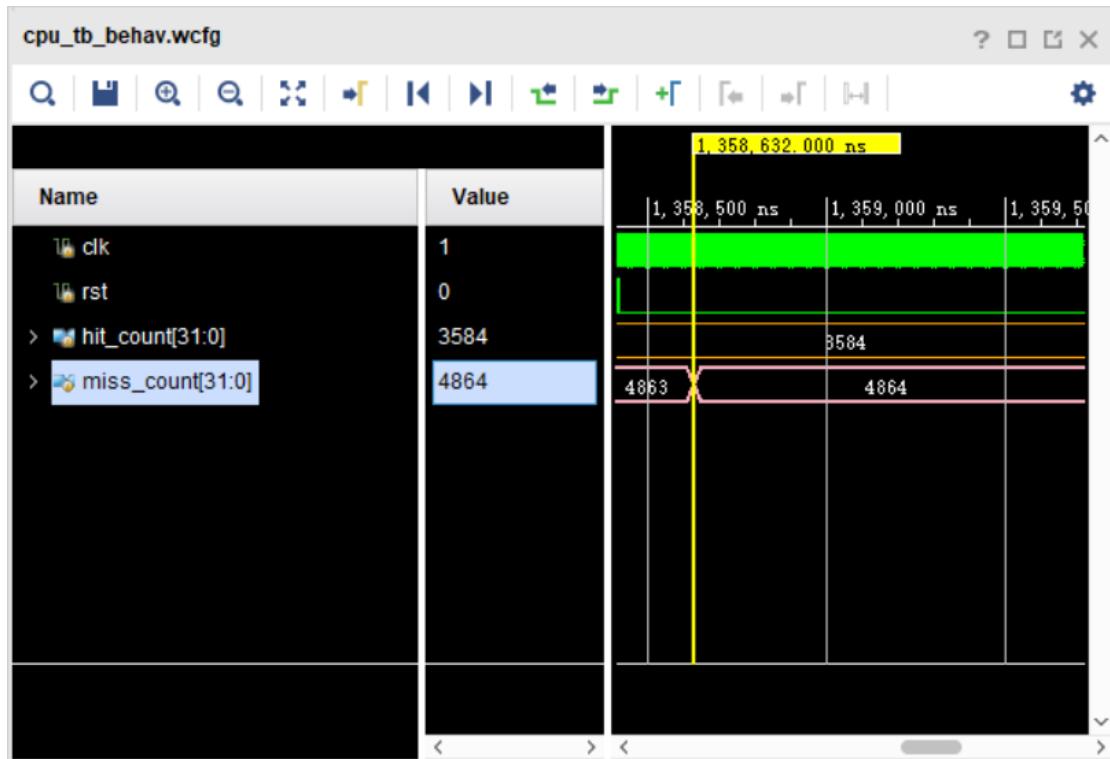
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TAG_ADDR_LEN	8
Test File	MATMUL



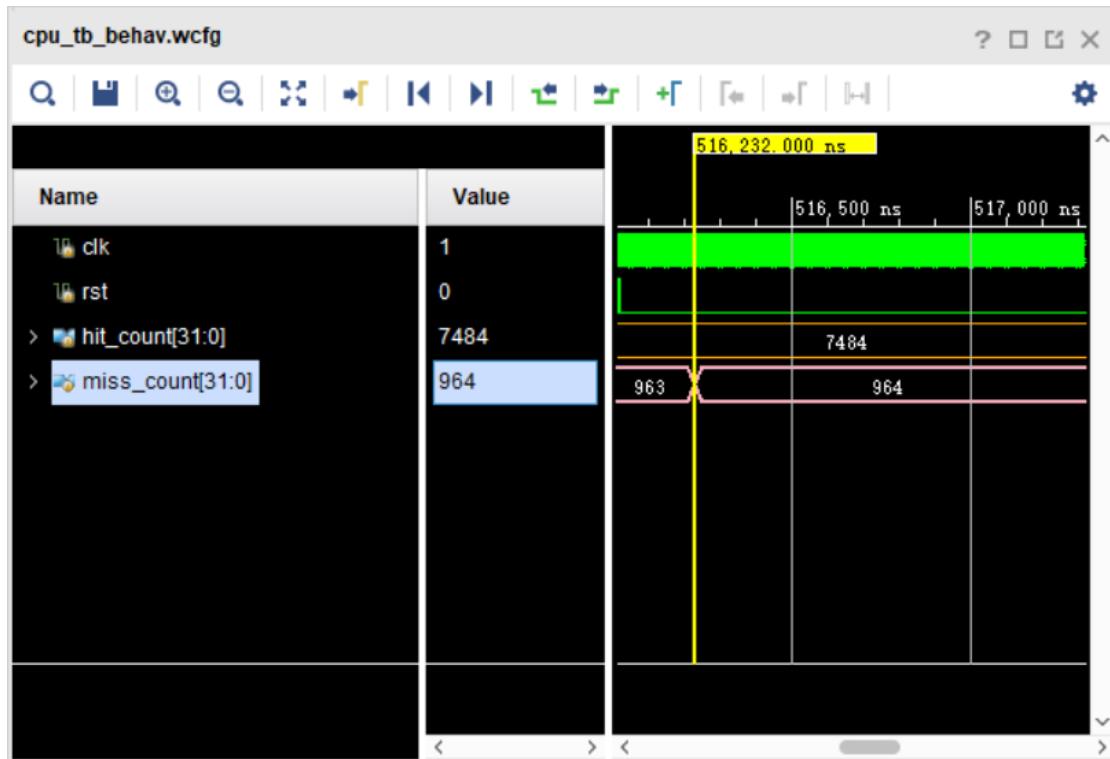
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Test File	MATMUL



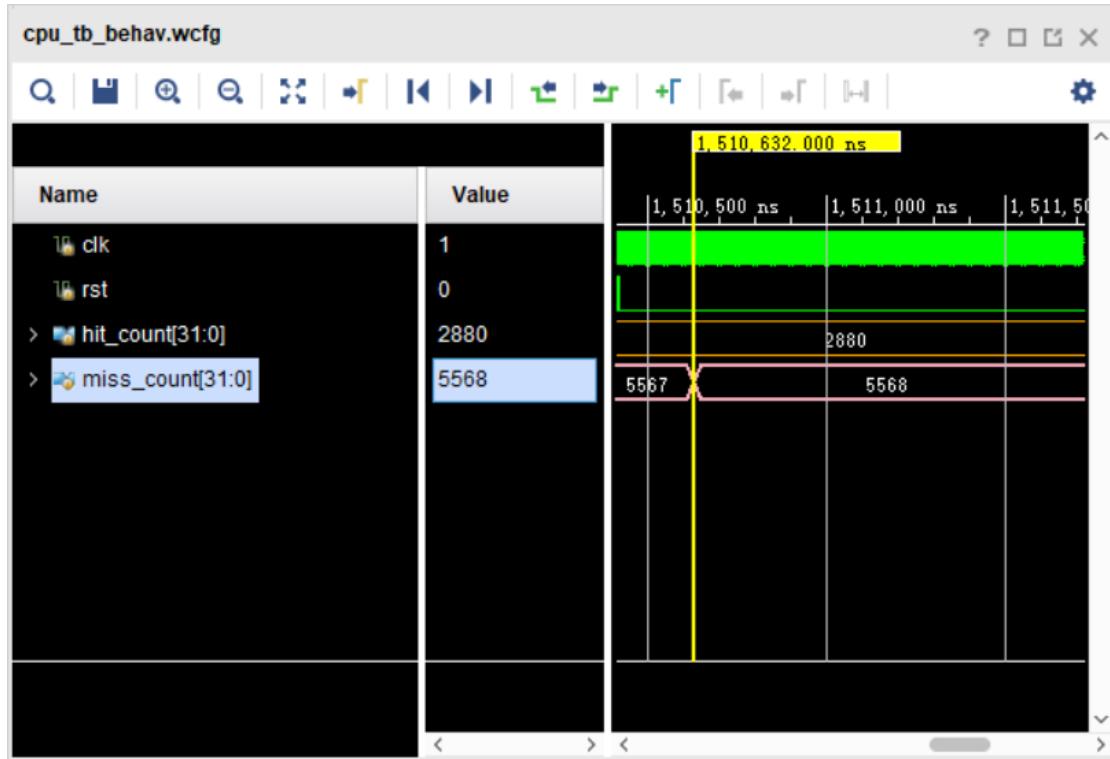
LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
Test File	MATMUL



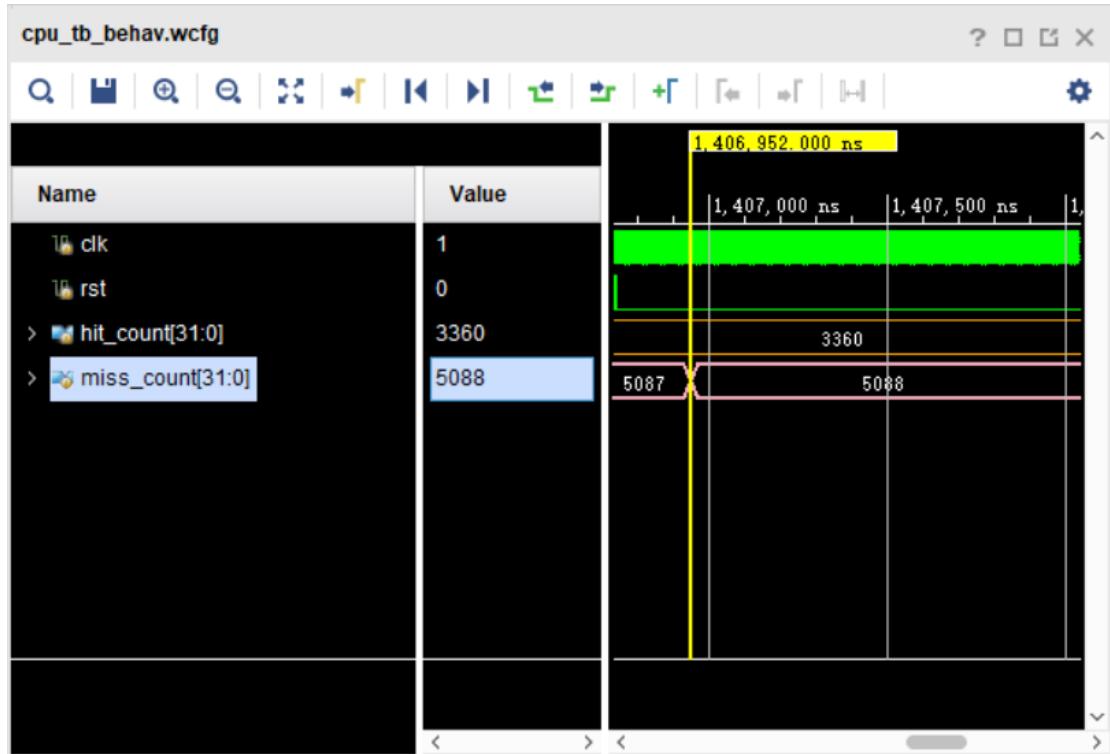
LINE_ADDR_LEN	3
SET_ADDR_LEN	5
TAG_ADDR_LEN	5
Test File	MATMUL



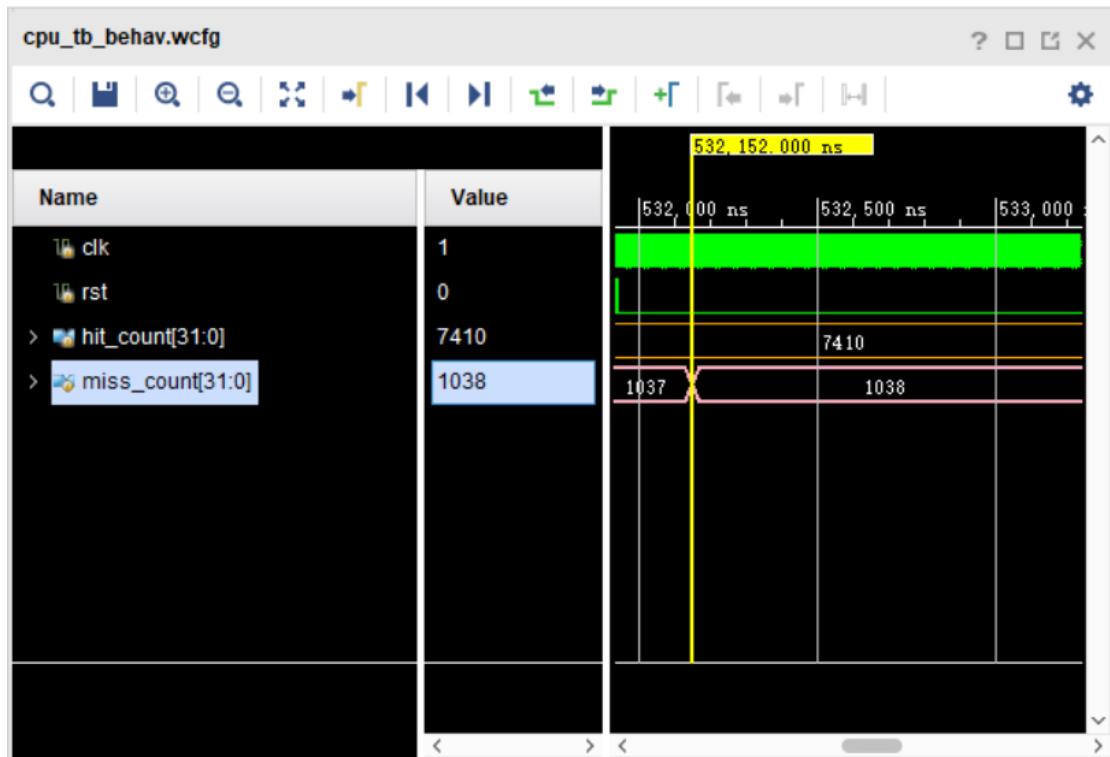
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SET_ADDR_LEN	2
TAG_ADDR_LEN	7
Test File	MATMUL



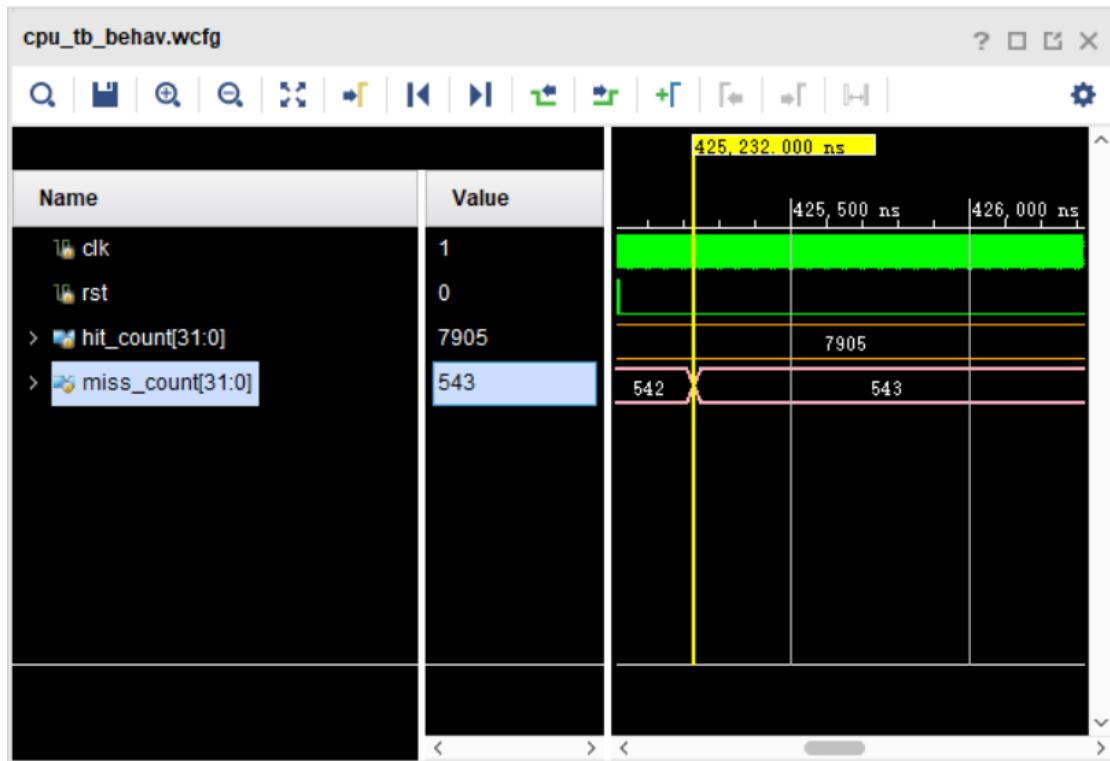
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TAG_ADDR_LEN	6
Test File	MATMUL



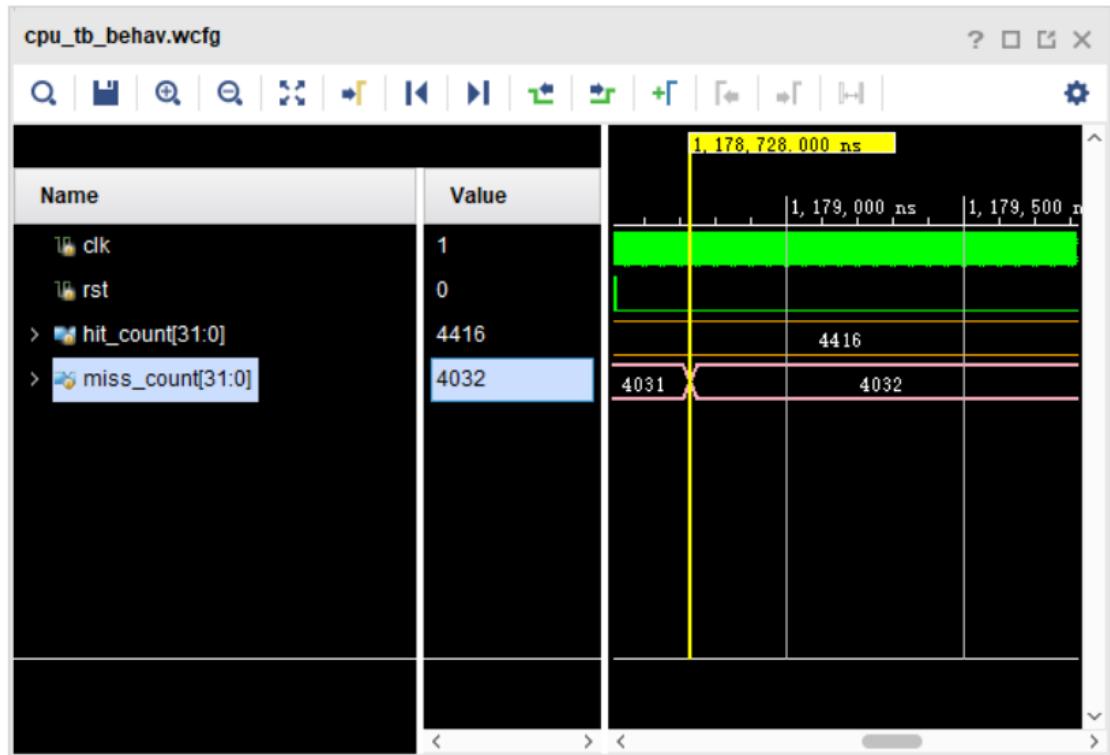
LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
Test File	MATMUL



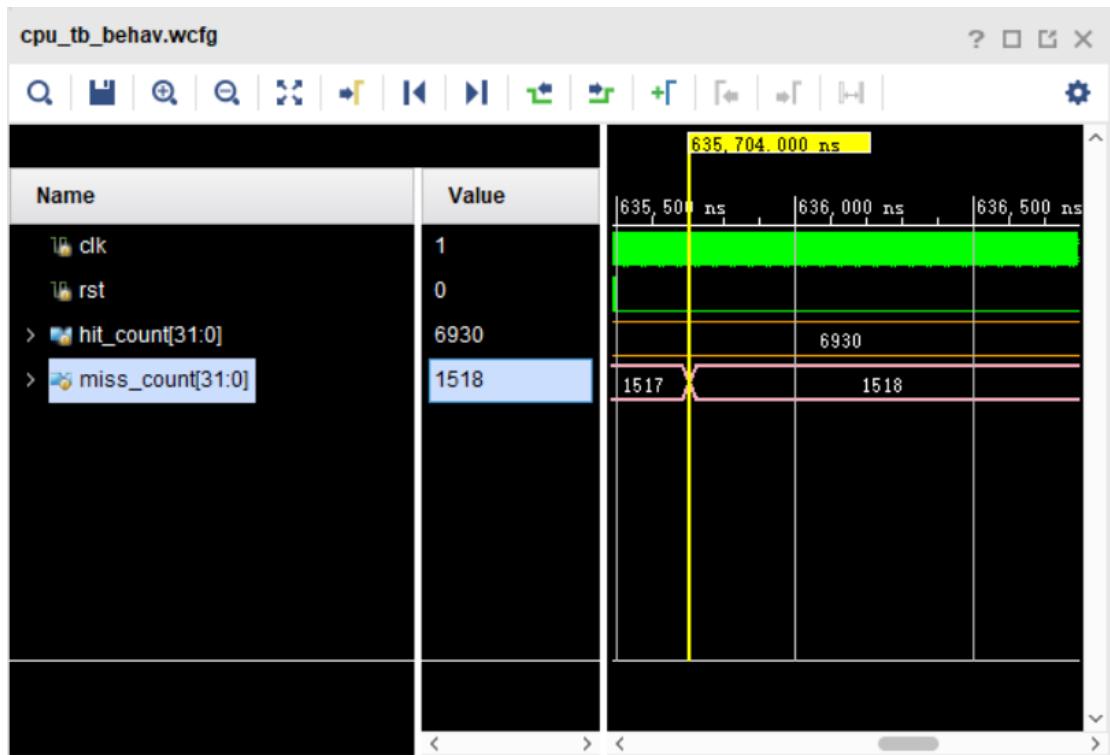
LINE_ADDR_LEN	4
SET_ADDR_LEN	5
TAG_ADDR_LEN	4
Test File	MATMUL



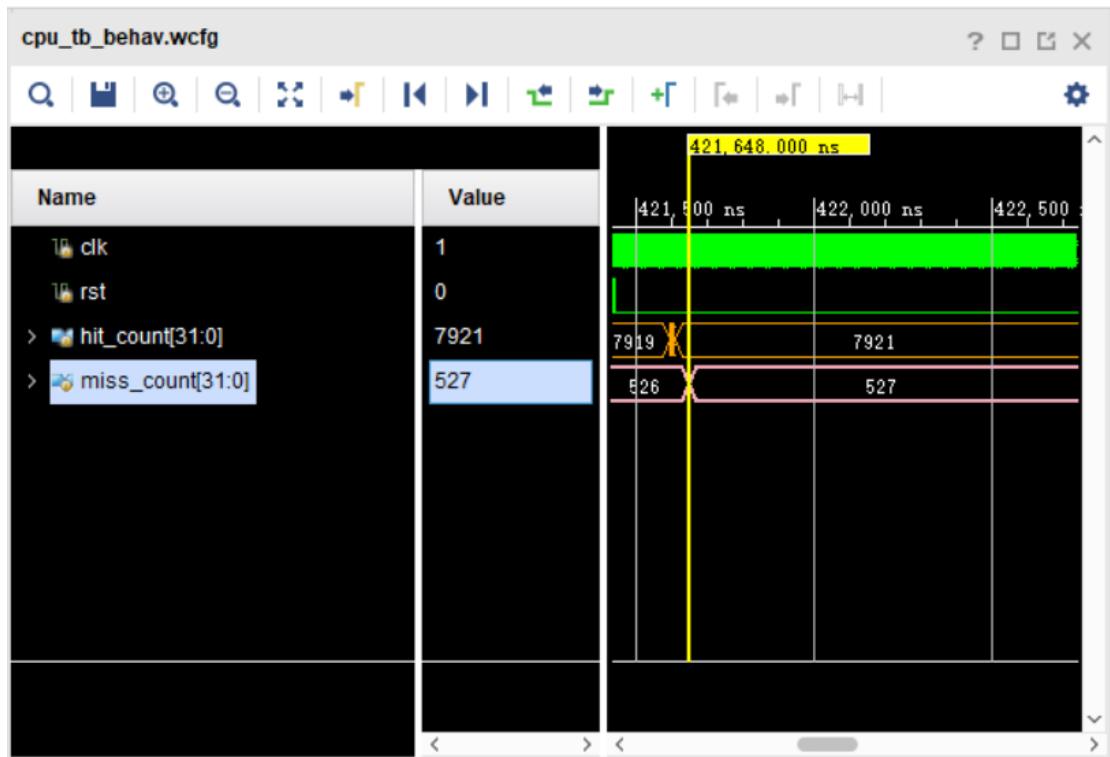
LINE_ADDR_LEN	5
SET_ADDR_LEN	2
TAG_ADDR_LEN	6
Test File	MATMUL



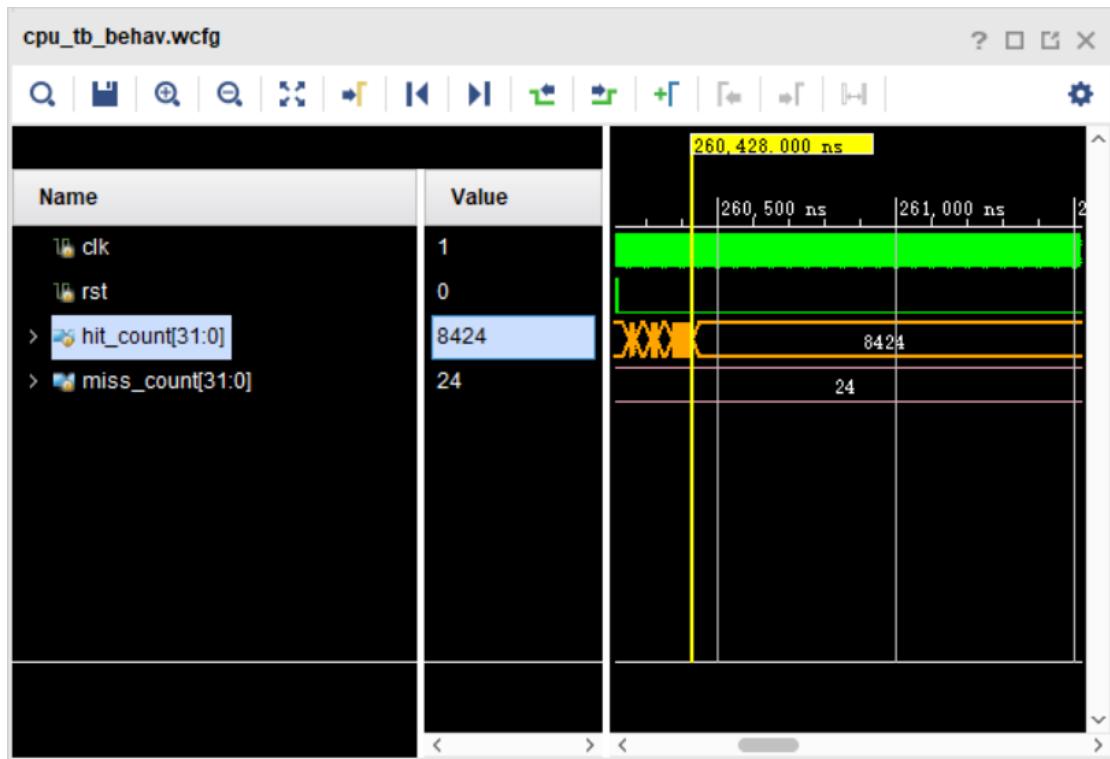
LINE_ADDR_LEN	5
SET_ADDR_LEN	3
TAG_ADDR_LEN	5
Test File	MATMUL



LINE_ADDR_LEN	5
SET_ADDR_LEN	4
TAG_ADDR_LEN	4
Test File	MATMUL

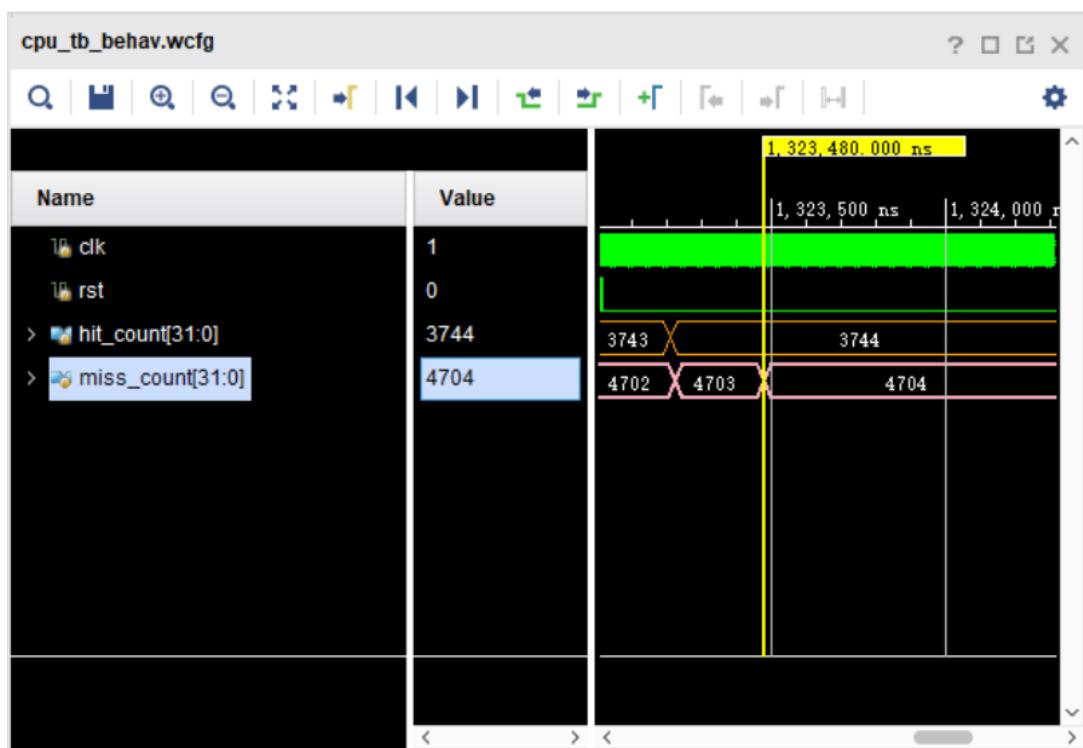
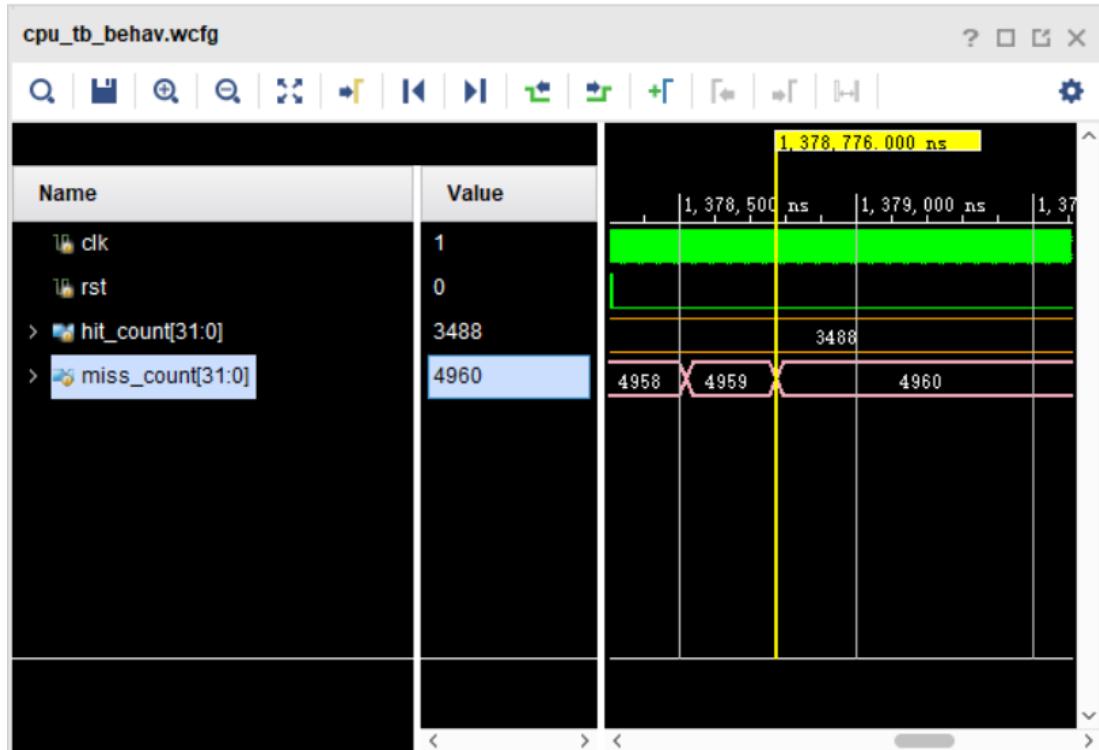


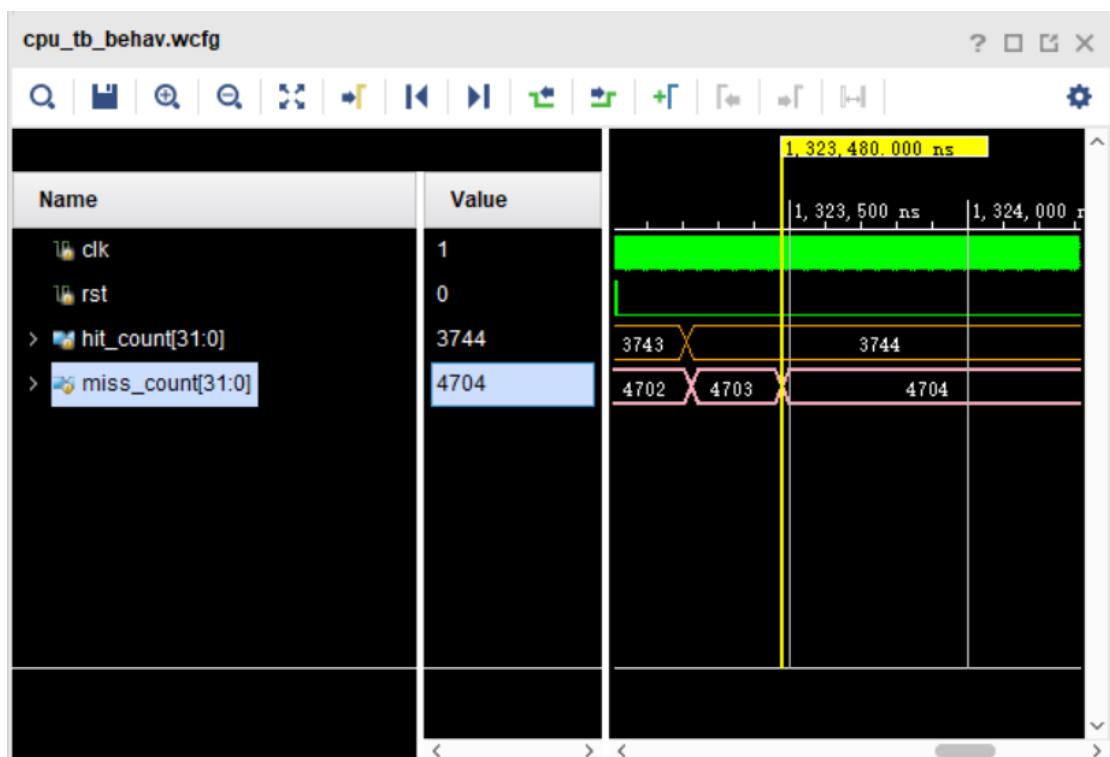
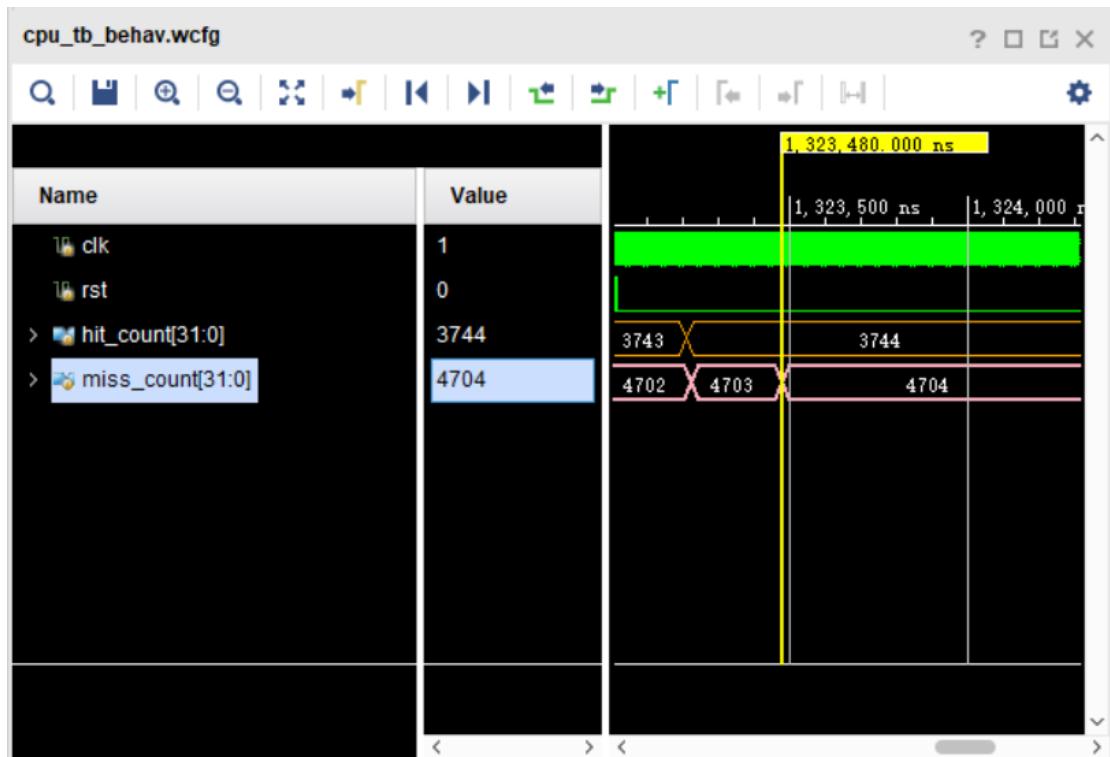
LINE_ADDR_LEN	5
SET_ADDR_LEN	5
TAG_ADDR_LEN	3
Test File	MATMUL

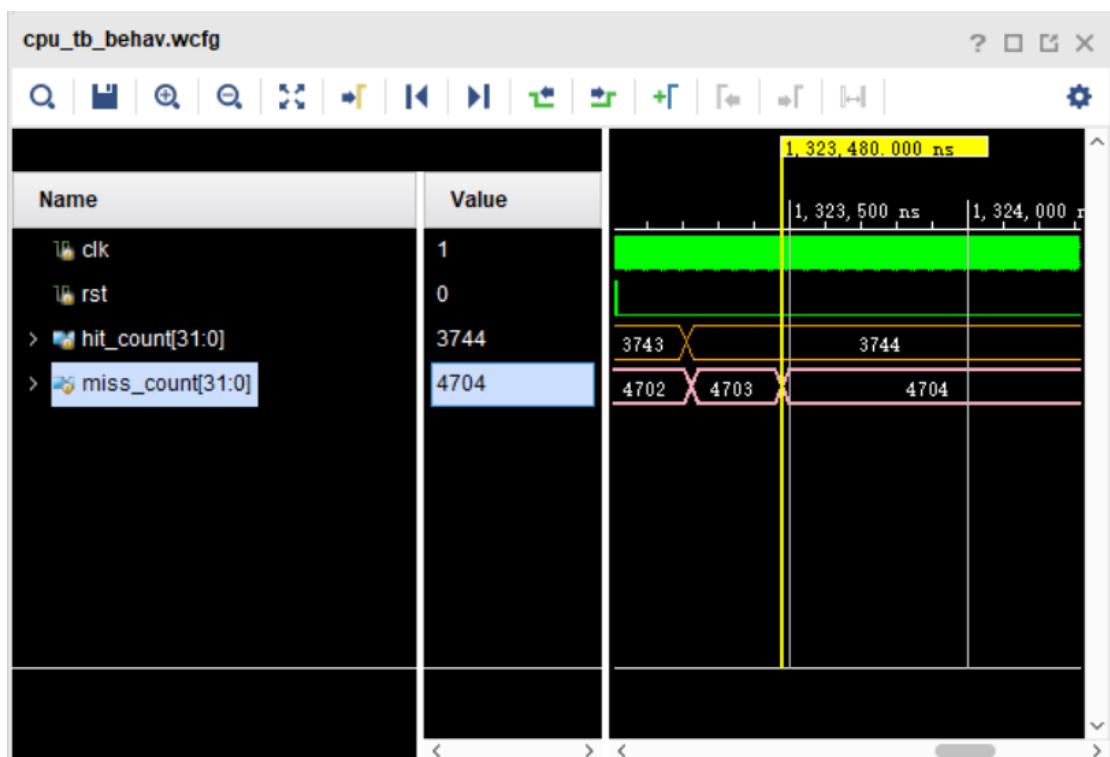
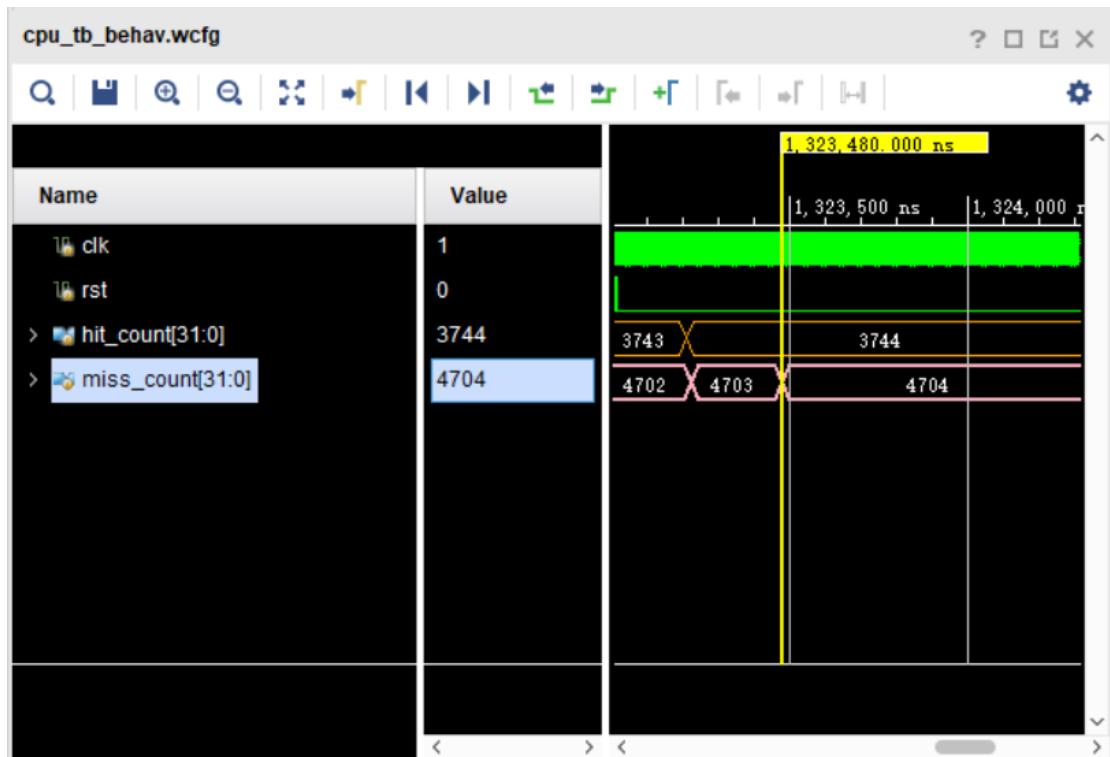


仿真数据
(16X16MATMUL、FIFO)

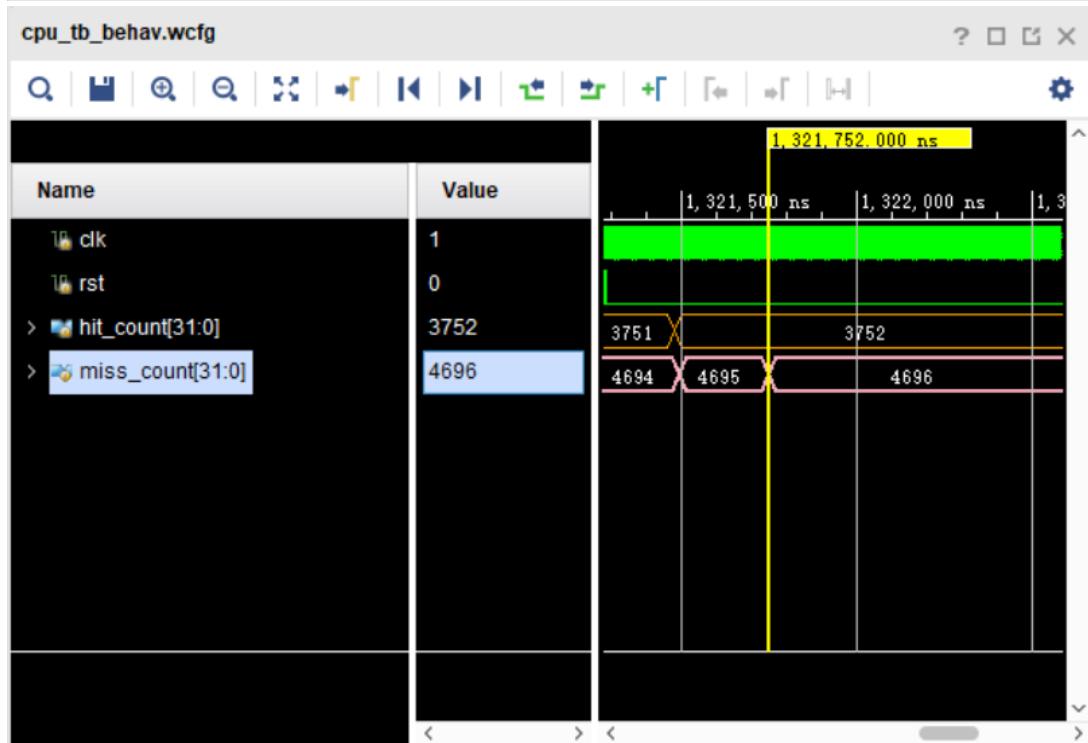
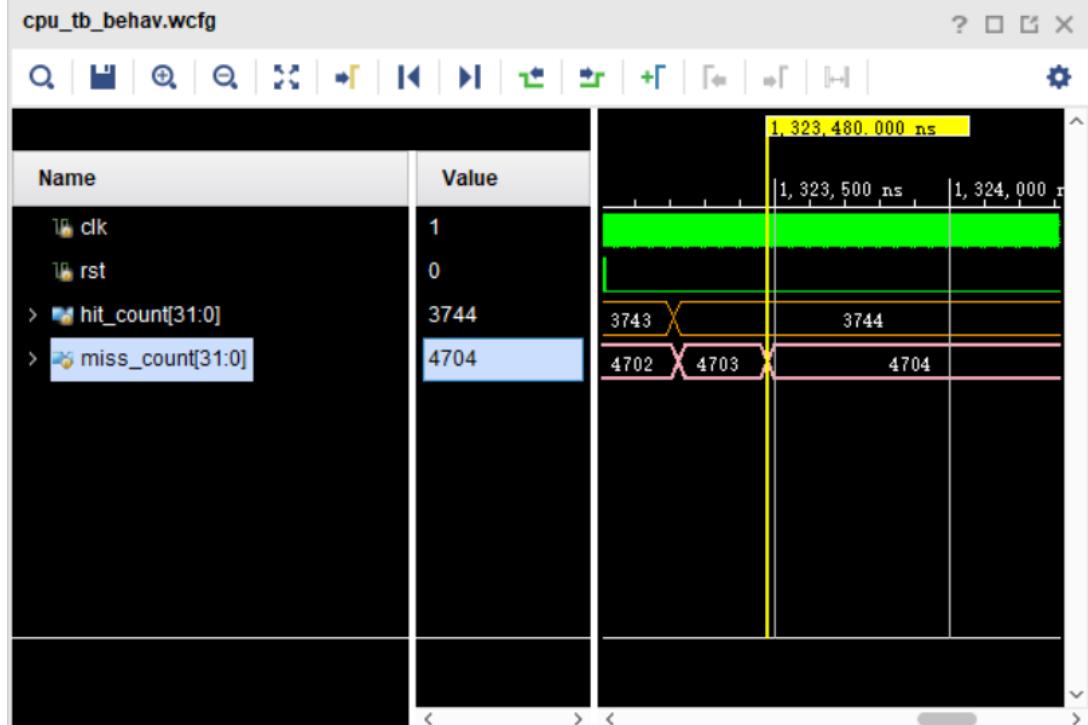
LINE_ADDR_LEN	2
SET_ADDR_LEN	2
TAG_ADDR_LEN	9
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

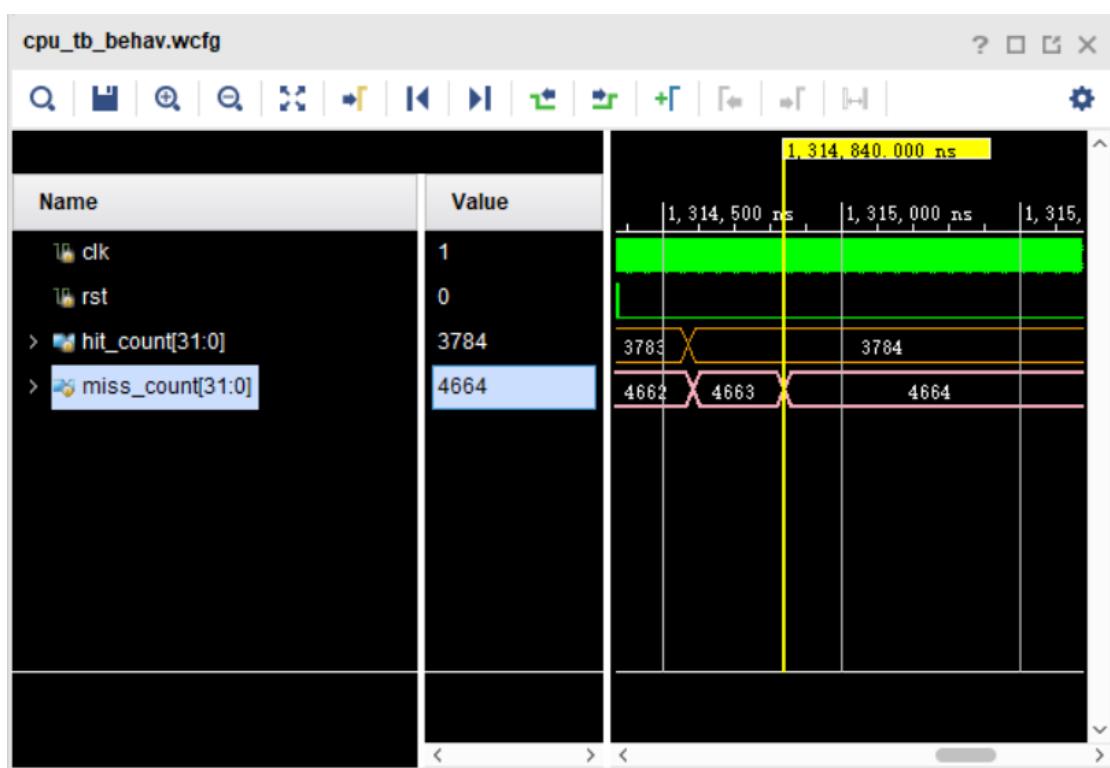
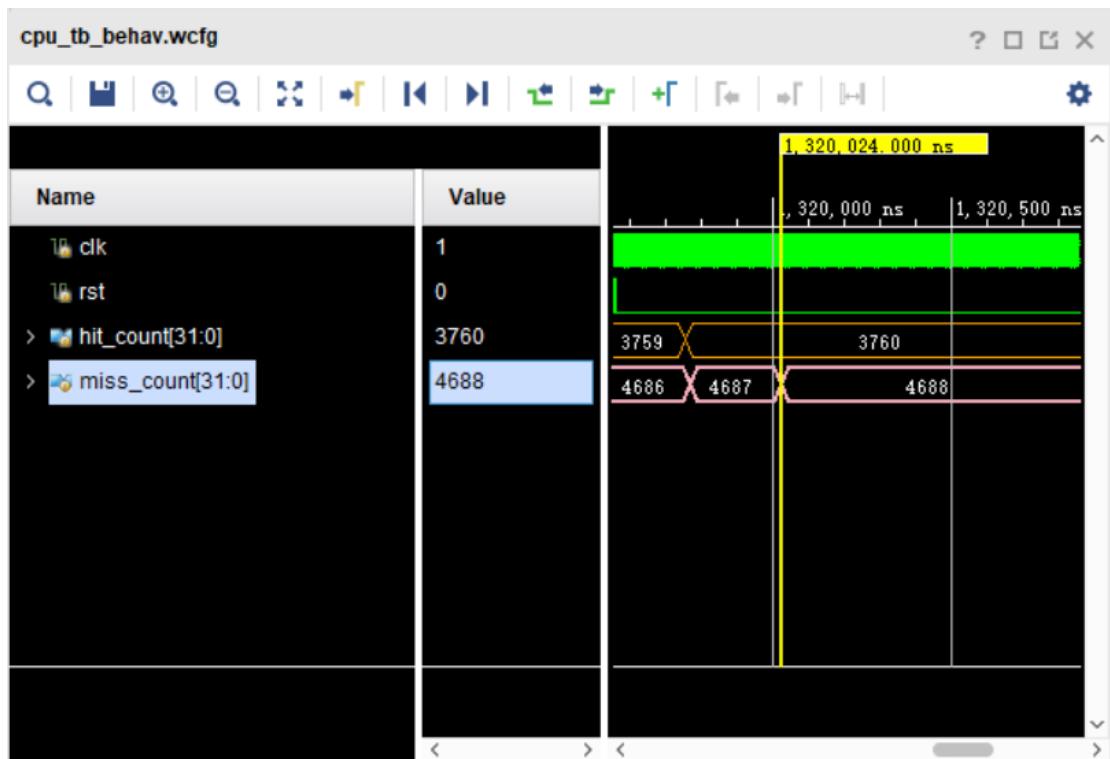


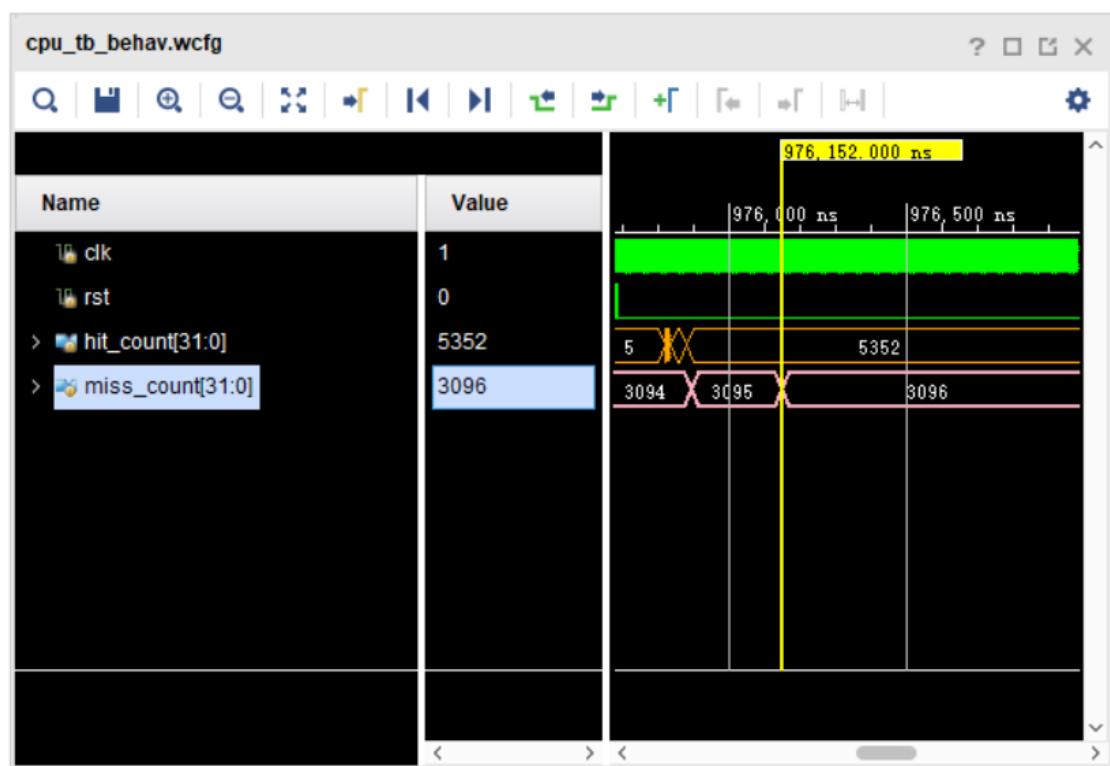
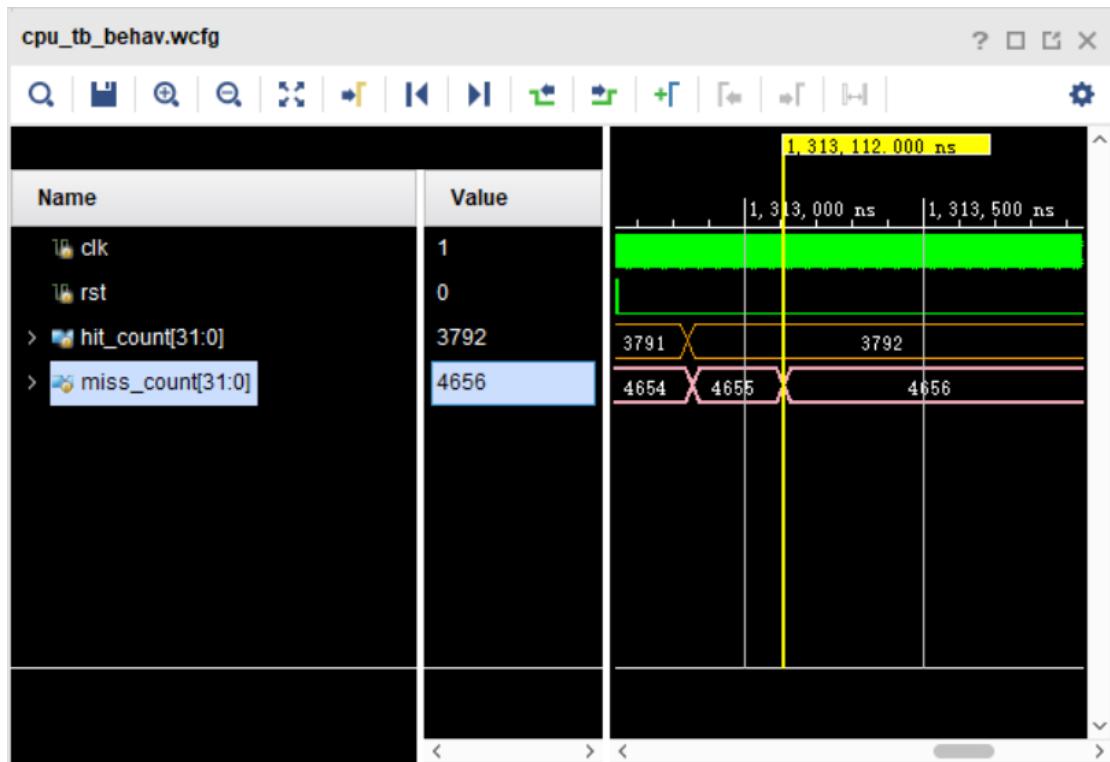




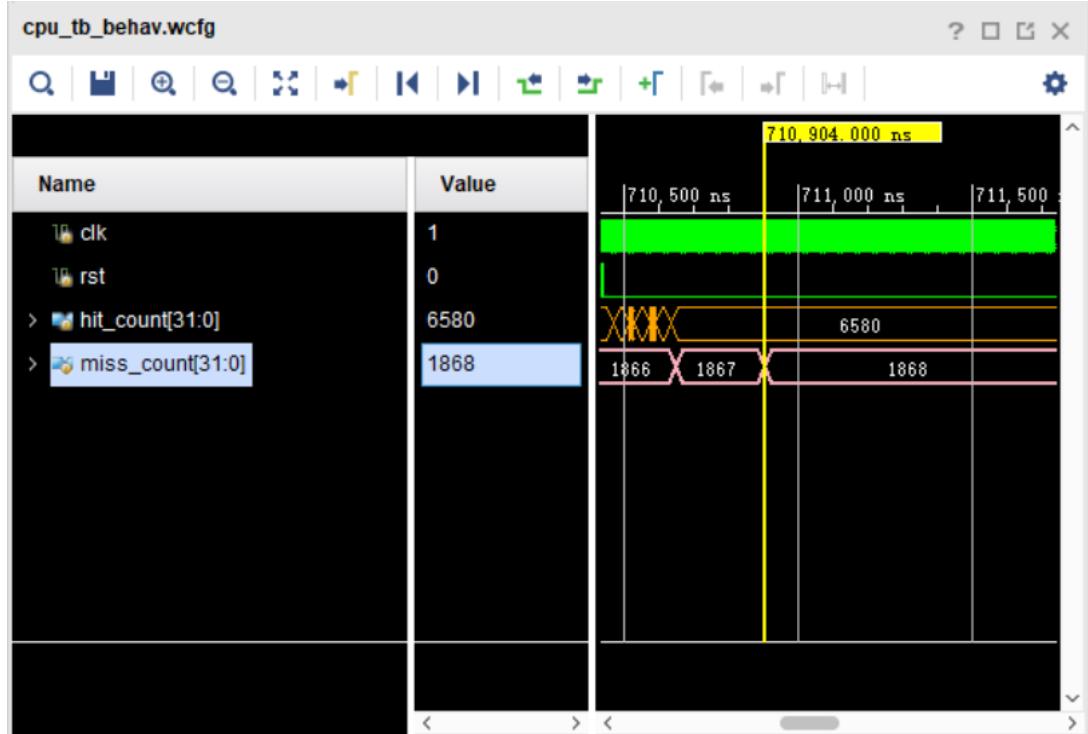
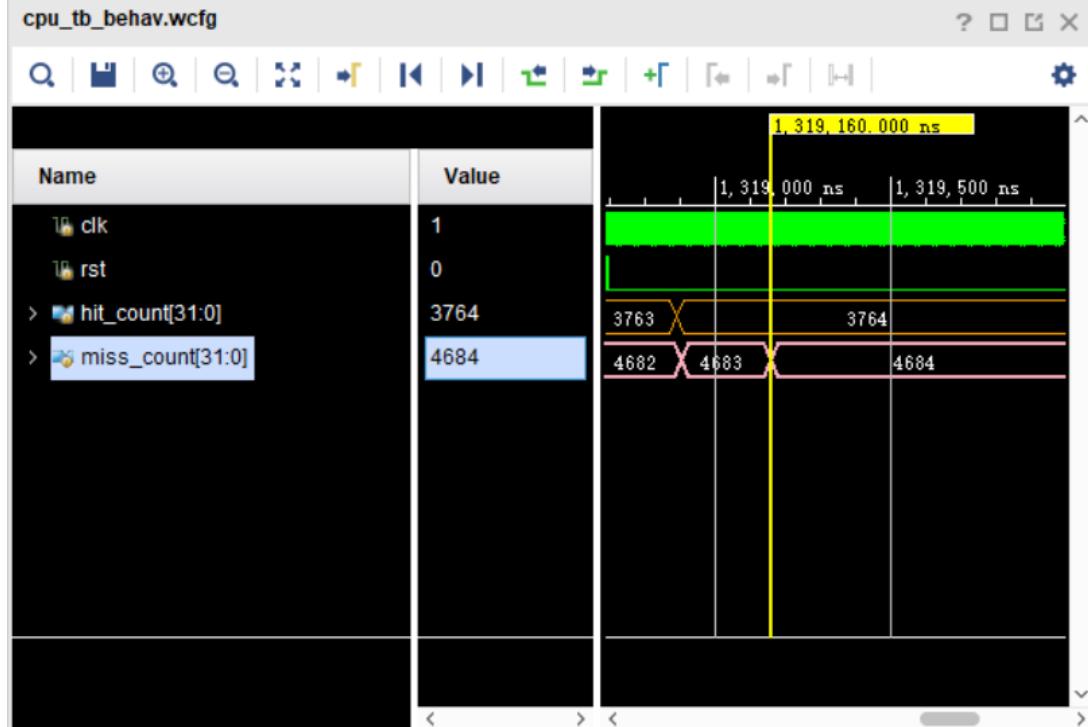
LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

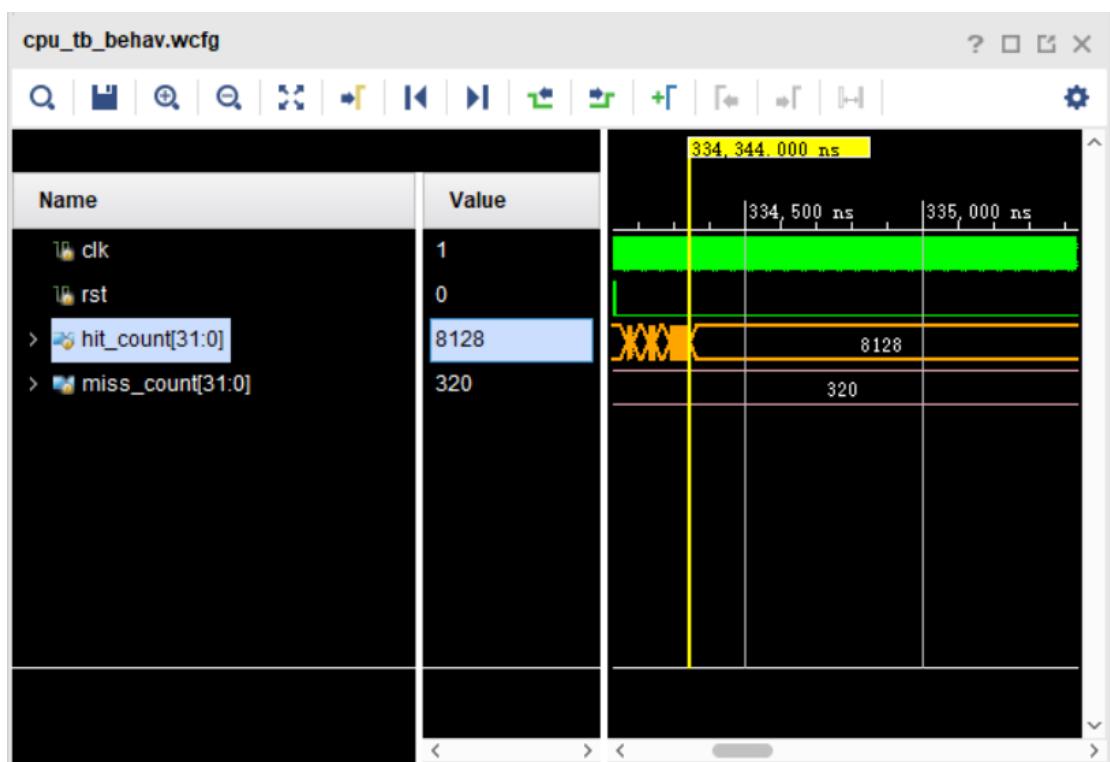
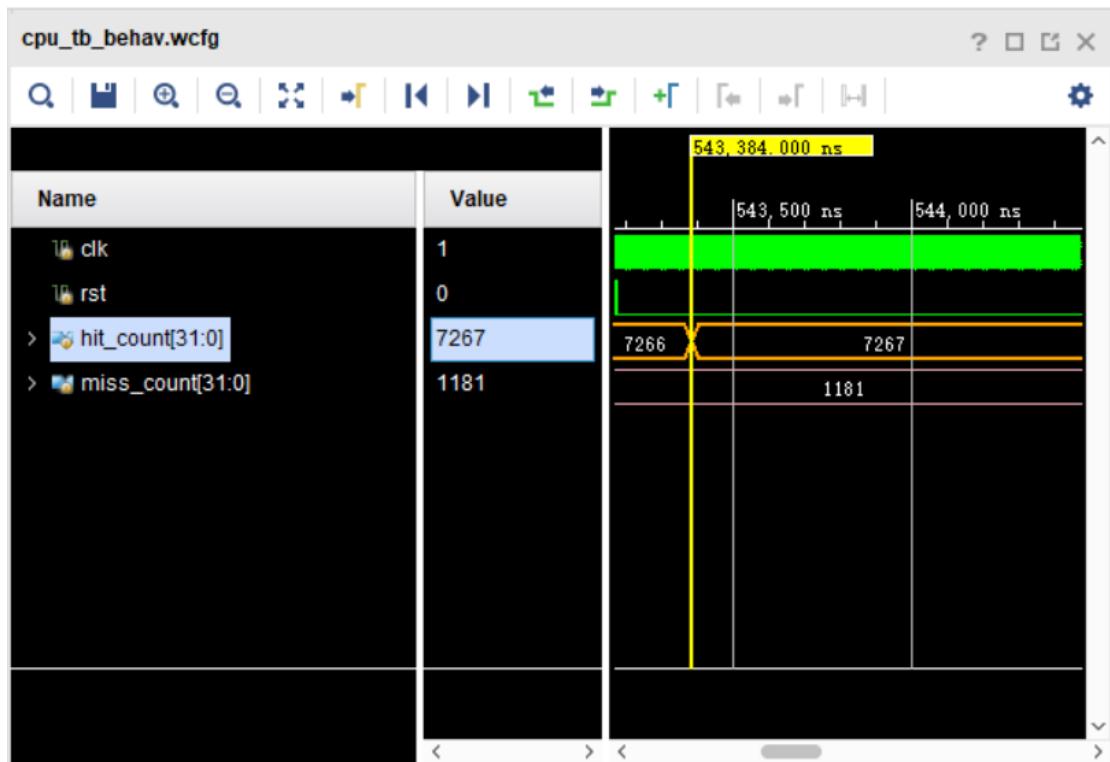


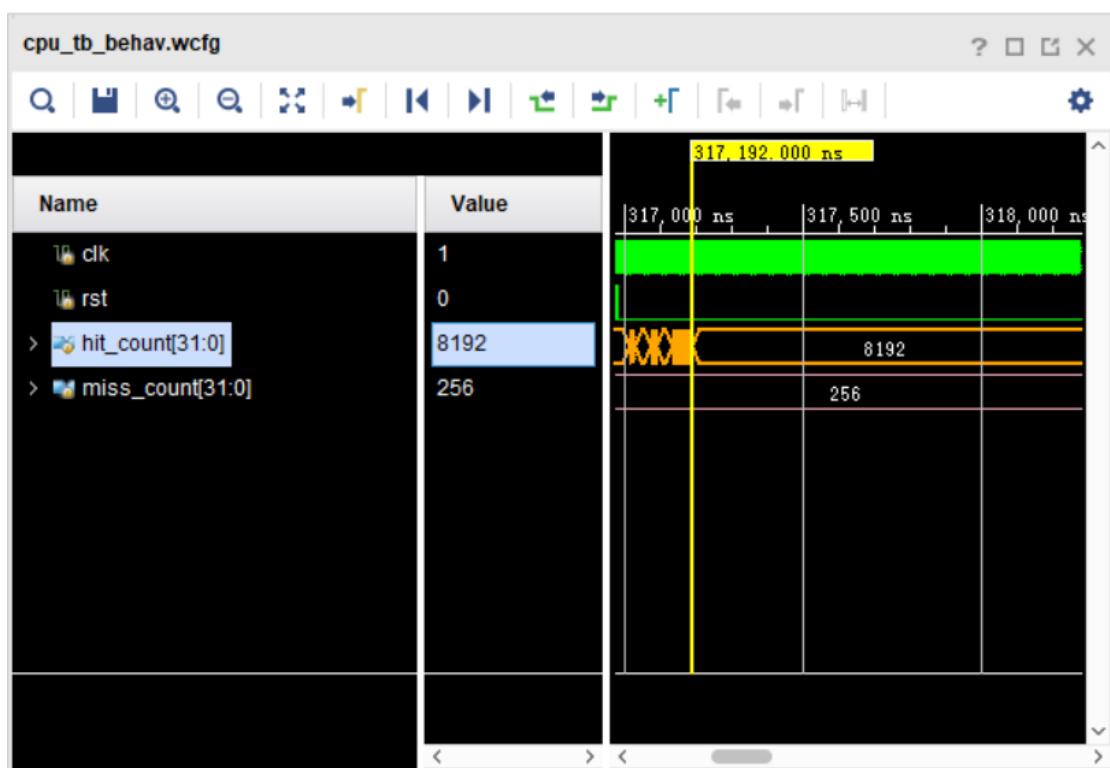
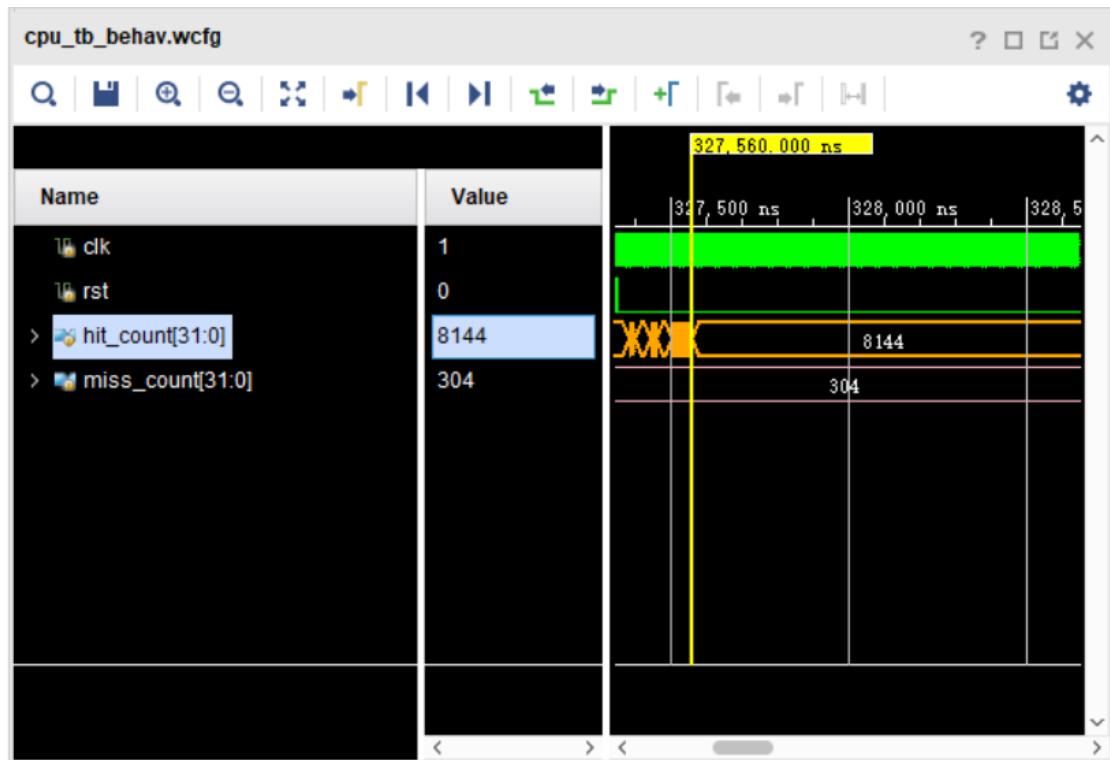




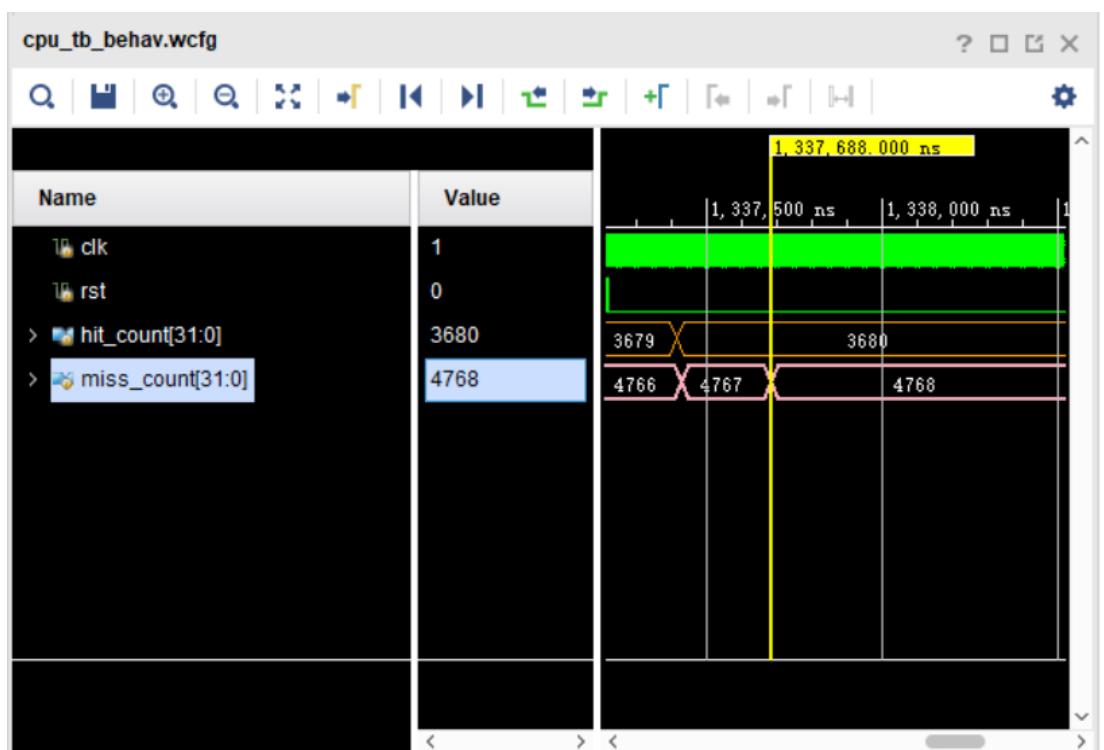
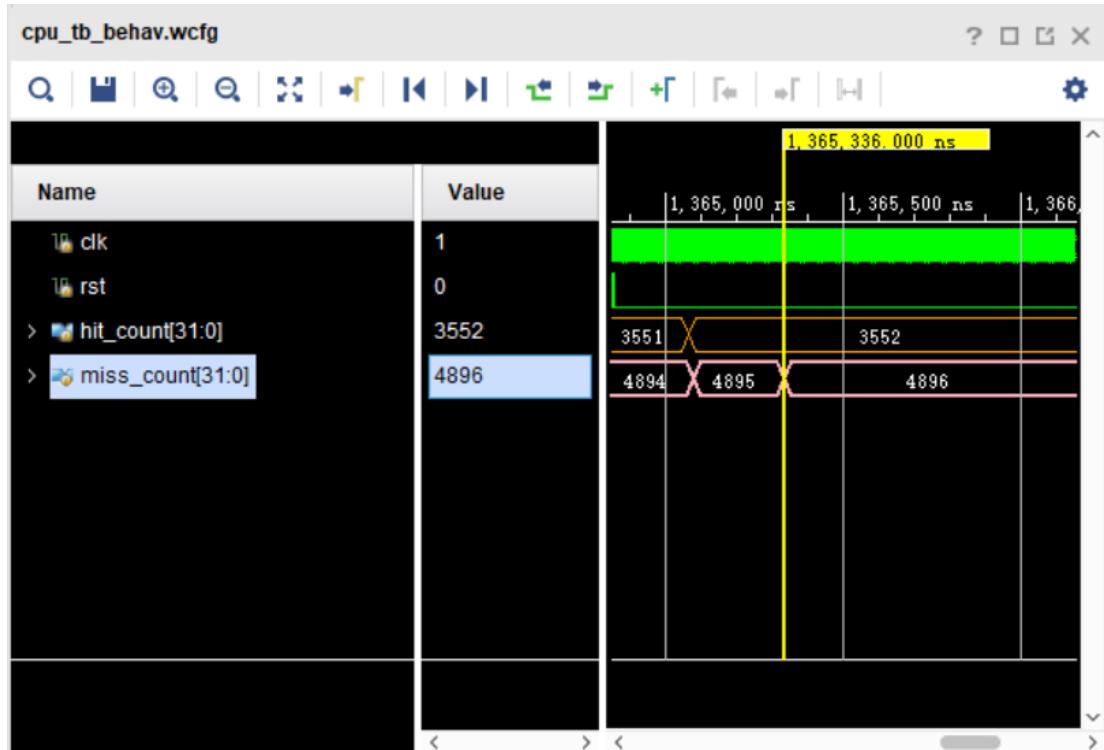
LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

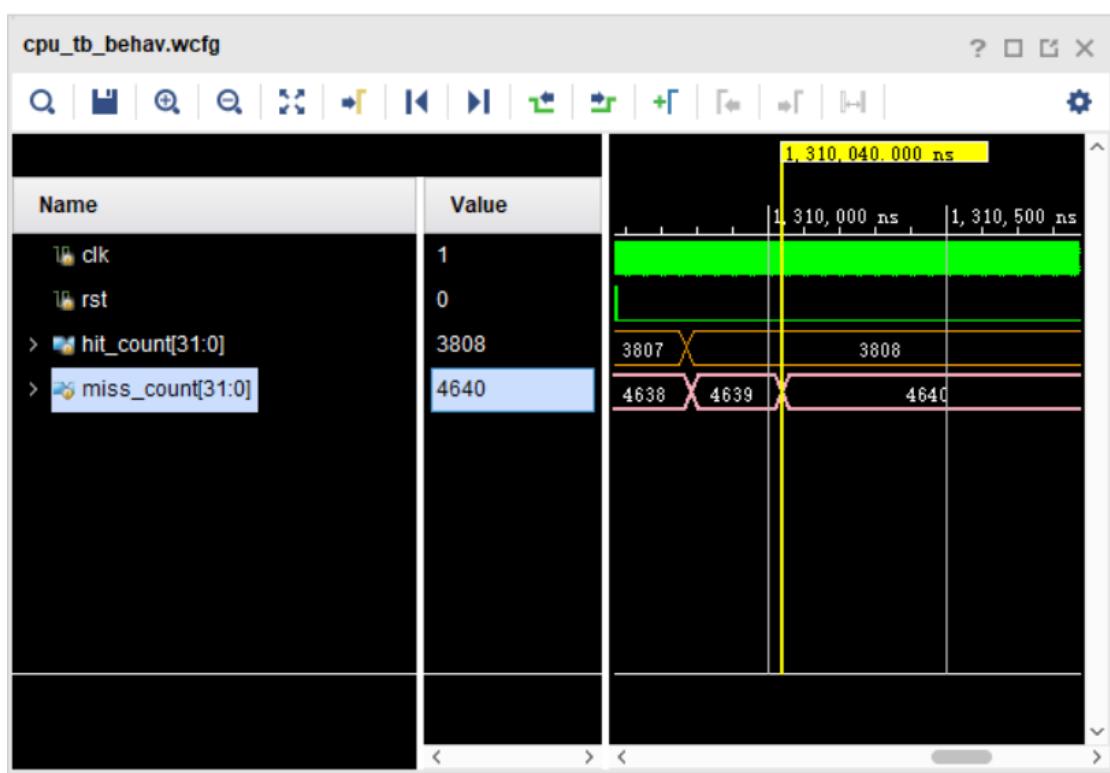
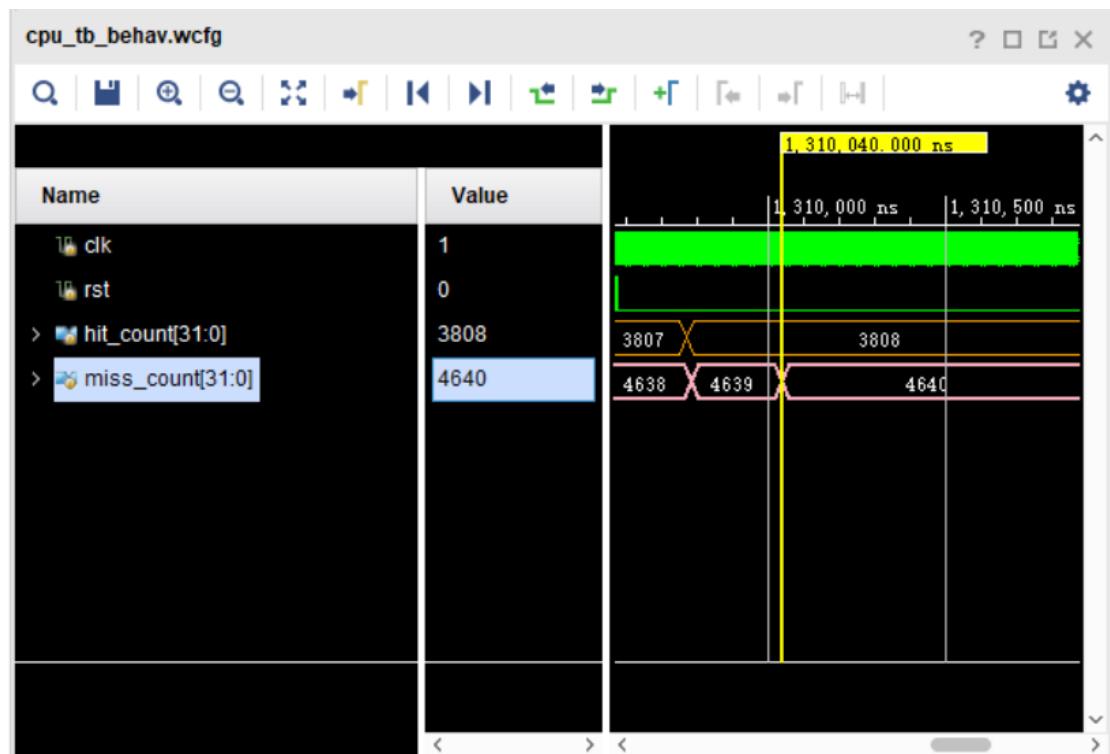


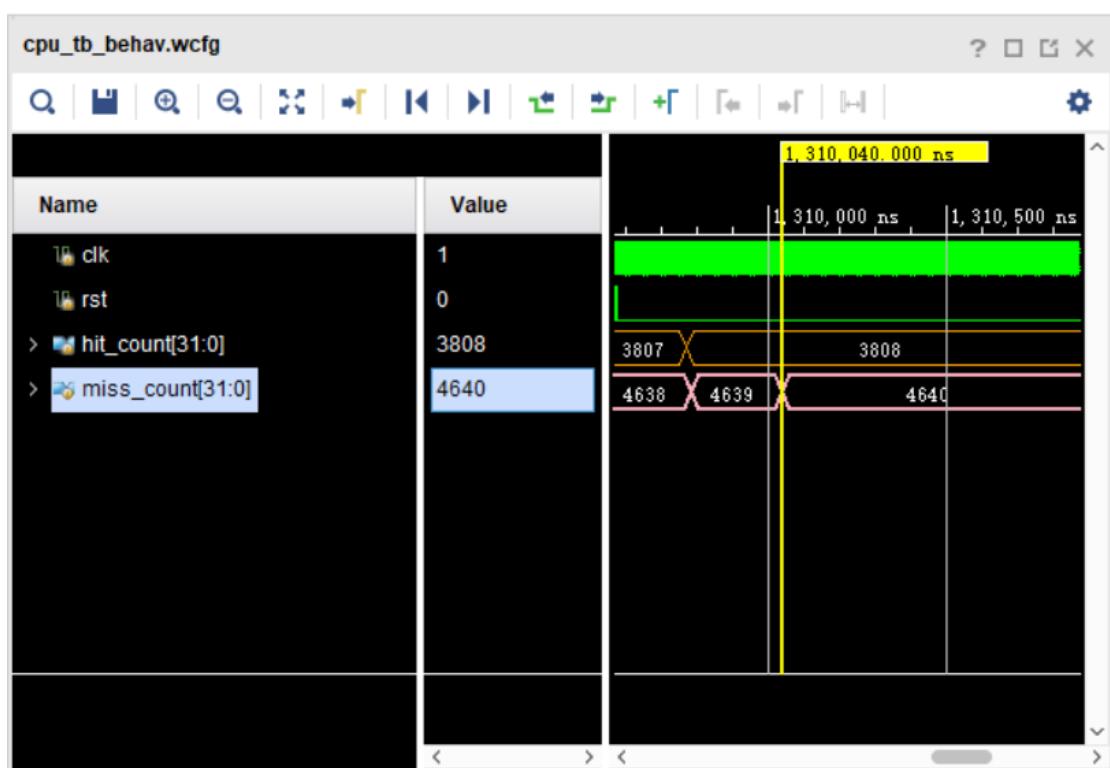
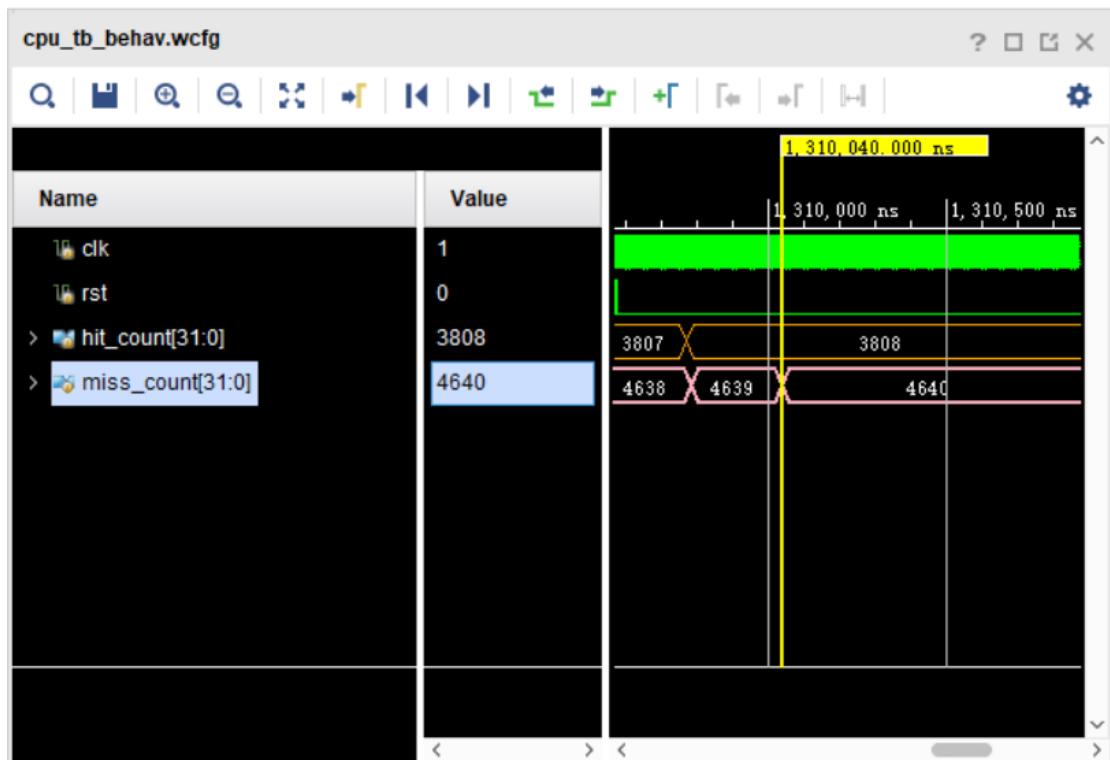




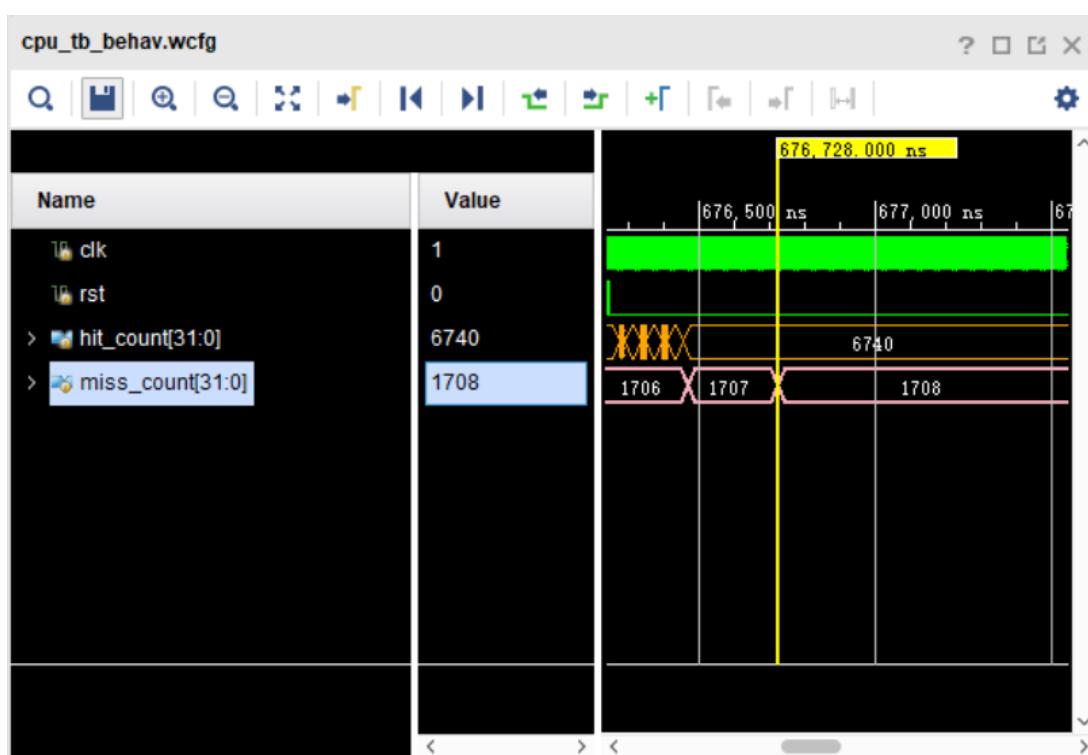
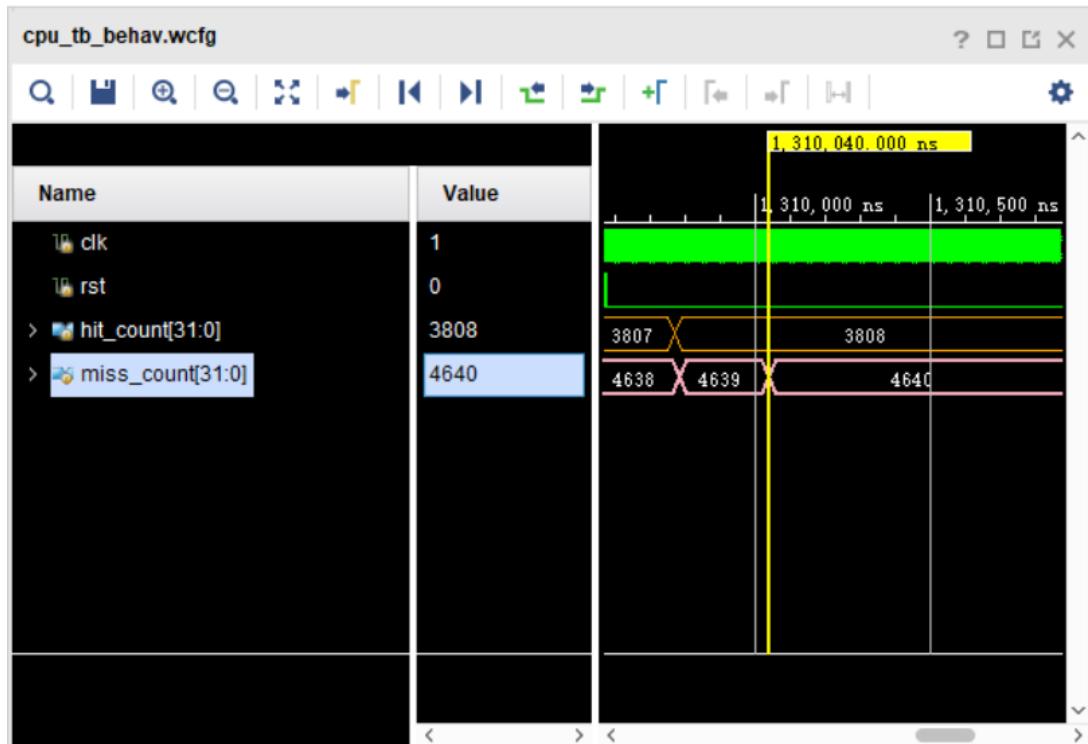
LINE_ADDR_LEN	3
SET_ADDR_LEN	2
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

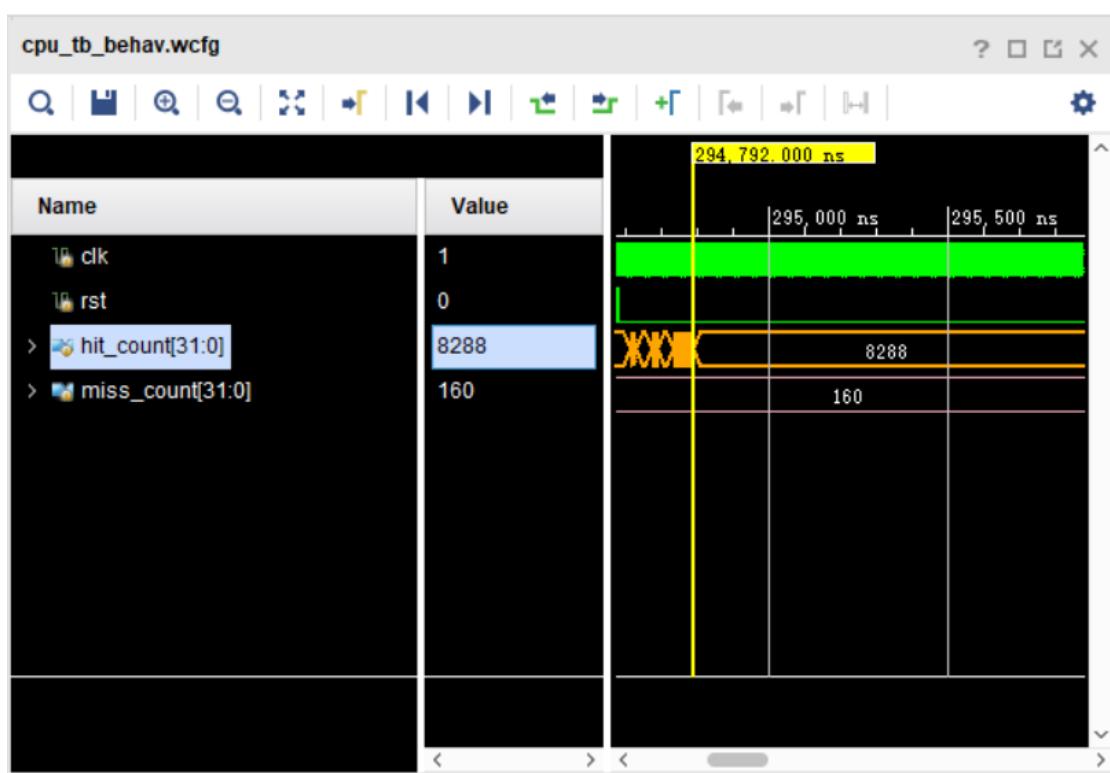
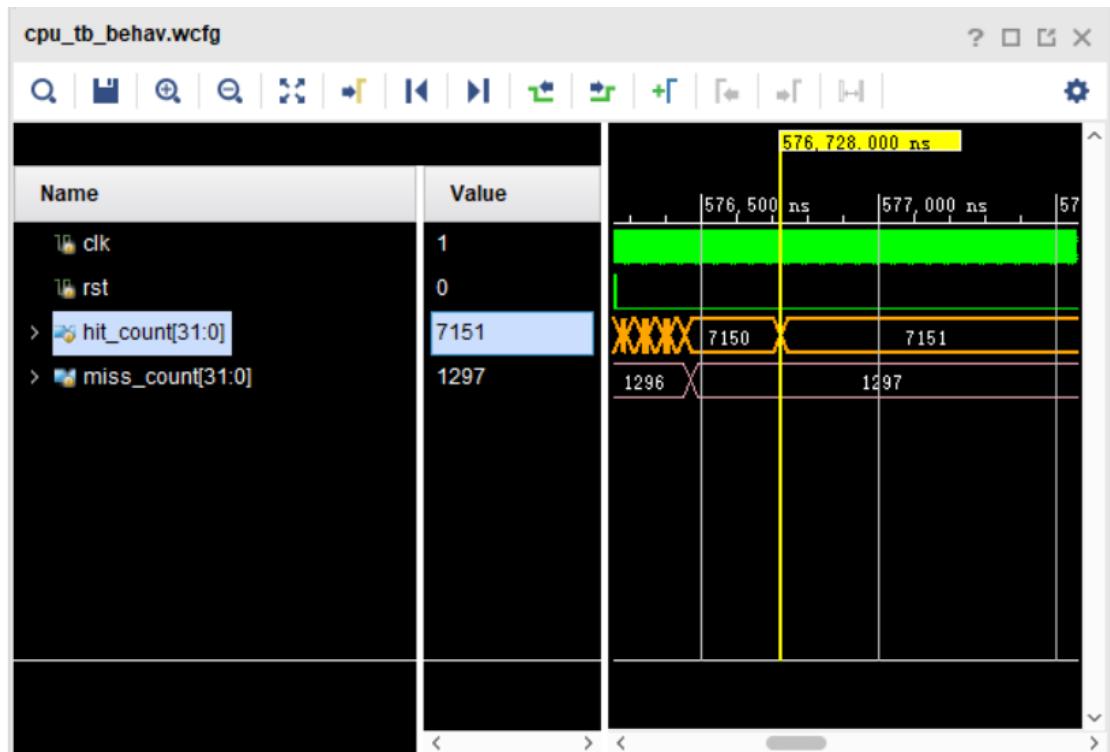


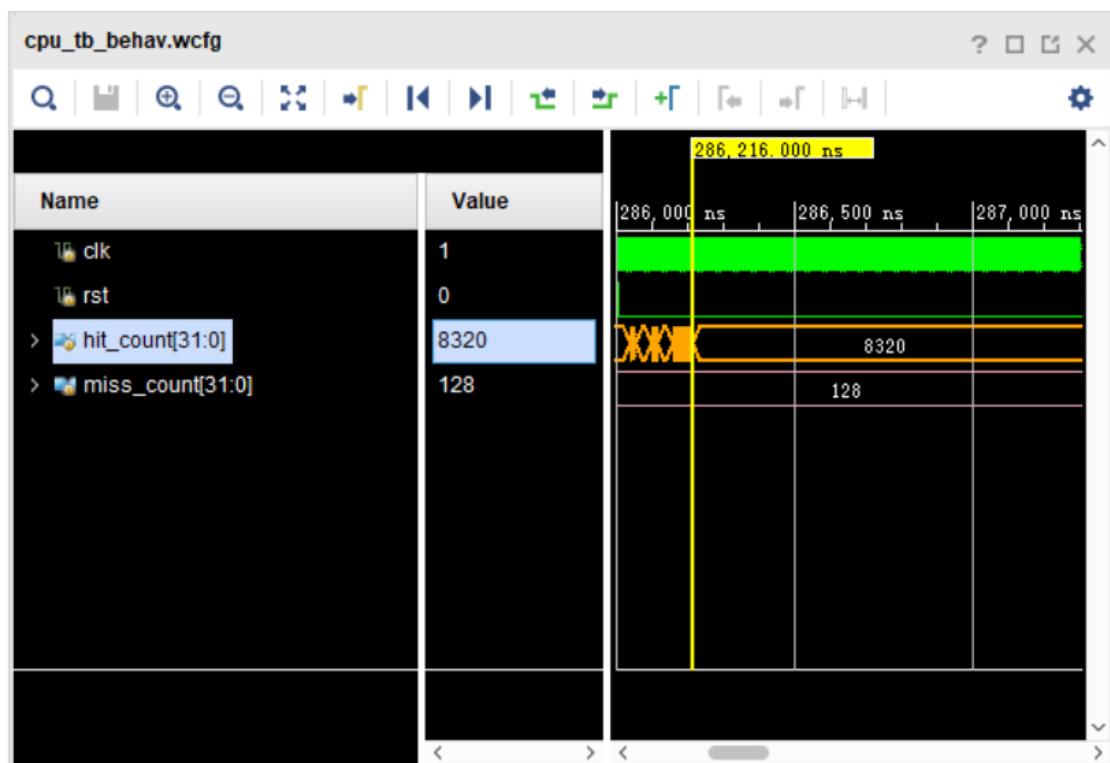
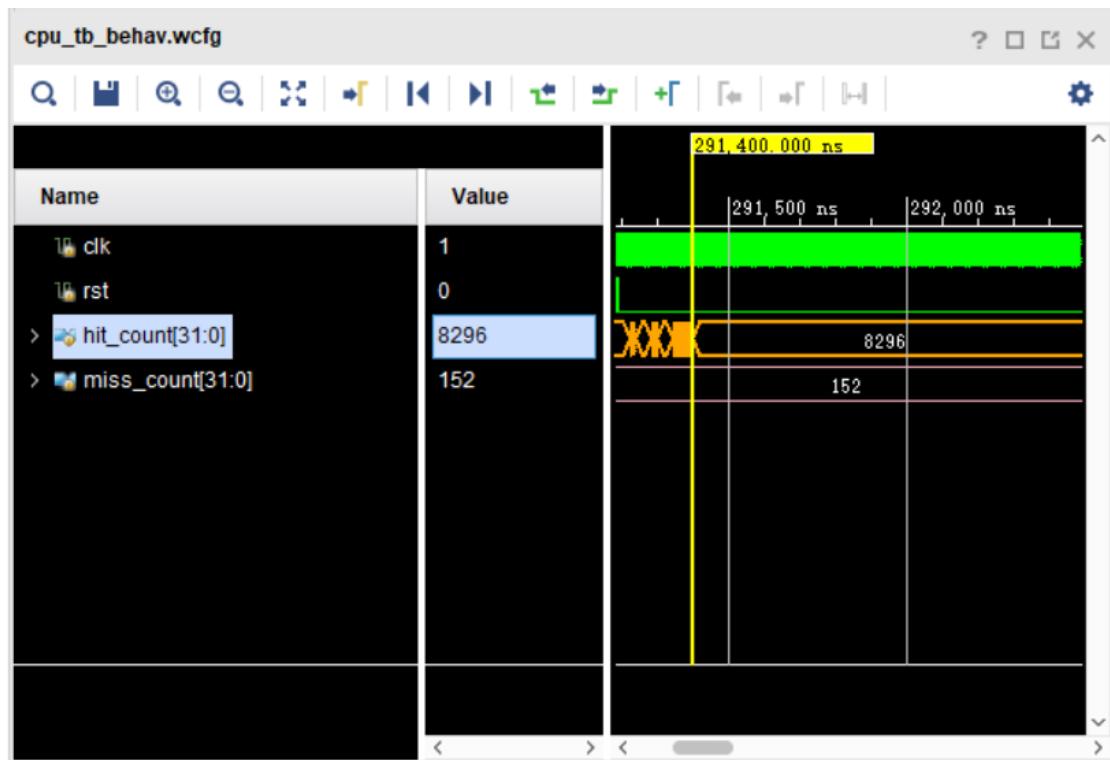




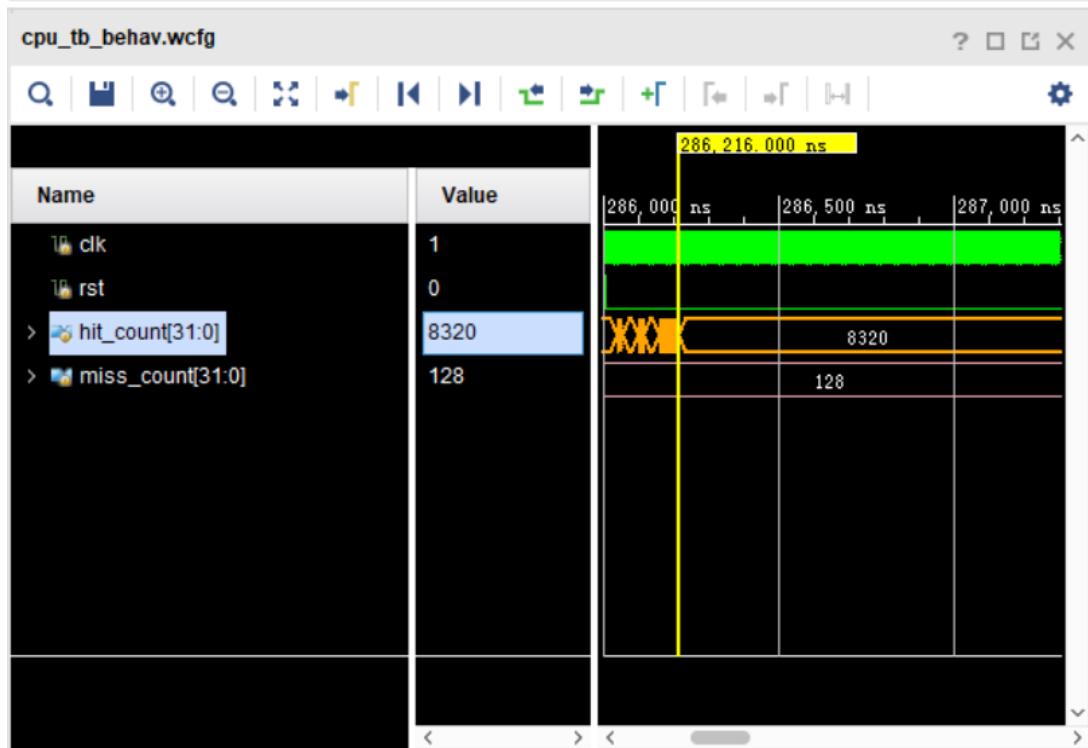
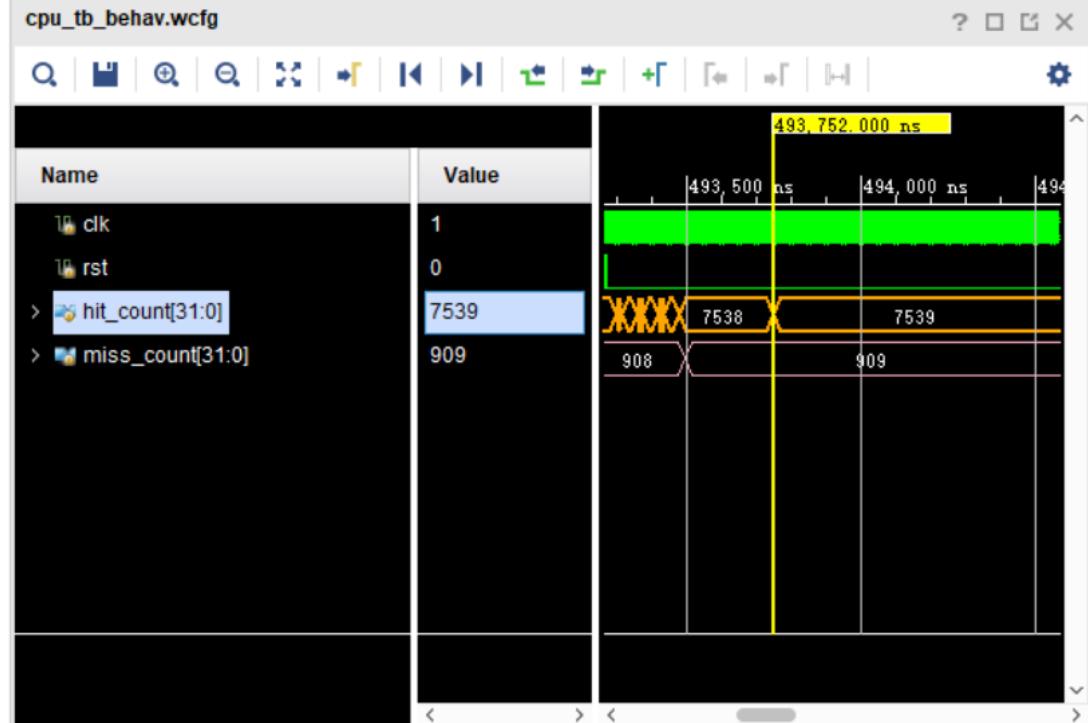
LINE_ADDR_LEN	3
SET_ADDR_LEN	3
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

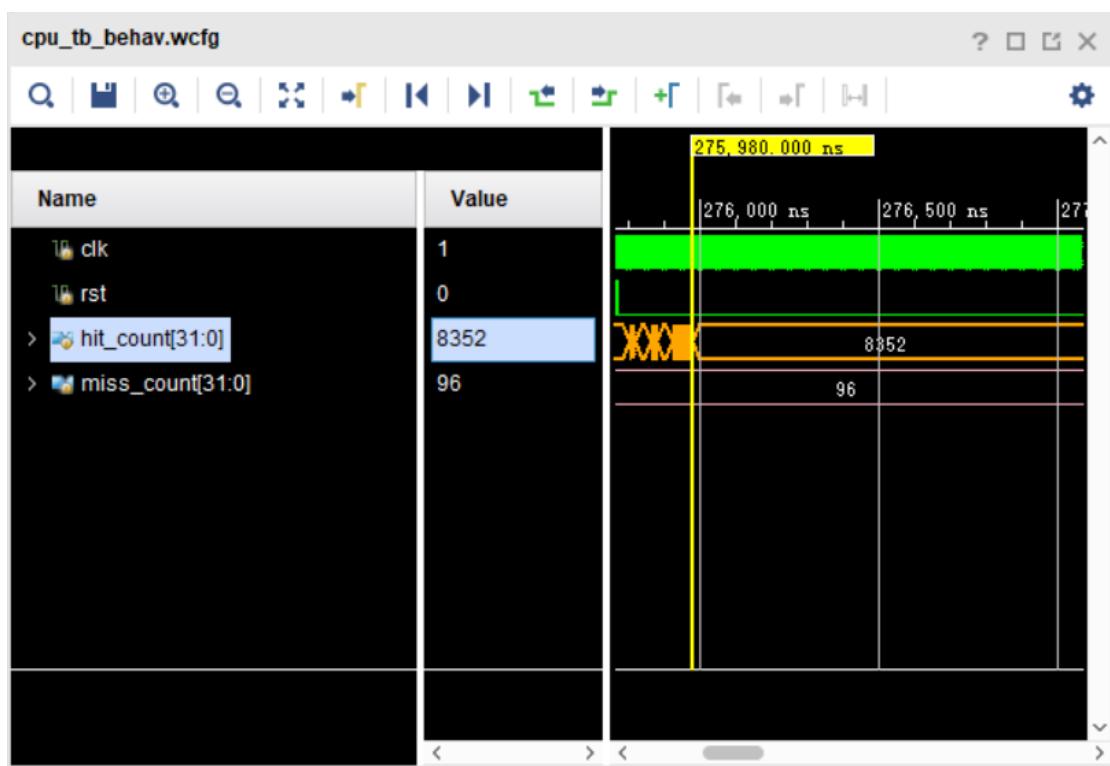
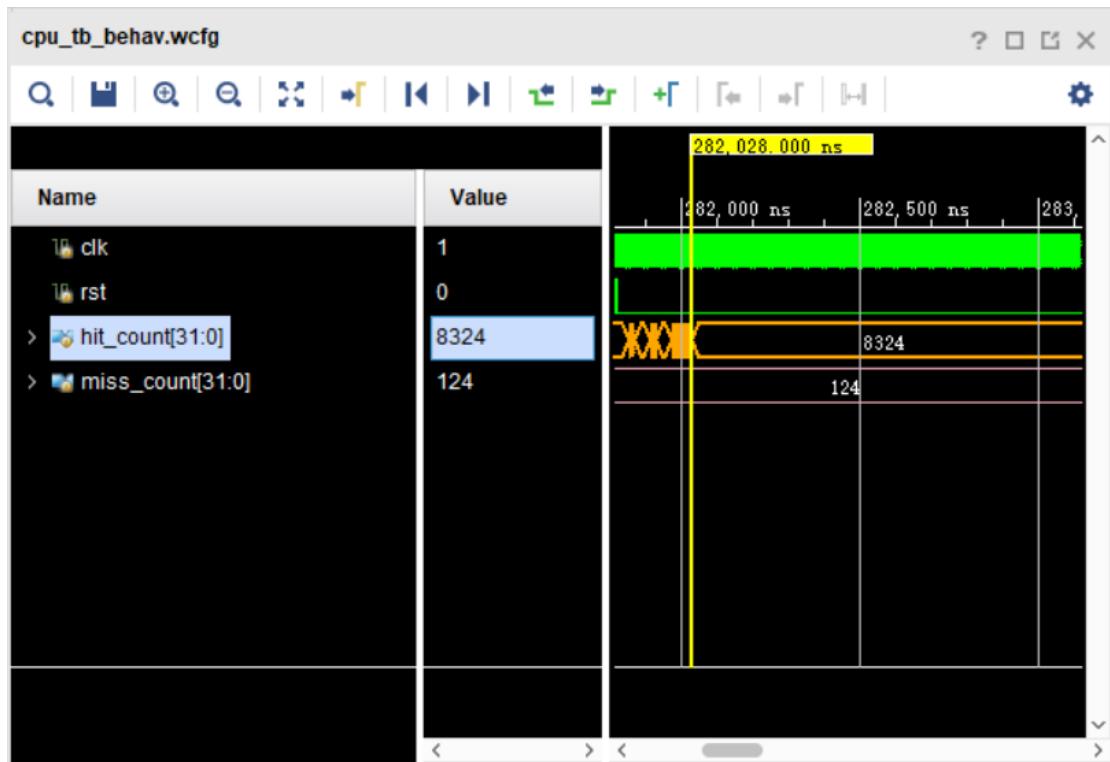


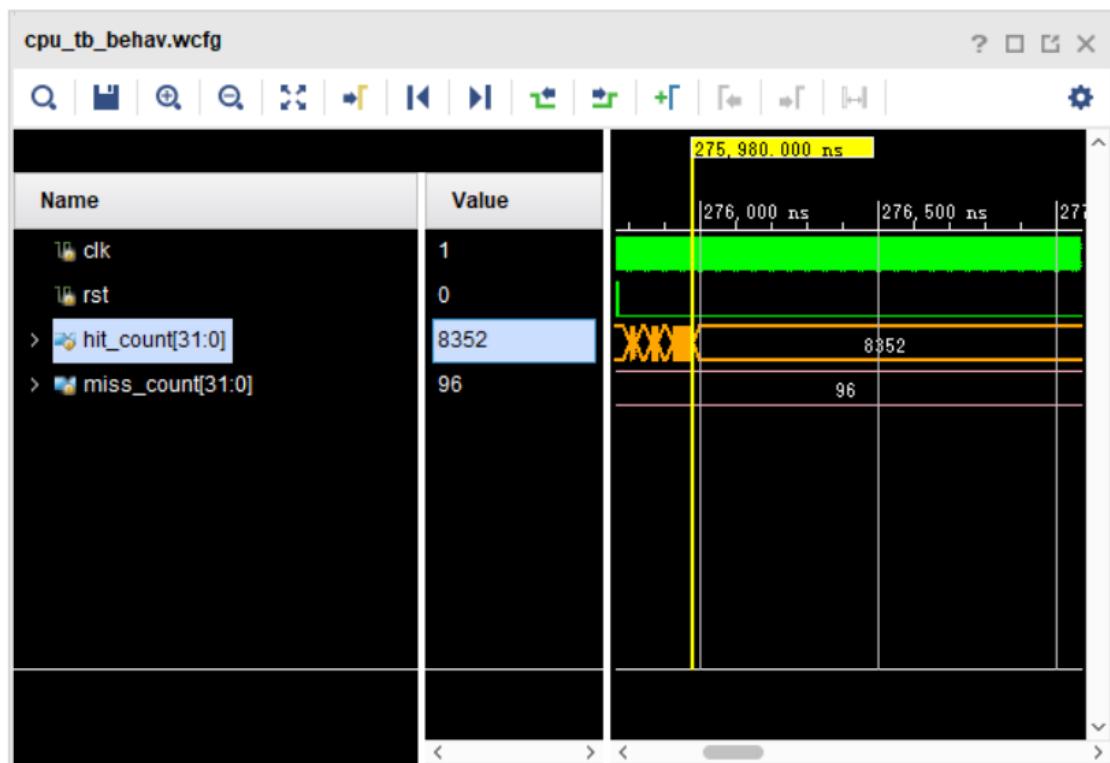
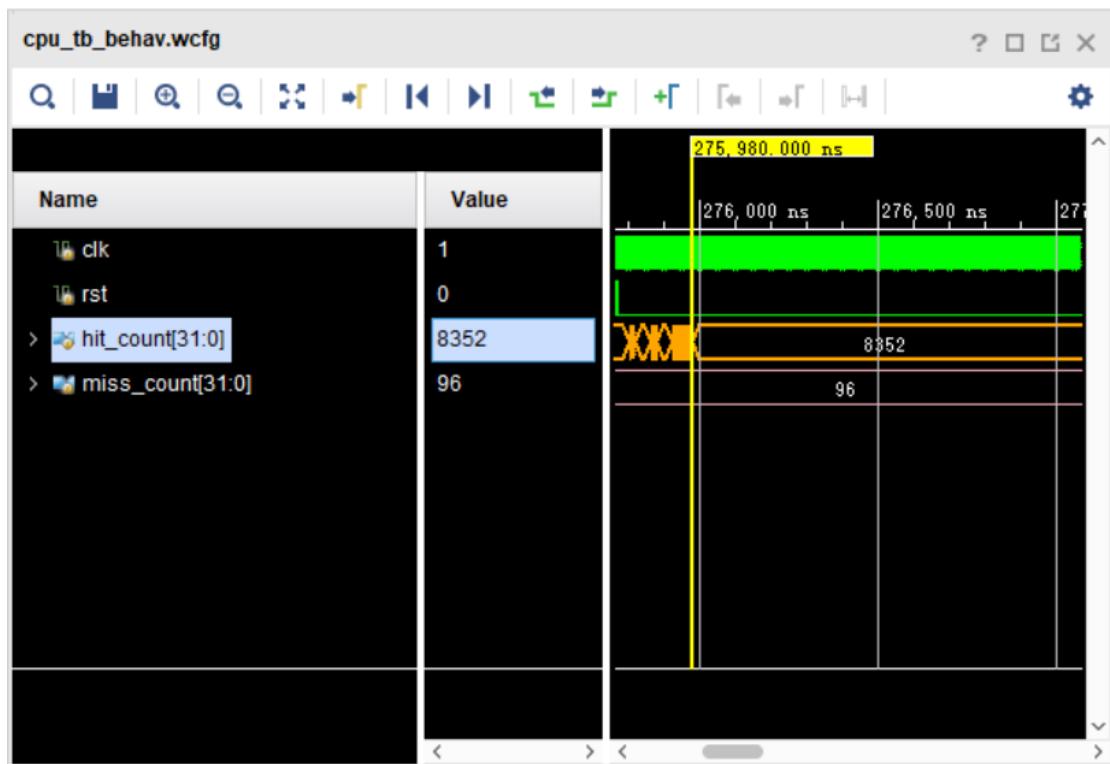




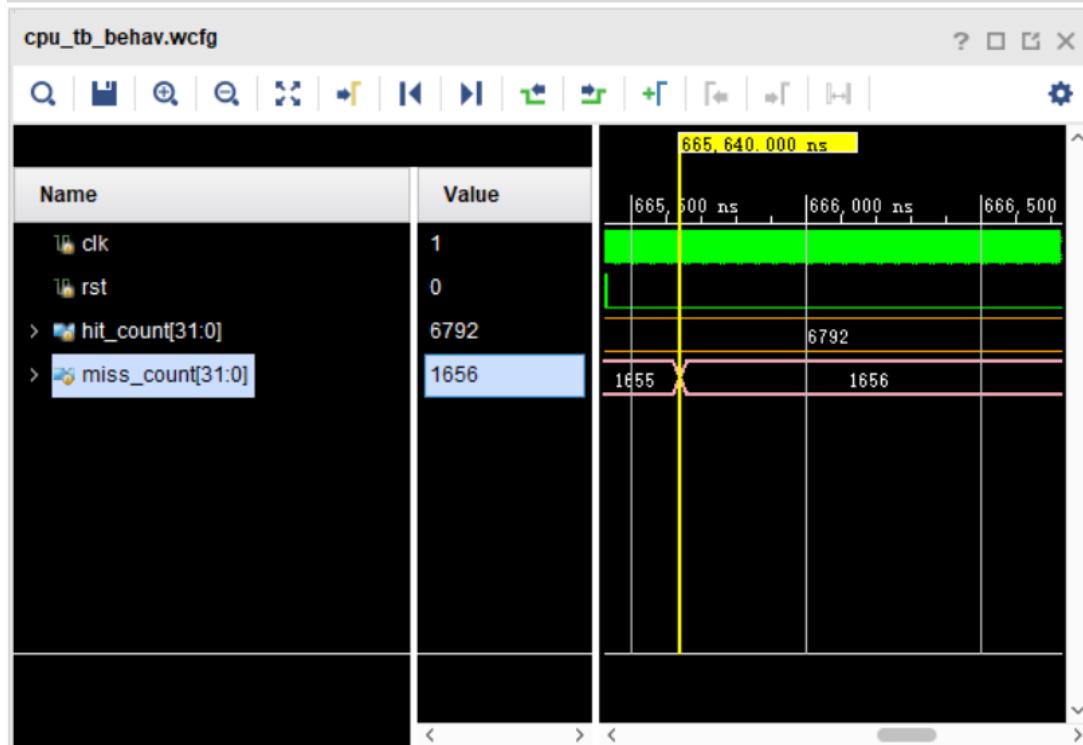
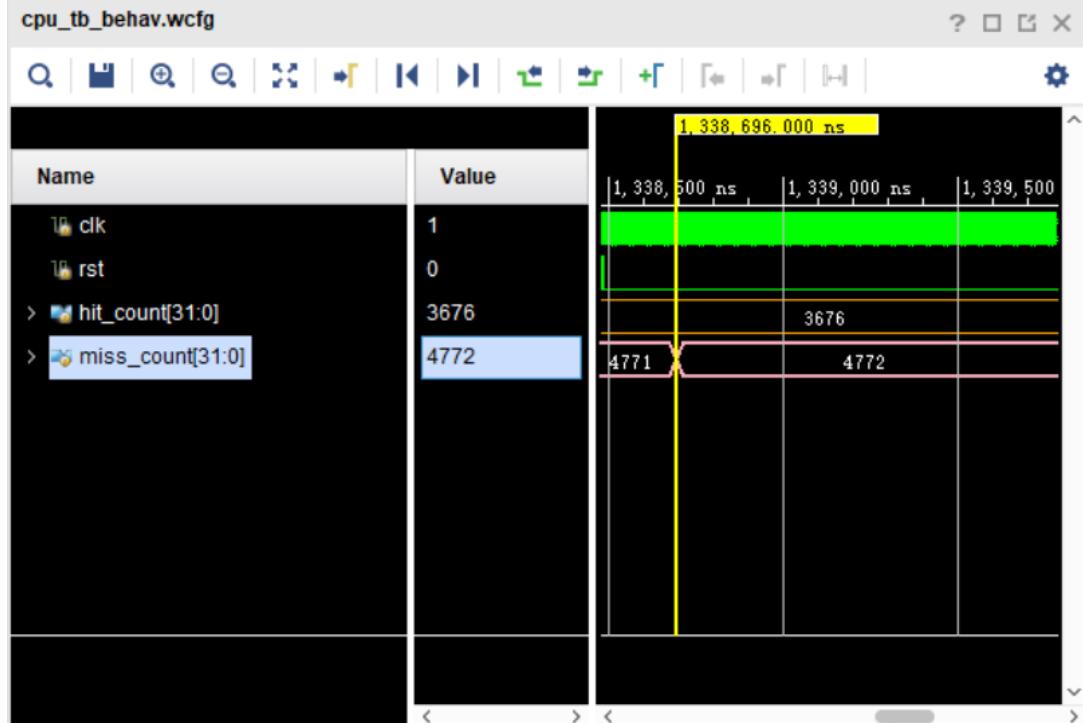
LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

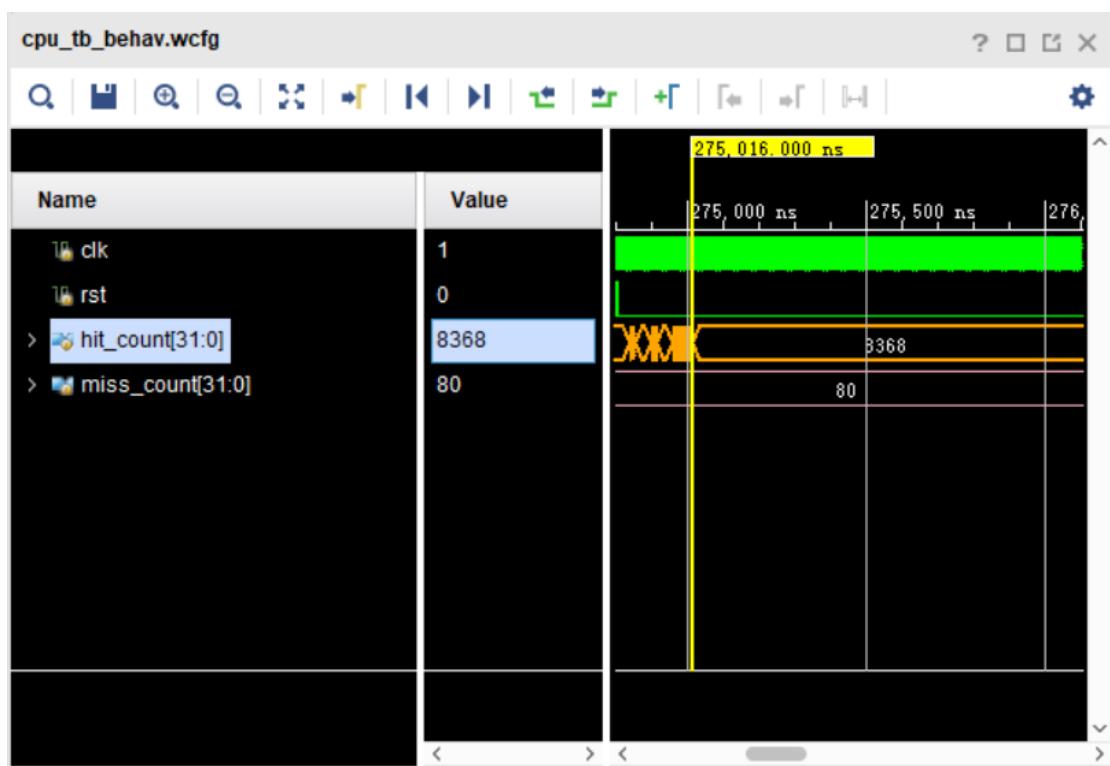
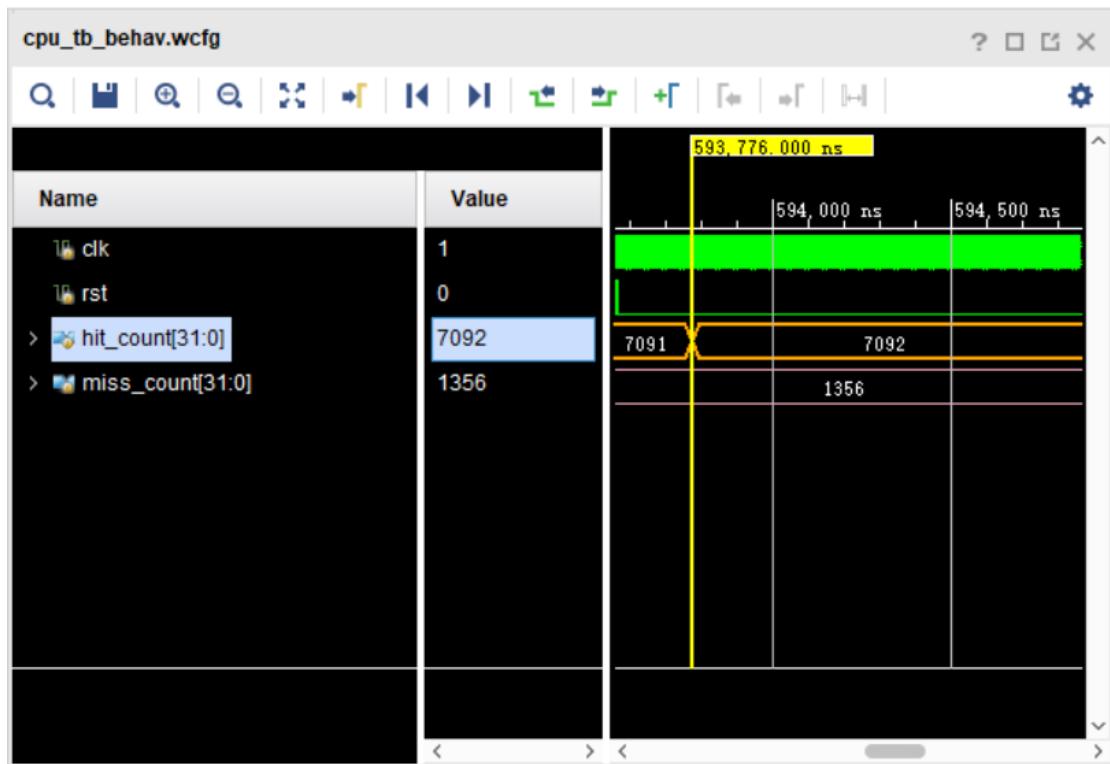


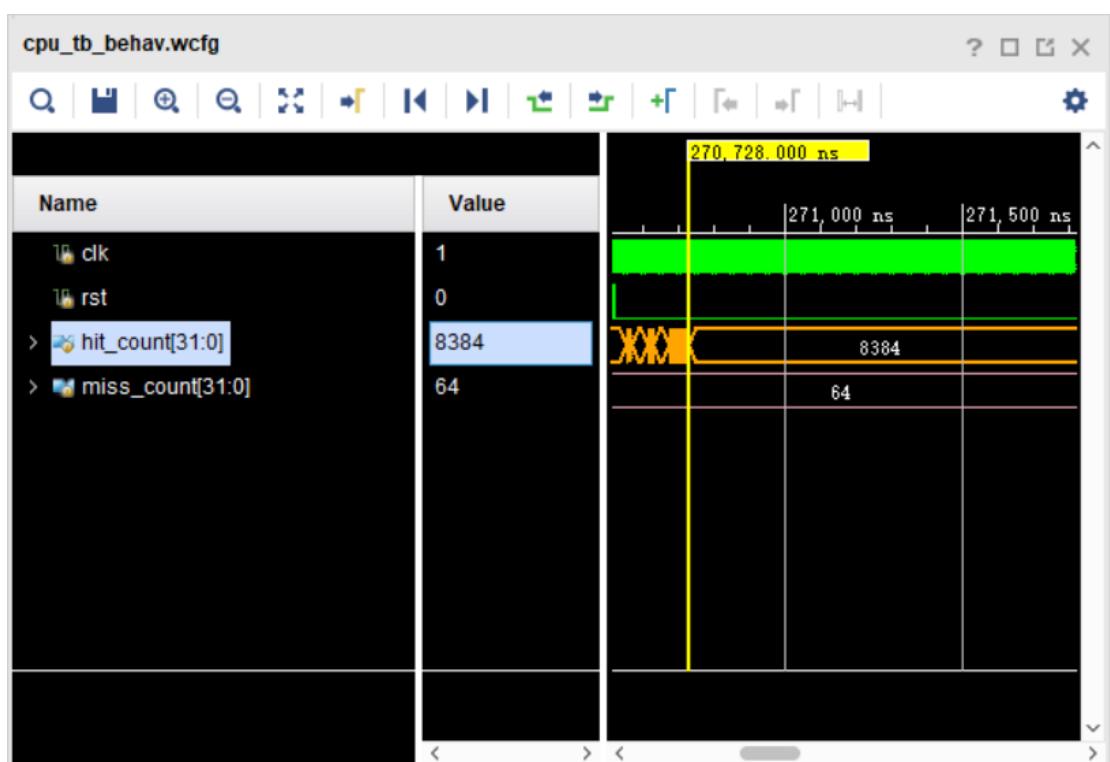
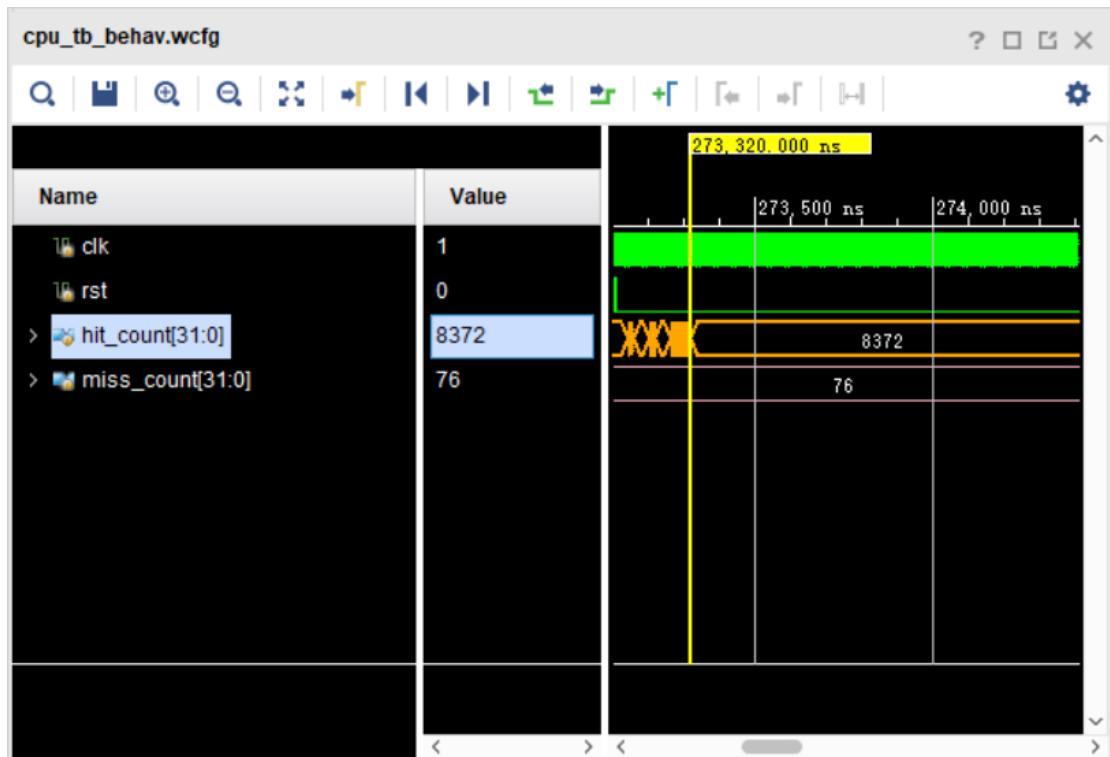




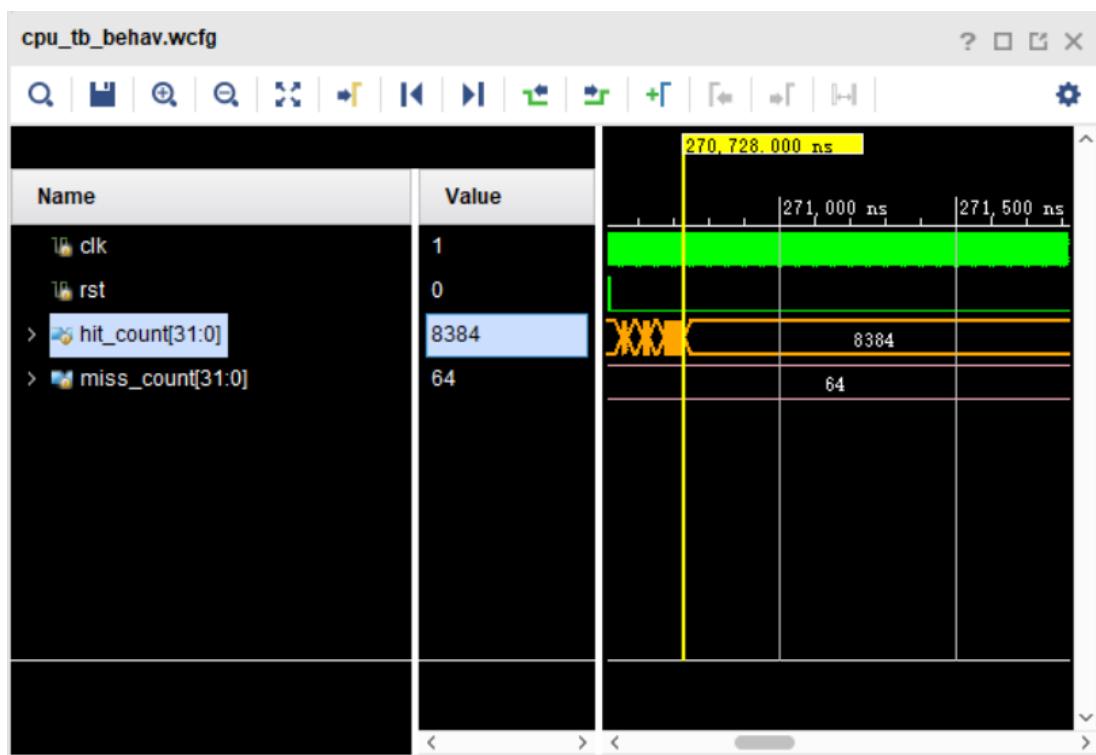
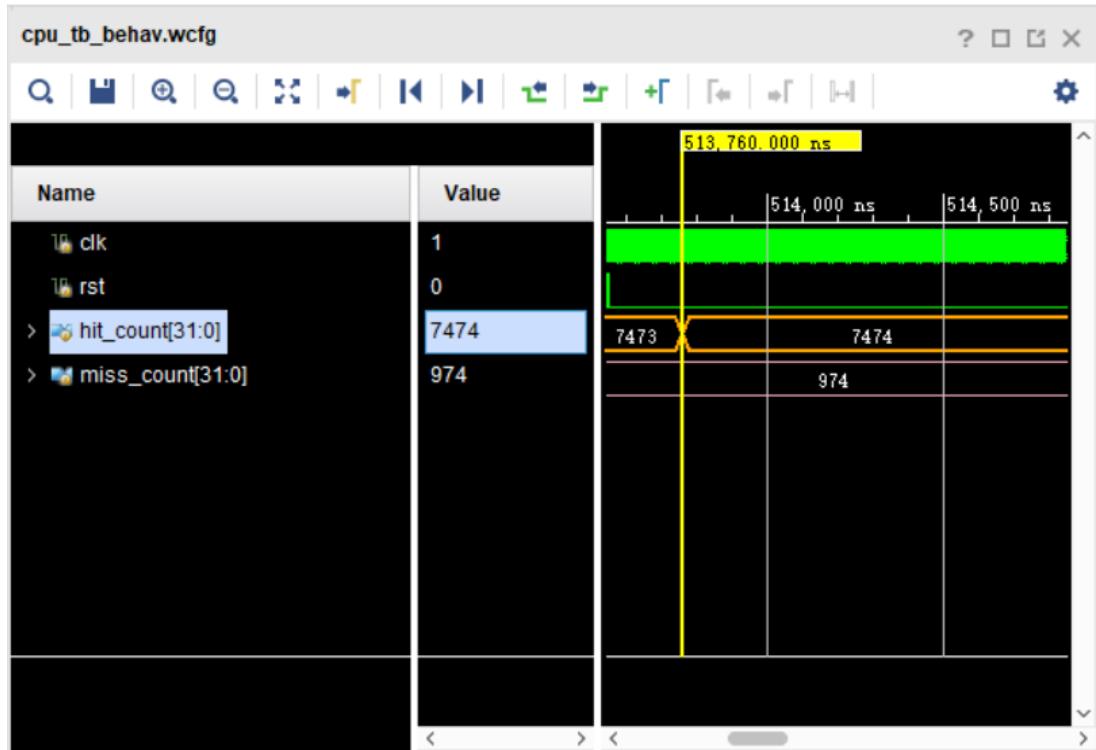
LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

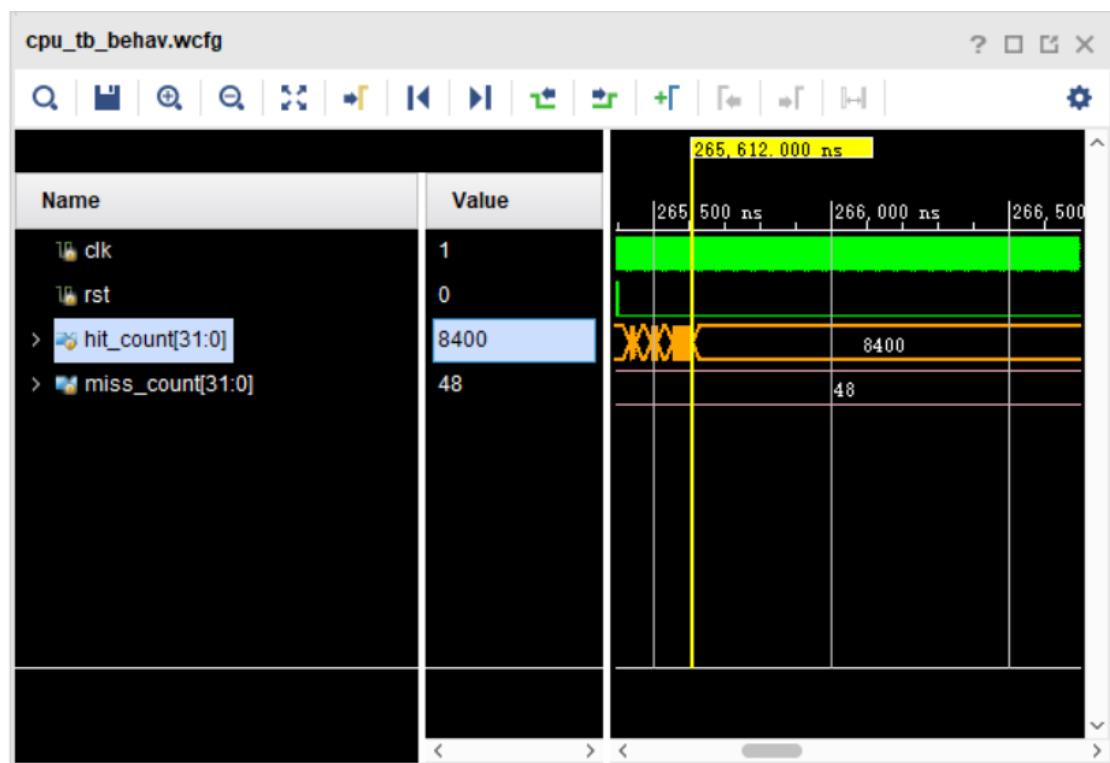
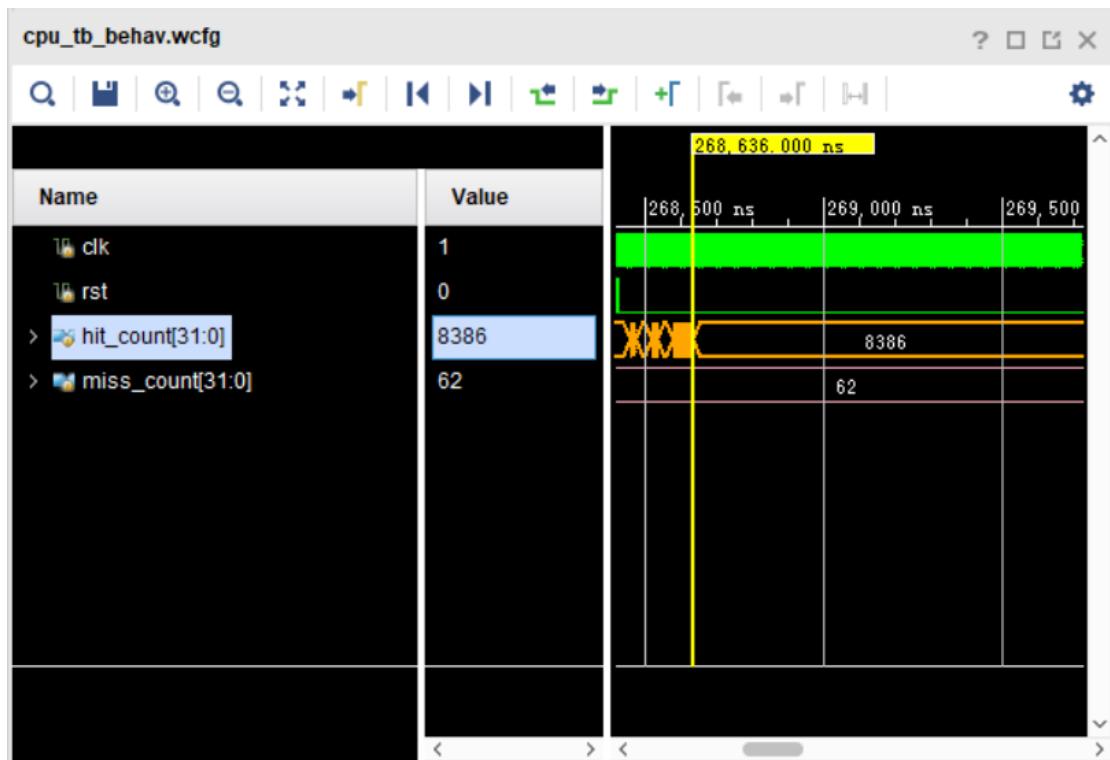


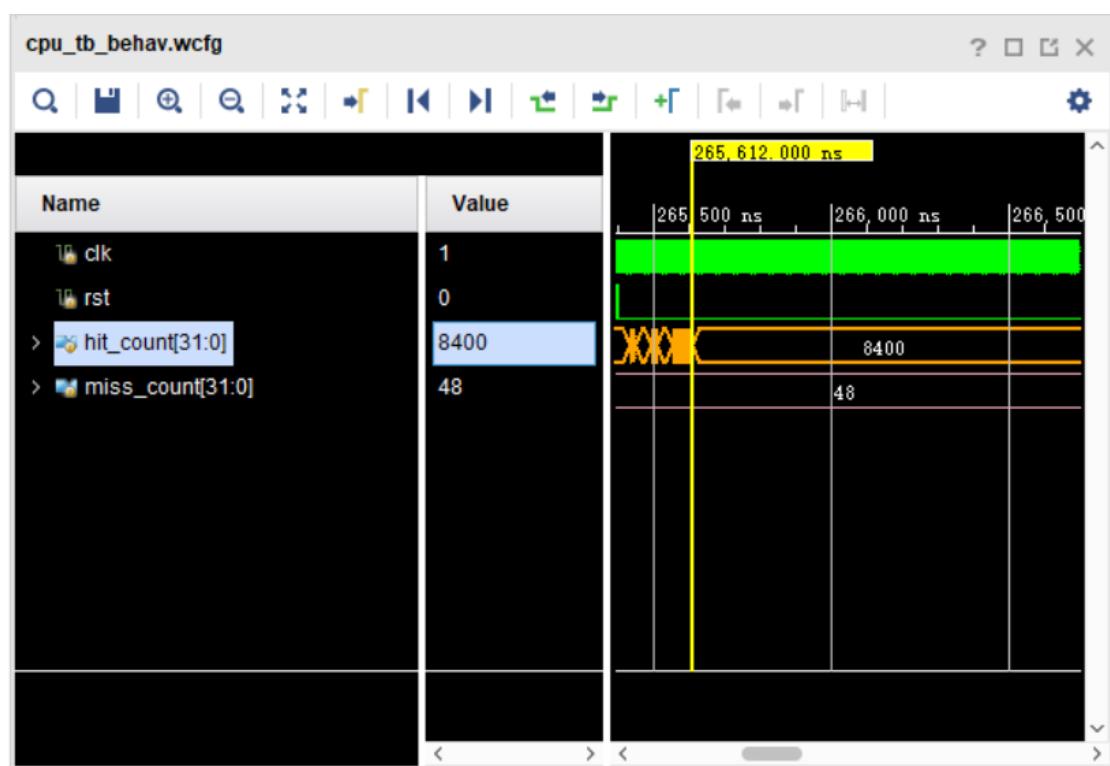
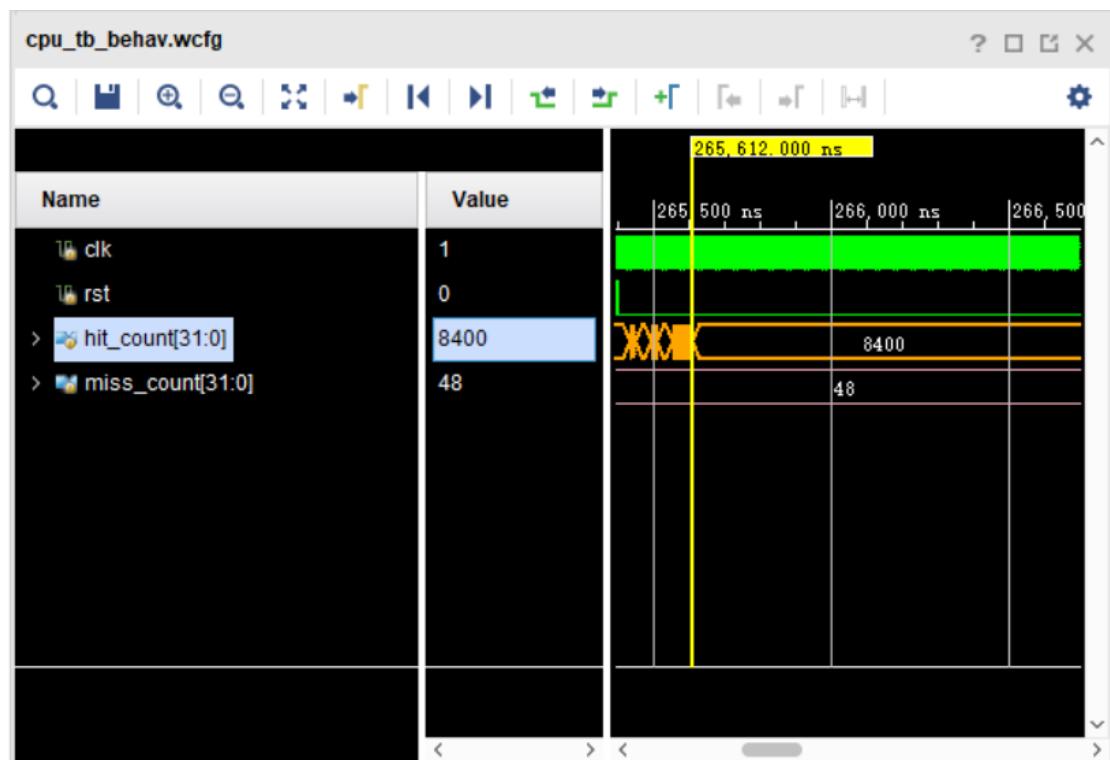




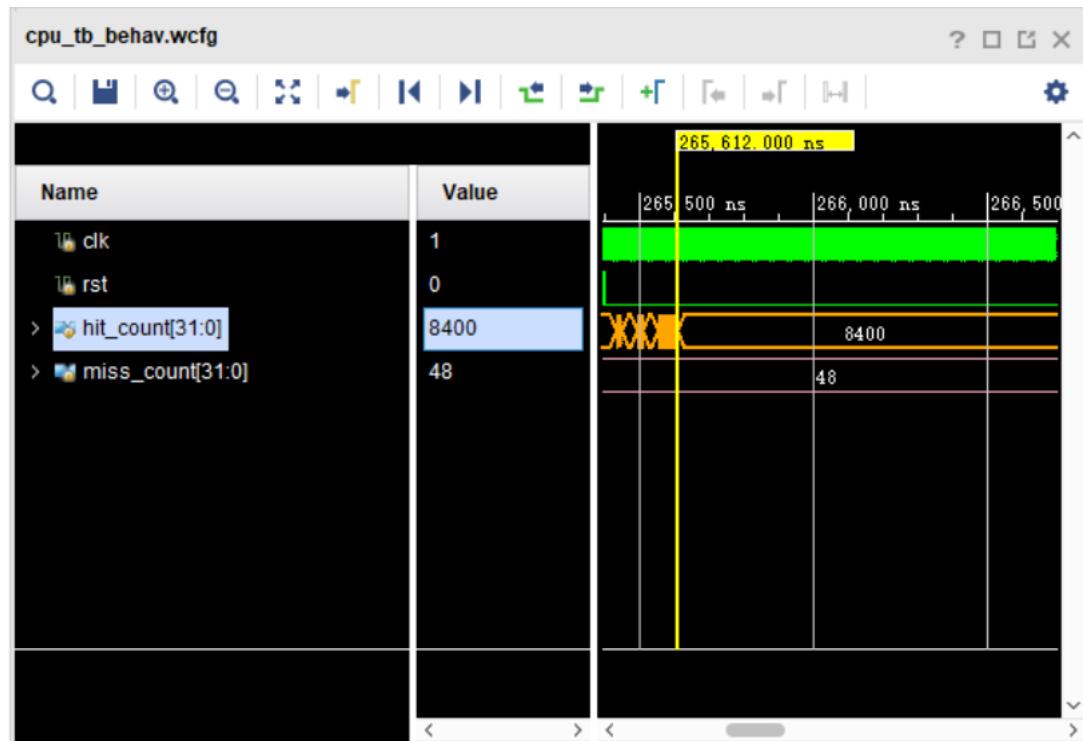
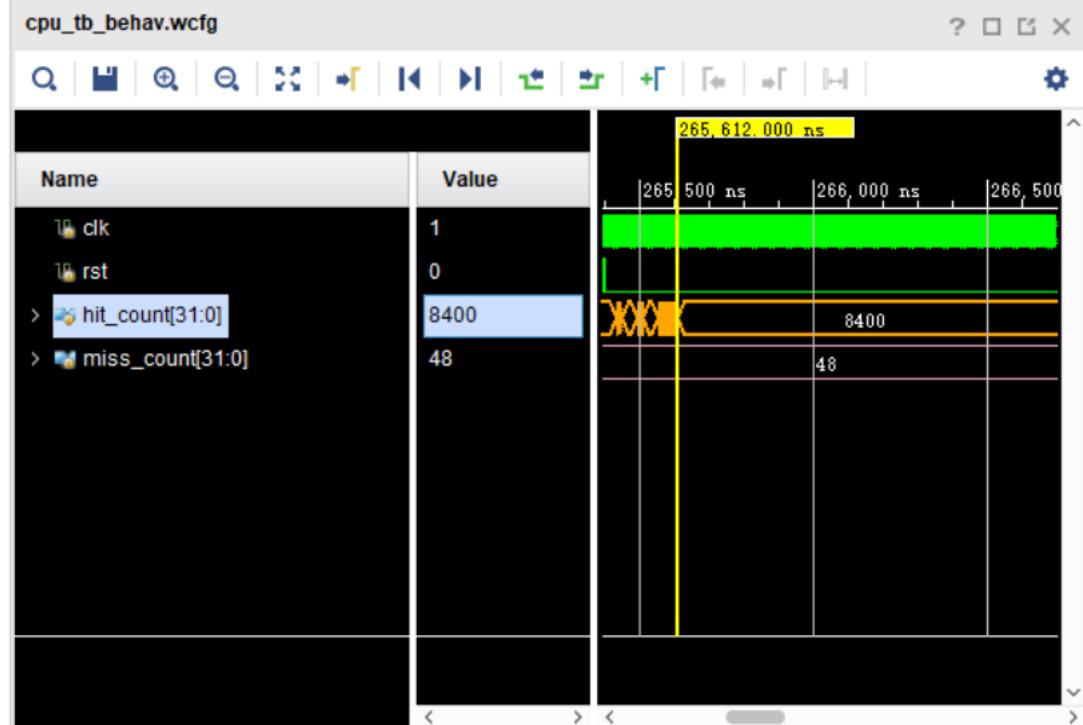
LINE_ADDR_LEN	4
SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

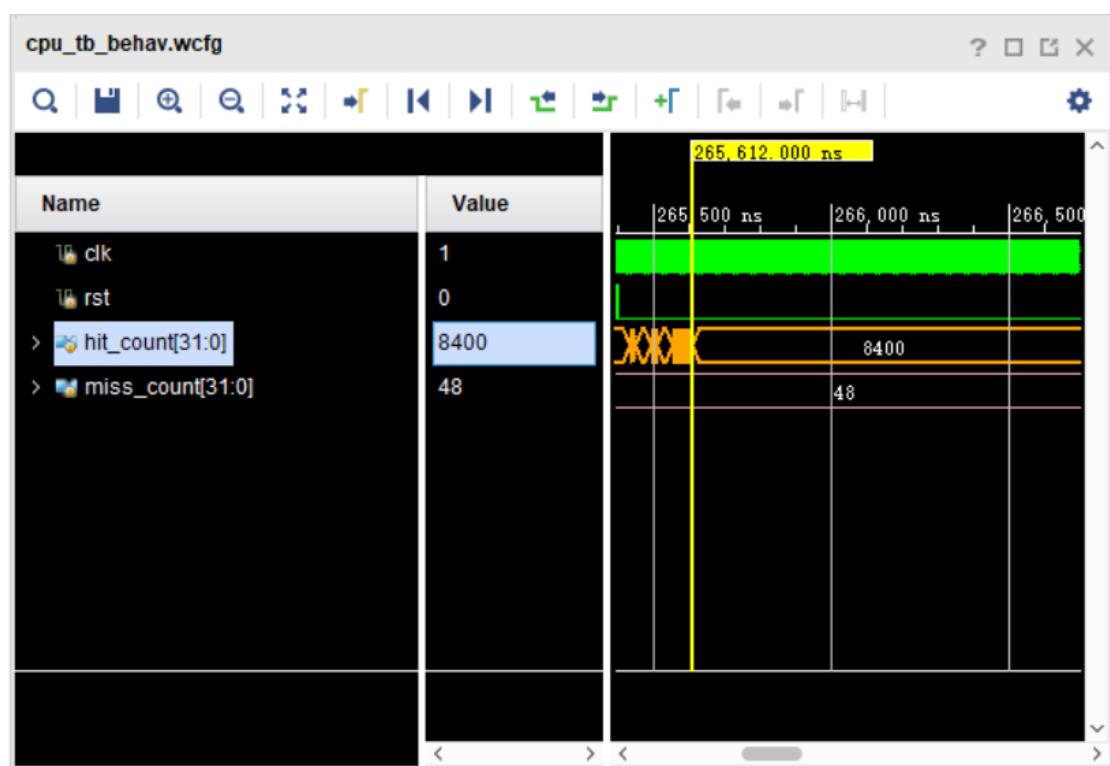
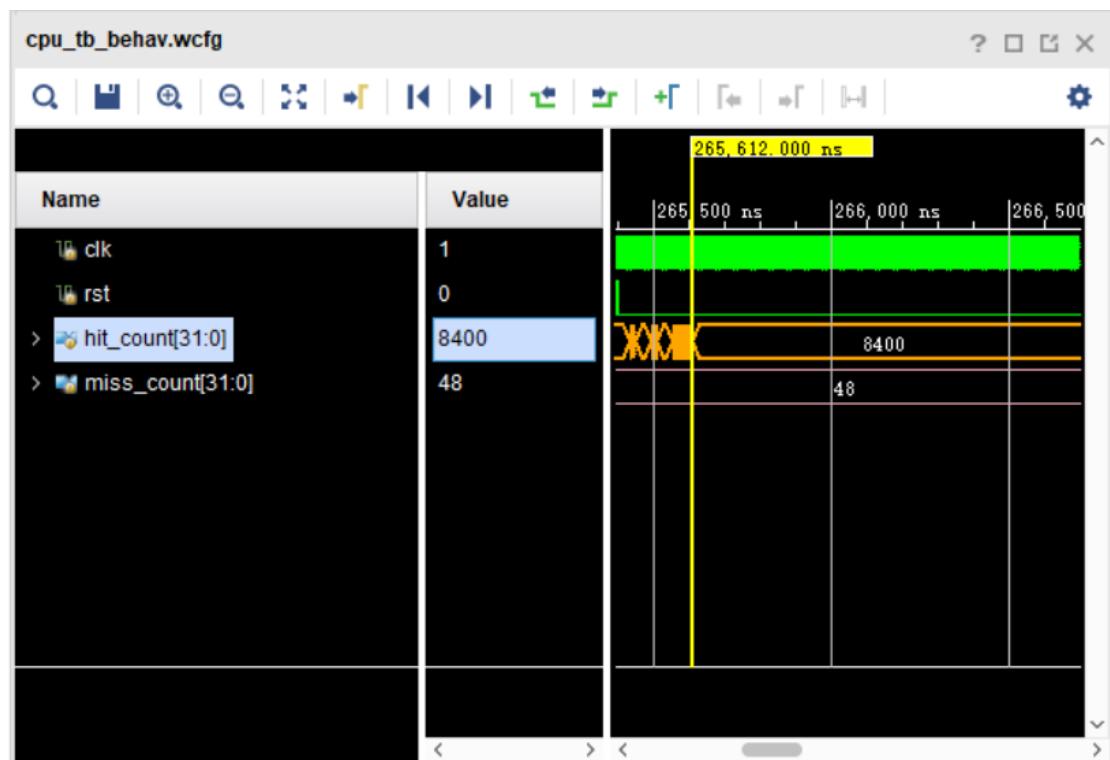


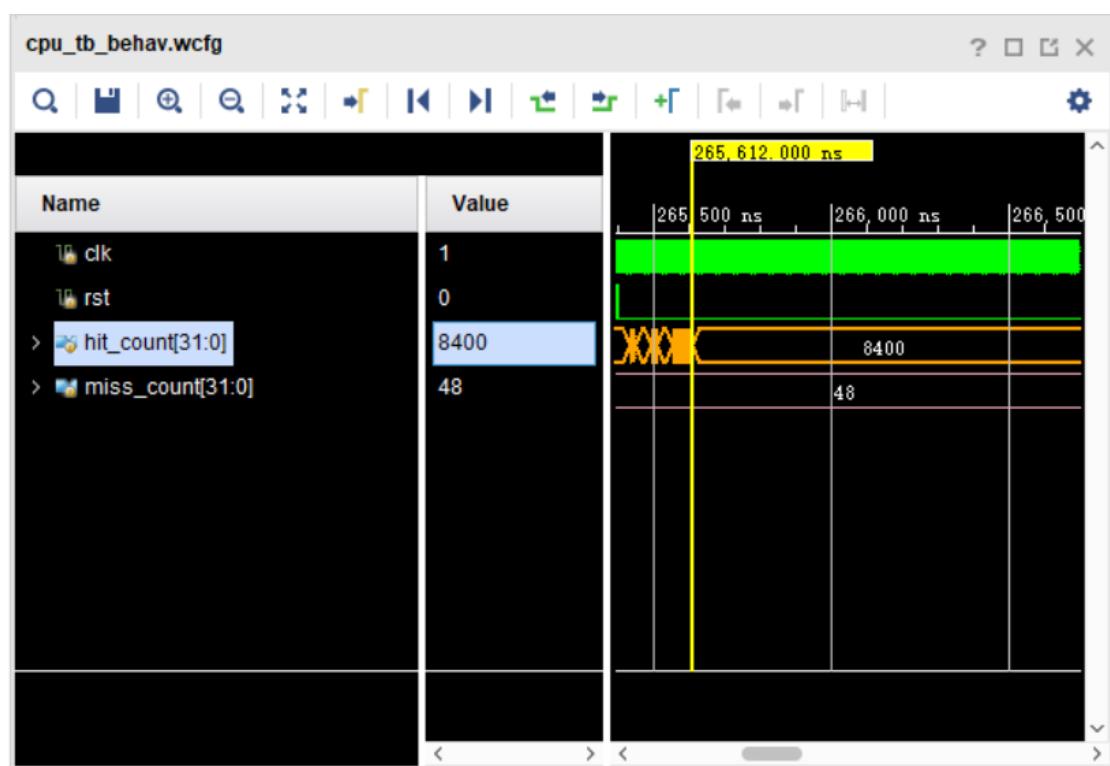
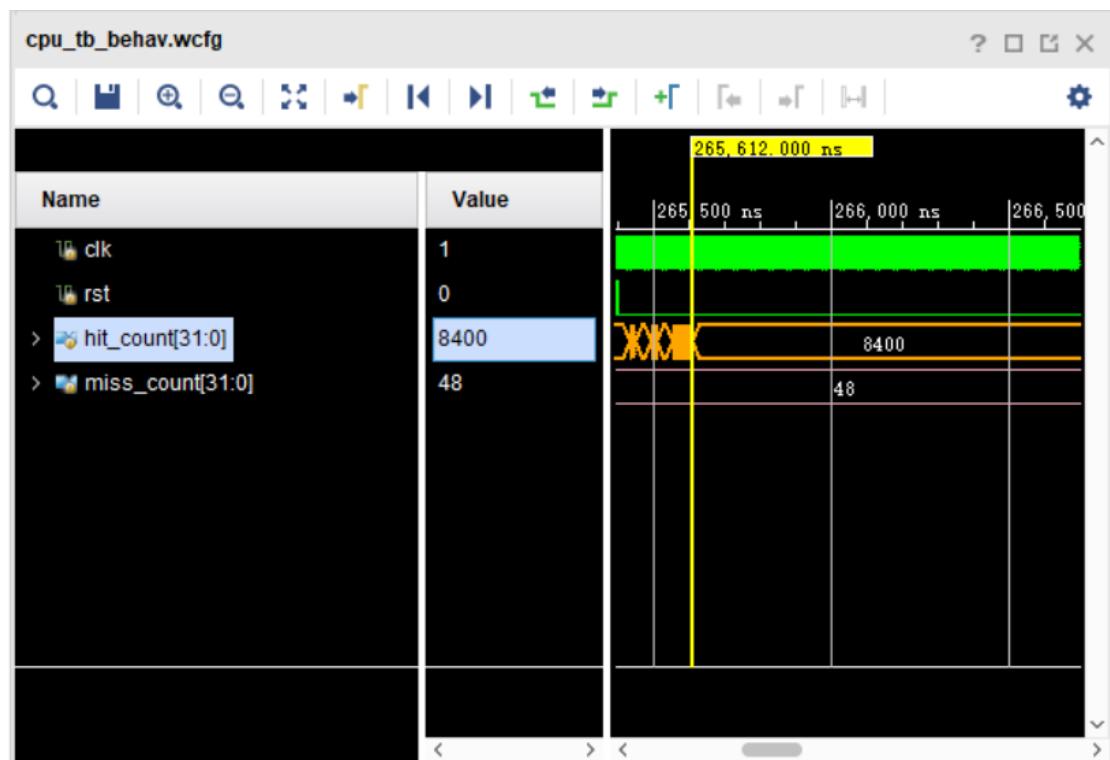




LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	FIFO
Test File	MATMUL

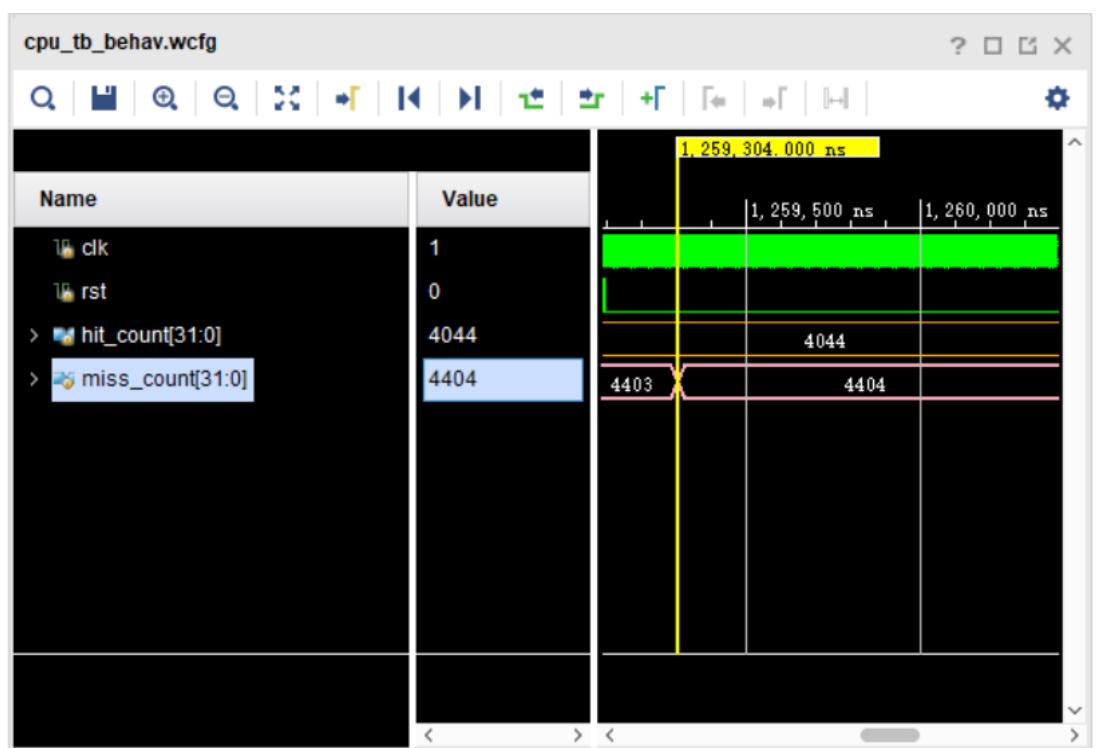
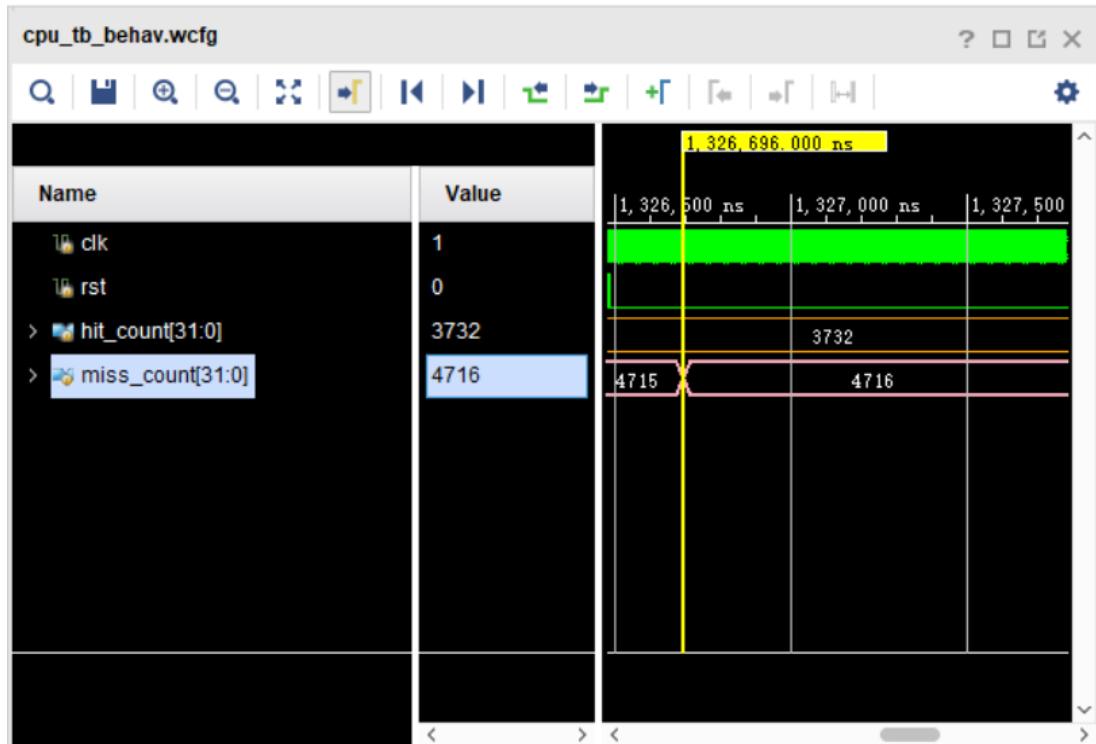


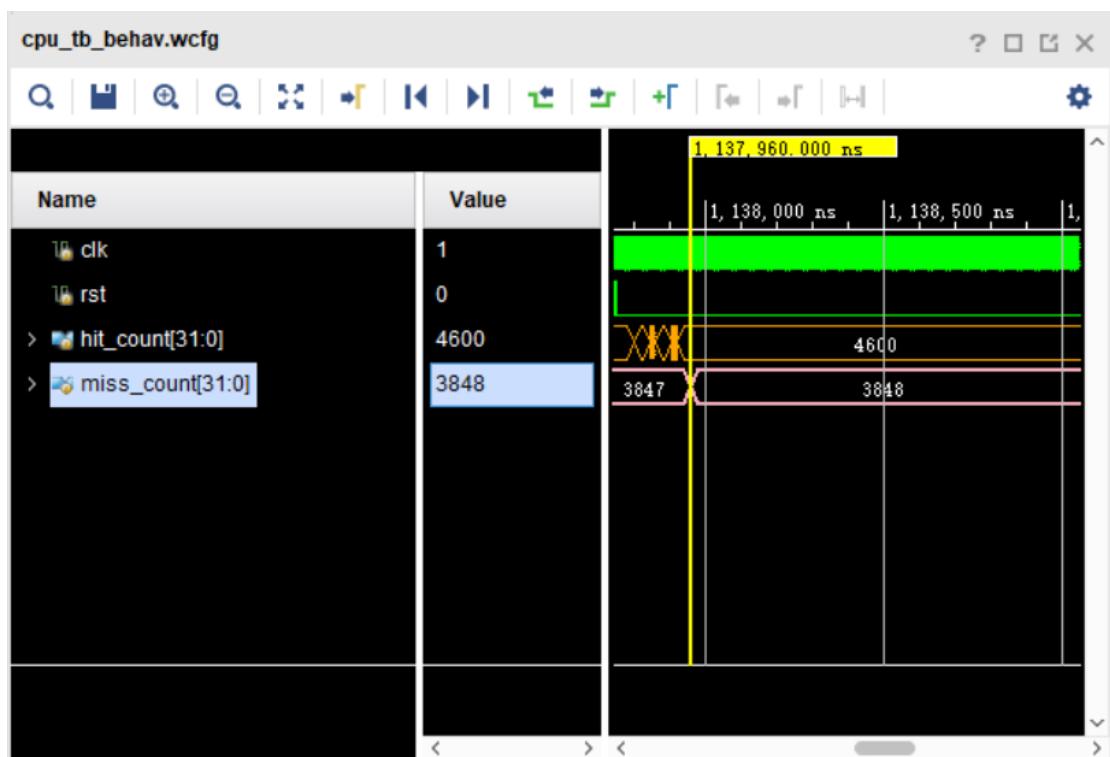
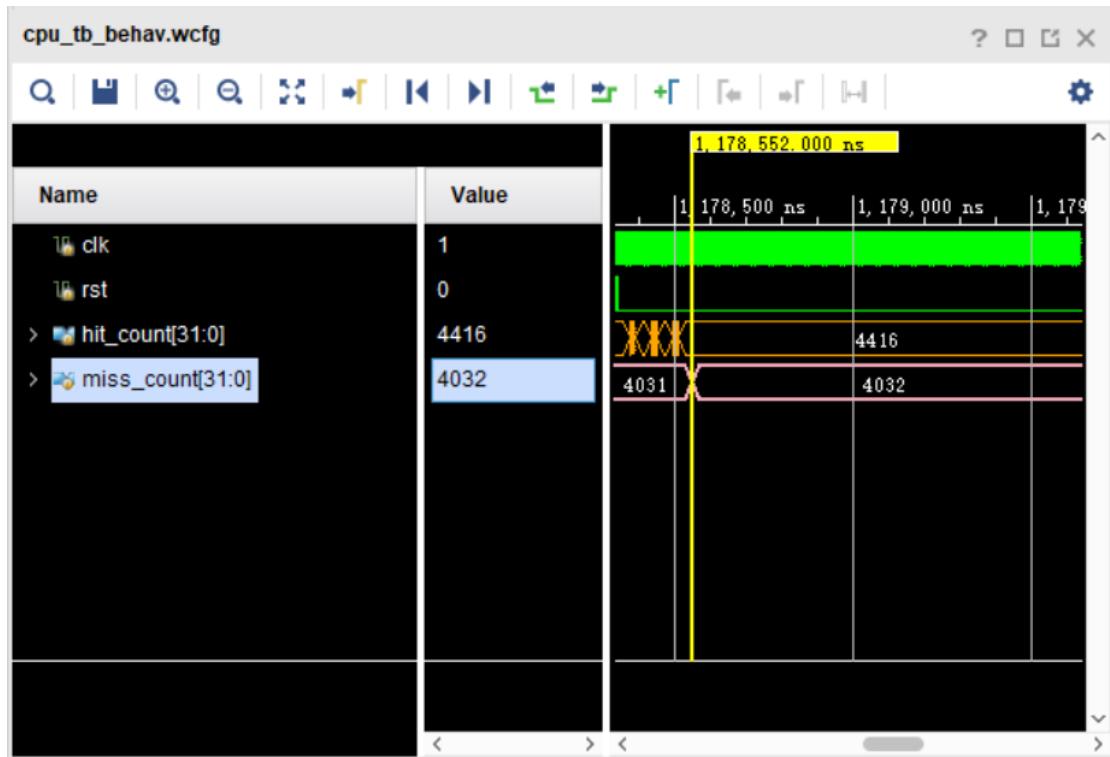


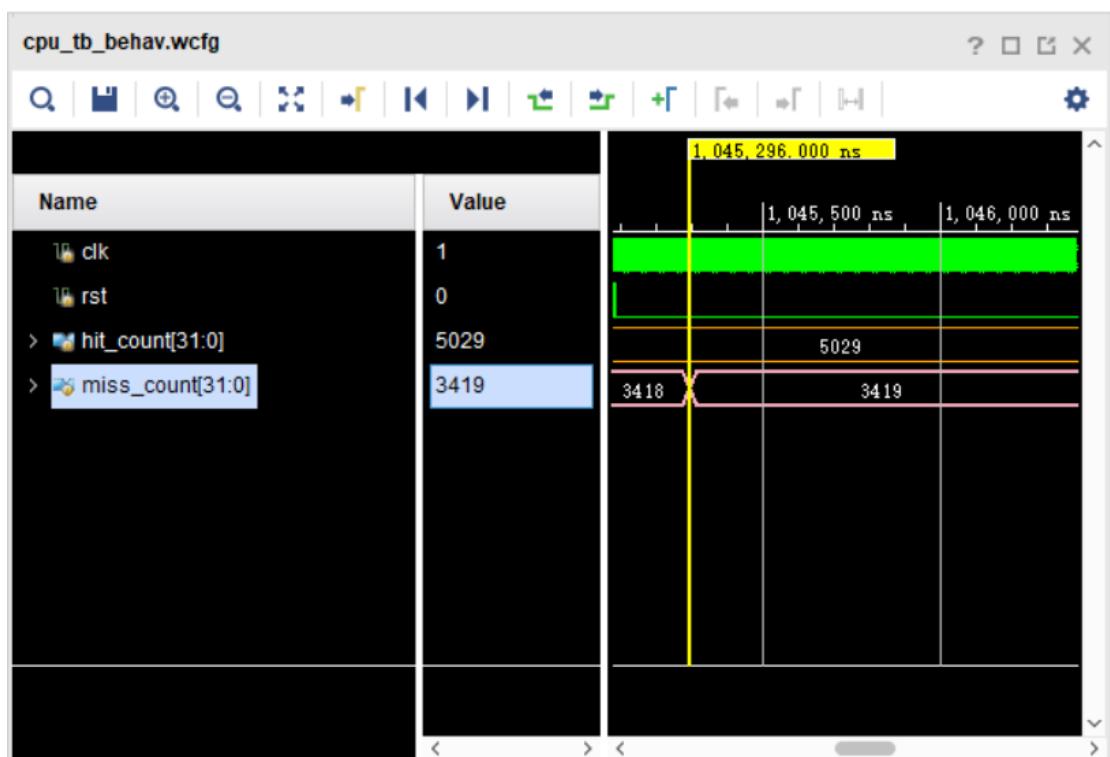
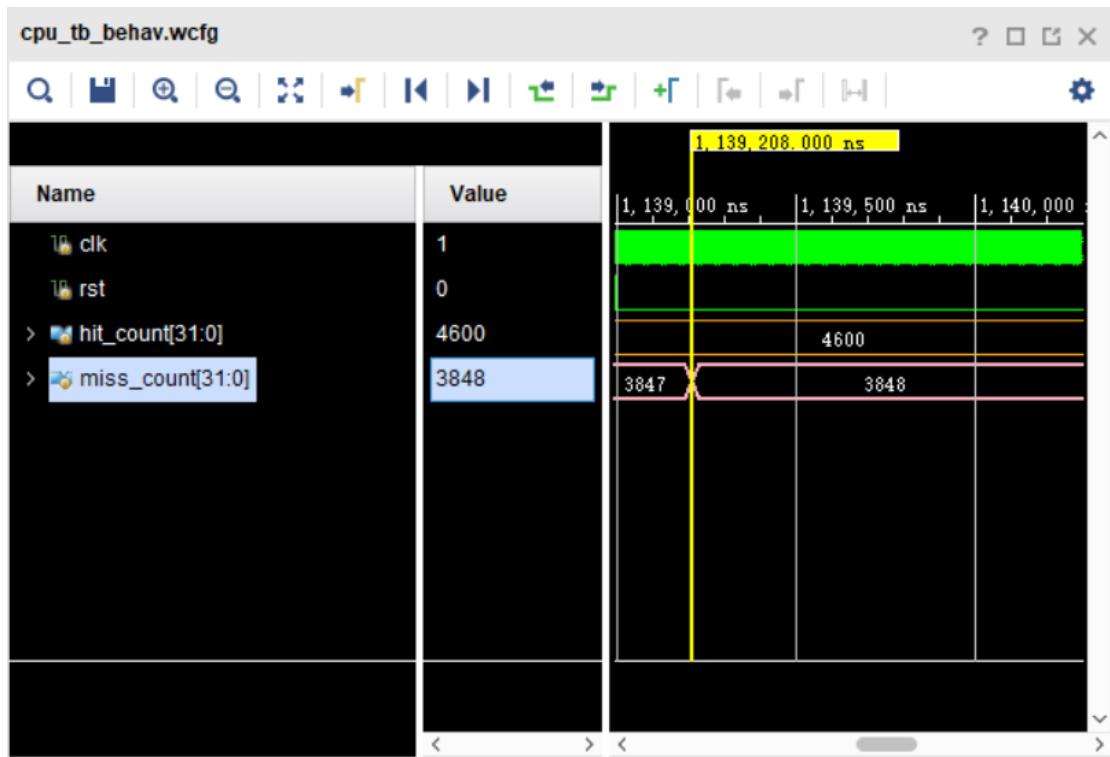


仿真数据
(16X16MATMUL、LRU)

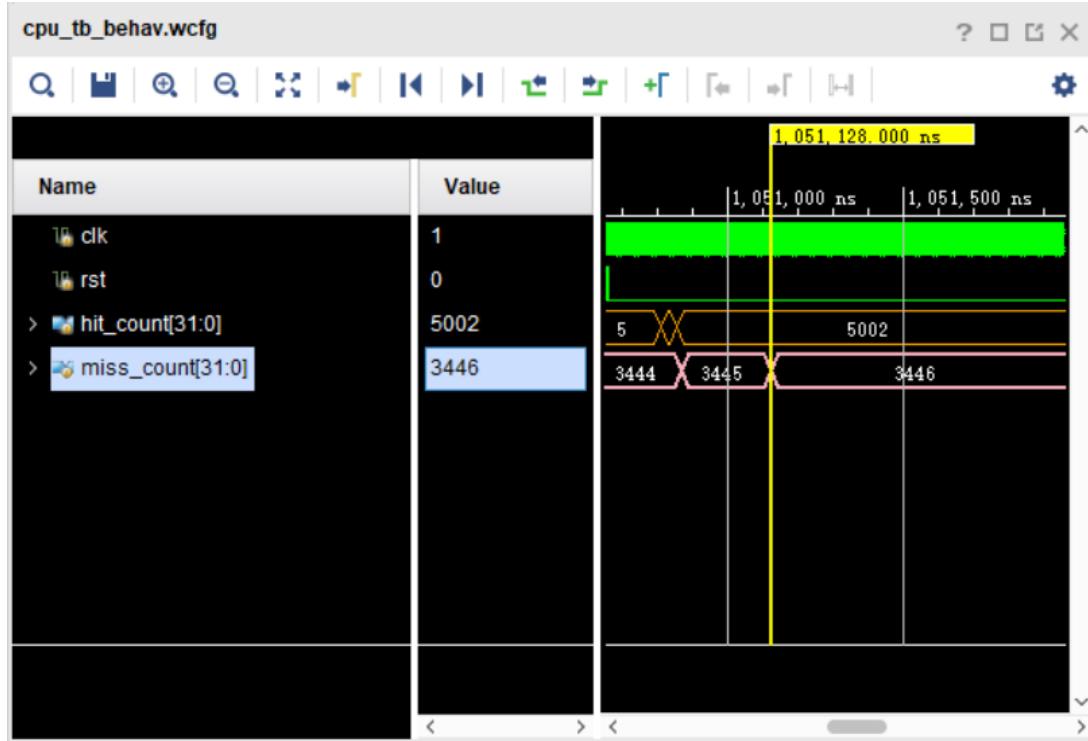
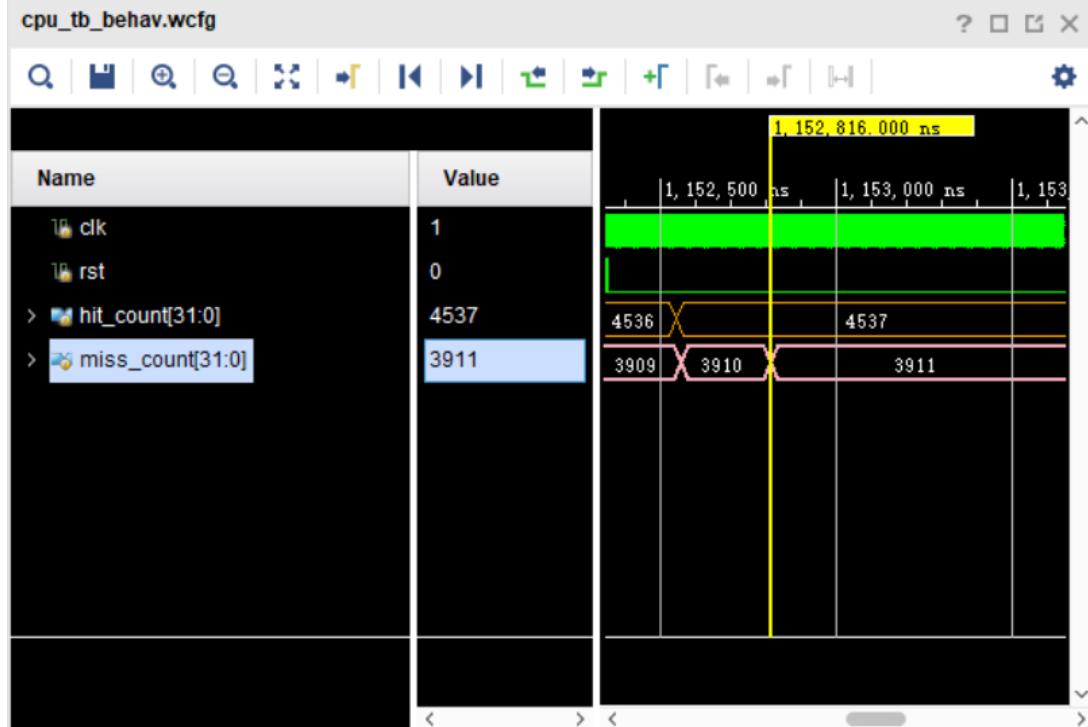
LINE_ADDR_LEN	2
SET_ADDR_LEN	2
TAG_ADDR_LEN	9
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

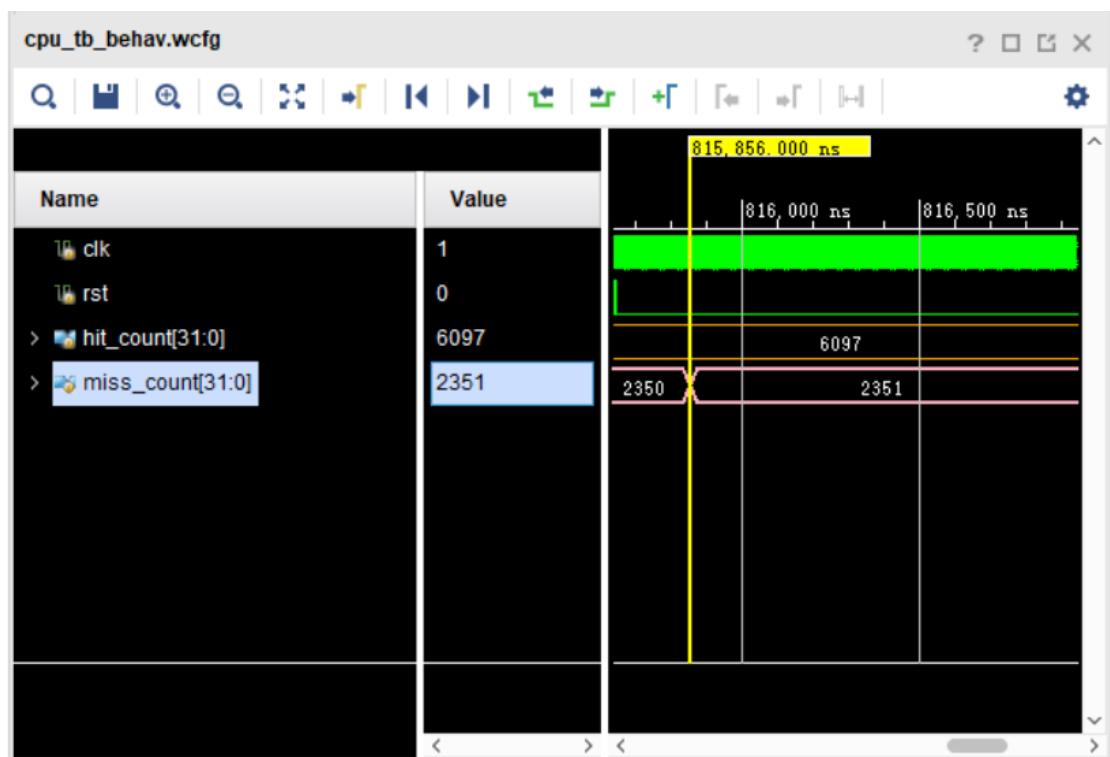
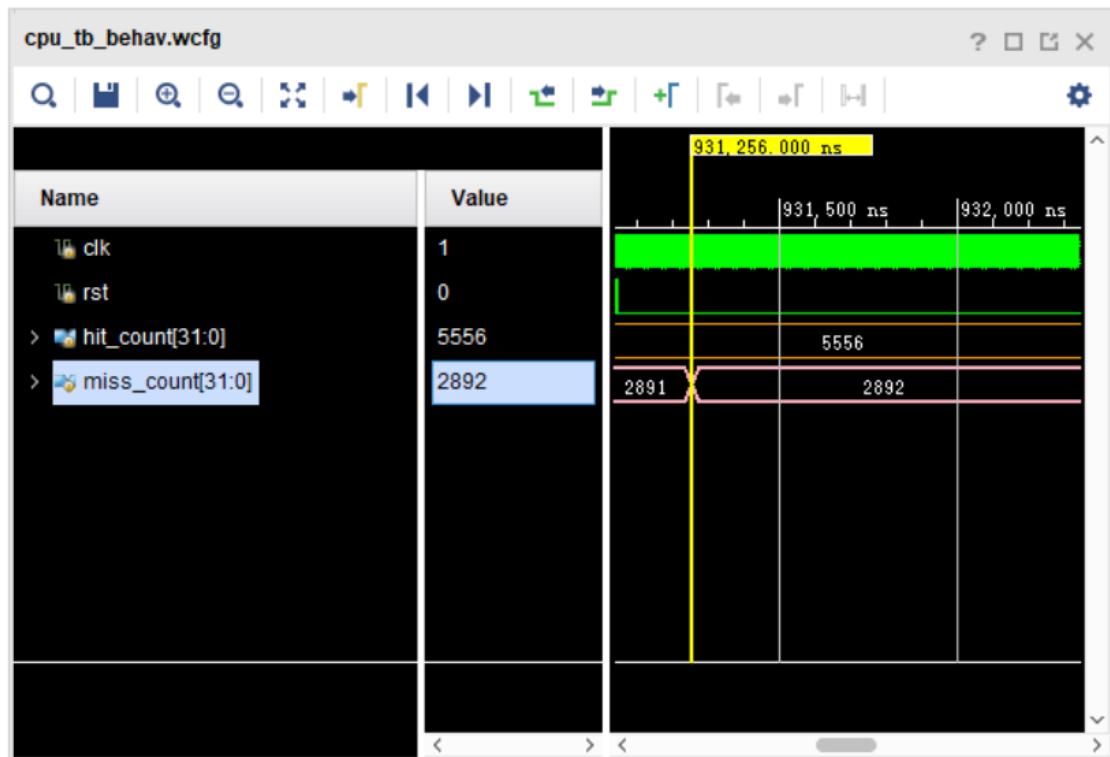


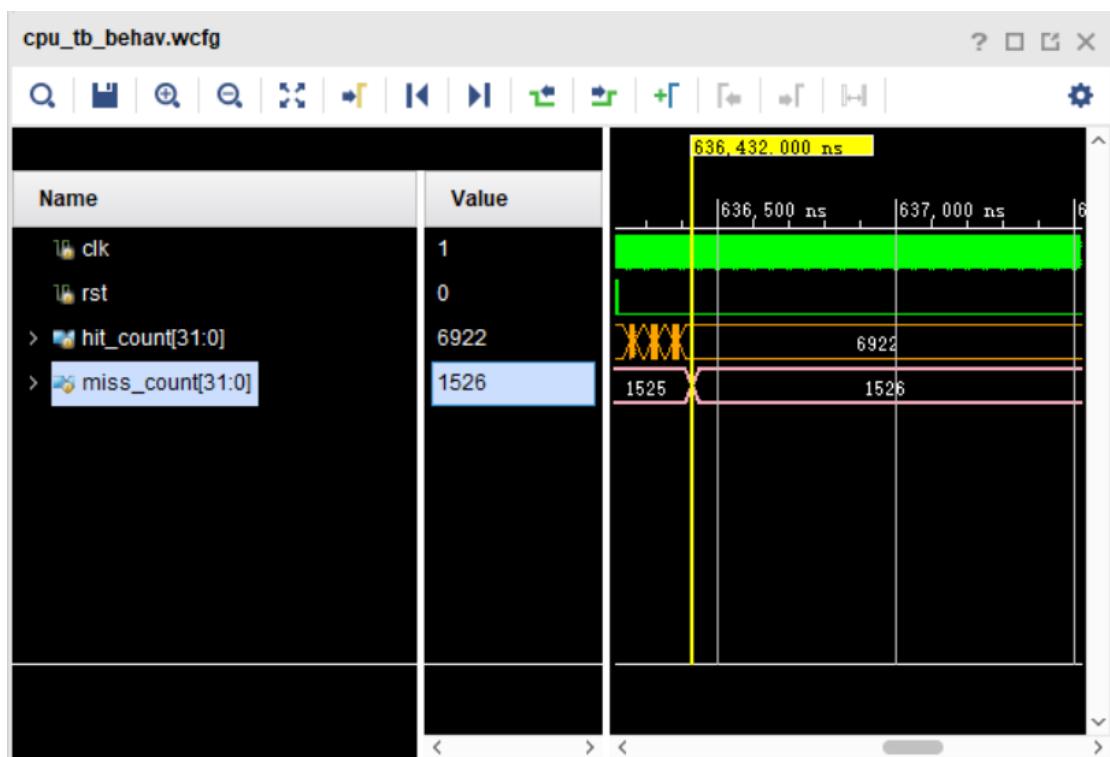
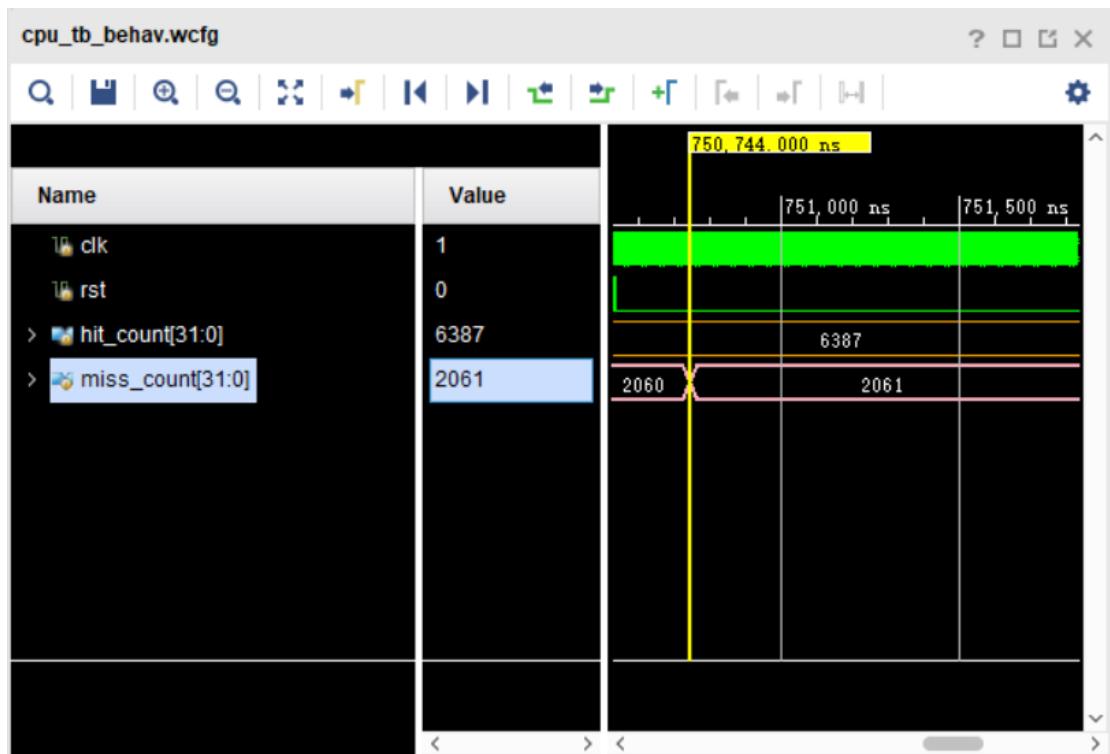




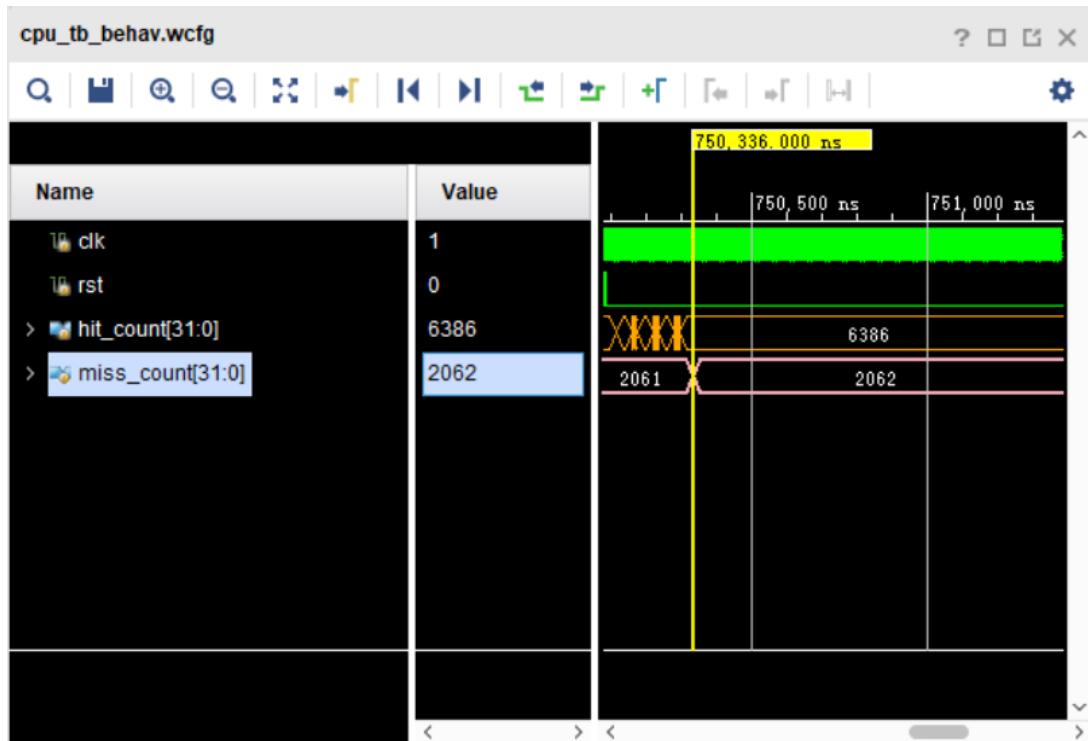
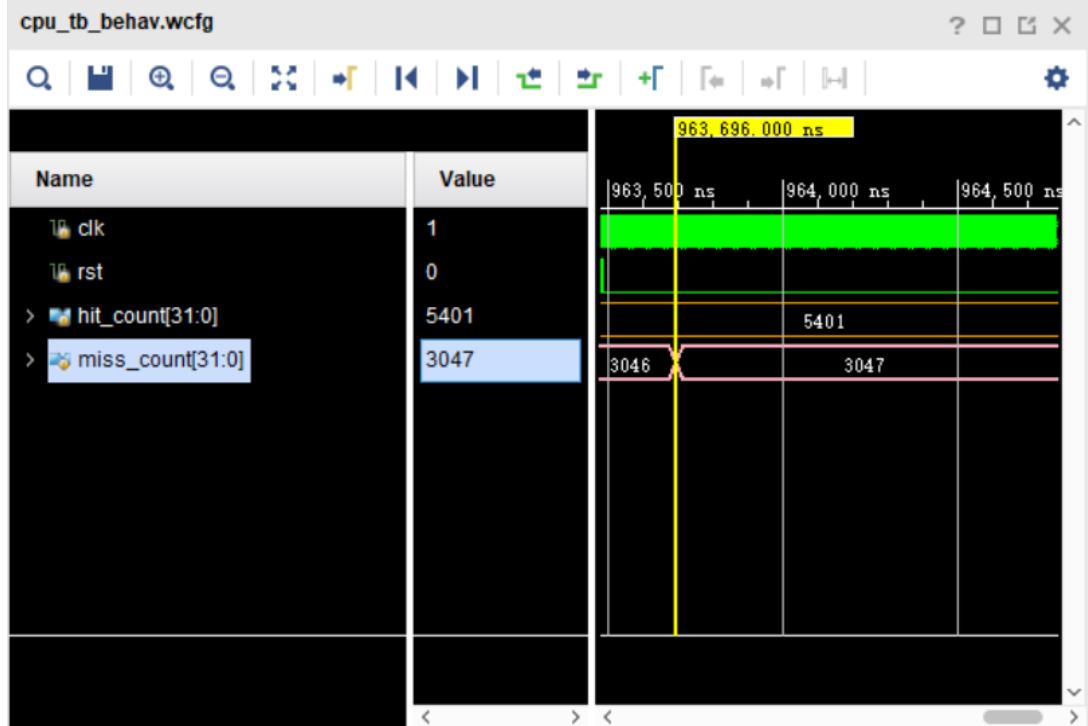
LINE_ADDR_LEN	2
SET_ADDR_LEN	3
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

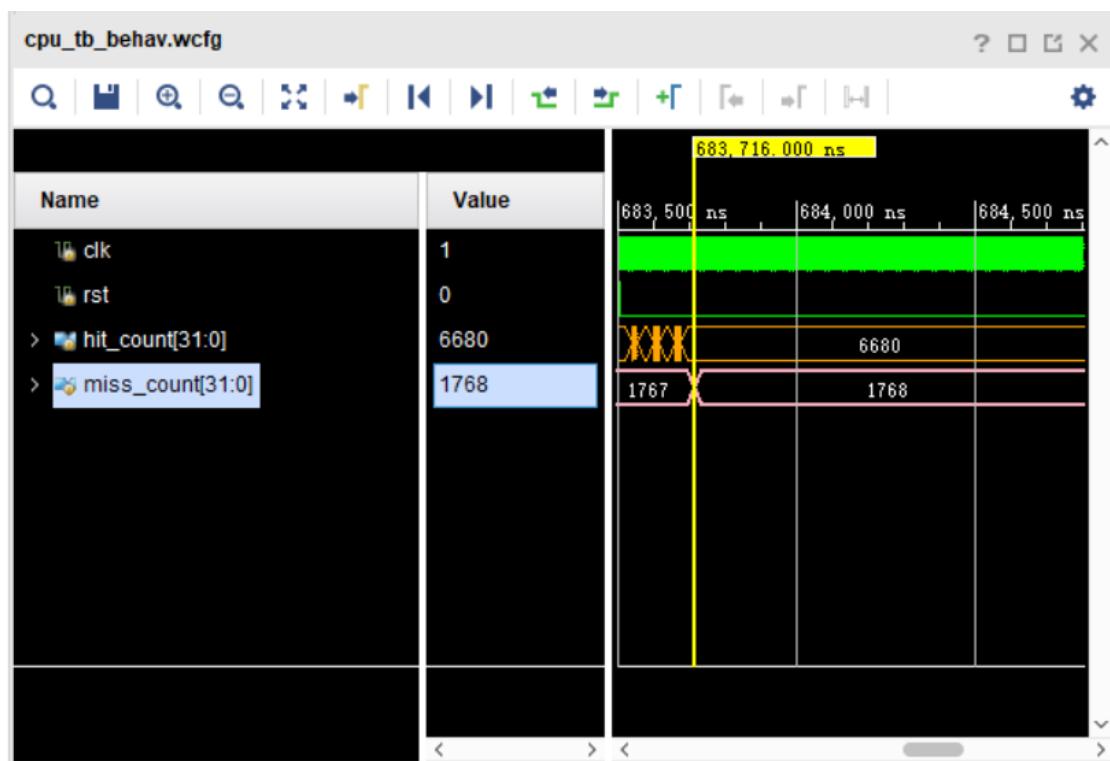
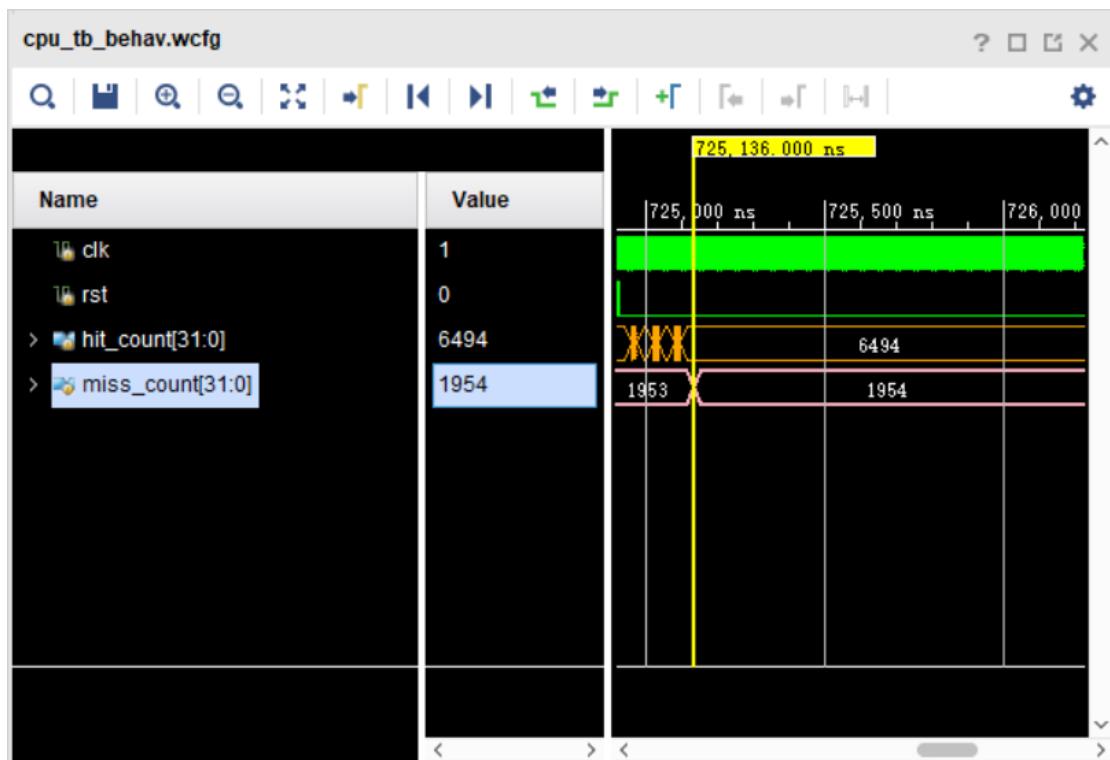


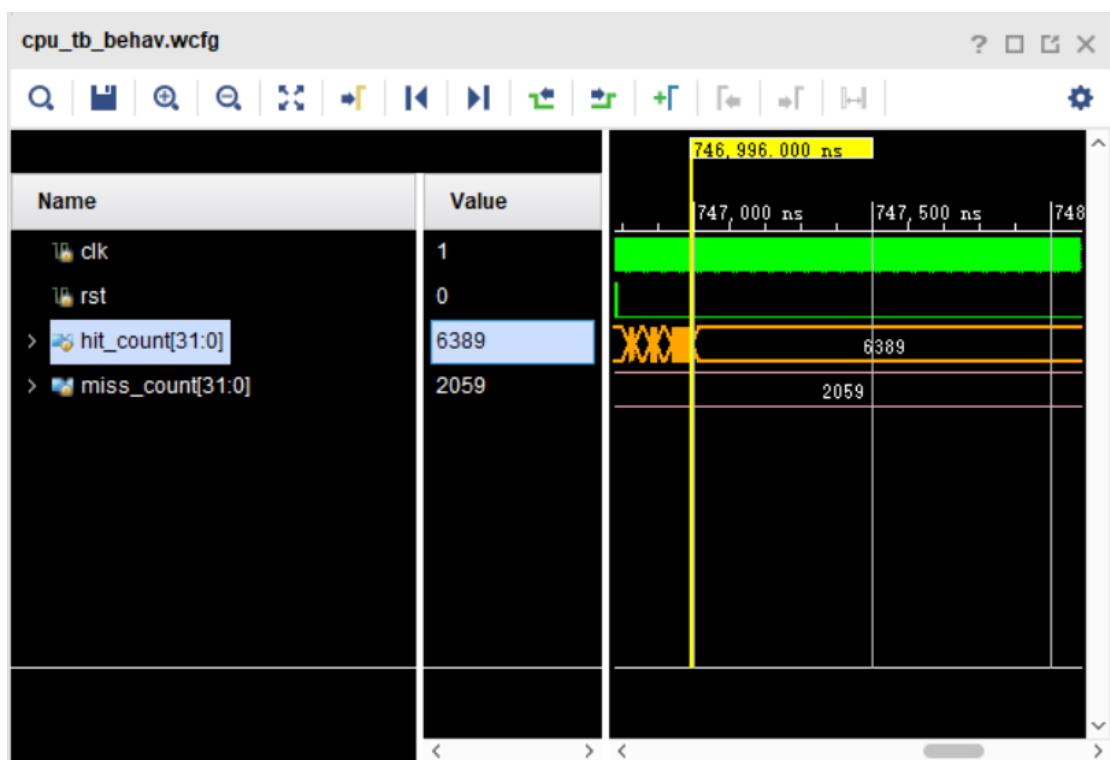
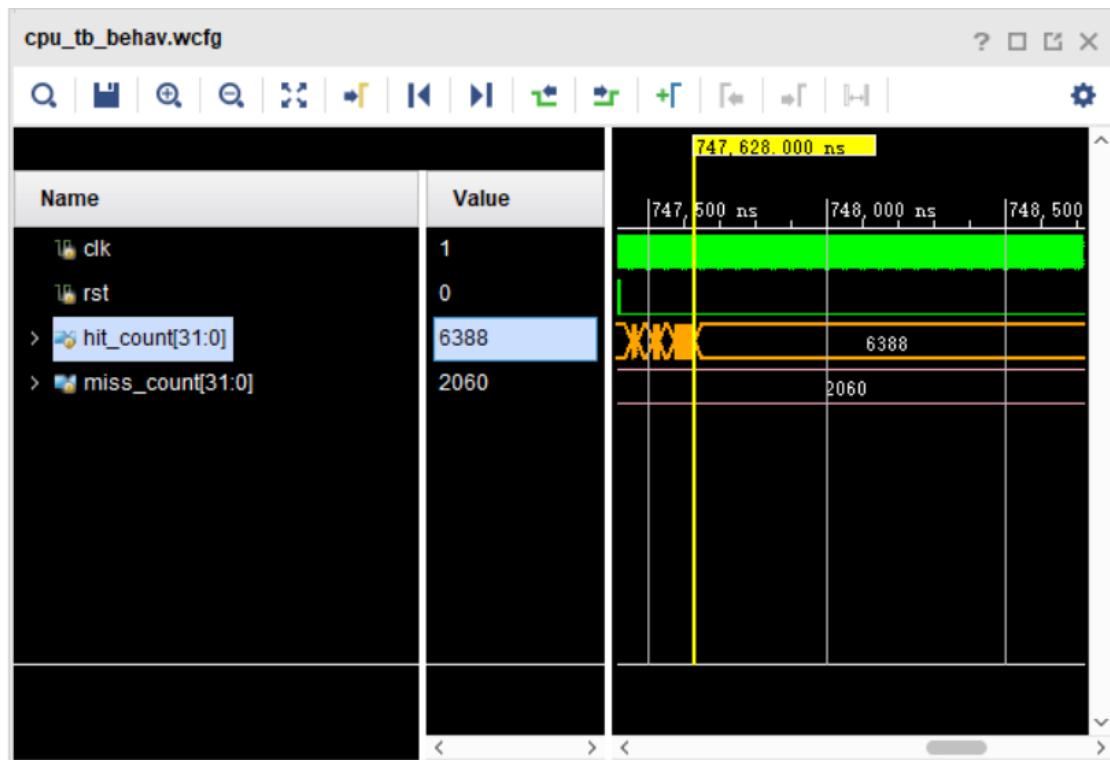




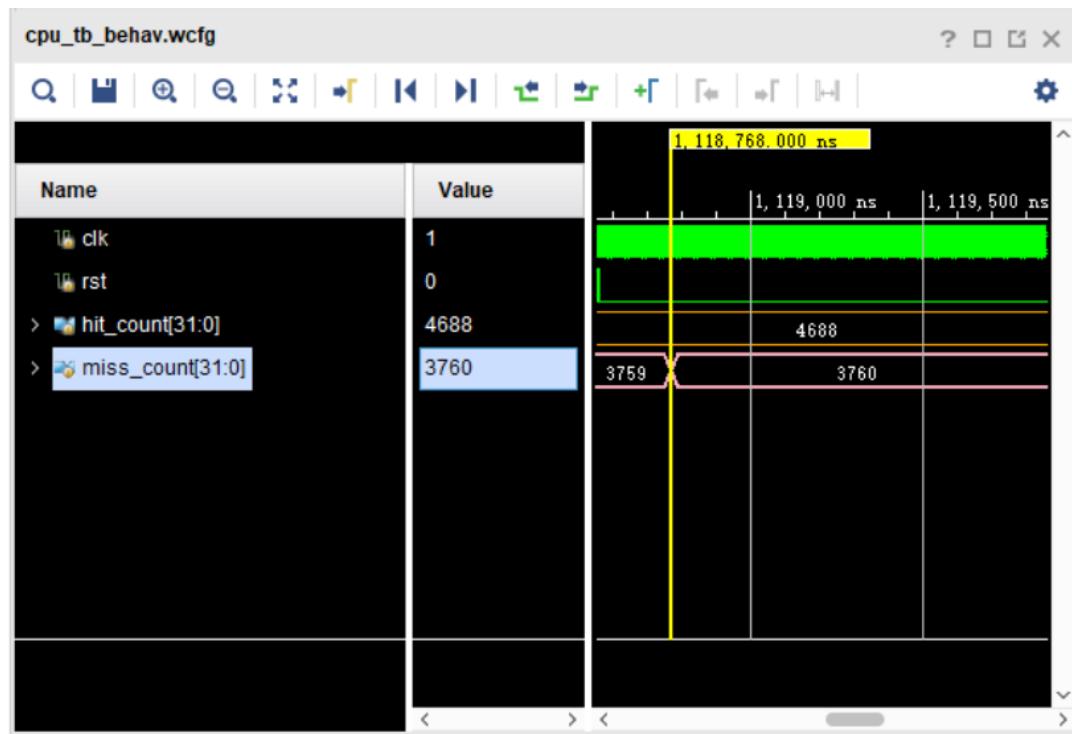
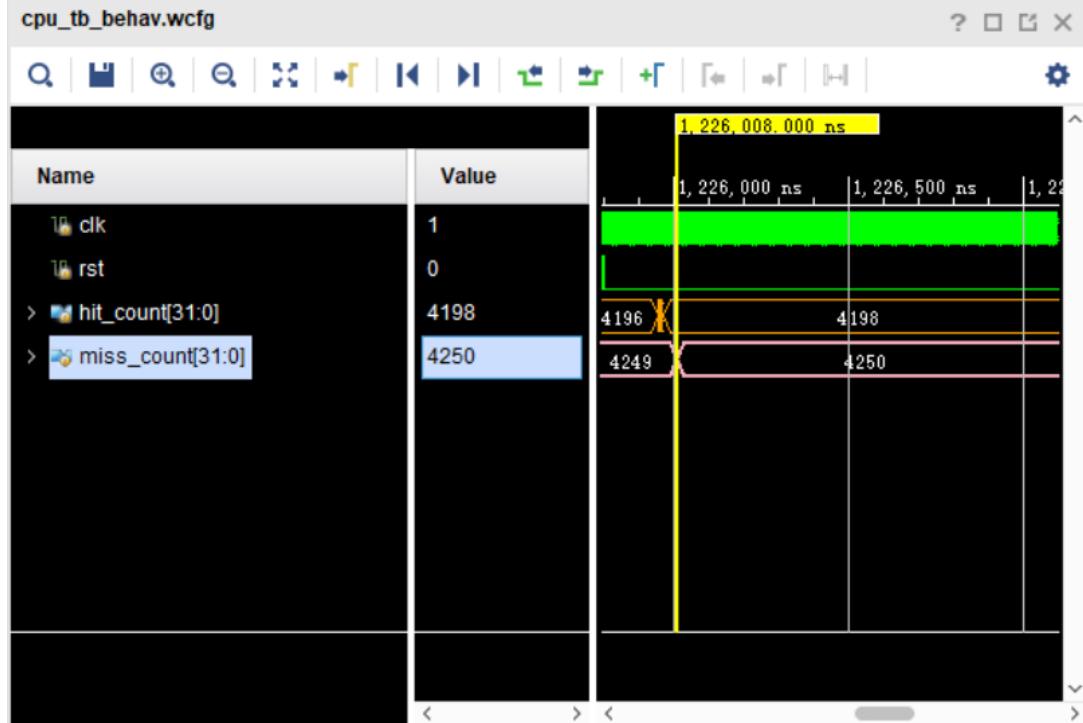
LINE_ADDR_LEN	2
SET_ADDR_LEN	4
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

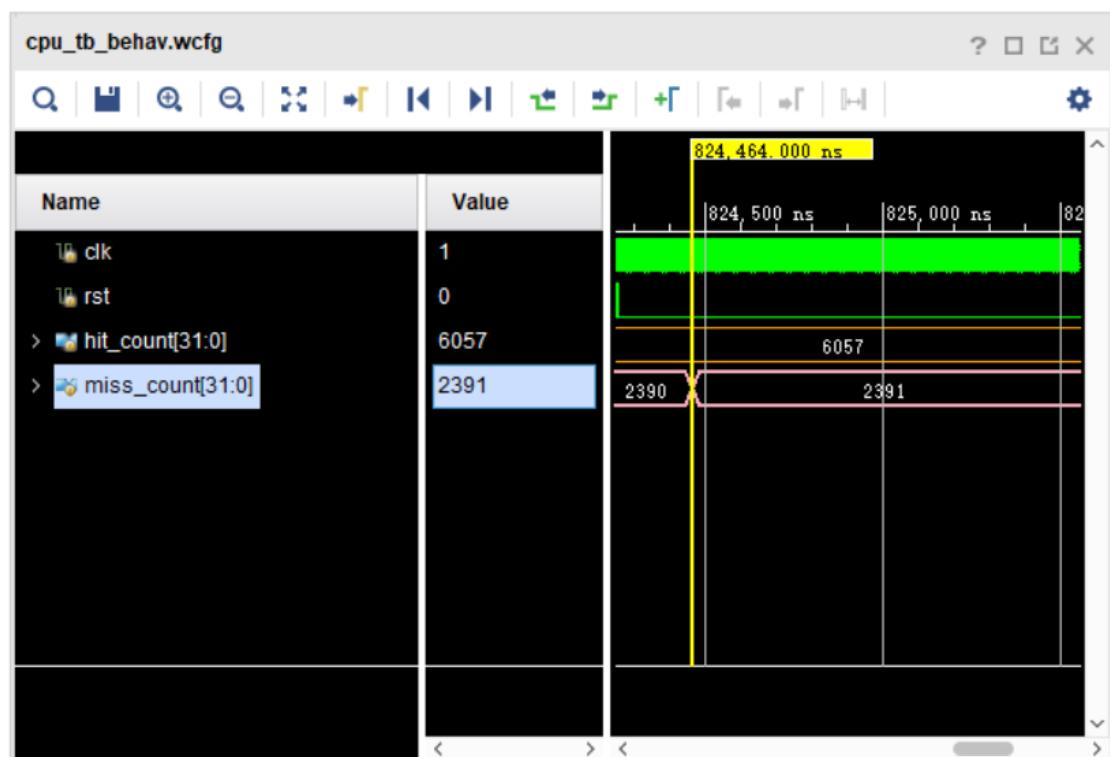
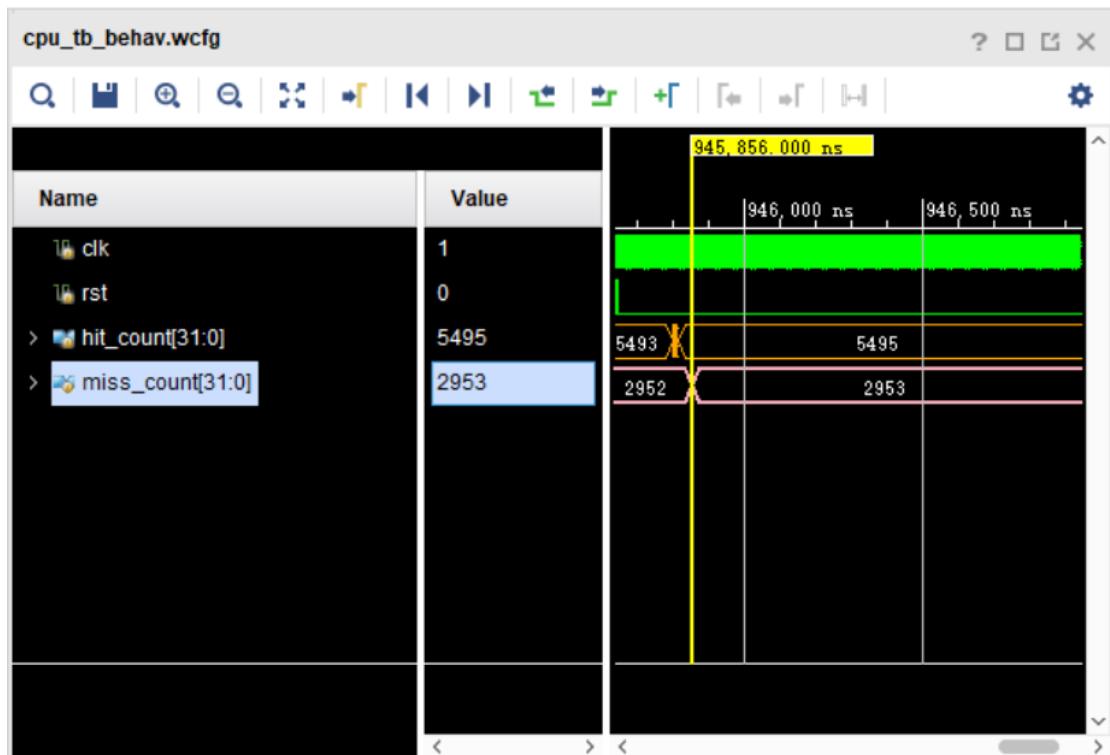


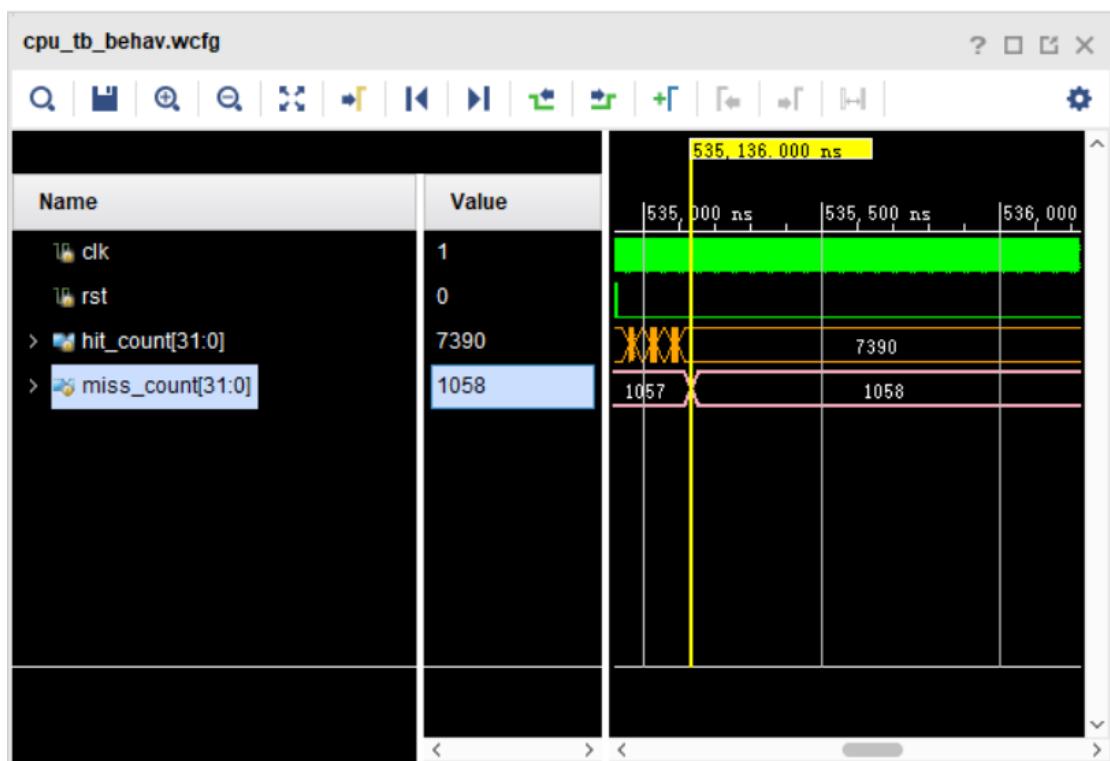
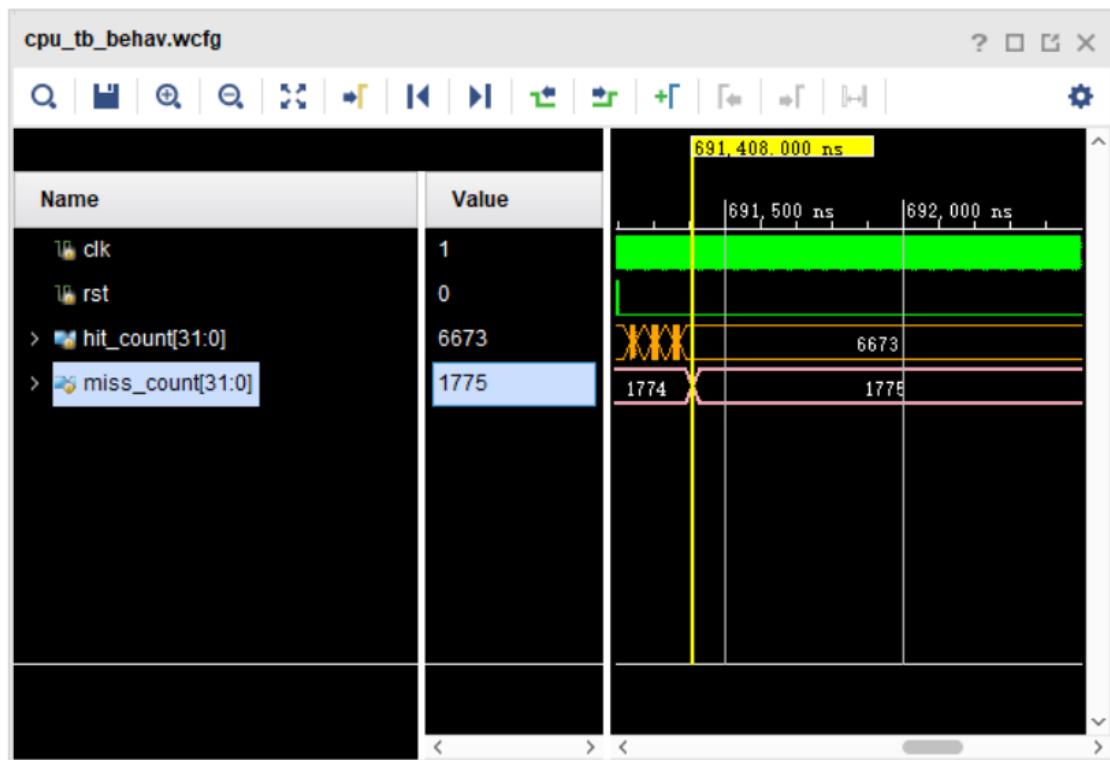




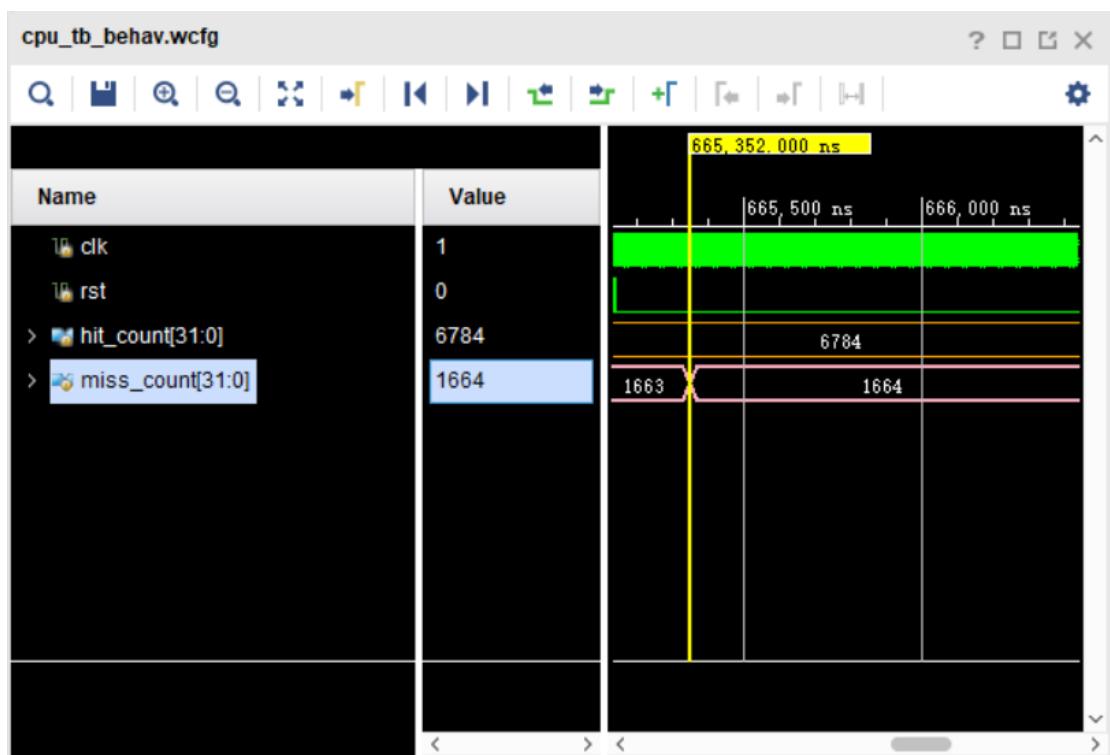
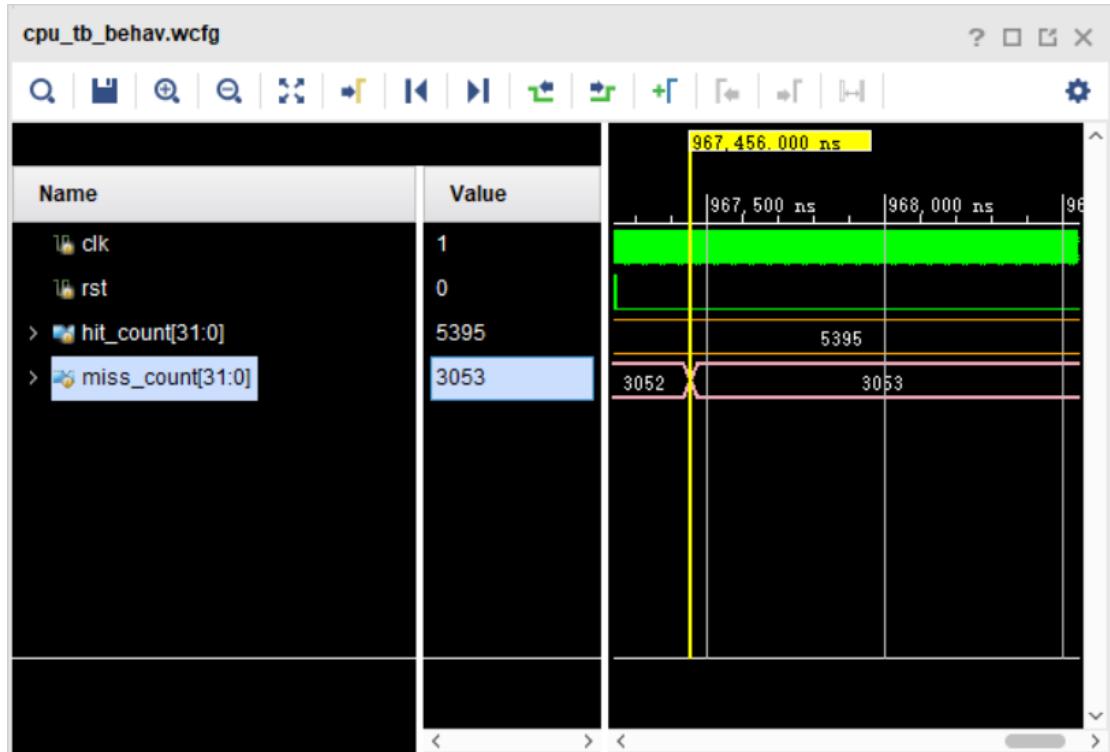
LINE_ADDR_LEN	3
SET_ADDR_LEN	2
TAG_ADDR_LEN	8
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

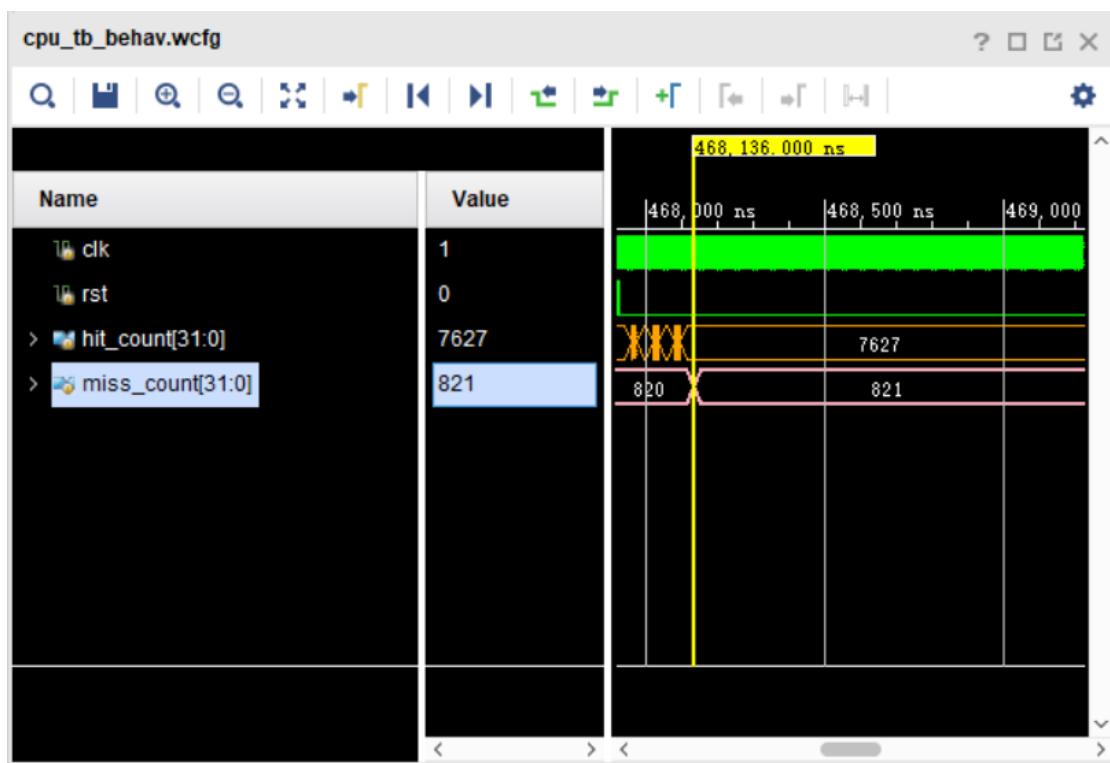
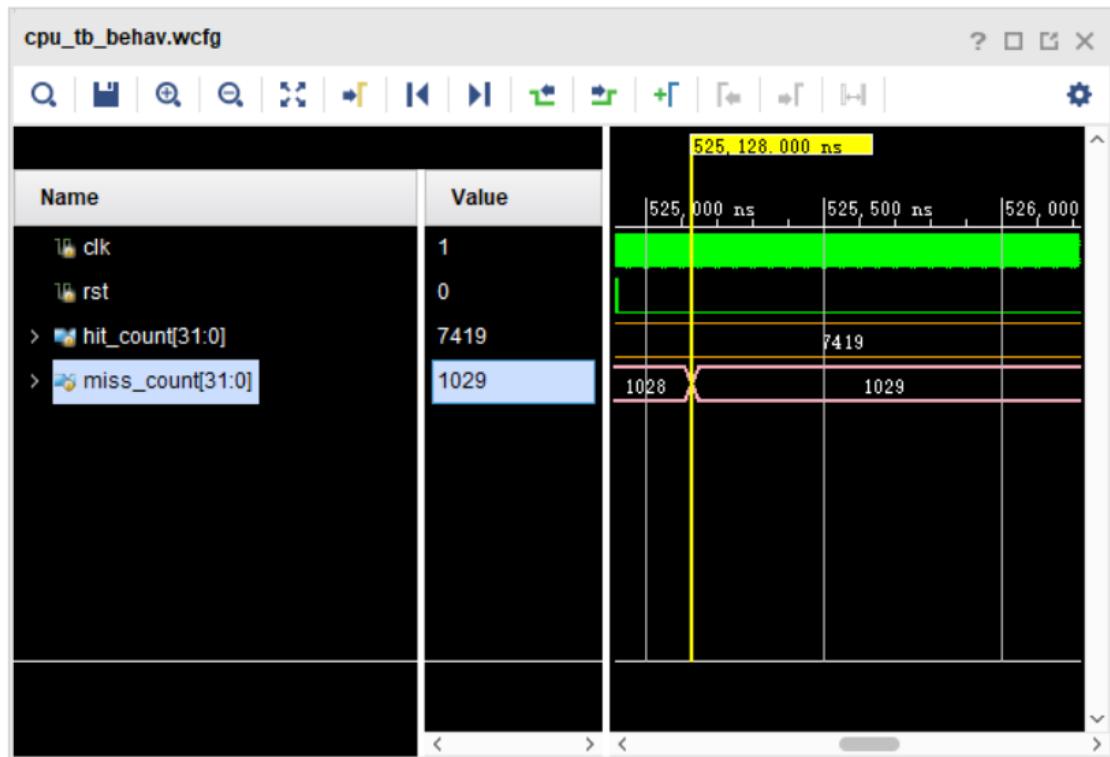


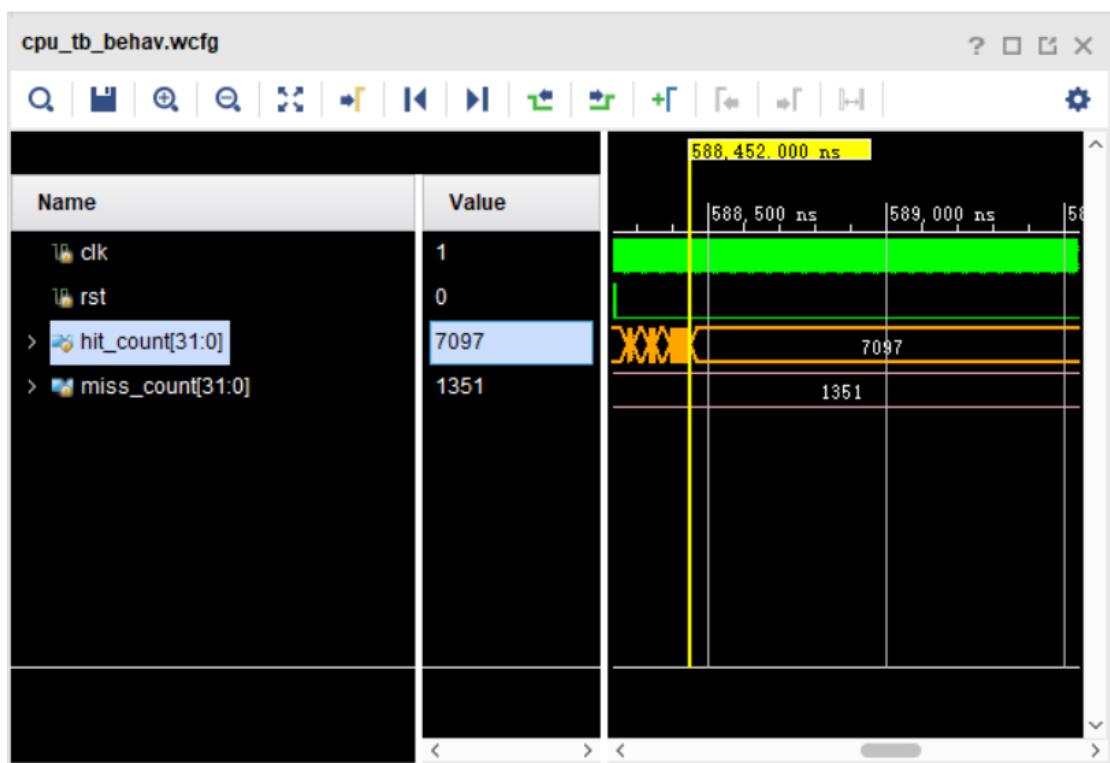
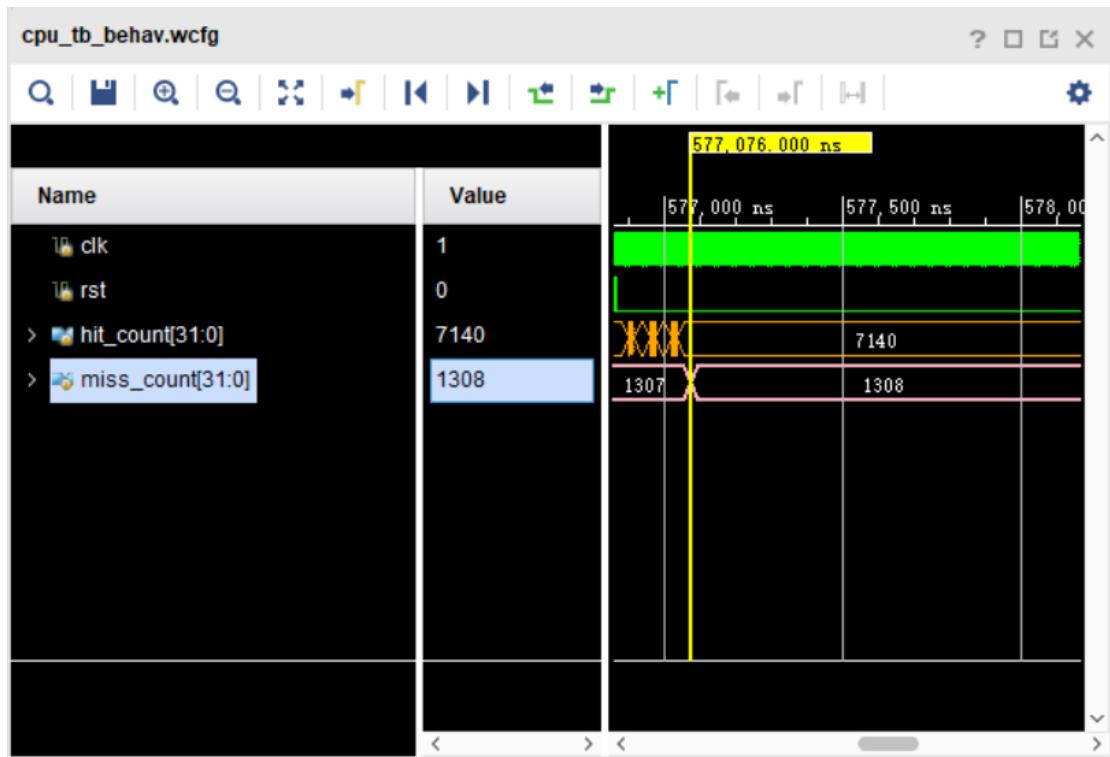




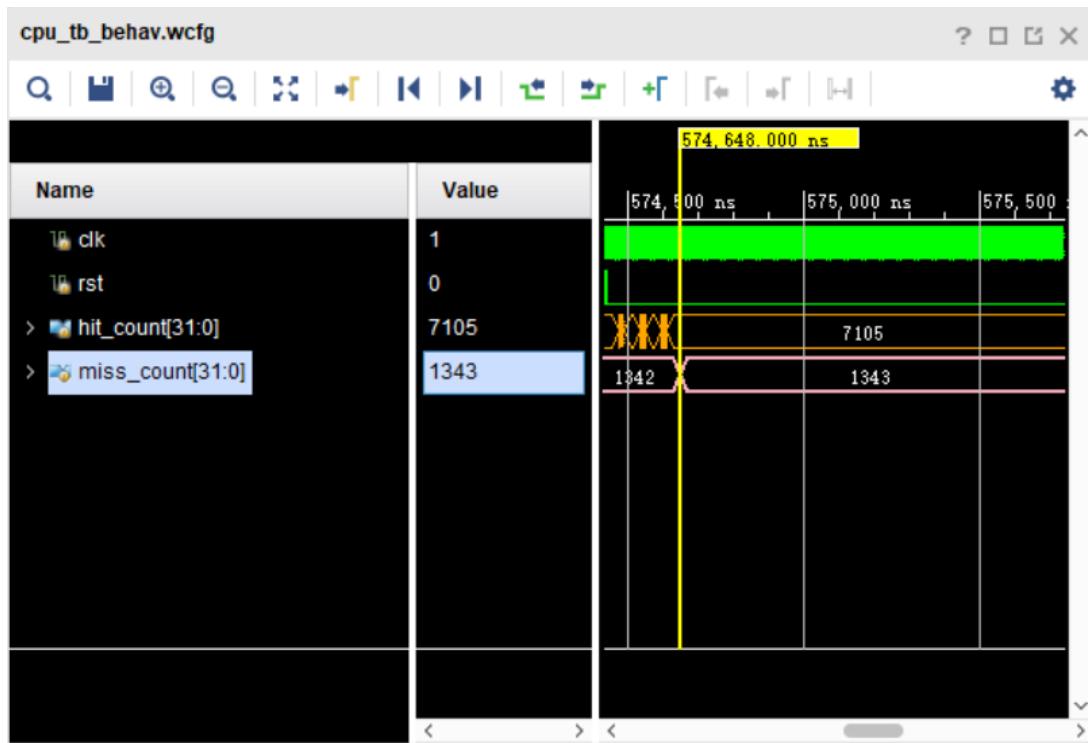
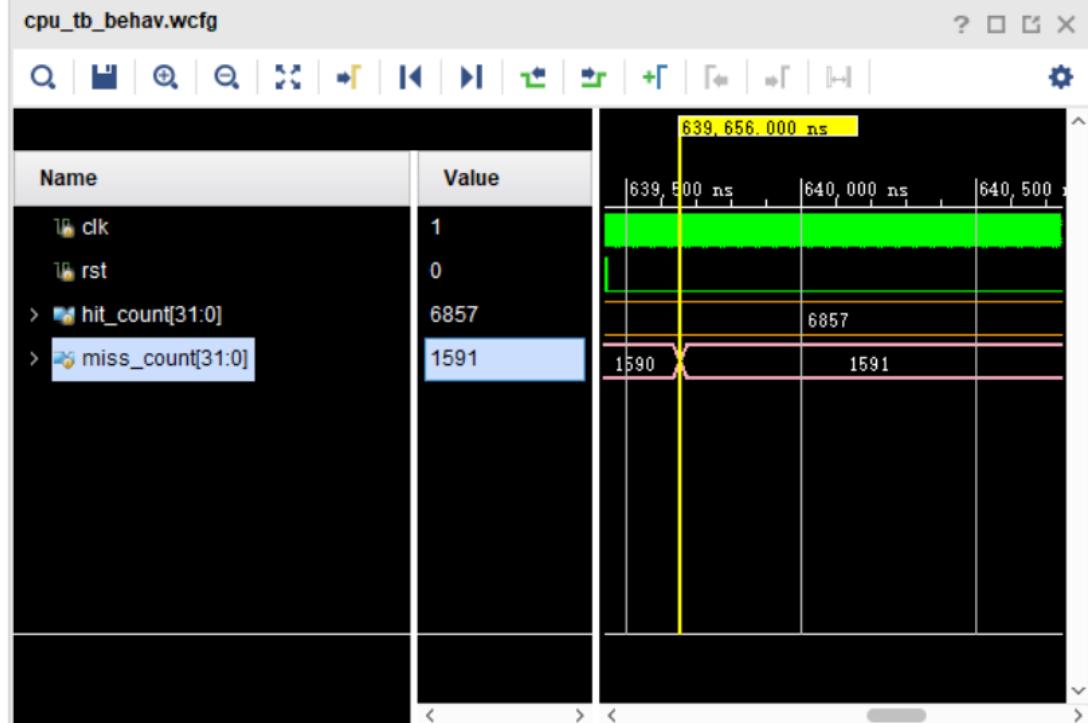
LINE_ADDR_LEN	3
SET_ADDR_LEN	3
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

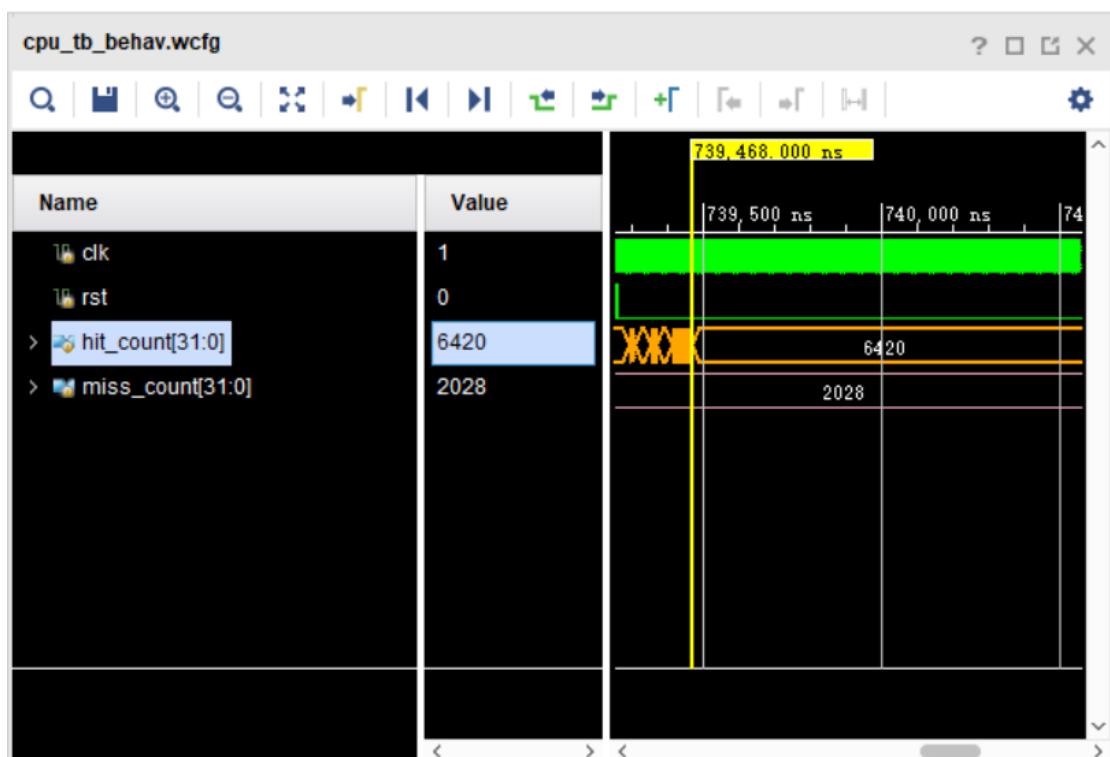
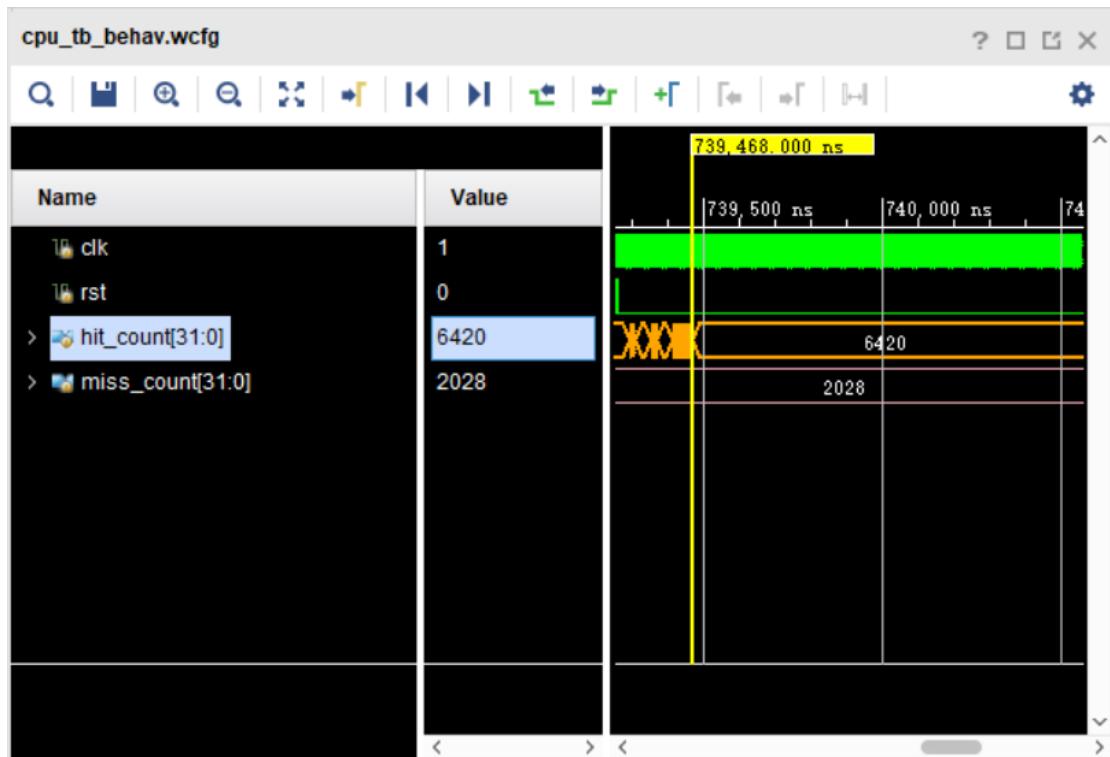


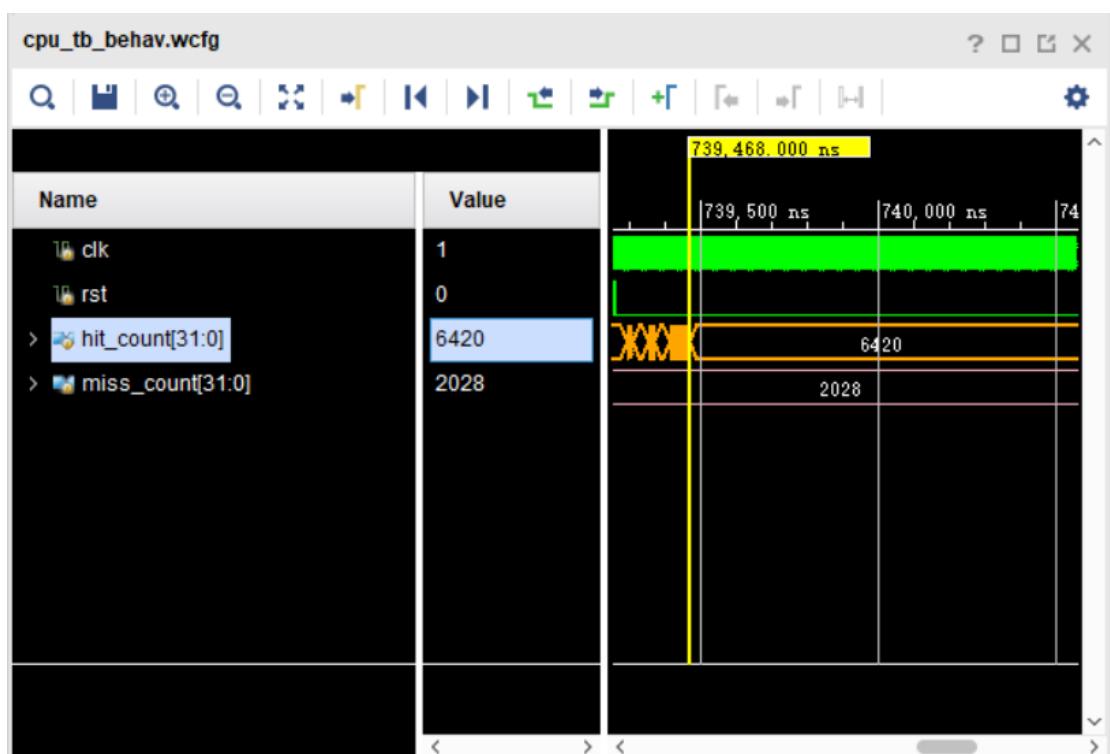
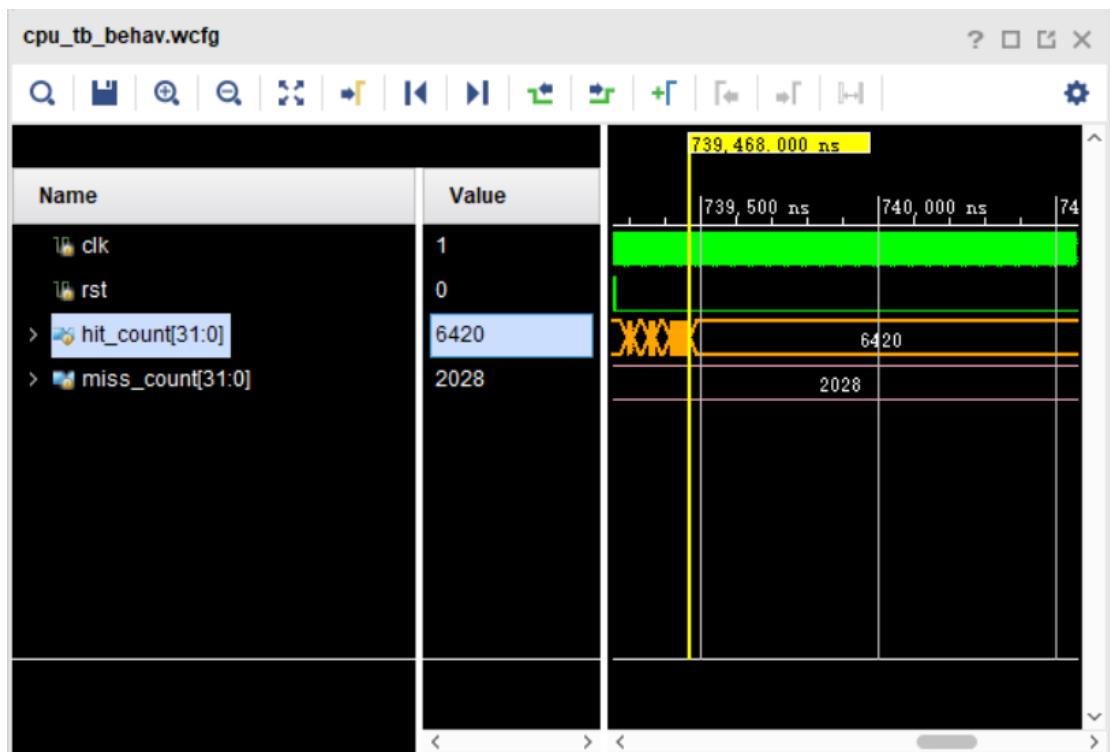




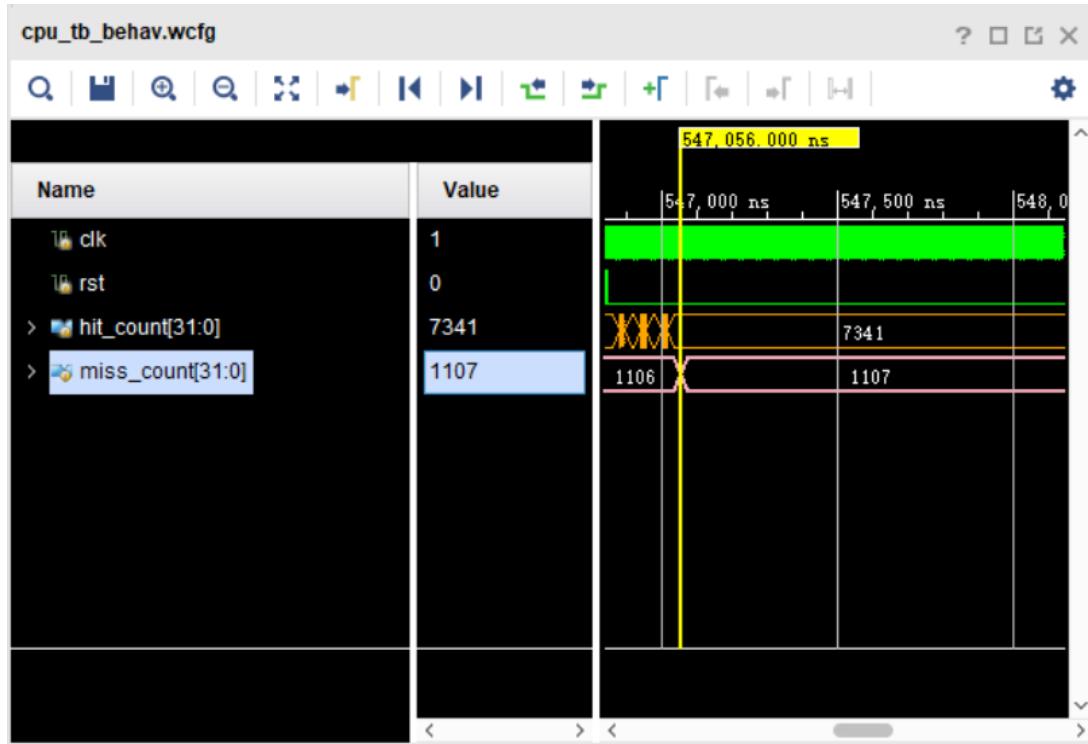
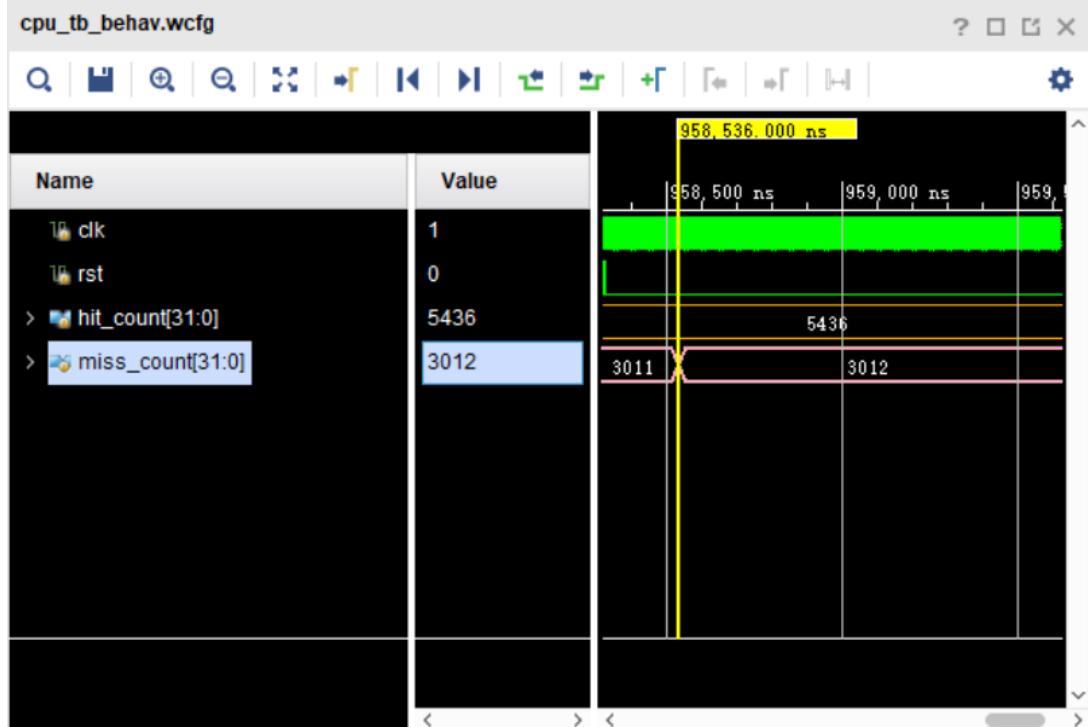
LINE_ADDR_LEN	3
SET_ADDR_LEN	4
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

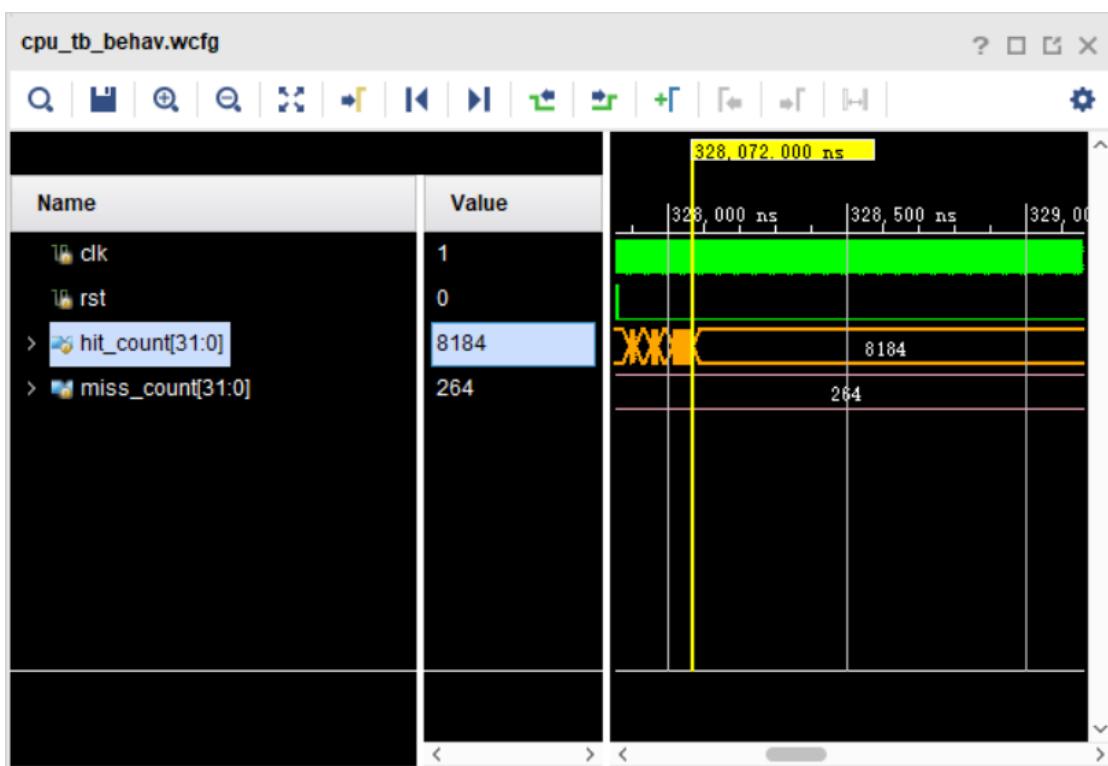
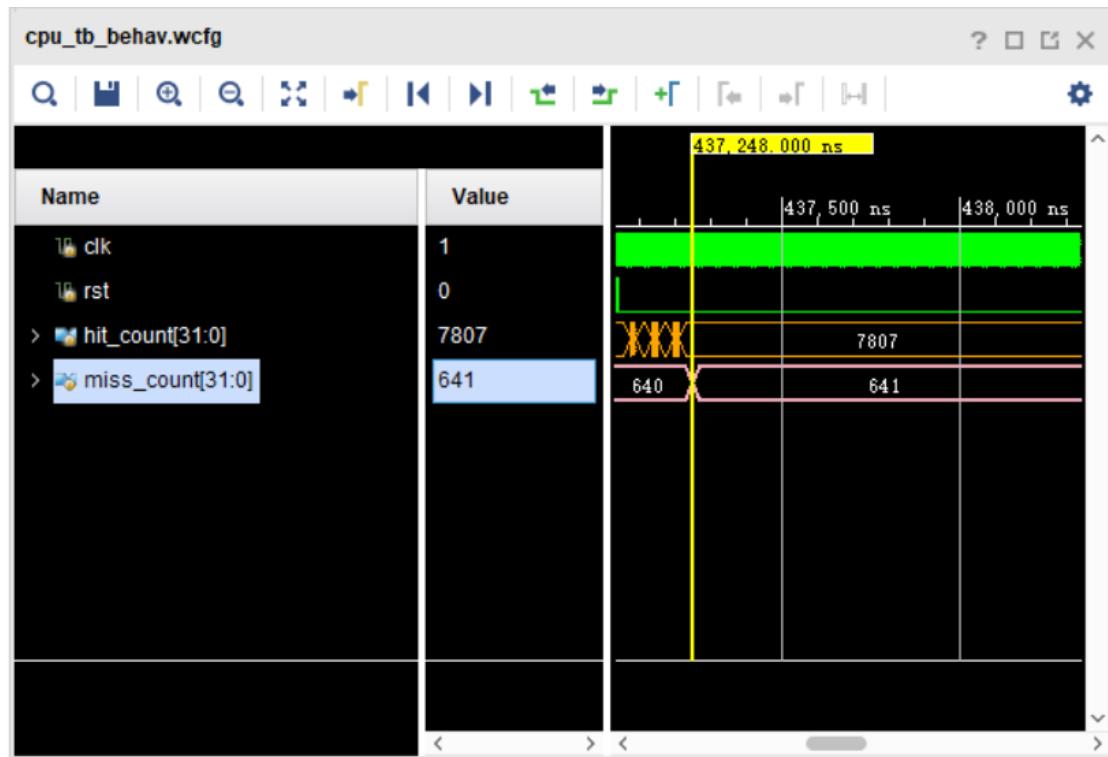


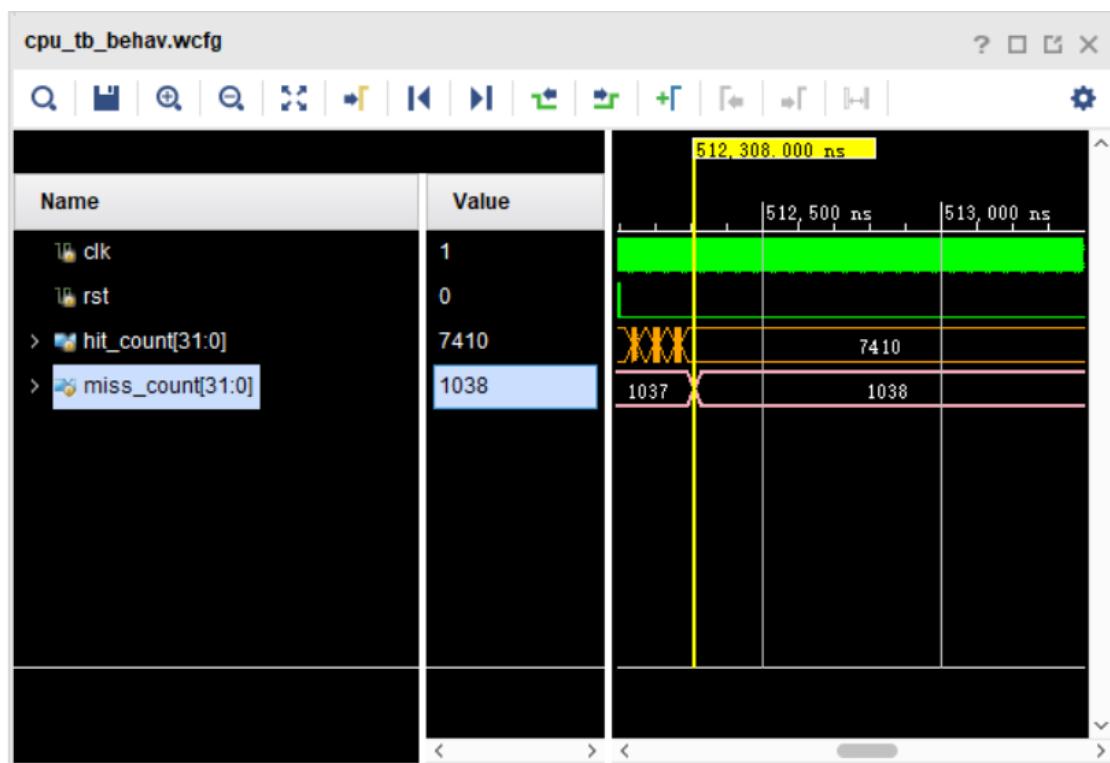
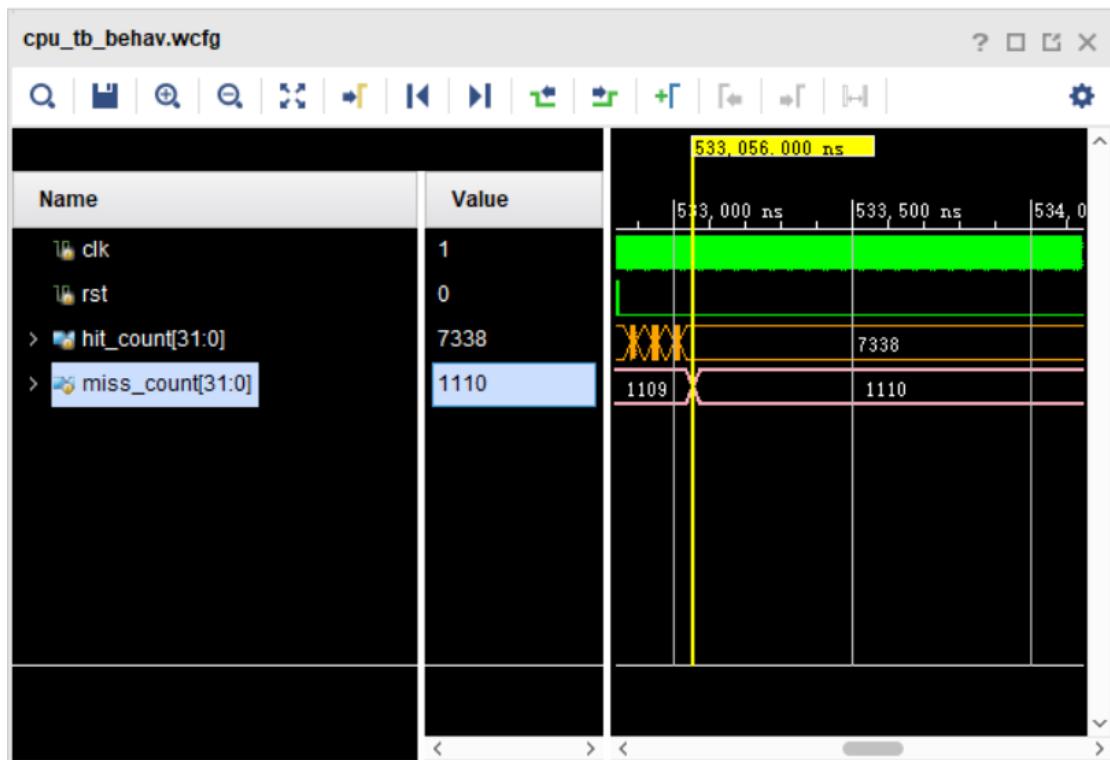




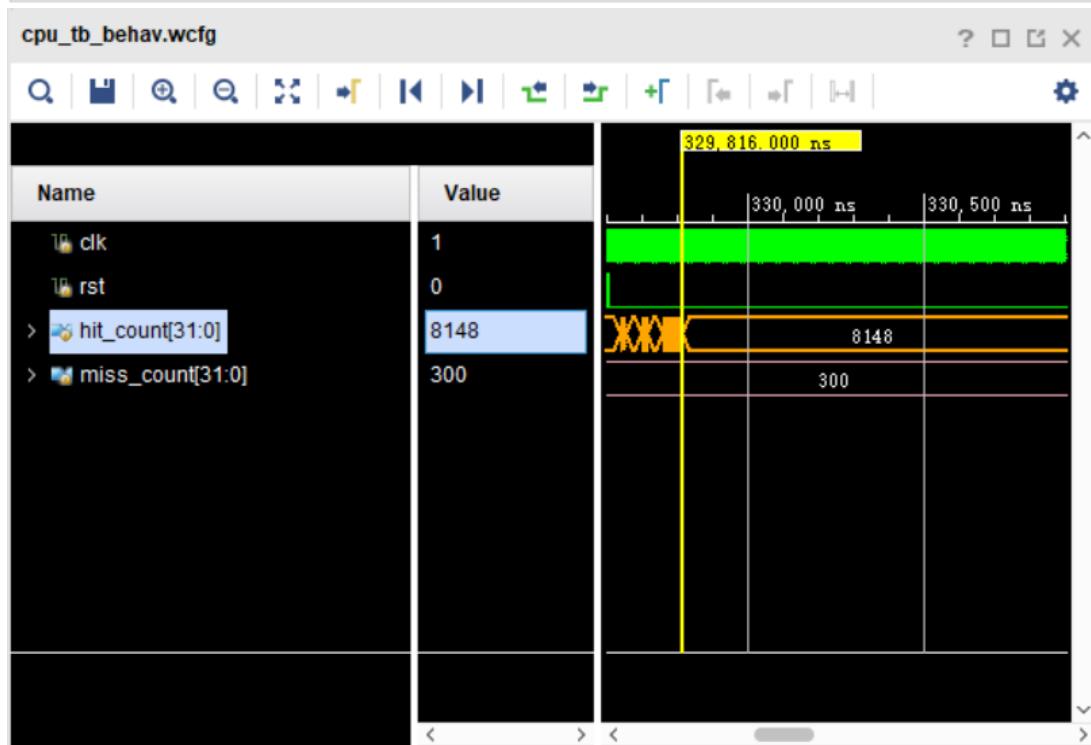
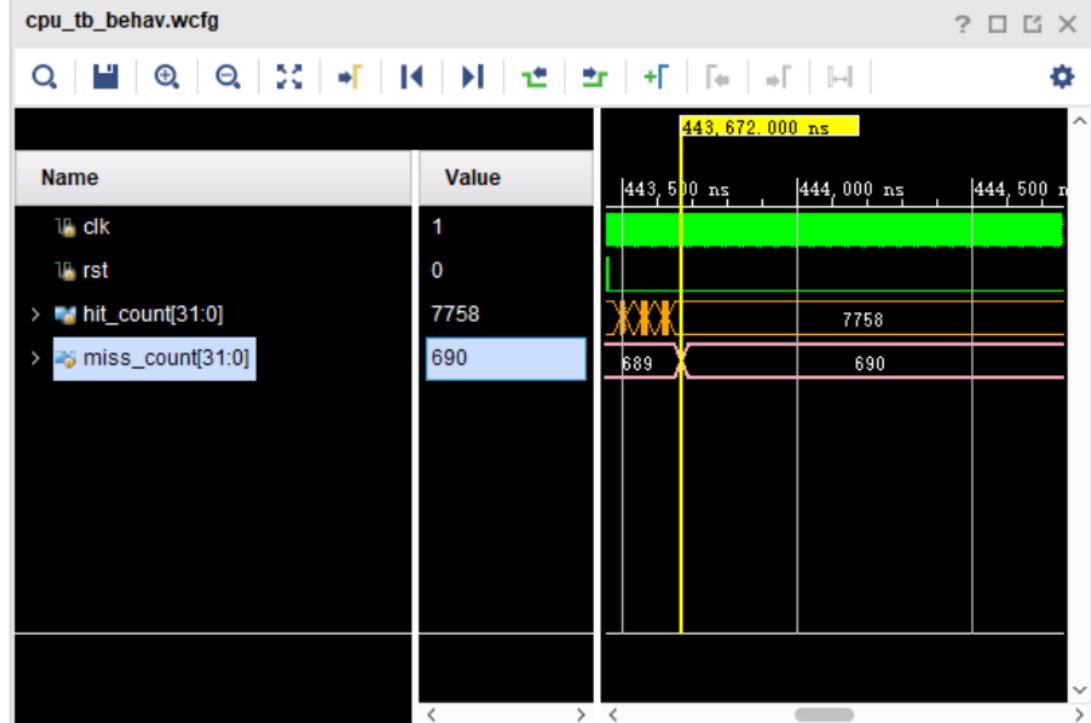
LINE_ADDR_LEN	4
SET_ADDR_LEN	2
TAG_ADDR_LEN	7
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

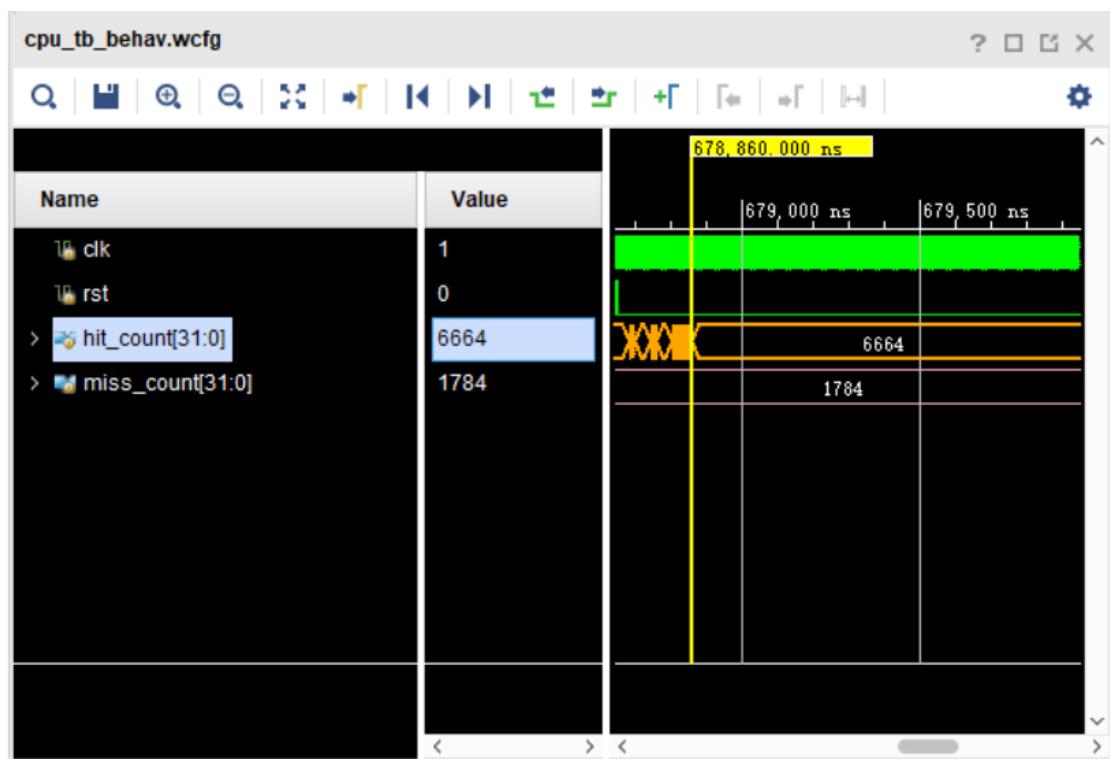
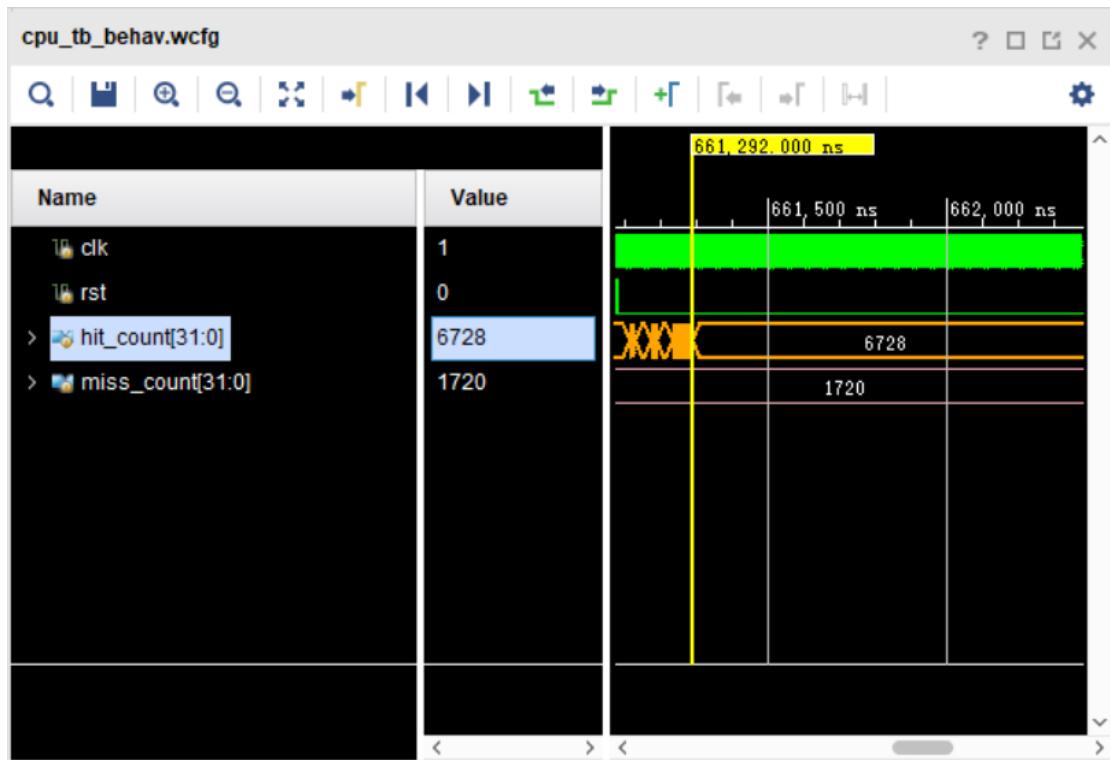


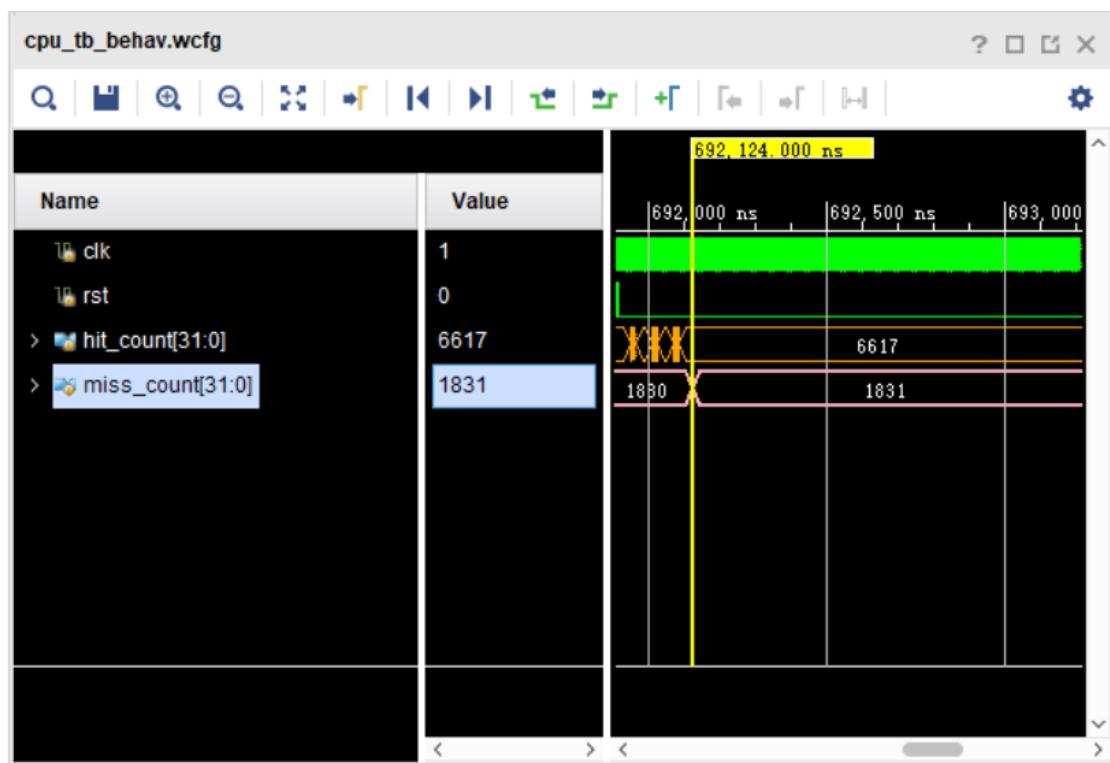
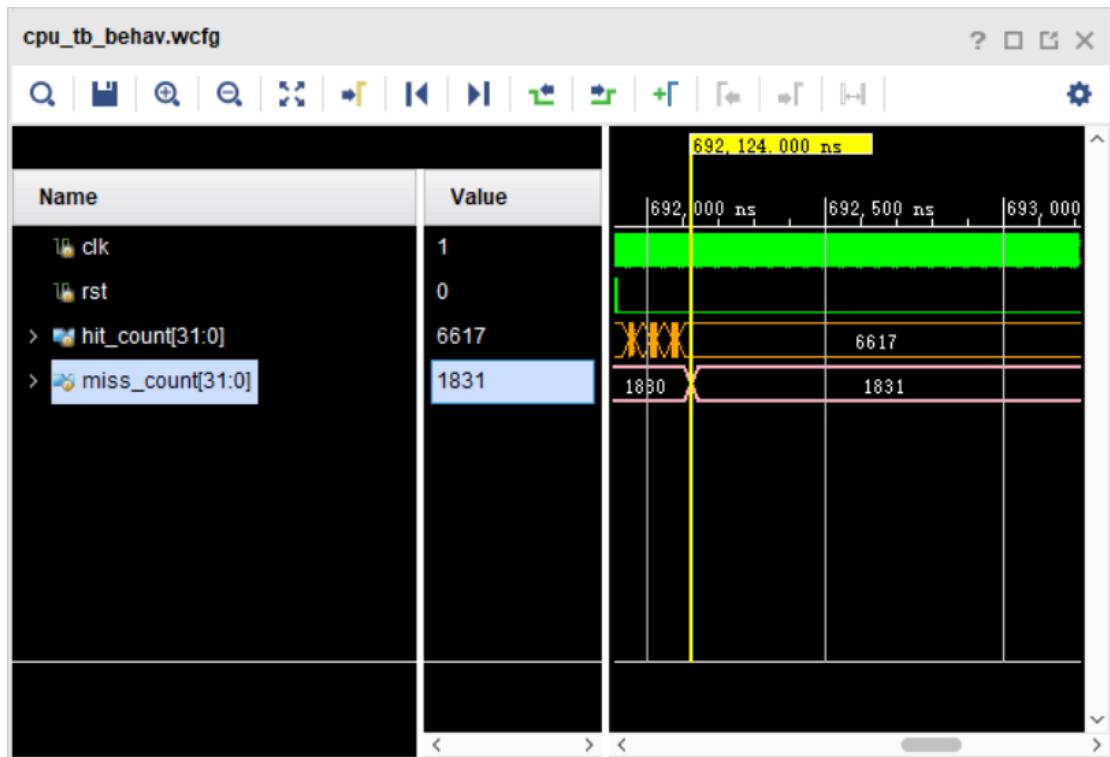




LINE_ADDR_LEN	4
SET_ADDR_LEN	3
TAG_ADDR_LEN	6
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL







LINE_ADDR_LEN	4
SET_ADDR_LEN	4
TAG_ADDR_LEN	5
WAYCNT	3, 4, 5, 6, 7, 8
Policy	LRU
Test File	MATMUL

