#### UNIT-IX

## Bridge

-> 21-18 a simplest from consist of a network of foral eregistance army borning a closed when with a desource of Charlet applied to two opposit sunctions and a charlent detector Connected to the other, two junctions, of shown intigues -> Boidge circuit are wed box measuring component such as RIL and C. Boildge court comparing the Value of an unknown component with that of an component, High according is arrivered by using bridgy

#### Type of Bridge

- 1) DC boildad -> med por washing susistance
- 2) Ac boildges. -> wed box neasure capacetance and inductance.

De boidges are => wheatstone boidge, kelvin boidge.

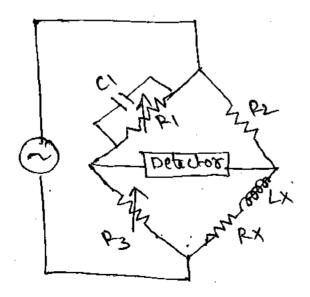
Ac boidges are somarwell's boidge

@Hay's boidge

3)Anderson boilage

g scheving boidge Swein boidge

Bridge: CInductana Capacitana bridge) DHaxwel<u>k</u>



-> It weather outlown inductance intering of a known capacietos.

-> the oughstor Ris boardles much of oug hence Pt 13 easy to vosite balance equation wing asm 1.

```
Boidge bolonaing condition 18
   \frac{2}{12}x^{2} = \frac{2}{12}x^{2}; \frac{2}{12}x^{2} = \frac{2}{12}x^{2}; \frac{2}{12}x^{2} = \frac{2}{12}x^{2}
         ZI=RI in posabled with CI ie. VI= =
       > 35-55 F8= 63 , Fr= Bx in series mith
              Zx = Putowhn
Zx substitution (1) ie Lx
        Rn+3wLn = 2223
           PHT3104 P2P3 [==7
                                              (: y1= ===
            PRAJULUS REPORT ExtouciJ
            PN+3W4= P2P3 + 3WC1 P2P3
         Equating real terms & imaginary termy
        Rus P2R3
        Q = WLN = WC(P2P3XR) = WC(R)
            limited to measurement of low or values
(1-10). The measurement is independent of Excitation
 posed nowch.
  Marwell's Indudance boidge:
          )A
        supply (2)
                     P3727 2013
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AL Balance condition

R3 having internal surgistance

RITANTSWLX] = P2 ((P3+8) + SWL3)

RIRX+RISWLX = P2(R3+8) + SWL3 R2

equating imajinary termy

RISWLY = SWLZPZ

RILX = LBR2

Lx= L3R2 Ris unknown inductance value in Maxwells induction Maxwell's induction

peigde.

-> This boildge having two separate arms RIRE third own is ouristance Ro is social with inductance L3. Fourth arm is unknown inductiona Furo x1 service and 18 tance Px.

Advantages of Maxwells. boidge independent of lower The bolanced equation is

ansocrated with inductance.

The balanced equation is independent of tought nog

of measurement.

The exale of the outstance can be caliculated to (₹) occid the inductance disputy.

#### Example problem

nductive impedance. The boidge constant at bolonce one G=0.01UF, P= 470KIR, P=5.1KIR, P=200KIR.

Find the societ equivalent of the unknown impedance sol.

Find Rx, Lx

Px = P2P3
P1; Lx = C1P2P3

Pr= 100KX5.1K; Lx = 5.1KX 100KX0.01UF

Pr= 1.09KD; Lx = 5.1H

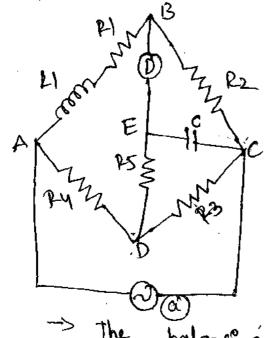
Equivalent socies circust is

Px=1.09KD Lx=5.1H.

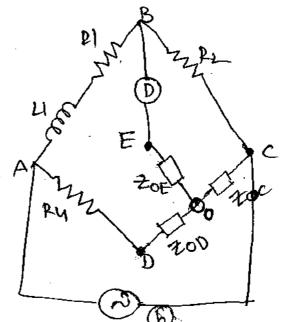
Px=1.09KD Lx=5.1H.

#### ANDERSON BRIDGE

This way impostant and we to I modification of the Maxwell with boidge as shown below.



Can be obtained



balancing: Condition for this boildge obtained by converting the mesh

impedances c, P3, R5 (brom EDC) to an equivalent star by using star delta transformation.  $\frac{200 - \frac{P_8 P_5}{(P_3 + P_5 + \frac{1}{100})}}{(P_3 + P_5 + \frac{1}{100})}$ ;  $\frac{200 - \frac{P_3}{(P_3 + P_5 + \frac{1}{100})}}{(P_3 + P_5 + \frac{1}{100})} = \frac{23}{(P_3 + P_5 + \frac{1}{100})}$ => == (Py+3W4)  $\Rightarrow \frac{2}{28} = \frac{P_8}{3wc}$   $\Rightarrow \frac{2}{28} = \frac{P_8}{3wc} = \frac{P_8}{3wc}$ ⇒ = Ru+ 200 For balance Condition Z123= 2124 (PI+5WLI) ZOC = P2(PU+200) => CP,+3\omega\_L) \ \frac{P3\sinc}{(P2+P5+\sinc)} = P2\P4+\frac{P3P5}{(P3+P5+\sinc)} => (Py+'sw2) x [ P3|swc ] = Px Px(P3+P5+\frac{1}{3}wc)+

(P3+P5+\frac{1}{3}wc) = Px Px(P3+P5+\frac{1}{3}wc)+ (By Partinon) => (R+5wL) x = P2 P4 (P3+ P5+ 5wc) + P2P3P5 => - F1R3 + L1R3 = P2R3R4 + P2R4R5 + P2R3R5 + P2R3R5 => 13 P1P3 + 21P3 = P2P3 Put P2P4PS+ 3P2P4 + P2P3P25 (:) is added for ; teems) (12-2-1) => -R1PS) + 4P3 = P2P3P4+ P2P4P5 + 5P2P4 + P2P8P5 equating ough tomy A imaginary temy 1,R3 = R2R3R4 + R2R4R5 + R2R3R5

$$L_1 = \frac{C}{P_3} \left( \frac{P_3 P_4}{P_3 P_4} + \frac{P_4 P_5}{P_4 P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5} \right)$$

$$L_1 = \frac{C}{P_2} \left( \frac{P_4}{P_4} + \frac{C}{P_2} \frac{P_4 P_5}{P_5} + \frac{C}{P_4} \frac{P_5}{P_5} \right)$$

$$L_1 = \frac{C}{P_4} \frac{P_4}{P_4} + \frac{P_4 P_5}{P_5} + \frac{P_5}{P_5} + \frac{P_5}{P_5} \right)$$

$$L_2 = \frac{C}{P_4} \frac{P_4}{P_4} + \frac{P_4 P_5}{P_5} + \frac{P_5}{P_5} \right)$$

$$L_3 = \frac{C}{P_4} \frac{P_4}{P_5} + \frac{P_5}{P_5} + \frac{P_5}{P_5} \right)$$

$$L_4 = \frac{C}{P_4} \frac{P_4}{P_5} + \frac{P_5}{P_5} + \frac{P_5}{P_5} \right)$$

$$L_5 = \frac{C}{P_4} \frac{P_5}{P_4} + \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \right)$$

$$L_5 = \frac{C}{P_5} \frac{P_5}{P_4} + \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5}$$

$$\frac{P_5}{P_5} \frac{P_5}{P_5} = \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5} \frac{P_5}{P_5}$$

$$\frac{P_5}{P_5} \frac{P_5}{P_5} = \frac{P_5}{P_5} \frac{P_5}{P_5} \frac{P_5}{P_5} + \frac{P_5}{P_5} \frac{P_5}{P_5}$$

ARTERSON
$$\begin{array}{c}
P_1 = P_2 P_4 \\
P_3
\end{array}$$
Anterson
$$\begin{array}{c}
P_1 = P_2 P_4 \\
P_3
\end{array}$$

Example Dooblem

D For an arderson boidge, arm AB Unknown inductance LI inductance having outlistance RI and inductance LI arm BC, CD, DA are outlistors having looper, looper and dooder outpectively. A Capacitor of lower and dooder outlistance your are connected between and outlistance your between A and c, religion of CF and ED. Sowelle

501! given R=200 m P3=1000 m, Pu=1000 m, C=10MF, 8=496

$$\Rightarrow R_1 = \frac{R_2 R_4}{R_3} = \frac{800 \times 1000}{1000} = 8000$$

$$\Rightarrow L_1 = \frac{R_2 R_4 + R_3 + R_4 R_5}{R_3}$$

$$= 10 \times 100 \times 1000$$

1

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Siven 
$$0.5m BC \rightarrow P_2 = 2000$$
  
 $0.00 \rightarrow P_3 = 1000$   
 $0.00 \rightarrow P_4 = 1000$   

= 7.9524

Advantages of Anderson beidge

1) There boidges are used for account measurement

Of Inductiona

2) In the other boidges we variable capacitos but in anderson boildge timed capacitors used.

3) The boidge is casy to balance.

SCHERING'S BRIDGIE OF C Measurement of capacitance schooling -> A very impostant boidge used too the poucision measurement of Capacitors and their insulating properties is the schering bridge.

12 P2

> the standard cospacitor c3 is high quality mica

capacitor 600 general

measurement or an oise

Departor for insulation

measurement.

-> For bolance general equation ZIZX = ZZZ3 (Potomally)

ZX = 2223

Here 
$$\frac{2}{2}x = \frac{2}{2}\frac{2}{3}\frac{3}{3}\frac{1}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac{1}{3}\frac$$

#### Example problem

DAn: schwing boidge has the tollowing constaint own AB - capacitor of o.suf in parallel with 11cm own AB - capacitor of o.suf in parallel with 11cm own strange, Arm AD - ouristance of 2kol, Arm BC - capacitor of o.suf, arm CD - unknown capacitor ox and Px in series boaquency - 1kHz, Determine the unknown capacitor.

From Schening boildge 
$$R_{x} = \frac{P_{x}C_{1}}{C_{3}}$$

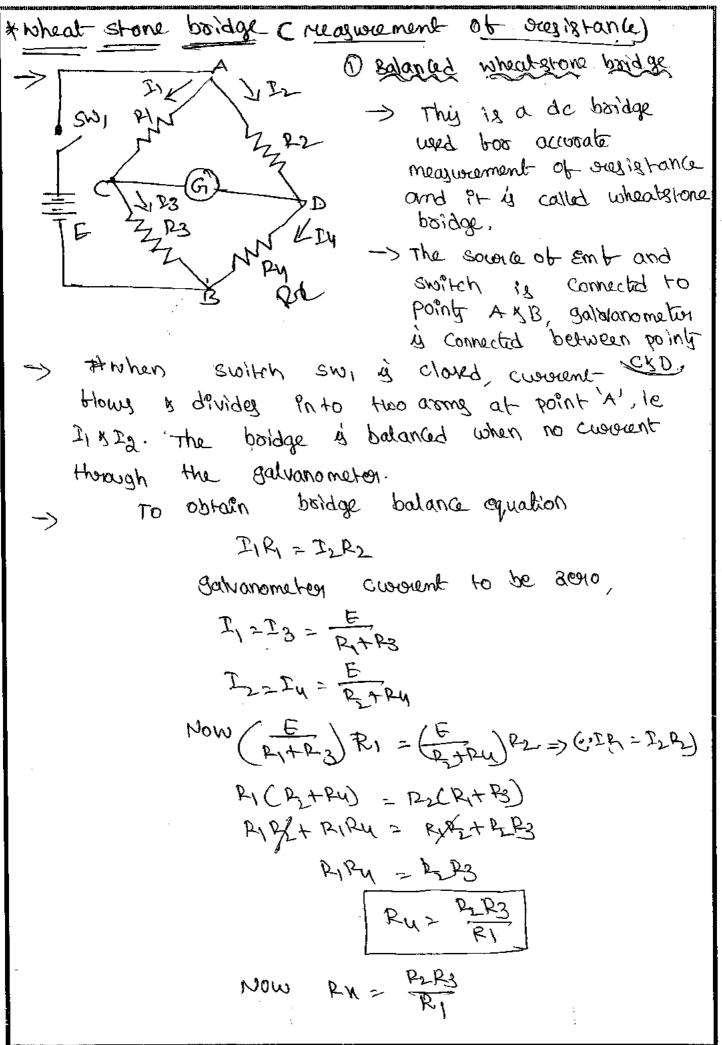
$$C_{x} = \frac{P_{1}}{P_{x}}C_{3}$$

Cr: 0,25114

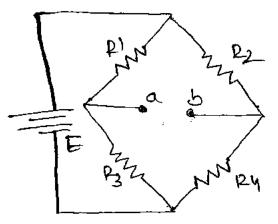
Discipotion bactor is given by

= 2 x3" 1x 1x 0121x12 x 8x13

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#### Unbalanced Wheatstone's Boidge



For obtaining the unbalanced whealstone Conditions, we the The vening throsem.

> For determining the cooperat thorough the galvanomation, we have to bind the thevening Equivalent.

-> Thevening equivalent is voltage is galvanometer toom psidge bound by disconnecting the Coccust From the coccust determining the open concust Voltage between terminally a and b.

 $\rightarrow$  Voltage at point a Ea =  $\frac{E \times P3}{P_1 + P3}$ 

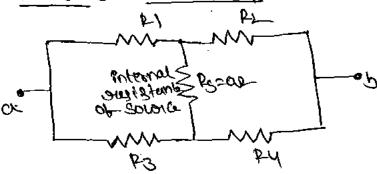
$$Ea = \frac{E \times P3}{P_1 + P3}$$

Therening equivalent Eth = Eab = Ea-Eb

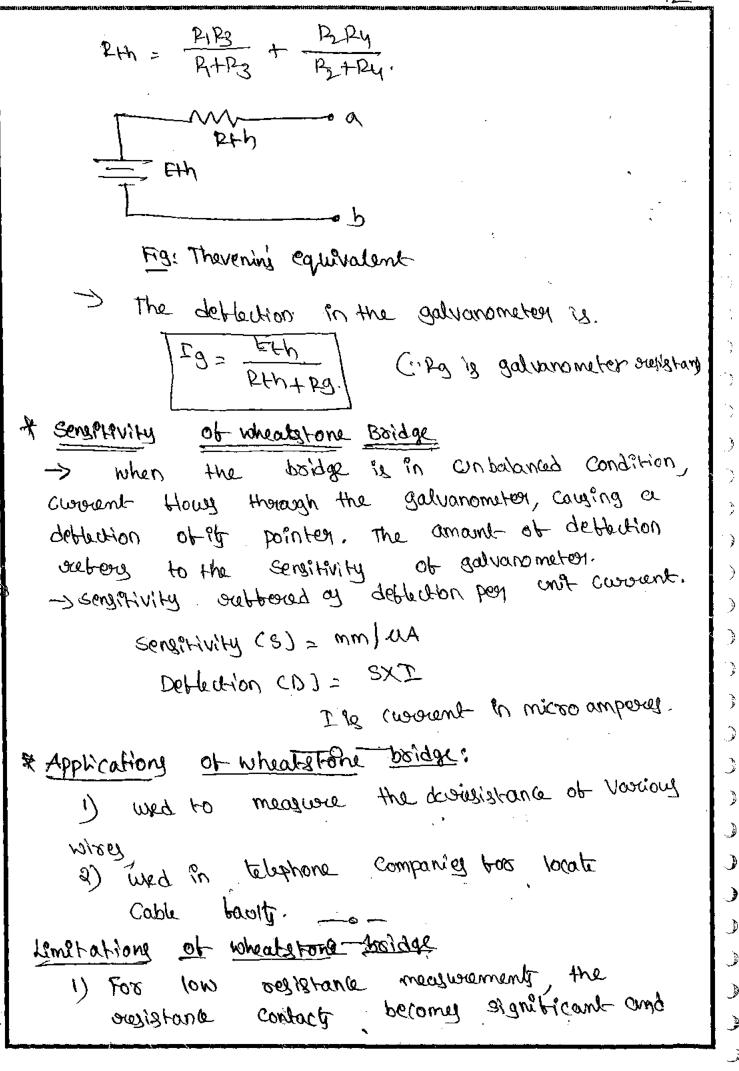
$$Eab = \frac{E \times R_3}{R_1 + R_3} - \frac{E \times R_4}{R_2 + R_4}$$

$$Eab = E \left[ \frac{R_3}{R_1 + R_3} - \frac{R_4}{R_2 + R_4} \right]$$

Therening registance



> NOW PILLES in series with Pell Ry ic 2,1123+B211RY



introduces an error. This can be eliminated by kelving double bridge.

4) for high sugistance measurement, thre origistance in the boidge becomes so large and galvanometer is intensive to Ambalance. It this high origistance in maga ohms, wheatstone bridge is can't be used.

Example Problems

1) The wheatstone balance have R1=10K, P2=15K & P3 = 40K, bind the unknown outside Rx.

For balanced wheatstone bridge  $P_1 R_1 = P_2 P_3 \quad ie \quad P_1 P_2 = P_2 P_3$   $\Rightarrow P_1 P_2 = \frac{P_2 P_3}{P_1} = \frac{15 k_1 k_2 k_3}{10 k_1} = 60 k_1 k_2$ 

(2) An unbalanced wheatstone boidge have the parameter  $R_1 = 1/c$ ,  $R_2 = 3.5/c$ ,  $R_3 = 3.5/c$ ,  $R_4 = 10/c$ ,  $R_5 = 3.00 L$ .

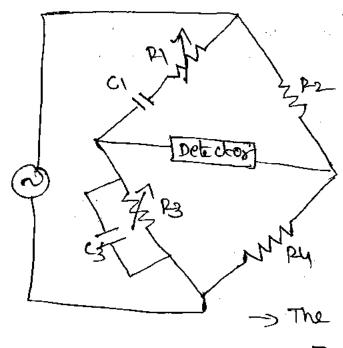
Coesistance in galavanometer =  $R_3$  -  $R_4$  = source  $R_4$  =  $R_4$  -  $R_4$  consist therough galavanometer.

<u>Sol</u>j

Eth = Ea - Eb - EaEth =  $E \left[ \begin{array}{c} PH \\ PL + PH \end{array} \right] - \begin{array}{c} P3 \\ R+P3 \end{array}$ Eth =  $6 \left[ \begin{array}{c} 10K \\ 2.5F + 10K \end{array} \right] - \begin{array}{c} 3.5FK \\ 1K + 3.5K \end{array} \right] = 0.138V$ The worning equivalent engistance Rth is  $RH = \begin{array}{c} P_1P3 \\ R_1P3 \end{array} + \begin{array}{c} P_2PH \\ P_3 + P_4 \end{array}$  $Rh_2 \begin{array}{c} IK \times 3.5K \\ IK + 3.5K \end{array} + \begin{array}{c} 2.5K \times 10K \\ 2.5K + 10K \end{array}$ 

Current (Ig) = Ph + Pg . = 0.132V = 42.88 MA.

#### Wien's Boidge



- > Wien bridge has a series

  Rc Combination in one

  arm and postable

  Combination in adjoining

  arm.
  - > It is designed to measure the tocaquency, and also and also and also and box measurement of orknown becapacitas

-> The impedance of one arm is

$$Z_1 = R_1 + \frac{1}{5\omega c_1} = R_1 + \frac{1}{5\omega c_1}$$

> admittance of possalled arm is

-> boidge balance Condition 2124

2124 = 2123 2> 225 23

$$P_{2} = \frac{P_{1}P_{1}}{P_{2}} + P_{1}P_{1}S_{00}S_{0} - \frac{P_{1}S_{0}}{P_{1}S_{0}} - \frac{1}{2S_{1}P_{1}S_{0}}$$

$$P_{3} = \frac{P_{1}P_{1}}{P_{2}} + \frac{P_{1}P_{1}S_{00}S_{0}}{P_{2}} - \frac{P_{1}S_{1}}{P_{2}} + \frac{P_{2}S_{1}}{P_{2}} - \frac{1}{2S_{1}P_{2}} + \frac{P_{2}S_{1}P_{2}}{P_{2}}$$

$$P_{3} = \frac{P_{1}P_{1}}{P_{3}} + \frac{P_{2}S_{1}P_{2}}{P_{3}} + \frac{P_{1}P_{2}}{P_{2}} + \frac{P_{2}S_{1}P_{2}}{P_{2}} - \frac{1}{2S_{1}P_{2}} + \frac{P_{2}S_{1}P_{2}}{P_{2}}$$

$$P_{4} = \frac{P_{1}}{P_{3}} + \frac{P_{2}S_{1}P_{2}}{P_{3}} + \frac{P_{2}S_{1}P_{2}}{P_{2}} - \frac{1}{2S_{1}P_{2}} + \frac{P_{2}S_{1}P_{2}}{P_{2}} + \frac{P_{$$

 $\frac{P2}{PU} = \frac{P1}{P3} + \frac{1}{\omega^2 c_1^2 P_1 P_3}$   $\frac{P2}{PU} = \frac{1}{P3} \left( P_1 + \frac{1}{\omega^2 c_1^2 P_1} \right)$   $\frac{P3}{P2} = \frac{P4}{P2} \left( P_1 + \frac{1}{\omega^2 c_1^2 P_1} \right)$ 

Now be is enpolitable in colonolog

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$$\frac{P_{1}}{P_{1}} = \frac{P_{1}}{P_{3}} + \frac{c_{3}}{c_{1}}$$

$$\frac{P_{2}}{P_{1}} = \frac{P_{1}}{P_{1}} = \frac{P_{1}}{P_{1}} + \frac{c_{3}}{c_{1}}$$

$$\frac{P_{2}}{P_{1}} = \omega^{2}c_{1}c_{3}P_{1}^{2} + \frac{c_{3}}{c_{1}}$$

$$\frac{P_{2}}{P_{1}} = c_{3}\left(\frac{1}{c_{1}} + \omega^{2}c_{1}P_{1}^{2}\right)$$

$$\frac{P_{2}}{P_{1}} = \frac{P_{2}}{P_{1}}\left(\frac{c_{1}}{1 + \omega^{2}c_{1}P_{1}^{2}}\right)$$

$$\frac{P_{2}}{P_{3}} = \frac{P_{2}}{P_{4}}\left(\frac{c_{1}}{1 + \omega^{2}c_{1}P_{1}^{2}}\right)$$

Example problem

A wien boidge consist of Rizhitka C1 = 5nf, R2=20KD, C3=10nf, P3=10KD, Ry=100KD that boug of component.

.<u>' loz</u>

@ Find the equivalent passallel sogistance and capacitance that causes a when boidge to now with the tollowing component values Capacitante Ry=3.1KVL, C1=5.2WF, P2=25KVL, P=8.5KHZ, Ry = 100 KD

XL is industive oceastance R is Coll ousighand.

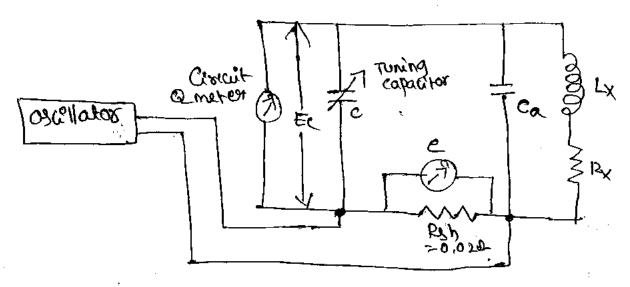


Fig: a\_meter

> From the concert, oscellator have the bouguency dange from sorther to souther delivere consumt to ocegistance Resh having a value of 0.0202.

-> The voltage acook shunt 18 measured with a restem edgesomesett.

The voltage across capacitor is measured by an cleanance voltmeton corresponding to Ec and

calibrated directly to seed 12.

The Circuit is timed to sousonance by vasying C until the dutronic vortneton sunds the

maximum Value.

the sugarance output voltage & corresponding to Ec is F= exe!

ely known

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> The inductance of the coil determined by Connecting it to the terminally of instrument, the creater is tuned to sugmance by varying either the capacitance or the oscillator tocquency.

The capacitana is varied the oscillator bacquents is obtained. If the capacitance see to degired value, obtained. If the capacitance see to degired value, occurs.

Oscillator bouguency is varied until occupance occurs.

> The inductance of the coil bear unity and susonaling known Values of the coil bear unity and susonaling Capacitosco.

The actual Q of Coil is greated than the indivated Q.

Effect of St on Si

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$$u(c_{2}+c_{6}) = c_{1}+c_{5}$$

$$u(c_{2}+uc_{6}) = c_{1}+c_{5}$$

$$u(c_{2}+uc_{5}-c_{1}-c_{5}=0)$$

$$u(c_{2}-c_{1}+3c_{5}=0)$$

$$3c_{5} = c_{1}-uc_{2}$$

$$c_{5} = \frac{c_{1}-uc_{2}}{3}$$

Impedance reasonment using a meter

To a-metery

> An unknown impedance measured cyling a-mater either by serves or shown substitution

method.

The unknown impedance  $\exists x is determined by individually desimining the Component RxxLx.

The control of the Component RxxLx.

The series substitution unknown empedance is adjusted to the connected and twined accuse is adjusted to suspend a oscellator to equency. The value of our own of a oscellator to early when the notion.$ 

The unknown impedance is connected then.

Capacitos is vascied too seconance, and new values a'x c' are noted.

From Fig @  $\omega L = \frac{1}{\omega c}$  — 0subtracting 0 - 0

where 
$$-\omega L = \frac{1}{\omega c} - \frac{1}{\omega c}$$
 $x_{1} = \frac{1}{\omega c} \left( \frac{c}{c^{2}} - \frac{c^{2}}{c^{2}} \right)$ 
 $x_{2} = \frac{1}{\omega c} \left( \frac{c}{c^{2}} - \frac{c^{2}}{c^{2}} \right)$ 
 $x_{3} = \frac{1}{\omega c} \left( \frac{c^{2}}{c^{2}} - \frac{c^{2}}{c^{2}} \right)$ 
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 $x_{7} = \frac{1}{\omega c} \left( \frac{c^{2}}{c^{2}} - \frac{c^{2}}{c^{2}} \right)$ 
 $x$ 

> The unknown impedance Zuzhutixu > Yn ocepoceunt the sunt admittance of unknown impedance. It consist of two short element Condudance Gru and Superfance Bn.

 $Y_{N} = G_{N} + \frac{1}{3}BN$   $G_{N} = \frac{1}{\omega_{L}} \left( \frac{G_{L} - G_{L}^{2}}{G_{L} G_{L}^{2}} \right), B_{N}$   $B_{N} = \omega_{C} - \omega_{C}^{1}$   $Y_{N} = \frac{G_{L} - G_{L}^{2}}{\omega_{L} (G_{L} G_{L}^{2})} + \frac{1}{3}\omega_{C} - C_{L}^{1}$ 

Measurement of a by sujeptone method

i) > the cost ander test is connected in

society with a capacitor, metor is an

indicator, the circuit is timed for

outprance to the ascertator forequency,

by timing the variable capacitor to a

) <del>)</del>

> ) ;

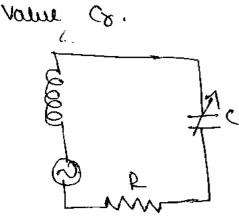
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capacitancy CE CON

a) superforme method of a & Resonance conve measurement

> The capacitor is then detuned to a value cb n the organish voltage, which meter oreading bally to 70.7% of the organish voltage. Now Capacitor is set to high capacitance value of ca and the deblacion doops to 70,7% of sugarant Voltage.

Capacitance at obsonance

Yea = won x xcb = woch

At hast power point

WL - WCa = R and Wch - WL = R

adding two equations

WL-wat web-WL = 2R

 $\Rightarrow$   $\frac{1}{\text{with}} - \frac{1}{\text{with}} \ge \& \varrho$ .

but G = Calb.

1> value of capacitanear

=>: -wco-wcb = 28: ocesonance.

(0-CP) = 86 (0-CP) = 8

but a = wcop. Now = 3. a. (ca-(b) = 2.

Q\_((a-(b)=2 cx

Pose Cautions to be taken when using a bridge

> Assuming that a suitable method of measurement has been selected and that sowell s detector one given theretore some poucautions.

must be observed to obtain accurate oundings.

- The body should be constully laid out in such way that no loops with magnetic thux are produced
- -> WPH lange L, self-capacitana of leady is more
- impostant than their inductance,

  In measuring a capacitor lead apocitance as low as parrible. For this exactor ledg should not be too close together and should be made of

tine wise.

> In inductive and capacitive measurement leadque encosed in metal tubes to shield them brom MM) words derpeamagnatic action.

\* Sources of Edgor -0-

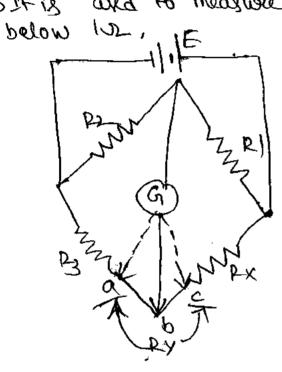
- D In subticient knowledge of parametery and design Lorditions
- 2) poor degign
- 3) change in proof parametery
- a) poor maintenana.

5) Errors could by person operating the instrument 6) contain design limitations.

# kelving Boidge

> A Hadified boom of wheat-grone's bridge is the kelving boidge.

It is used to measure the value of sugistance



-> by supergent the oughstance of the connecting leady brom A3 to AX

-> galvanometer conected to point a and point condb

-> when it is connected to a point a, by 18 odded to PX

-> when it is connected to a point c, Ry is added to boidge

00m (23.

-> when it is connected to point cand a, then in permern b

the ratio of vasistance team ctob (RCB) & to team auxiltance from a top equal to patio

66 RIKB then

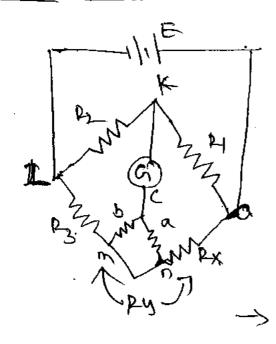
PCb -> (PN+RCb) Rab -> (P3+Pab)

> PRAPED = PI (PATRCb) = PZ (PSTRab) - 0

Post but Past Past Past V

**\*** 

## kelving Double bridge



-simple to a point cat the potential between my n connection ie by. The sation of sugistant of arms as the sation of RIXPZ. The galvanor maken the potential at the when the potential at the are equal

> [Erk=Ermc]

FOR BOD E ON ERK

ERK = P2 [ ] [ P3+P4+ (a+b)Py]

Smilerly Elmc =  $\frac{1}{2}$   $\left[ \frac{b}{a+b} \left[ \frac{(a+b)py}{a+b+py} \right] \right]$ 

Elk=Elmc

$$P_{3}+P_{44}+\frac{(a+b)Py}{a+b+Py}-\frac{(p_{1}+1)}{(p_{2}+1)}$$

$$P_{4} = \frac{P_{1}P_{3}}{P_{2}}+\frac{bP_{1}Py}{P_{2}(a+b+Py)}\frac{bP_{1}y}{(a+b+Py)} + \frac{bP_{1}y}{(a+b+Py)}$$

$$P_{5} = \frac{P_{1}P_{3}}{P_{2}}+\frac{bP_{1}Py}{P_{2}(a+b+Py)}+\frac{aP_{1}y}{(a+b+Py)}$$

$$P_{6} = \frac{P_{1}P_{3}}{P_{2}}+\frac{bP_{1}y}{P_{2}(a+b+Py)} + \frac{aP_{1}y}{(a+b+Py)}$$

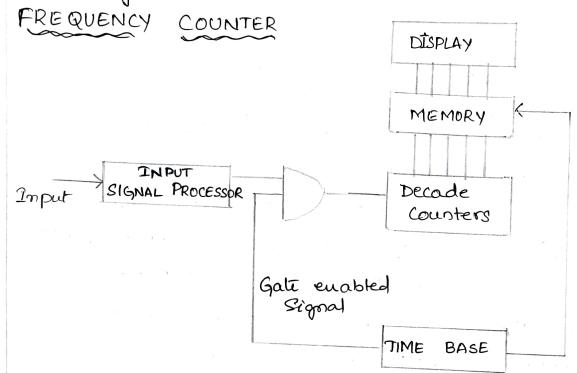
$$P_{6} = \frac{P_{1}P_{3}}{P_{2}}+\frac{bP_{1}y}{P_{2}} + \frac{aP_{2}y}{p_{2}}$$

$$P_{6} = \frac{P_{1}P_{3}}{P_{2}}$$

$$P_{7} = \frac{P_{1}P_{3}}{P_{2}}$$

### COUNTERS

\* Counters are the first electionic circuits designed to count the frequency values using the clock pulses.



-fig: Basic block of Forequency Counters.

The figures shows the block diagram of a Simple frequency counter.

Principle of operation

\* The frequency Counter operates on the Principle of "gating the cuput frequency isto the Counter for a pre-determined time".

Eg: It an unknown frequency was gated into the Counter for an exact 1second, the number of Counts allowed into Counter would be the frequency of the input.

\* The team "gated" stems from the fact that an AND or an or gate is used in circuit input Gate enabled the waveforms in tige ishows the use of AND gate. working of the Counter. \* As long as the Gate enabled signal is of 1-sec pulse and of logic 1, the output of AND gate is same as the unknown input. \* when the 1-sec pulse refusors to logic o, The output of AND gate is O. \* Therefore, exactly usec of unknown input Pulses is allowed at the output of AND \* These pulses are to be Counted and displayed. \* when the Gate enabled signal is of more than usec duration, then the decimal point of the display is switched with the gate time Selector switch to Count Correct the frequency display eg: Gate was open for 10 sec, the accumulated Count would be average frequency in 0.14z.

#### 1. DECADE COUNTERS

\* The heart of the Forequency Counter is the decorde Counter.

\* The output of the decorde counters follows the Sequence in fig.

Clock	counter State			
	D	c	В	A
	מ	0	0	1
2	0	0	1	O
3	0	0	+	1
4	٥	1	0	0
5	0	1	0	1
6	20	(	i	O
7	0	t	t =	1
8	1	0	O	0
9		b	0	1

figs: Brinary coded decimal Counting sequence.

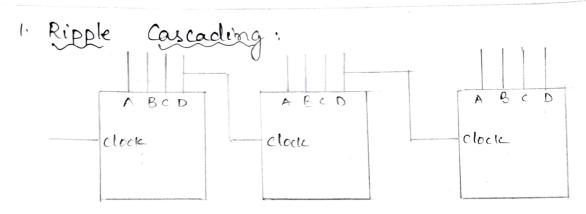
Eg: decimal no 138 is coded as 0001 0011 1000 is BCD.

\* Each BCD Counter allows one decade of Counting. So, BCD Counters must be cascaded.

G: To Count between 0-999 three BCD Counters are required.

# There are two methods of cascading BCD Counters. 1. Ripple Cascading

2. Synchronous Cascading



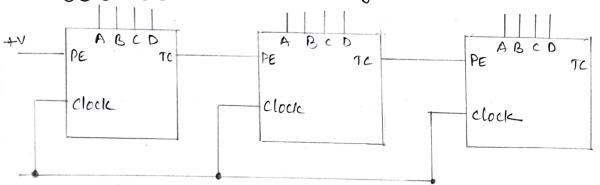
figy: Cascading Ripple Counters.

\* This type of Cascading is very slow.

\* This is used only for low-frequency measurements. It is not used for servious frequency measurement equipment.

\* In this type of cascading, the last output of the least significant Counter to derive the clock input of the next significant Counter.

2. Syncheronous Cascading.



figs: Cascaded Synchronous Counters.

\* A Terminal Count (TC) or Carry output is used for the purpose of Cascading.

\* This output is used to enable the following Counter to be incremented on the mext clock-pulse.

\* This Ensures that the State of the next Counter is coincident with the clock. Logic dicegram of Binary Synchronous Counter the Benary Synchronous Counter requires that all the flip-flop clocks be connected together which greatly reduces the propogation delay and outpuls. allows higher Counting Speeds. J LH Q figs: Binary Synchronous Counter. Logic diagan of Ripple BCD Counter. The Ripple BCD Counter is constructed by four flip-flops and an AND gate. The clock of one flip-flop is derived from the output, of the previous flip-flop. J UP fegs Ripple BCD Counter

#### 2. DISPLAY

- \* The BCD information available at the sufput of the Counter must be Convented to some form of visible display.
- \* This Conversion depends on the type of display designed.
- \* It is desirable in a frequency Counter to display the Count continuously.
- \* The Counter is sreset to 3000 and allowed to Count obscring the gate period. During this time the cutput of the Counter is Constantly Changing.
- \* The output of the counter cannot be displayed during this period.
- \* The Count of the end of the measurement period is Stered in a simple memory and displayed. du
- \* Digital logic usually Cannot supply the suggested convert to deriving a display.

  \* A display driver is included between the decode Counter and the displays.
- \* This Technique is called Drisplay multiplexing and neduces the number of drivers and decoders required to implement large Counters.

- 3. Time Base.
- the Sequence of events within the frequency Counter is Controlled by the time base, which must provide the timing for the following events.
- -> Dreselling the counter
- -> opening the count gate
- -> Closing the Count gate
- -> Storing the counting frequency.
- \* The opening and closing of the count gate determine the accuracy of Trime Base signal. Arequency Counter.
- \* so, the accuracy of the frequency counter depends directly on the accuracy of the time Base Signals.
- \* These signals our deriven from Coystal-Controlled
- 4. Input Signal Priocessing
- \* The unknown frequency is not the Correct logic level to drive the frequency Counter.
- \* A processing circuit is orequired.
- \* This processing circuit may be
  - -> an amplifier to invuouse signal level
  - -> an attenuator to adjust vouvations in input amplitudes.
  - -> a Comparator

# ELECTRONIC COUNTER

#The decade Counter can be easily incorporated in a Commercial Test instrument Called an Electronic Counter.

\* A decade Counter by itself behaves as a totalised by totalling the pulses applied to it during the time interval that a gate pulse is present.

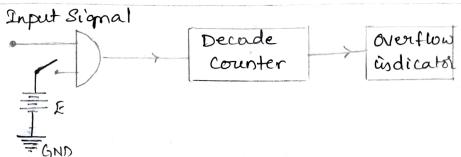
## modes of operation

- -> Totalising Mode
- -> Faequency mode
- → Period Mode
- -> Ratio mode
- -> Time Interval mode.

## 1. Potalising Mode.

\* In the totalising mode the input pulses are counted (totalised) by the decade counter as long as switch is closed.

\* It the Count pulse exceeds the capacity of the decade Counter, the overflow isolicator is activated and the Counter starts Counting again.



figs: Block diagram of the Totalising Mode of an Electronic Counter.

## 2. Frequency Mode

\* If the time interval in which the pulses are being totalised is accurately controlled the Counter operates in the frequency mode.

Accusate Control of the time interval is achieved by applying a rectangular pulse of known duration to the AND gate is place of de voltage source. This is called galing of the Counter.

\* The frequency of the input signal is Computed as  $f = \frac{N}{t}$ .

Where f: frequency of input signal

N: pulse Counted

t: duration of the gate pulse.

3. Period Mode

\* In some applications it is desirable to

measure the period of the Signal nather than ils frequency. Since period is the neciprocal of the frequency, it can easily measured by using the input signal as a gating pulse and counting the elvele pulses.

\* The period of the insput signal is determined from the number of pulses of known frequency

 $T = \frac{N}{f}$  N: pulse counted f: freq. of clock.

## 4. Ratio Mode

\* The ratio made of operation simply displays
the numerical value of the ratio of the trequency
of the two signals.

It the low frequency signal is used is place of the clock to provide a gate pulse. The number of cycles of the high frequency signal which are stored in the decade counter during the presence of an externally generated gate pulse is read directly as a ratio of the frequency. 5. Time interval Mode.

Start
Stop

Decade

Counter

Counter

Readout

f=1mHz

Clock

Right diagram of Electronic

figs: Block diagram of Electronic

\* The time interval mode of operation measures
the time elapsed between two events:

\* The measurement Can be done using the
Circuit of figure 9

# The gate is Controlled by two independent cispuls. > The Start input, which enables the gate

> The Stop cisput, which disables the gate

\* During the time is been believed.

\* During the time interval between the start and Stop signal, clock pulses accumulate in the Register, providing an indication of the time interval between the Start and Stop of the event.

#### Applications.

- -> Research and Development laboratories
- → in standard Laboratories
- → On Service benches
- → is everyday operations of many electronic cirstallations.
- \* Counters are used is in Communication to measure the carrier frequency, in in the digital system to measure clock

frequency.

## MEASUREMENT ERRORS / Sources OF ERRORS

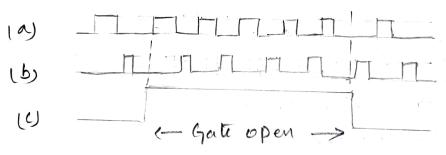
1. Gating error

\* occurs with frequency and period measurements.

\* For frequency measurement the main gate
in opened and closed by oscillator output
pulse.

\* this allows the isput signal to pass through the gate and be counted by the decade counters.

the gating pulse is not Synchronised with the input Signal. They are infact two totally unrelated Signals.



figio: Gating ever.

\* Fig 10 Shows the waveforms of two in put signals (a) and (b) out of phase with respect to the gating signal (c)

\* In (a) 5 pulses can be counted (b) 4 pulse can be only allowed

\* Therefore \$1 Count ambiguily in the measurement occurs.

\* In measuring low frequencies, this earch shows major effect on the result. for eg: where f=10 Hz to be measured gating time = 1 sec. The decade counter indicates 10±1 count. Hence an inaccuracy of 10%. \* Period measurements are preferred over frequency measurements at lower frequencies \* In period measurements, the no. of pulses counted Np = fe to: clock frequency fx: unknown frequency of input signal. \* In frequency measurement, with lace time No of pulses Countée Mf = fx \* The crossover frequency to at Np=Ngils  $\frac{f_c}{f_x} = f_x \qquad \Rightarrow \qquad f_x = \sqrt{f_c}$   $f_0 = \sqrt{f_c}$ \* Signals with a frequency lower than to Should therefore be measured in the period mode

\* Signals with a frequency above to should be measured in frequency mode

- 4.

- 2. Time Bouse Error:
- \* Inaccuracies in the lime base also cause exolors is the measurement.
- \* In frequency measurements the time base determines the opening and closing of the signal gate and it provides the pulses to be counted.
- \* Time Base everde Consists of
  - → Oscillator Calibration Ervors
  - -> Short term crystal stability earls
  - -> long term Crystal Stability eaross.
- # Improved Calibration accuracy Can be obtained by using very low Frequency Stations souther thigh Frequency because the Transmission paths for Nery Low frequencies is shorter than high frequency Transmission.
- \* Short team Crystal errors are caused by monetary frequency variations due to voltage transients, shock and vibration, electrical interference ex
- # These evolors can be minimised by taking frequency measurements over long gate times
- \* Long-team Stability evorors are due to aging and deterioration of the crystal

# 3. Trigger Level Error

In frequency Measurements the Signal gate is opened and closed by the input signal.

\* The accuracy with which the gate is opened and closed is a function of the trigger level error. \* These errors can be minimised with large Signal amplitudes and fast rise times.

Maximum accuracy can be obtained if the following Suggestions are followed.

(a) the effect of the one-count gating course Can be minimized by making frequency measurements above the and period measurements below the where he is clock frequency of counter to Since long term stability has a cumulative effect the accuracy of measurement is mostly a function of the time since the last calibration against a primary of secondary standard.

(c) The accuracy of time measurement is greatly affected by the slope of the incoming signal Controlling the signal gate. Large signal amplifiede

and fast sisse lime assure maximum accuracy.