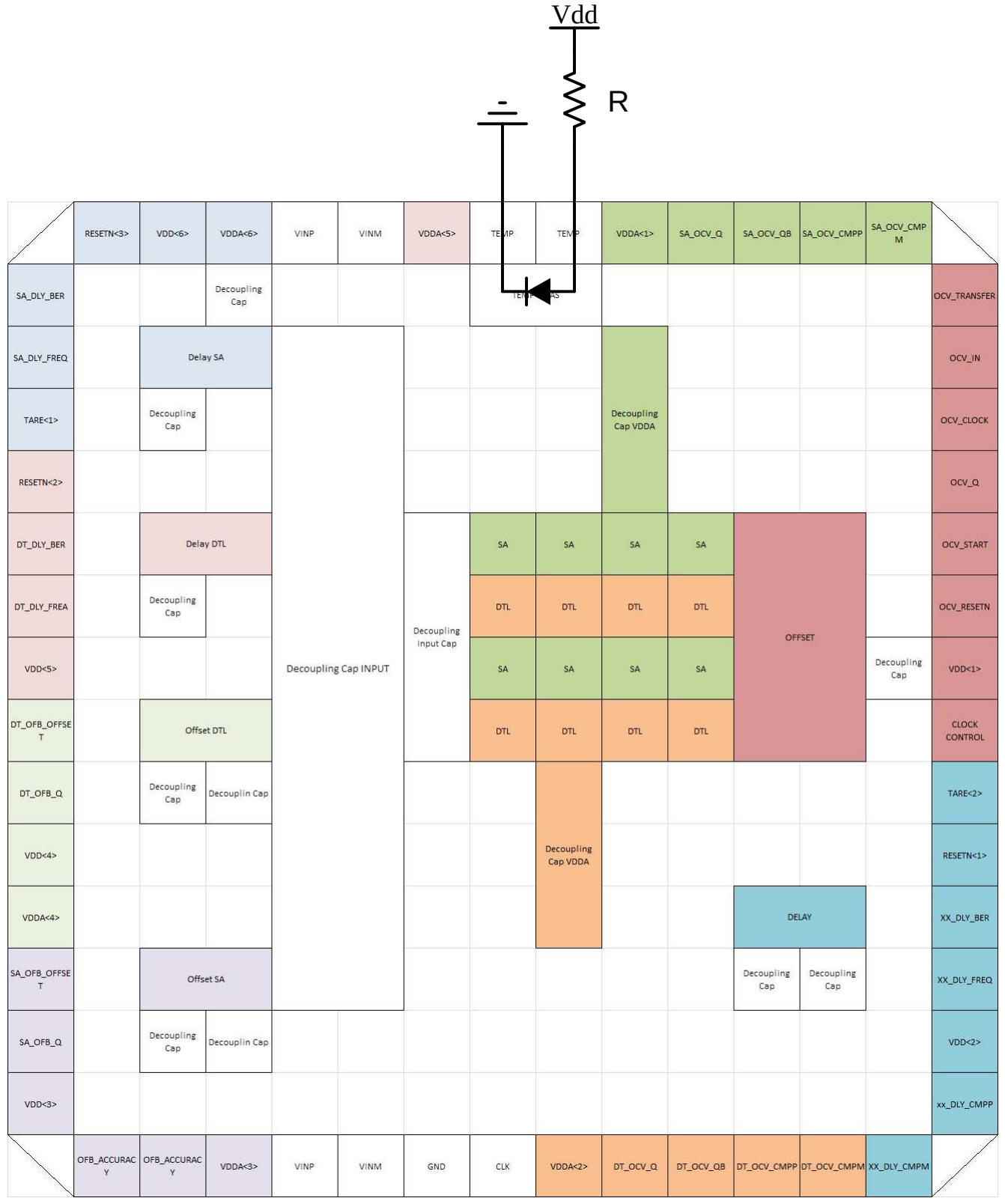
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OCV_TRANSFER
SA_DLY_FREQ
Delay SA
OCV_IN
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Decoupling Cap VDDA
OCV_CLOCK
RESETN<2>
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Delay DTL
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Decoupling Cap
DTL
OCV_RESETN
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Decoupling Input Cap
OFFSET
Decoupling Cap
VDD<1>
DT_OFB_OFFSET
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CLOCK CONTROL
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DELAY
XX_DLY_BER
SA_OFB_OFFSET
Offset SA
XX_DLY_FREQ
SA_OFB_Q
VDD<2>
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xx_DLY_CMPP
OFB_ACCURACY
VDDA<3>
GND
CLK
VDDA<2>
DT_OCV_Q
DT_OCV_QB
DT_OCV_CMPP
DT_OCV_CMPM
XX_DLY_CMPM