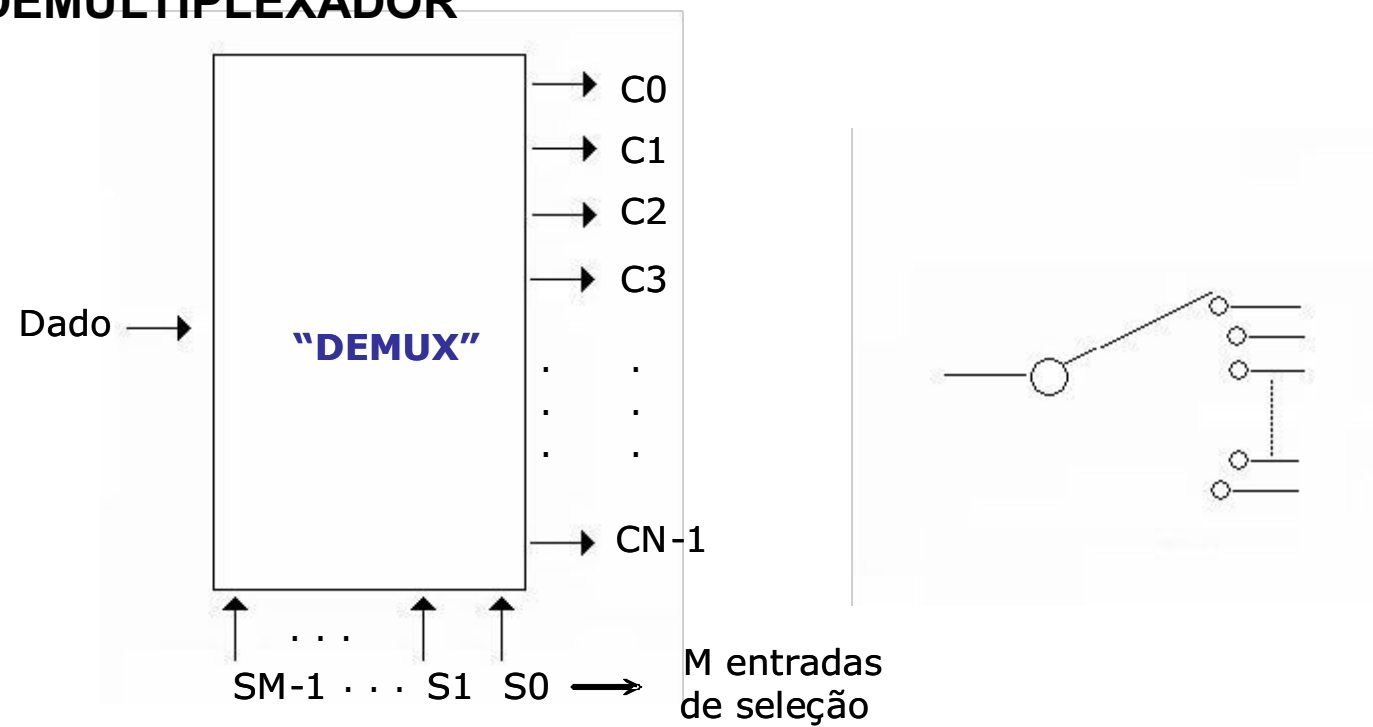
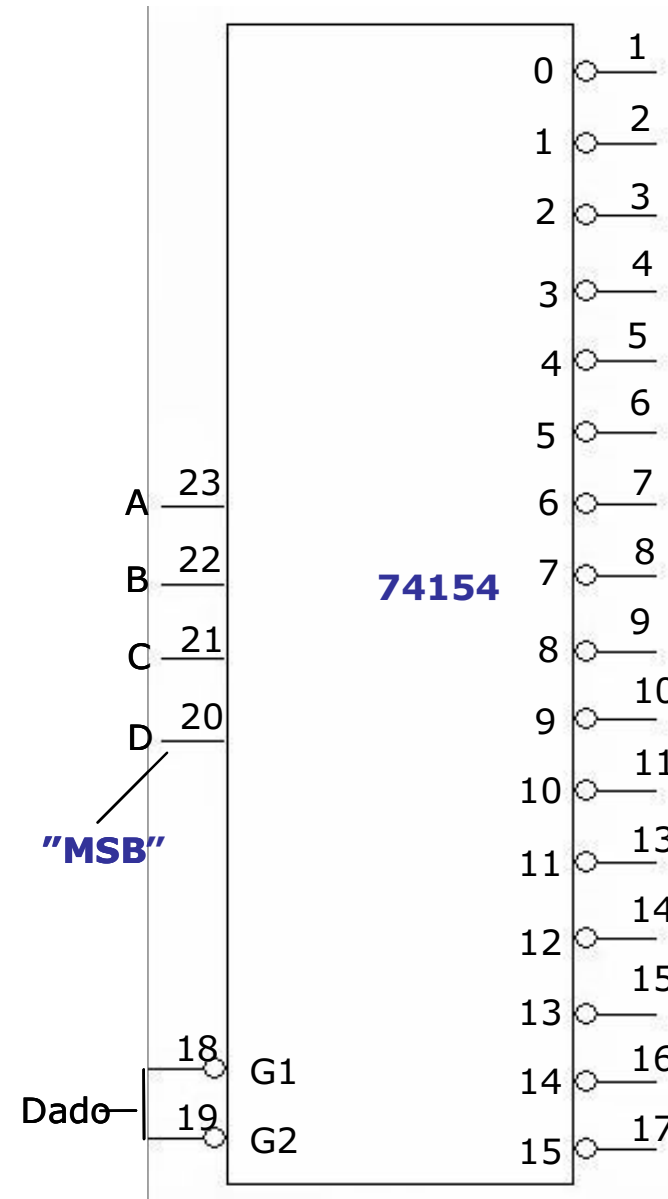
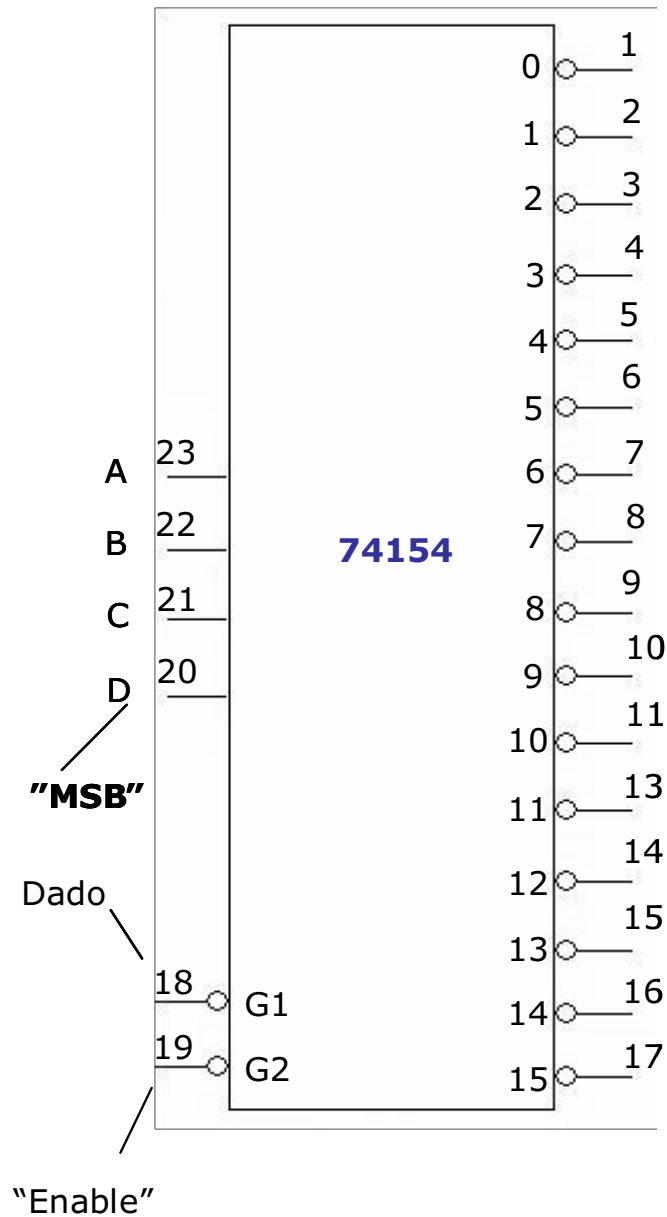


DEMULTIPLEXADOR

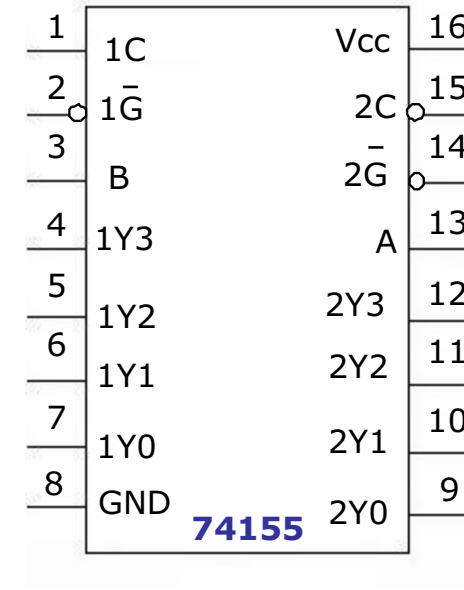
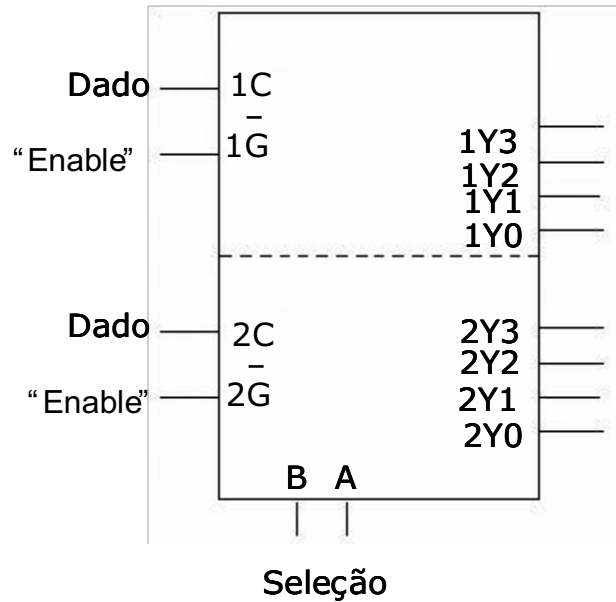


Entradas					Decimal	
S0	...	S3	S2	S1	Equivalente	Destino
0	0	0	0	0	C0
0	0	0	1	1	C1
0	0	1	0	2	C2
.....				
.....				
1	1	1	1	2^M	C(N-1)

CIRCUITOS INTEGRADOS DEMULTIPLEXADORES



Circuito Integrado 74155: Duplo Demultiplexador com Quatro Canais



ENTRADAS				SAÍDAS			
Seleção		"Enable"	Dado				
B	A	2G	2C	2Y0	2Y1	2Y2	2Y3
X	X	1	X	1	1	1	1
0	0	0	0	0	1	1	1
0	1	0	0	1	0	1	1
1	0	0	0	1	1	0	1
1	1	0	0	1	1	1	0
X	X	X	1	1	1	1	1

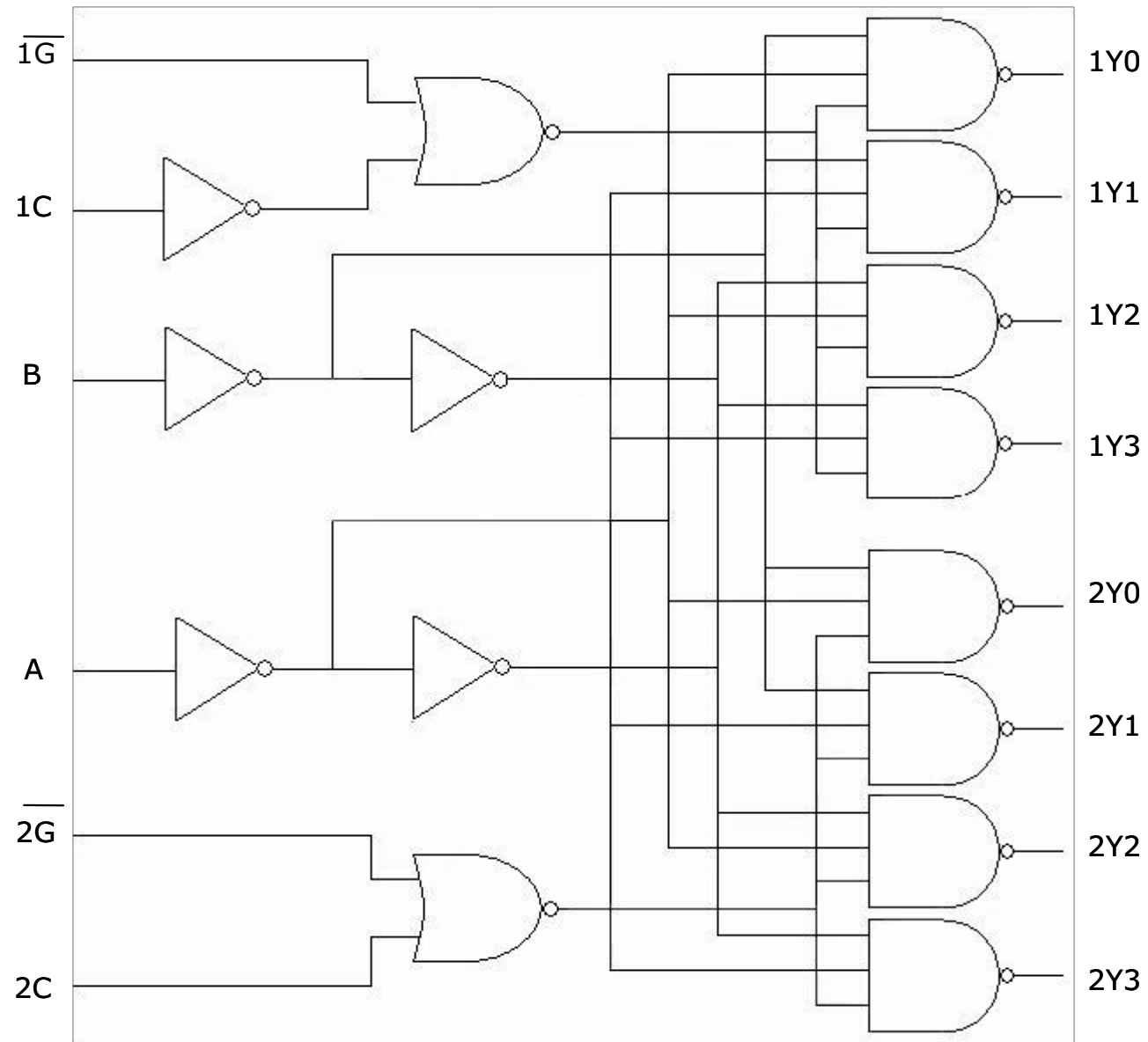
ENTRADAS				SAÍDAS			
Seleção		"Enable"	Dado				
B	A	1G	1C	1Y0	1Y1	1Y2	1Y3
X	X	1	X	1	1	1	1
0	0	0	1	0	1	1	1
0	1	0	1	1	0	1	1
1	0	0	1	1	1	0	1
1	1	0	1	1	1	1	0
X	X	X	0	1	1	1	1

ENTRADAS					SAÍDAS				
Seleção		“Enable”		Dado					
B	A	1G	2G	1C	2C	nY0	nY1	nY2	nY3
X	X	1		X	X	1	1	1	1
0	0	0		1	0	0	1	1	1
0	1	0		1	0	1	0	1	1
1	0	0		1	0	1	1	0	1
1	1	0		1	0	1	1	1	0
X	X	X		0	1	1	1	1	1

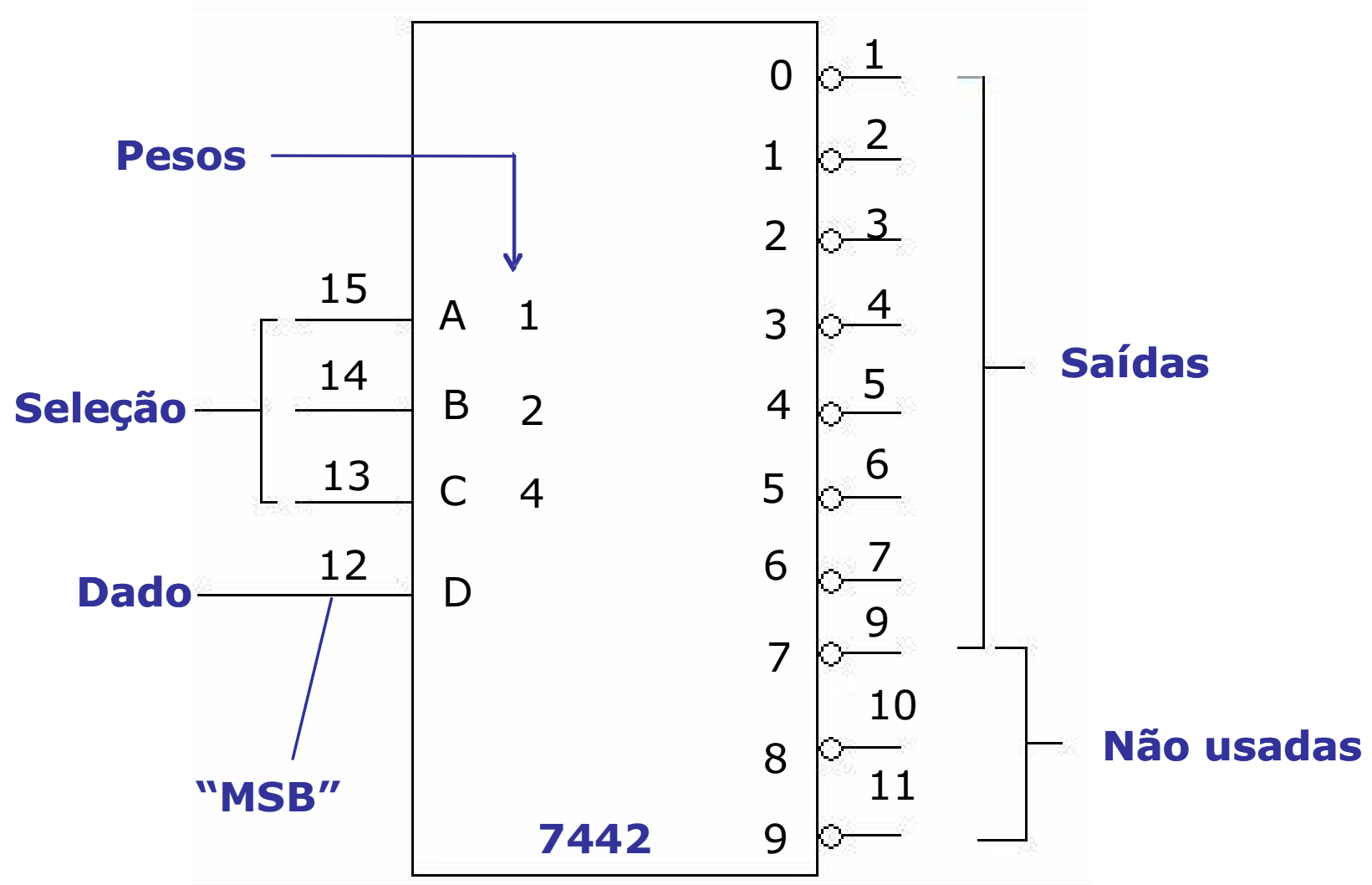
Onde n=1 ou 2

Para n=1

Para n=2



Uso do Decodificador como Demultiplexador



[illegible]