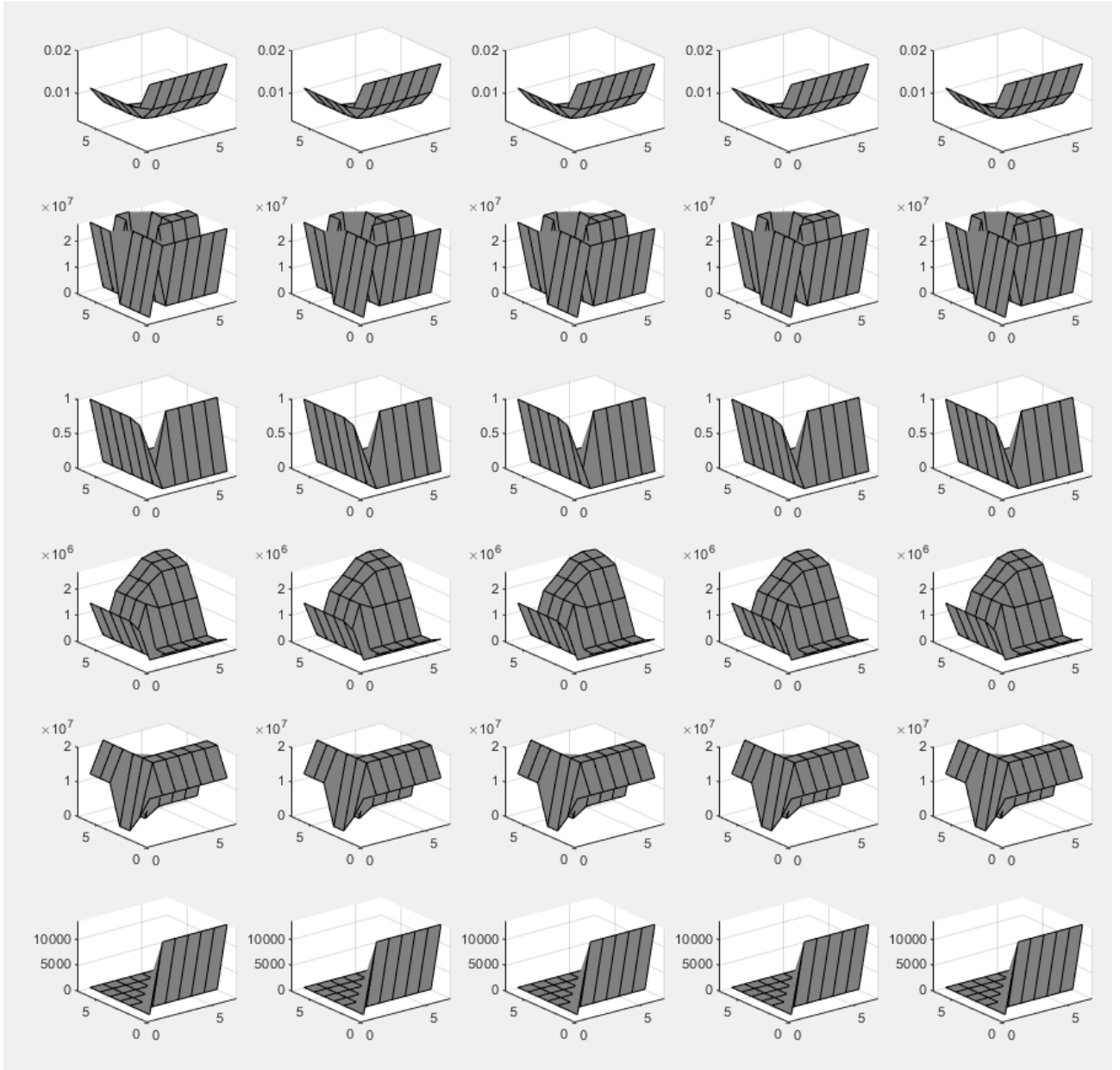


CES 560M - HW3

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This figure contains all information we need to analysis, all X axes are num-rob-entries, all Y axes are num-iq-entries, all Z axes are the parameters to record, num-phys-float-regs increasing from left to right, the parameter to record varis from top to down flowing the order shown below.

Parameters to record.

Number of seconds simulated
Number of floating rename lookups
Instruction Issue Rate
Idle cycles from register renaming
Number of times rename has blocked due to ROB full
Cache miss rates

So num-phys-float-regs didn't affect those parameters, that mostly because we start from 256, and that might be enough already. First row tell us that the number of seconds simulated is decreasing when one or both of num-rob-entries and num-iq-entries is increasing. The 6th row tell us that the number of times rename has blocked due to ROB full Cache miss rates is decreasing when num-rob-entries increasing. Other rows tell us some other similar informations. But notice that 2nd row(Number of floating rename lookups) has many abnormal peaks.