

## TVM Bootcamp - Backends

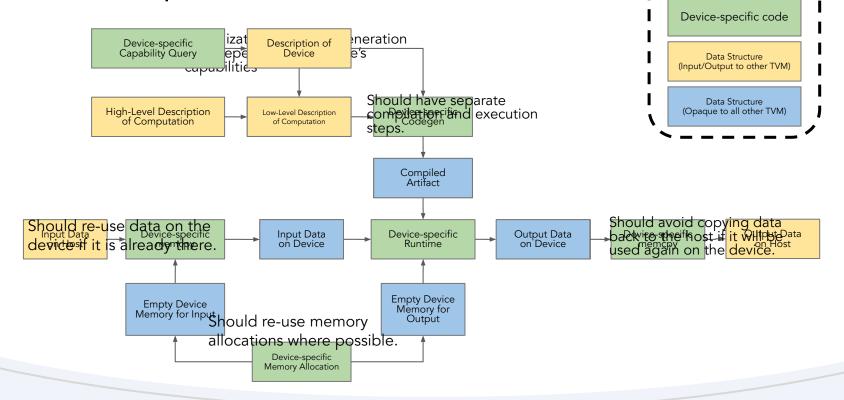
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#### Overview

- TVM supports several backend frameworks (e.g. CUDA, Vulkan, ROCm)
- Each backend must conform to a standard interface when interacting with the TVM remainder of the framework.
- This walkthrough is aimed at developers who want to implement/extend support for additional devices, and aren't necessarily going to be adding compiler features/optimizations.
  - Everything outside of the device-specific code will be treated as opaque.
- All descriptions and links refer to commit 6c8ed60ec, the most recent commit on main as of 2021-12-03.

## TVM Compilation/Runtime Flow





Key

#### DeviceAPI

- Main interaction point with the physical device. (Additional details are on later slides.)
- Each DeviceAPI subclass must have a function to return the global object Implemented as a singleton for each device type.

  o e.g. <u>VulkanDeviceAPI::Global()</u>, <u>CUDADeviceAPI::Global()</u>
- Register the global function as "device api.\$NAME"

  - e.g. TVM REGISTER GLOBAL ("device api.cuda")
    Global function is called from DeviceAPIManager::GetAPI to interact with a device.
- Add an entry to the TVMDeviceExtType enum representing the new DeviceAPI.
  - The value should be an unused value greater than DLDeviceType::kDLExtDev, but less than DeviceAPIManager::kMaxDeviceAPI.
  - This value is used to represent the device type both internally to TVM and when interacting with types defined in <a href="mailto:dipack">dipack</a>.
- Add the conversion from enum value to string in tvm::runtime::DeviceName. This string representation should match the name used earlier in TVM\_REGISTER\_GLOBAL.
- Add the conversion from enum value to string in tvm.runtime.Device.MASK2STR.
- Add the conversion from string to enum value in tvm.runtime.Device.STR2MASK.

Device-specific Capability Query

Device-specific Memory Allocation

Device-specific memcpy

Device-specific Runtime

DeviceAPI::GetAttr DeviceAPI::GetTargetProperty

DeviceAPI::AllocDataSpace DeviceAPI::AllocWorkspace

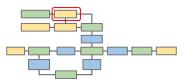
DeviceAPI::CopyDataFromTo

runtime:: PackedFunc



## tvm::Target Object

- Contains properties about the device (e.g. warp size), and the code generator to be used (e.g. llvm/nvptx/cuda). Can be serialized/deserialized for remote
- New targets should be registered with the TVM REGISTER TARGET KIND macro.
  - Defines the mapping from compile-time code generator to runtime device. (e.g. TVM\_REGISTER\_TARGET\_KIND("cuda", kDLCUDA) defines the "cuda" code generator, which should run on a device of type kDLCUDA.)
  - Defines attributes for the target. (e.g. <a href="mailto:add\_attr\_option<Integer>("thread\_warp\_size", Integer(32))</a>) defines the "thread\_warp\_size" option, with default value of 32.)
- Multiple code generators may be associated with the same runtime device.
  - e.g. The "Ilvm" and "c" code generators both run on the CPU.







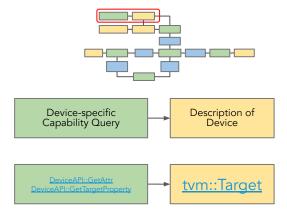


## Device Capability Query

- Typically stored into a tvm::Target object.
- DeviceAPI::GetAttr
  - Earlier API, looks up a device attribute listed in the enum DeviceAttrKind.
  - Most cases that require device information should look up the cached values in the tvm::Target object.
- DeviceAPI::GetTargetProperty
  - More recent API, looks up a device attribute by string.
  - Called during construction of a Target object, if the

```
\hbox{\tt "-from\_device=\$DEVICE\_NUM"} \textbf{property is given}.
```

```
■ e.g. target = tvm.target.Target("vulkan -from device=0")
```



## Low-Level TIR

- Some construct's in TVM's IR are high-level structures that are removed during the lowering process. Device-specific codegen only needs to handle the low-level structures that remain after the lowering has completed.

Low-Level Description of Computation

Allowed in low-level TIR

IRModule containing only Low-Level TIR

- tir::PrimFunc function definitions.
- Control flow (e.g. <u>ForNode</u>, <u>IfThenElseNode</u>)
  Variable definition and access (e.g. <u>LetNode</u>, <u>VarNode</u>)
- Computations (e.g. AddNode, SubNode)
- Memory allocation and access (e.g. AllocateNode, StoreNode<sup>1</sup>, LoadNode<sup>1</sup>)
- Not allowed in low-level TIR
  - relay::Function function definitions.

  - TE-specific nodes such as <a href="ProducerLoad/ProducerStore">ProducerStore</a>.
    Attributes that indicate transformations to be performed. (e.g. <a href="AttrStmtNode::attr-key">AttrStmtNode::attr-key</a> set to <a href="attr::double-buffer-scope">attr::double-buffer-scope</a>)
    Calls to TVM-specific built-in functions. (e.g. <a href="CallNode::op">CallNode::op</a> set to
  - builtin::tvm call packed)

#### runtime::Module

 A container with a <u>GetFunction</u> method, mapping from function name to <u>runtime</u>::PackedFunc.

Compiled Artifact

 Exact contents vary for each type of device, depending on what will be most useful during runtime.



- o e.g. LLVM codegen contain the compiled llvm::Module.
- o e.g. Vulkan generates a binary SPIR-V shader.
- For cross-compiling or distribution of models, should be able to read/write to disk.
  - Override virtual function ModuleNode::SaveToFile.
  - Register a function named "runtime.module.loadfile\_\$EXTENSION" (e.g. runtime.module.loadfile\_so). This is called from runtime::Module::LoadFromFile, after using the file extension to determine which loader to run.

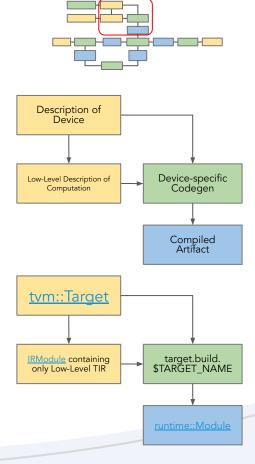


#### Code Generation

- From low-level TIR, build a runtime::Module that will be used at runtime.
  - Function signature

  - runtime::Module BuildCUDA(IRModule mod, Target target)
    Must be registered as "target.build.\$TARGET\_NAME"

    - e.g. <u>TVM\_REGISTER\_GLOBAL("target.build.nvptx").set\_body\_typed(BuildNVPTX);</u>
      Note: \$TARGET\_NAME must be the name of the target code generator, previously used in the TVM REGISTER TARGET KIND macro, not the name of the device. In some cases these are identical (e.g. "cuda" target uses "target.build.cuda" codegen, then runs on "cuda" device), but it is not always the case (e.g. "llvm" target uses "target.build.llvm" codegen, then runs on "cpu" device).
  - Called from codegen::Build
- Typical flow for a code generator.
  - Loop over all functions in <a href="IRModuleNode::functions">IRModuleNode::functions</a>
  - Generate a function signature from <a href="PrimFuncNode::params">PrimFuncNode::ret type</a>.
  - Generate a function body from PrimFuncNode::body. Iteration over the function body can be done by writing a class that inherits from both <a href="Exprf-unctor<void(const PrimExpr&">ExprFunctor<void(const PrimExpr&">PrimExpr&">ExprFunctor<void(const PrimExpr&">PrimExpr&">ExprFunctor<void(const PrimExpr&")></a> StmtFunctor<void(const Stmt&)>, then implementing handlers for the overloaded ExprFunctor::VisitExpr\_and StmtFunctor::VisitStmt\_methods.
  - If the output code generated requires it, perform any additional compilation steps.
    - e.g. target.build.cuda produces CUDA source code, which is then compiled. e.g. target.build.vulkan produces binary SPIR-V bytecode, which does not require
    - additional compilation.
  - 5. Wrap the build artifacts in a subclass of <a href="mailto:runtime::ModuleNode">runtime::ModuleNode</a>





## Execution Streams, Synchronization

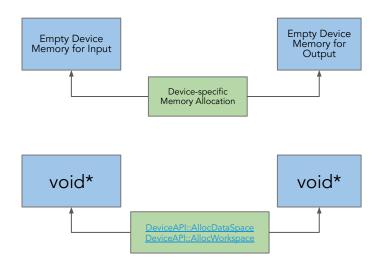
- Streams represent separate queues of work, which execute independently.
  - o In TVM, streams can contain either data transfers or computations.
- Operations
  - <u>DeviceAPI::CreateStream</u> Allocate a new stream of execution. TVM treats the return value as an opaque pointer, and passes it unmodified into other stream-related functions
  - <u>DeviceAPI::StreamSync</u> Synchronize between host and device. Host should wait until all operations queued to that device/stream execution have completed.
  - <u>DeviceAPI::SyncStreamFromTo</u> Add a synchronization point between two streams.
  - <u>DeviceAPI::FreeStream</u> Deallocate the stream.
- For runtimes that are synchronous (e.g. CPU), CreateStream should return nullptr, and stream manipulation functions should be no-ops.
- For runtimes that support only a single stream of execution (e.g. TVM's Vulkan runtime, as of end-of-year 2021), CreateStream should return nullptr, StreamSync should synchronize that single stream with the host, and all other stream manipulation functions should be no-ops.

Device-specific memcpy

Device-specific Runtime

## Memory Allocation

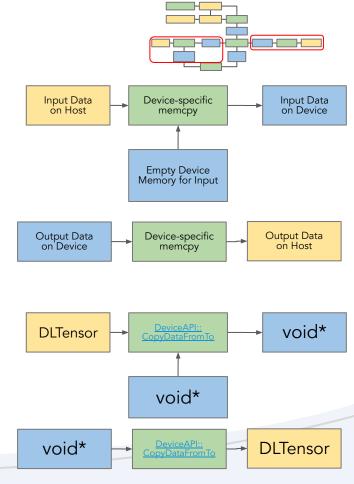
- Data space
  - Large, infrequently allocated
  - Allocated with <u>DeviceAPI::AllocDataSpace</u>.
  - Free with <u>DeviceAPI::FreeDataSpace</u>
  - May be triggered by an end user
    - CAPI, <u>TVMDeviceAllocDataSpace</u>
    - Python, tvm.nd.array
  - May be allocations internal to a Relay graph.
- Work space
  - Like data space, but with usage patterns typical of a stack. Devices may implement special handling for improved performance (e.g. <a href="OpenCL's AllocWorkspace">OpenCL's AllocWorkspace</a>, which uses <a href="mailto:runtime::WorkspacePool">runtime::WorkspacePool</a>), but it isn't required.
  - o If not implemented by a subclass, will fall back to using data space.
  - Allocate with <u>DeviceAPI::AllocWorkspace</u>
  - Free with DeviceAPI::FreeWorkspace
- The return value is a void\*, but is treated as an opaque pointer by everything outside of the device-specific code.
  - e.g. TVM's Vulkan runtime must track use of both VkBuffer and VkDeviceMemory, so <u>VulkanDeviceAPI::AllocDataSpace</u> returns a structure <u>that contains both handles</u>.
  - e.g. <u>CUDADeviceAPI::AllocDataSpace</u> does not require any additional bookkeeping, and returns the result of cudaMalloc.



## Memory Transfer

- DeviceAPI::CopyDataFromTo copies data in either direction
  - between the host and the device, or between multiple devices.

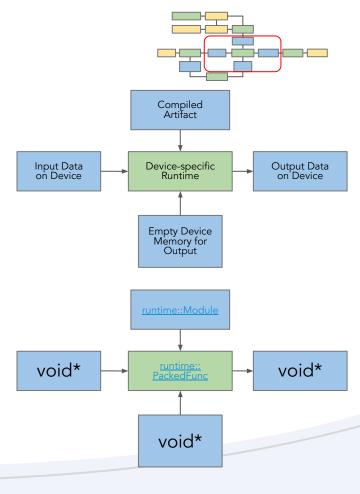
    o <u>public DeviceAPI::CopyDataFromTo</u>, whose <u>implementation</u> is a wrapper around the device-specific overload and takes <u>DLTensor</u> objects as arguments.
    - <u>protected DeviceAPI::CopyDataFromTo</u>, to be implemented by subclasses, takes arguments as described below.
- Arguments "dev\_from" and "dev\_to" are <a href="DLDevice">DLDevice</a> objects, and indicate which the device holds the source and destination locations.
- Arguments "from" and "to" give the source and destination memory locations.
  - These will be C-style memory pointers if the <u>Device::device\_type</u> for the corresponding device argument is kDLCPU.
  - Otherwise, these will be pointers generated by AllocDataSpace or AllocWorkspace.
- If the <u>TVMStreamHandle</u> passed in is non-null, the copy should be queued onto that execution stream and performed asynchronously. Otherwise, the copy should be performed synchronously.



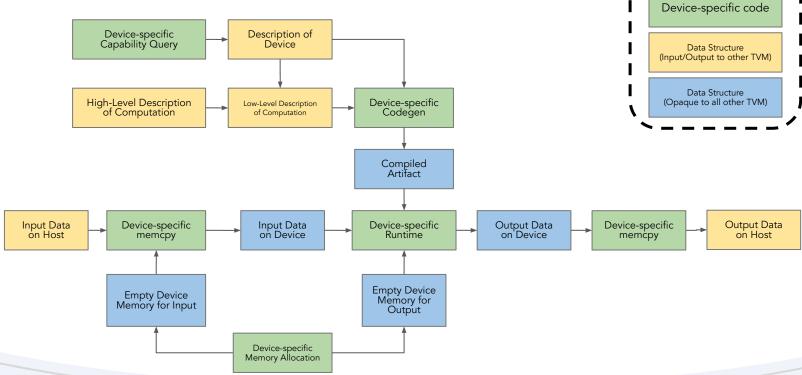


### Kernel Execution

- Uses <u>runtime::PackedFunc</u>, a wrapper around std::function and TVM's primary FFI structure.
- A PackedFunc generated from the <u>runtime::ModuleNode::GetFunction</u> is called.
  - Function arguments give the array inputs and any scalar parameters.
  - <u>DeviceAPI::SetDevice</u> and <u>DeviceAPI::SetStream</u> determine the execution stream for the computation.
- Things the device-specific code should do
  - Load the stored data if necessary.
    - e.g. cuModuleLoadData for CUDA
    - e.g. vkCreateShaderModule for Vulkan
  - Launch the shader using the buffers given.
    - e.g. execute a function pointer for LLVM modules, cuLaunchKernel for CUDA
    - e.g. vkQueueSubmit for Vulkan
- Things the device-specific code doesn't need to do
  - Argument/array type checks.
    - Already inserted as part of <u>tir::transform::MakePackedAPI</u>, part of the compiled code.
  - Transfer data to/from the host.
    - Already handled by <u>DeviceAPI::CopyDataFromTo</u>.
  - Wait for execution to complete, unless strictly necessary.
    - Synchronization handled by <u>DeviceAPI::StreamSync.</u>
    - Rare exception: Device-specific resource conflict, such as a <u>Vulkan</u> descriptor set already being in use.



# TVM Compilation/Runtime Flow



Key

#### TVM Compilation/Runtime Flow Key Device-specific code tvm::Target Data Structure (Input/Output to other TVM) Data Structure target.build. \$TARGET\_NAME (Opaque to all other TVM) High-Level Description IRModule containing of Computation only Low-Level TIR runtime:: void\* void\* DLTensor **DLTensor** PackedFunc DataFromTo<sup>'</sup> void\* void\*

## Potential Future Improvements

- Low-level runtime-specific test suite
  - Quantify support of features across backends.
  - Easier debugging after a test failure. High-level end-to-end tests of models can have backend-specific failures, which require a non-trivial amount of effort to track down to a root cause.
  - Provide a roadmap for additional runtimes to be implemented, by defining which low-level features are required.
- Low-level TIR validation
  - Would be an explicit check on the TIR being low-level, rather than relying on error checks within the code generation.