

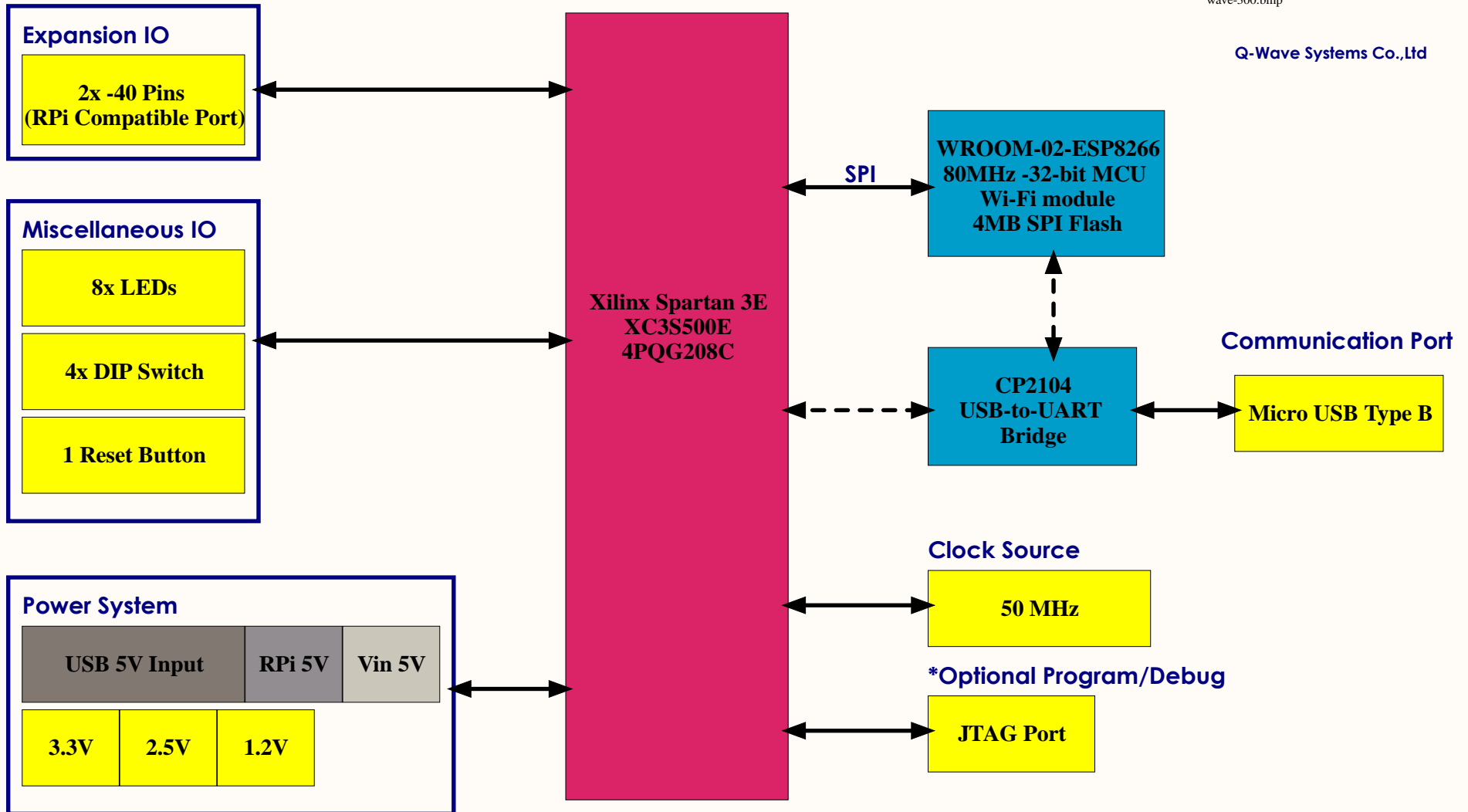
Cannot open file  
C:\Users\THEP\Desktop\MeconS3-FPGA\q  
wave-300.bmp

Title		
Size	Number	Revision
C		
Date:	4/24/2017	Sheet of
File:	C:\Users\THEP\Desktop\MeconS3-FPGA\q	Drawn By:

# Melon S3 FPGA Architecture

Cannot open file  
C:\Users\THEP\Desktop\MelonS3-FPGA\q  
wave-300.bmp

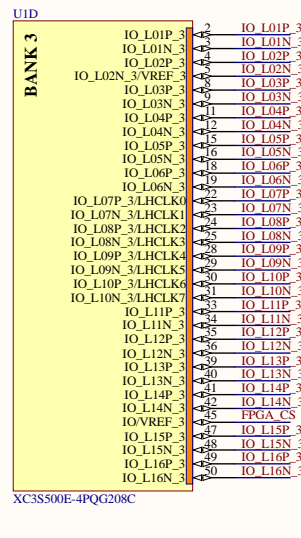
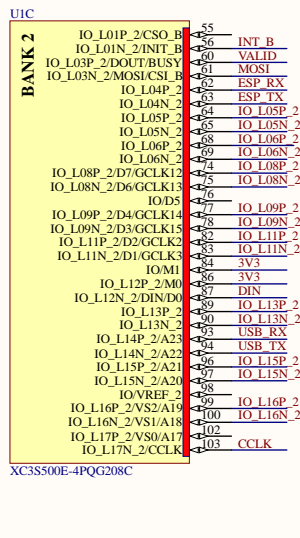
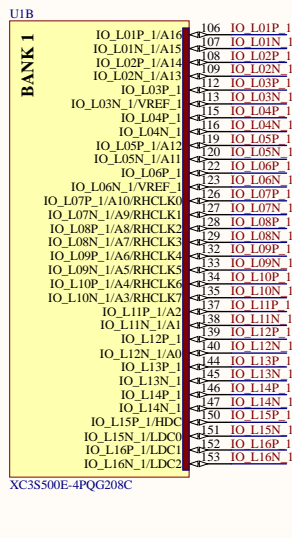
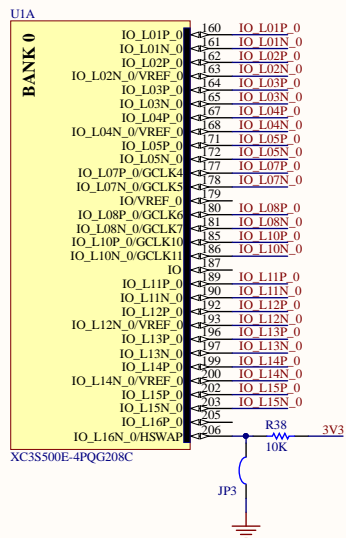
Q-Wave Systems Co.,Ltd



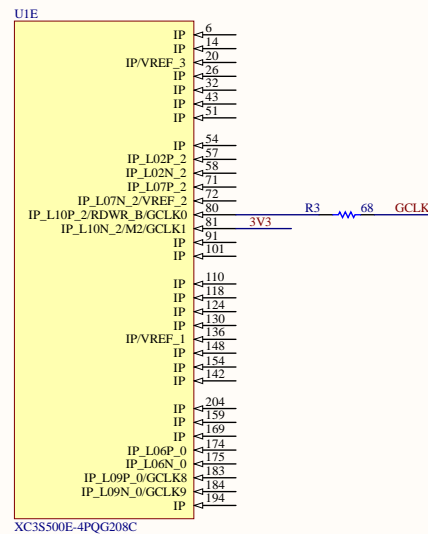
Title		
Size A4	Number	Revision
Date:	4/24/2017	Sheet of
File:	C:\Users\...\Block Diagram.SchDoc	Drawn By:

Cannot open file  
C:\Users\THEP\Desktop\MelonS3-FPGA\q  
wave-300.bmp

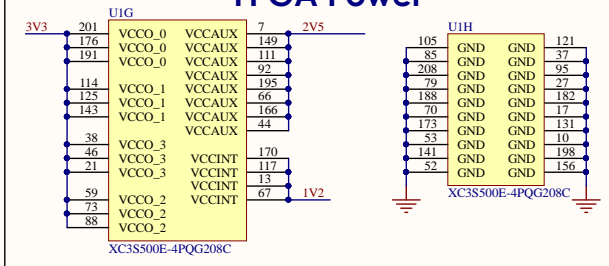
## FPGA Bank0,1,2,3



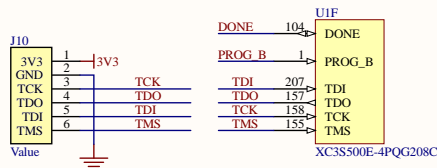
## FPGA Clock-In



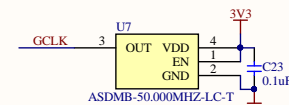
## FPGA Power



## JTAG



## 50MHz Clock



Title		
Size	Number	Revision
B		
Date:	4/24/2017	Sheet of
File:	C:\Users\...FPGA.SchDoc	Drawn By: