

Report

工科海洋三 B09505029 羅士朋

Inferred memory devices in process
in routine ALU line 161 in file
'/home/raid7_2/userb09/b9505029/CA_HW2/HW2.v'.

Register Name	Type	Width	Bus	MB	AR	AS	SR	SS	ST
alu_in_reg	Flip-flop	32	Y	N	Y	N	N	N	N
shreg_reg	Flip-flop	64	Y	N	Y	N	N	N	N
state_reg	Flip-flop	3	Y	N	Y	N	N	N	N
counter_reg	Flip-flop	5	Y	N	Y	N	N	N	N