mcu/usb/power

File: mcu.kicad_sch

baseband

File: baseband.kicad_sch

frontend

File: frontend.kicad_sch

LPC4320FBD144 (U23)

W25Q80 (U20)

TPS562410 (U21)

GSG-XC2C64A-7VQG100C (U24)

ECMF02-4CMX8 (L15)

Connector_USB_C_Receptacle_USB2.0 (S1)

Cortex Debug Connector (10 pin 50 mil)

LPC_ISP (P25) — DNP

LPC_JTAG (P26) — DNP

RTC_XTAL (X3) — DNP

MCU_XTAL (X2)

C128 C129 C130 C131 C132 C133 C134 C135 C137 C138 C139 C140 C141 C142
100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF 100nF

Default boot configuration is SPIfi.
Press SW1 during reset to switch to USB0 (DFU mode).

U23 starts up on this 12 MHz crystal. This makes it possible to use USB0 DFU boot mode. It should be possible to switch to GP_CLKIN provided by the clock generator IC (MCL_CLK) if desired.

Series resistors are here because of a possible overshoot/undershoot problem. They may be able to be removed safely. Anyway, they probably will minimize damage in the event of SGPIO/CPLD misconfiguration.

See also I2S0 pins on P20.

Boot selection:
	P2_9	P2_8	P1_2	P1_1
USART0	GND	GND	GND	GND
SPIFI	GND	GND	GND	VCC
USB0	GND	GND	VCC	GND
SSP0	GND	VCC	GND	VCC
USART3	GND	VCC	VCC	GND

PCB trace jumper

+1.8V

BSS84 (Q5)

BANK2_AUX (P30) — DNP

SD (P28)

LED5 (P5) — DNP