Chendi Li

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Carlon Linkein

Github

I'm a graduate student from Institute of Computing Technology, Chinese Academy of Sciences(ICT.CAS). I have some experience on optimizing algorithms on CPUs like ARMv8 and X86.

EDUCATION

University of Chinese Academy of Sciences Aug 2019 - Jun 2022 | Beijing

Computer Science

Hunan Agricultural University Aug 2014 - Jun 2018 | Hunan

Computer Science

EXPERIENCE

Graduate Research Assistant

Sep 2019 - Present | ICT.CAS

Graduate research assistant at ICT.CAS. I participate in two projects, OpenVML and OpenBLAS.

Undergraduate Research Assistant

Dec 2017 - Jun 2019 | ICT.CAS

Undergraduate research assistant at ICT.CAS. I was responsible for optimizing small-scale FFT on ARMv8 architecture.

PUBLICATIONS

AutoTSMM:An Auto-tuning Framework for Building High-Performance Tall- First author. and-Skinny Matrix-Matrix Multiplication on CPUs (IEEE ISPA 2021)

This paper proposes an auto-tune framework, AutoTSMM, to build high-performance tall-and-skinny matrix-matrix multiplications on multi-core platforms.

PROJECTS

OpenBLAS Contributor.

OpenBLAS is an open-source BLAS library, I'm responsible for optimizing pre-pack matrix-matrix multiplication and triangular solve with multiple right-hand-sides(TRSM) on ARMv8 and X86 platforms.

OpenVML Contributor.

OpenVML is a vector math library. I'm responsible for optimizing the math functions on ARMv8 architecture.

AutoFFT Contributor.

I'm responsible for optimizing small-scale FFT on ARMv8 architecture.

SKILLS

Soft Skills Self-Motivation, Self-Confidence, Handling Pressure, Effective Communication, Teamwork

Hard Skills C, Latex, Assembly, Python, Neon intrinsic, Parallel Programming
Tools Linux, Git, Vim, CMake, OpenMP, Pthreads