

TÕNIS LUSMÄGI

ELECTRICAL- AND COMPUTER AND SYSTEMS ENGINEER

PROFILE

29-year old Electrical- and Computer and
Systems Engineer from Tallinn with 5
years of experience.

CONTACT

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CODE

С

VHDL

SYSTEMVERILOG

EDUCATION

COMPUTER AND SYSTEMS ENGINEERING @ TALLINN UNIVERSITY OF TECHNOLOGY

September 2017 - Present

I'm a 2nd year master's student at TTÜ. As a Computer and Systems Engineer, I am involved with Digital Systems and Systems-on-Chip Design, Verification and Testing of Digital Systems, and Software Engineering. I have studied Automation and Machine Learning, also Relational Database Management Systems. My main programming languages are C and VHDL. I learn new programming languages rapidly.

ELECTRICAL ENGINEERING @ TALLINN UNIVERSITY OF APPLIED SCIENCES

September 2013 - June 2017

I graduated *Cum Laude* in Electrical Engineering from Tallinn University of Applied Sciences. I studied Electrical- and Control Systems Design from Electronics to High Voltage. I have electrician's A-level competency certificate (permit to work on High Voltage Electrical Installations). Also, I have Electrical Engineering European Qualifications Framework Level 6 qualification. I wrote my diploma thesis on Battery Systems for EVs.

SKILLS

- Operating Systems: Windows, Linux
- Office: Microsoft Office, LibreOffice, WPS Office
- Programming: C, VHDL, SystemVerilog, SystemC, MATLAB, SQL, UML, ST, Git
- Programming (learning): C++, C#, Python, Java
- EDA tools: Xilinx Vivado, Mentor Graphics ModelSim/QuestaSim
- Electronics CAD: Autodesk EAGLE
- 2D CAD: Autodesk AutoCAD, CADS Planner
- 3D CAD: Autodesk Fusion 360, Solid Edge, Rhino
- Development Boards: AVR, FPGA
- Audio-Video-Photo Editing: Adobe Premiere, Adobe Photoshop, GIMP
- Languages: Estonian (native), English (C1), German (A1), Russian (A1)
- Interpersonal Skills: competent in communication, collaborative work experience
- Leadership Skills: software project leading, team leading, business leading

WORK EXPERIENCE

ELECTRICAL ENGINEERING INTERNSHIP @ ELEKTRILEVI

June 2016 - August 2016

Electrical Design for High Voltage Electrical Installations. Using CADS Planner, Microsoft Office and Network Information System (NIS).

ELECTRICAL TECHNICIAN INTERNSHIP @ ABB

June 2015 - August 2015

Building Low Voltage Systems, electric circuits for automation and power, cosmetic finishing. I learnt how to read complex schematic diagrams and build circuits accordingly. I took part in product QA process (Test Engineering).