

# TÕNIS LUSMÄGI

ELECTRICAL- AND COMPUTER AND SYSTEMS ENGINEER

### **PROFILE**

30-year old Electrical- and Computer and
Systems Engineer from Tallinn with 6
years of experience.

### CONTACT

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### CODE

С

VHDL

SYSTEMVERILOG

#### **EDUCATION**

# COMPUTER AND SYSTEMS ENGINEERING @ TALLINN UNIVERSITY OF TECHNOLOGY

September 2017 - Present

I'm a 2<sup>nd</sup> year master's student at TalTech. As a Computer and Systems Engineer, I design Digital Systems and Systems-on-Chip. I'm proficient in Verification, Validation and Testing. Along with Computer Hardware Development, I'm engaged in Automation, Software Engineering and Machine Learning. My main programming languages are C, C++, Java, Python and VHDL.

# ELECTRICAL ENGINEERING @ TALLINN UNIVERSITY OF APPLIED SCIENCES

September 2013 - June 2017

I graduated *Cum Laude* in Electrical Engineering from Tallinn University of Applied Sciences. I studied Electrical- and Control Systems Design from Electronics to High Voltage. I have electrician's A-level competency and Electrical Engineering EQF Level 6. I wrote my diploma thesis on Battery Systems for Electric Aircrafts.

### **SKILLS**

- Operating Systems: Windows, Linux
- Office: Microsoft Office, LibreOffice, WPS Office
- Programming: C, VHDL, SystemVerilog, SystemC, MATLAB, SQL, UML, ST, Git
- Programming (learning): C++, C#, Python, Java
- EDA tools: Xilinx Vivado, Mentor Graphics ModelSim/QuestaSim
- Electronics CAD: Autodesk EAGLE
- 2D CAD: Autodesk AutoCAD. CADS Planner
- 3D CAD: Autodesk Fusion 360, Solid Edge, Rhino
- Development Boards: AVR, FPGA
- Audio-Video-Photo Editing: Adobe Premiere, Adobe Photoshop, GIMP
- Languages: Estonian (native), English (C1), German (A1), Russian (A1)
- $\bullet \ \textbf{Interpersonal Skills:} \ competent \ in \ communication, \ collaborative \ work \ experience \\$
- Leadership Skills: software project leading, team leading, business leading

### **WORK EXPERIENCE**

# RESEARCH & DEVELOPMENT INTERN @ NIXOR

September 2018 - October 2018

Electronics Design and Software Development.

# ELECTRICAL ENGINEERING INTERNSHIP @ ELEKTRILEVI

June 2016 - August 2016

Electrical Design for High Voltage Electrical Installations. Using CADS Planner, Microsoft Office and Network Information System (NIS).

### ELECTRICAL TECHNICIAN INTERNSHIP @ ABB

June 2015 - August 2015

Building Low Voltage Systems, electric circuits for automation and power. Reading complex schematic diagrams and build circuits. Test Engineering.