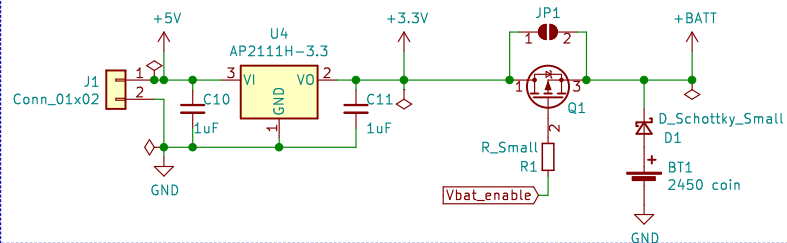
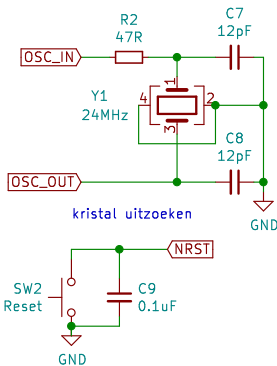
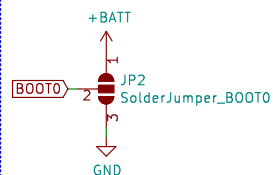
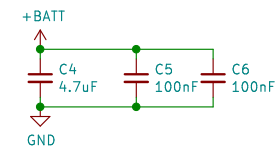
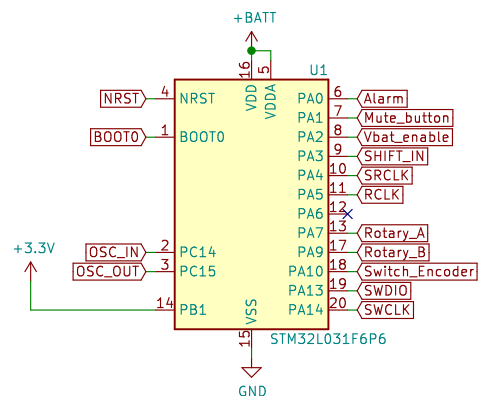


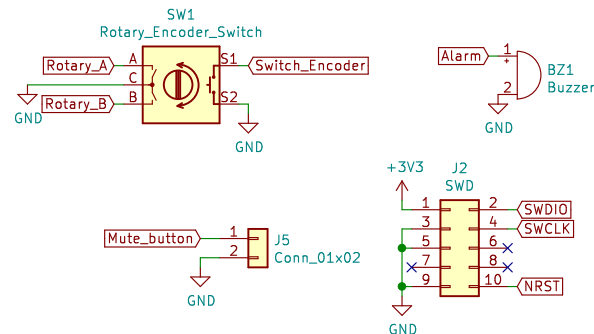
Power input. LDO regulates 5V down to 3.3V. With battery backup and a PMOS that prevents backpowering power hungry devices.



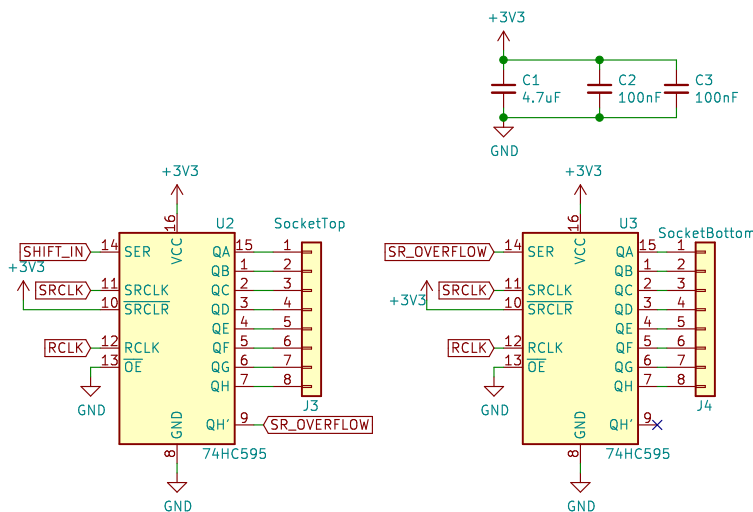
STM32L series chosen for its low power usage and PVD capabilities.



Input and output for the microcontroller, aswell the programming header for the microcontroller.



Shift register used for multiplexing the 14 segment display, with decoupling. For the display a COM-21217 is used.



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