## Daniel Lutes January 31 2015 COMP 273 Assignment 1

```
3.
Α
// RI stopping point for while loop;
RI = 0010\ 0110\ 0100_2;
//RII address of 8 bit output slot.
RII = 0000\ 0000\ 0000\ 1111_2;
// Address register stores the address 200<sub>16</sub>
AR = 0010\ 0000\ 0000_2;
//While less than 100 address have been loaded loop
while (AR<RI){
//Takes data from RAM are address AR
LOAD (DR,AR)
//Transfer data from DR to RAM slot which leads to machine.
SAVE (DR, RII)
// Increments address
AR++;
}
```

## B.

The Load command will send the first Byte of data at address  $0010\ 0000\ 0000_2$  into the DR via the read method. Then when the Save command will send the data from the DR, to address  $0000\ 0000\ 0000\ 1111_2$  via the write method. This is the path the data bytes take, assuming that the DR is not in the CPU.