Methodlology

Iterative top down process

Define algorithms:

The function of each large module is written in plain English

Define Finite state machine- Data FSM-D interface:

The control and data inputs and outputs required by the module to perform its functions are represented pictorially. It may thenn be required divide the tasks into submodules, smaller FSM-D within the larger module.

The data path of the module is defined:  
description of the path of data as it is processed to from input to output

The control path is defined:

The finite state machine of the module/submodule is developed according to the algorithm, this defined the various distinct states that the module will assume, that is the output of the system at different states and the inputs that cause transitions between states. Depending on the complexity of the algorithm and the nature of the tasks. It may be possible or necessary to divide the algorithm into concurrent processes. The processes and the communication signals between them can be described using a process graph model (PGM)