



HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY
COMPUTER ENGINEERING

Microcontroller



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Mục lục

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CHƯƠNG 1

Flow and Error Control in Communication



1 Introduction

Flow control and Error control are the two main responsibilities of the data link layer, which is a communication channel for node-to-node delivery of the data. The functions of the flow and error control are explained as follows.

Flow control mainly coordinates with the amount of data that can be sent before receiving an acknowledgment from the receiver and it is one of the major duties of the data link layer. For most of the communications, flow control is a set of procedures that mainly tells the sender how much data the sender can send before it must wait for an acknowledgment from the receiver.

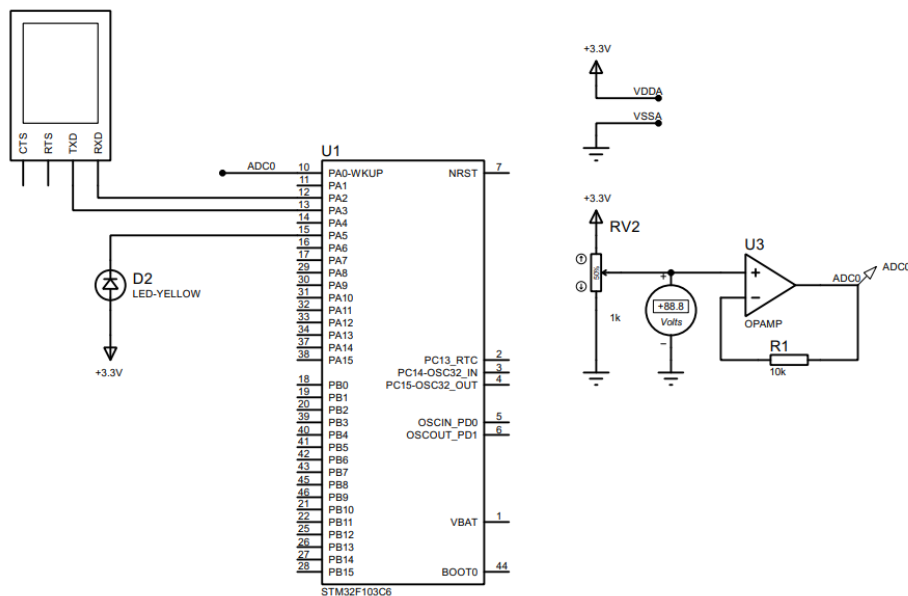
A critical issue, but not really frequently occurred, in the flow control is that the processing rate is slower than the transmission rate. Due to this reason each receiving device has a block of memory that is commonly known as buffer, that is used to store the incoming data until this data will be processed. In case the buffer begins to fill-up then the receiver must be able to tell the sender to halt the transmission until once again the receiver become able to receive.

Meanwhile, error control contains both error detection and error correction. It mainly allows the receiver to inform the sender about any damaged or lost frames during the transmission and then it coordinates with the re-transmission of those frames by the sender.

The term Error control in the communications mainly refers to the methods of error detection and re-transmission. Error control is mainly implemented in a simple way and that is whenever there is an error detected during the exchange, then specified frames are re-transmitted and this process is also referred to as Automatic Repeat request (ARQ).

The target in this lab is to implement a UART communication between the STM32 and a simulated terminal. A data request is sent from the terminal to the STM32. Afterward, computations are performed at the STM32 before a data packet is sent to the terminal. The terminal is supposed to reply an ACK to confirm the communication successfully or not.

2 Proteus simulation platform



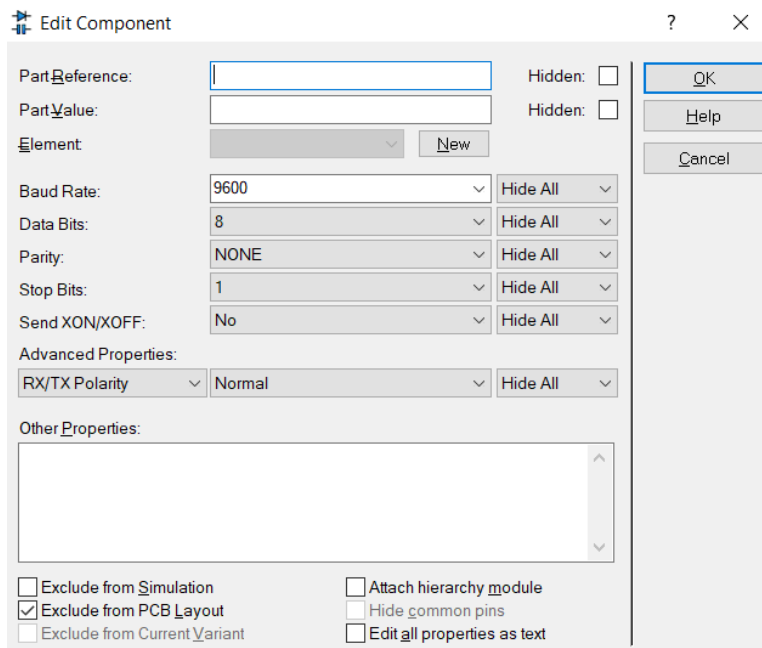
Hình 1.1: Simulation circuit on Proteus

Some new components are listed bellow:

- **Terminal:** Right click, choose Place, Virtual Instrument, then select VIRTUAL TERMINAL.
- **Variable resistor (RV2):** Right click, choose Place, From Library, and search for the POT-HG device. The value of this device is set to the default 1k.
- **Volt meter (for debug):** Right click, choose Place, Virtual Instrument, the select DC VOLTMETER.
- **OPAMP (U3):** Right click, choose Place, From Library, and search for the OPAMP device.

The opamp is used to design a voltage follower circuit, which is one of the most popular applications for opamp. In this case, it is used to design an adc input signal, which is connected to pin PA0 of the MCU.

Double click on the virtual terminal and set its baudrate to 9600, 8 data bits, no parity and 1 stop bit, as follows:



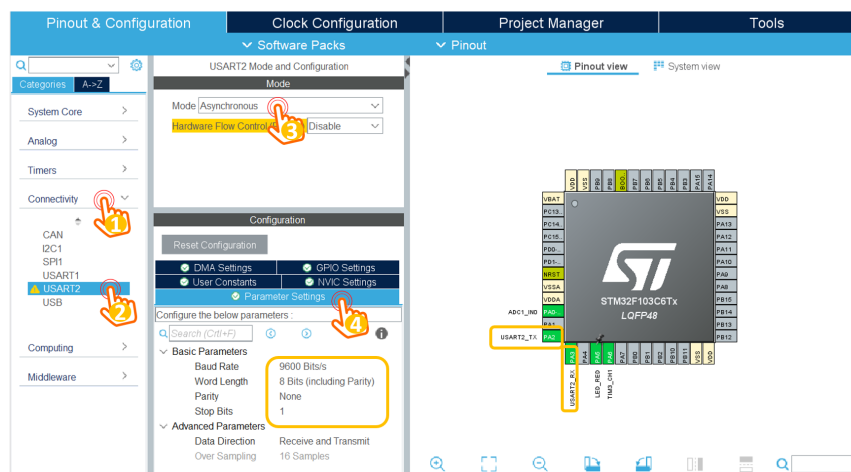
Hình 1.2: Terminal configuration

3 Project configurations

A new project is created with following configurations, concerning the UART for communications and ADC input for sensor reading. The pin PA5 should be an GPIO output, for LED blinky.

3.1 UART Configuration







From the ioc file, select **Connectivity**, and then select the **USART2**. The parameter settings for UART channel 2 (USART2) module are depicted as follows:



Hình 1.3: UART configuration in STMCube

The UART channel in this lab is the Asynchronous mode, 9600 bits/s with no Parity and 1 stop bit. After the uart is configured, the pins PA2 (Tx) and PA3(Rx) are enabled.

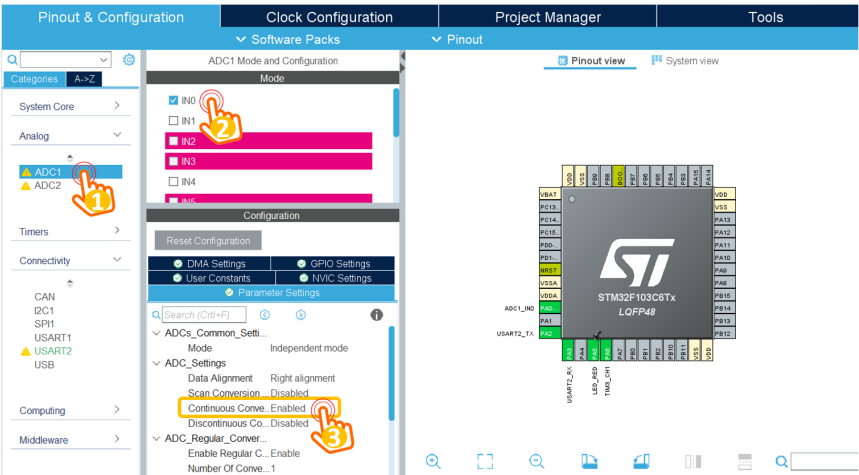
Finally, the NVIC settings are checked to enable the UART interrupt, as follows:

 DMA Settings	 GPIO Settings	
 User Constants	 NVIC Settings	
 Parameter Settings		
NVIC Interrupt Table	Enabled	Preemption P
USART2 global interrupt		0

Hình 1.4: Enable UART interrupt

3.2 ADC Input

In order to read a voltage signal from a simulated sensor, this module is required. By selecting on **Analog**, then **ADC1**, following configurations are required:



Hình 1.5: Enable UART interrupt

The ADC pin is configured to PA0 of the STM32, which is shown in the pinout view dialog.

Finally, the PA5 is configured as a GPIO output, connected to a blinky LED.

4 UART loop-back communication

This source is required to add in the main.c file, to verify the UART communication channel: sending back any character received from the terminal, which is well-known as the loop-back communication.

```

1  /* USER CODE BEGIN 0 */
2  uint8_t temp = 0;
3
4  void HAL_UART_RxCpltCallback(UART_HandleTypeDef *huart){
5      if(huart->Instance == USART2){
6          HAL_UART_Transmit(&huart2, &temp, 1, 50);
7          HAL_UART_Receive_IT(&huart2, &temp, 1);
8      }
9  }
10 /* USER CODE END 0 */

```

Program 1.1: Implement the UART interrupt service routine

When a character (or a byte) is received, this interrupt service routine is invoked. After the character is sent to the terminal, the interrupt is activated again. This source code should be placed in a user-defined section.

Finally, in the main function, the proposed source code is presented as follows:

```

1  int main(void)
2  {
3      HAL_Init();
4      SystemClock_Config();
5
6      MX_GPIO_Init();
7      MX_USART2_UART_Init();
8      MX_ADC1_Init();
9
10     HAL_UART_Receive_IT(&huart2, &temp, 1);
11
12     while (1)
13     {
14         HAL_GPIO_TogglePin(LED_RED_GPIO_Port, LED_RED_Pin);
15         HAL_Delay(500);
16     }
17
18 }

```

Program 1.2: Implement the main function

5 Sensor reading

A simple source code to read adc value from PA0 is presented as follows:

```

1  uint32_t ADC_value = 0;
2  while (1)
3  {
4      HAL_GPIO_TogglePin(LED_RED_GPIO_Port, LED_RED_Pin);
5      ADC_value = HAL_ADC_GetValue(&hadc1);

```

```

6 HAL_UART_Transmit(&huart2, (void *)str, sprintf(str, "%d\n"
    , ADC_value), 1000);
7     HAL_Delay(500);
8 }

```

Program 1.3: ADC reading from AN0

Every half of second, the ADC value is read and its value is sent to the console. It is worth noticing that the number ADC_value is convert to ascii character by using the sprintf function.

The default ADC in STM32 is 13 bits, meaning that 5V is converted to 4096 decimal value. If the input is 2.5V, ADC_value is 2048.

6 Project description

In this lab, a simple communication protocol is implemented as follows:

- From the console, user types **!RST#** to ask for a sensory data.
- The STM32 response the ADC_value, following a format **!ADC=1234#**, where 1234 presents for the value of ADC_value variable.
- The user ends the communication by sending **!OK#**

The timeout for waiting the **!OK#** at STM32 is 3 seconds. After this period, its packet is sent again. **The value is kept as the previous packet.**

6.1 Command parser

This module is used to received a command from the console. As the reception process is implement by an interrupt, the complexity is considered seriously. The proposed implementation is given as follows.

Firstly, the received character is added into a buffer, and a flag is set to indicate that there is a new data.

```

1 #define MAX_BUFFER_SIZE 30
2 uint8_t temp = 0;
3 uint8_t buffer[MAX_BUFFER_SIZE];
4 uint8_t index_buffer = 0;
5 uint8_t buffer_flag = 0;
6 void HAL_UART_RxCpltCallback(UART_HandleTypeDef *huart){
7     if(huart->Instance == USART2){
8
9         //HAL_UART_Transmit(&huart2, &temp, 1, 50);
10        buffer[index_buffer++] = temp;
11        if(index_buffer == 30) index_buffer = 0;

```

```

12
13     buffer_flag = 1;
14     HAL_UART_Receive_IT(&huart2, &temp, 1);
15 }
16 }

```

Program 1.4: Add the received character into a buffer

A state machine to extract a command is implemented in the while(1) of the main function, as follows:

```

1 while (1){
2     if(buffer_flag == 1){
3         command_parser_fsm();
4         buffer_flag = 0;
5     }
6 }

```

Program 1.5: State machine to extract the command

The output of the command parser is to set **command_flag** and **command_data**. In this project, there are two commands, **RTS** and **OK**. The program skeleton is proposed as follows:

```

1 while (1){
2     if(buffer_flag == 1){
3         command_parser_fsm();
4         buffer_flag = 0;
5     }
6     uart_communiation_fsm();
7 }

```

Program 1.6: Program structure

6.2 Project implementation

Students are proposed to implement 2 FSM in seperated modules. Students are asked to design the FSM before their implementations in STM32Cube.

```

1 void command_parser_fsm() {
2     switch (status) {
3     case INIT:
4         if (temp == '!')
5             status = READING;
6         break;
7     case READING:
8         if (temp != '!' && temp != '#') {
9             command_data[index_command] = temp;
10            index_command++;
11        }
12        if (temp == '#') {
13            status = STOP;

```

```

14     index_command = 0;
15 }
16 break;
17 case STOP:
18 //     str = RST
19     if (command_data[0] == 'R' && command_data[1] == 'S' &&
20         command_data[2] == 'T') {
21         command_status = RST;
22         setTimer1(1);
23     }
24 //     str = OK
25     else if (command_data[0] == 'O' && command_data[1] == 'K') {
26         command_status = OK;
27     }
28     status = INIT;
29     break;
30 default:
31     break;
32 }

```

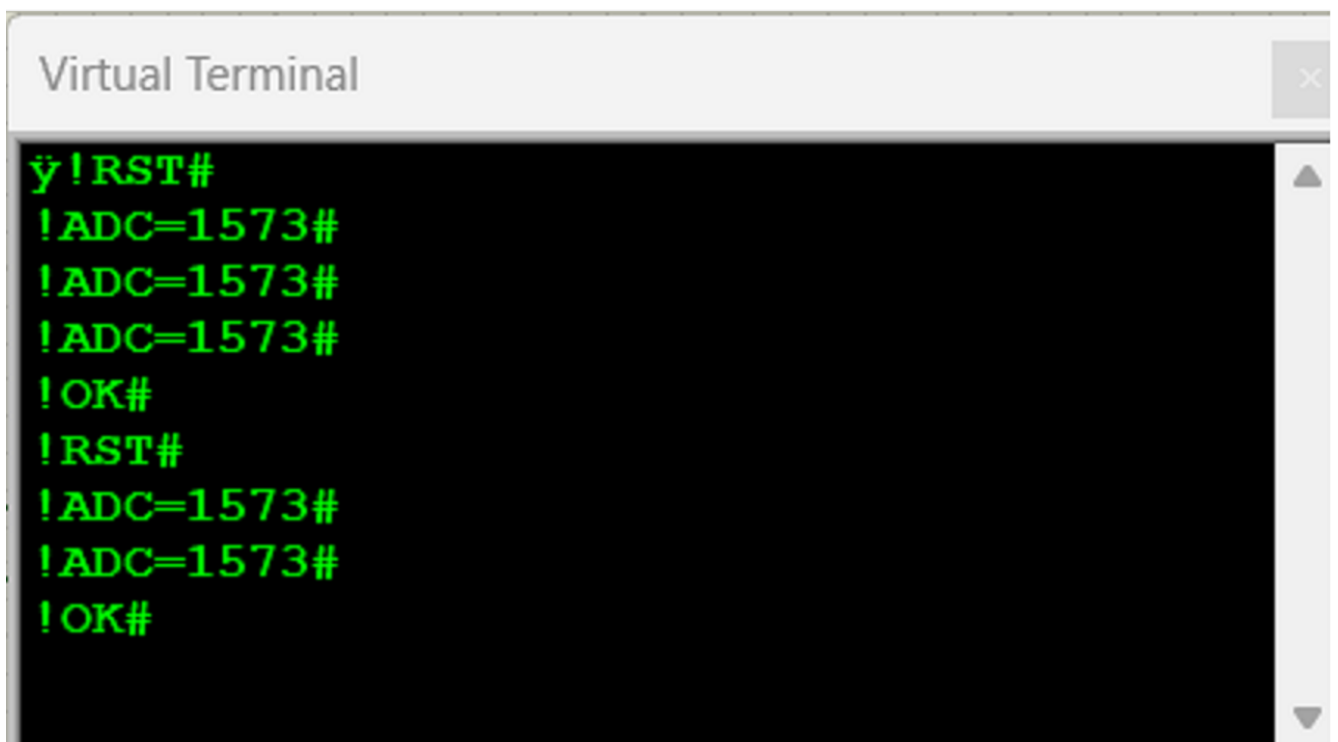
Program 1.7: *command_parser_fsm() function*

```

1 void uart_communication_fsm() {
2     switch (command_status) {
3     case INIT:
4         break;
5     case RST:
6         if (timer1_flag == 1) {
7             ADC_value = HAL_ADC_GetValue(&hadc1);
8             HAL_UART_Transmit(&huart2, (void*) str, sprintf(str,
9                 "!ADC=%d#\r\n", ADC_value), 1000);
10            setTimer1(TIME_WAITING);
11        }
12        break;
13     case OK:
14         ADC_value = 0;
15         command_status = INIT;
16         break;
17     default:
18         break;
19 }

```

Program 1.8: *uart_communication_fsm() function*



Hình 1.6: Communication Protocol