```
A inst A1
     A send C
     B send A
     A recv B
     A inst A2
     A send D
     B inst B1
     C inst C1
     C inst C2
     C recv A
     C inst C3
     C send B
     B recv C
    B inst B2
    D inst D1
    D send A
    A recv D
     A inst A3
     D inst D2
     D send B
22
    B recv D
```