ISL6236A Project

Variant Name = 5V Variant

Friday, February 23, 2018

V1I1

RELEASED 23-FEB-2018

DESIGN CONSIDERATION

DESIGN NOTE:
Example text for informational design notes.

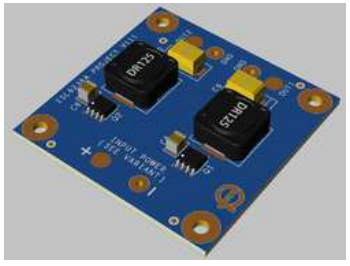
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for debug notes.

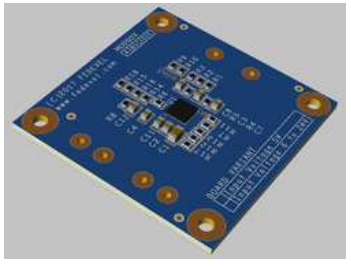
DESIGN NOTE:
Example text for critical design notes.

DESIGN NOTE:
Example text for critical layout guidelines.

TOP VIEW



BOTTOM VIEW



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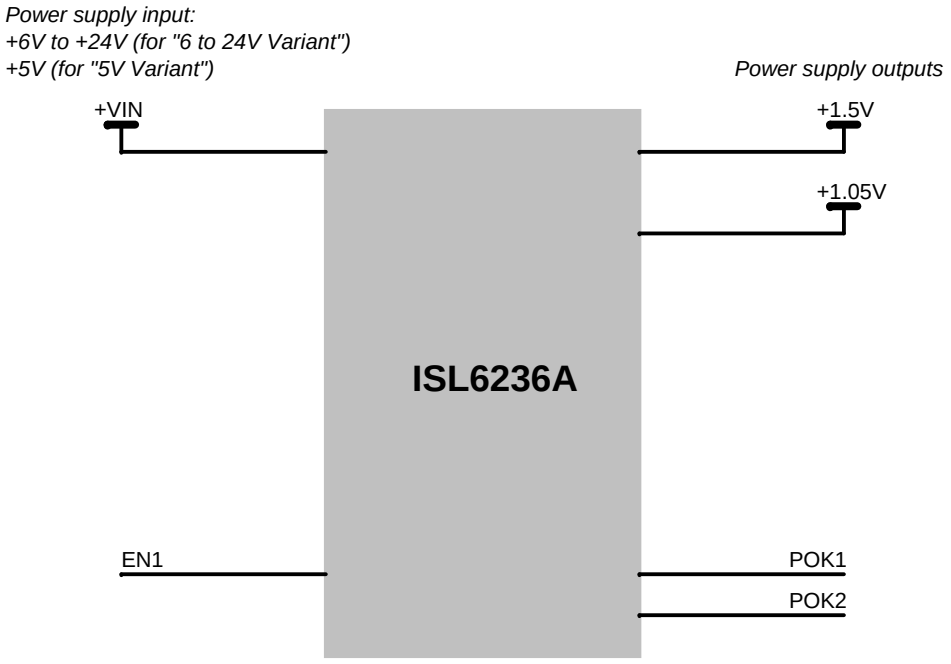
Title: ISL6236A Project			Variant: <Core Design>
Page: 01) COVER PAGE			Checked by: <Checked by>
Size: Custom	Document: LEOA	Designed by: Robert Feranec	Rev: V1I1
Date: Friday, February 23, 2018		Sheet: 1 of 5	

ISL6236A PROJECT
(Block Diagram)



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Page: 02) BLOCK DIAGRAM			Checked by: <Checked by>
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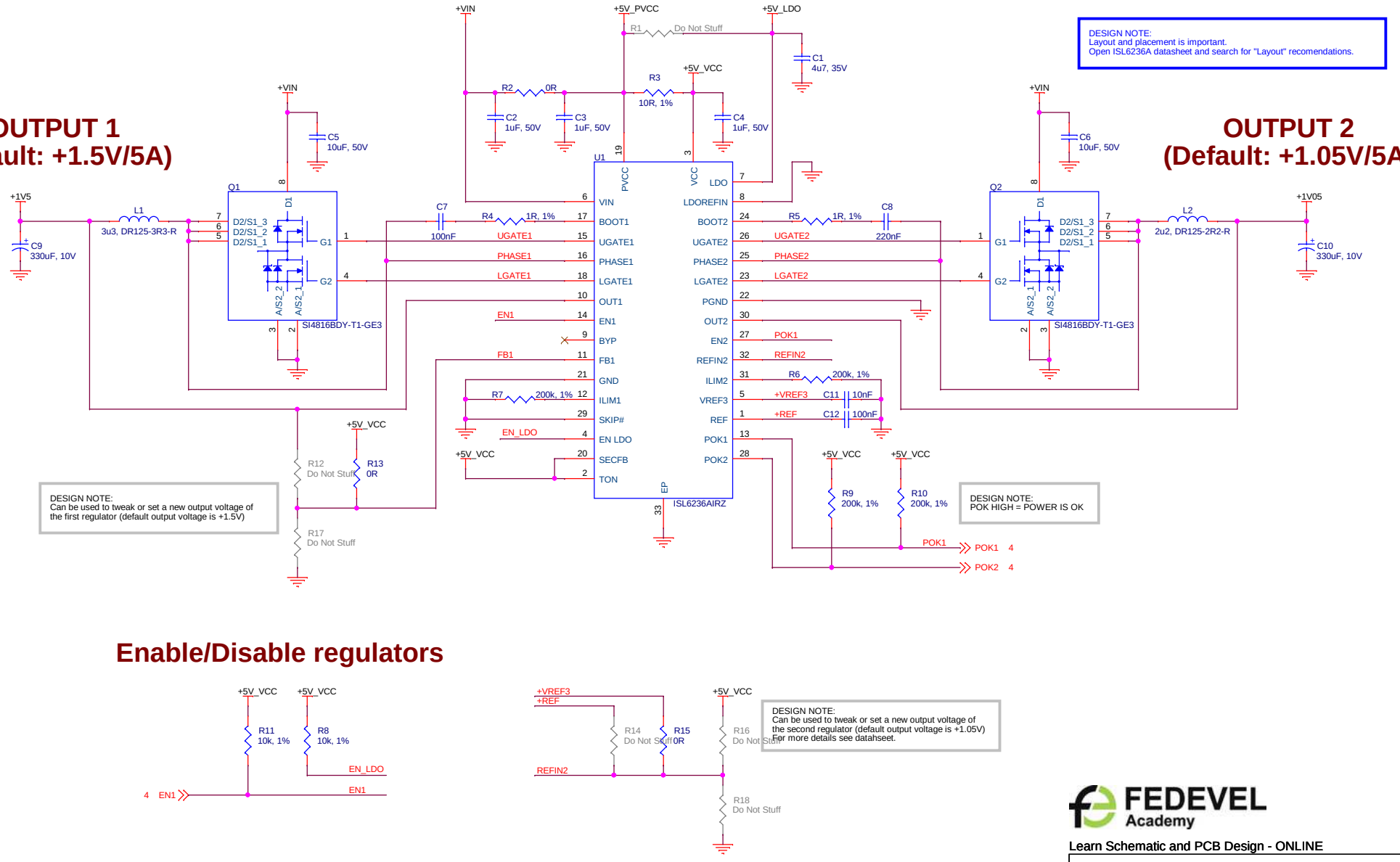
1.5V / 1.05V POWER SUPPLY



Enable/Disable regulators

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Page: 03) +1.5V / +1.05V POWER SUPPLY	Checked by: <Checked by>
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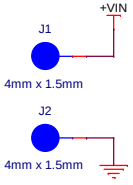
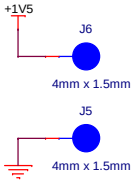
PADS, TESTPOINTS, MECHANICAL

Soldering pads for input and output wires

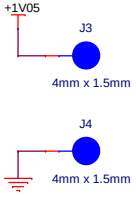
+VIN: Main input power
(Default from +6V to +24V)



Power supply output 1
(Default +1.5V)



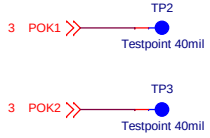
Power supply output 2
(Default +1.05V)



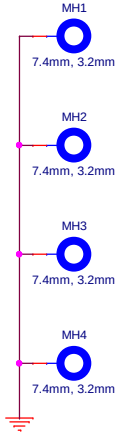
Testpoints

ENABLE Testpoint

POK Testpoints

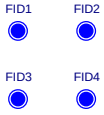


Mounting holes

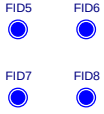


Fiducials

TOP



Bottom



PCB

PCB ISL6236A Project V1I1

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REVISION HISTORY

01-NOV-2017: **Put here a brief info about the change**
Detailed description of all the changes - Testing Lite

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