

RELEASED 23-FEB-2018

Page	Index	Page	Index	Page	Index
1	COVER PAGE	11	21
2	BLOCK DIAGRAM	12	22
3	+1.5V / +1.05V POWER SUPPLY	13	23
4	PADS AND TESTPOINTS	14	24
5	REVISION HISTORY	15	25
6	16	26
7	17	27
8	18	28
9	19	29
10	20	30

DESIGN CONSIDERATION

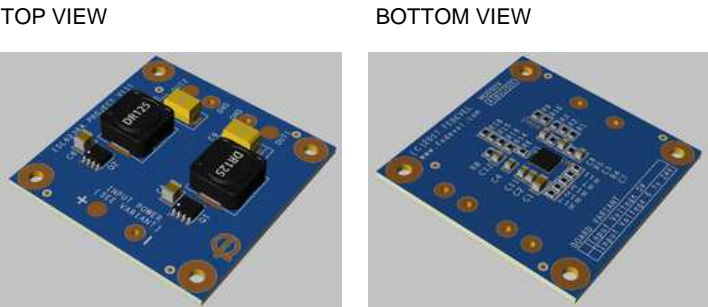
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for critical
layout guidelines.



ISL6236A PROJECT (Block Diagram)

Power supply input:
+6V to +24V (for "6 to 24V Variant")
+5V (for "5V Variant")

+VIN

ISL6236A

Power supply outputs

+1.5V

+1.05V

EN1

POK1

POK2



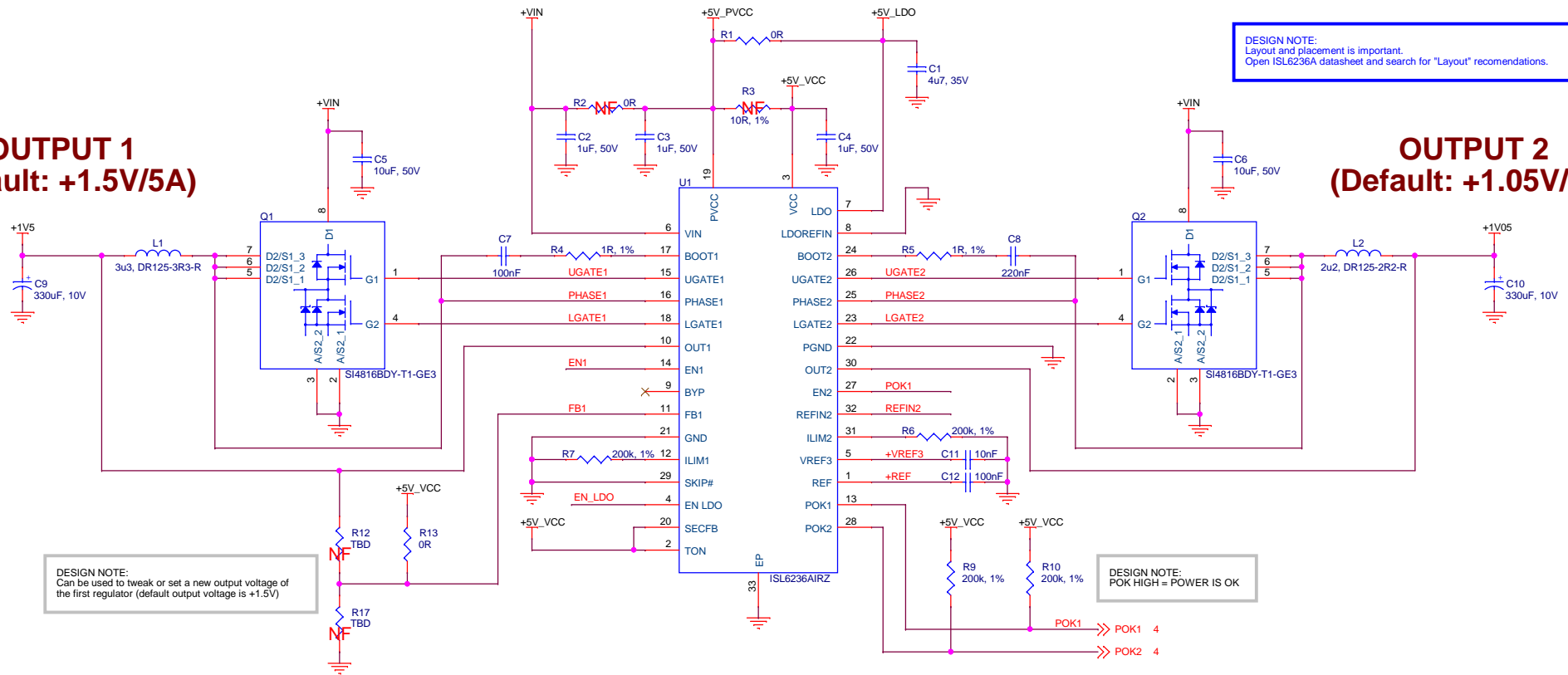
Learn Schematic and PCB Design - ONLINE

(c) 2017 FEDEVEL Academy		http://www.fedevel.com/academy	
Title: ISL6236A Project		Variant: <Core Design>	
Page: 02) BLOCK DIAGRAM		Checked by: <Checked by>	
Size: Custom	Document: LEOA	Designed by: Robert Feranec	Rev: V111
Date: Wednesday, February 21, 2018		Sheet: 2	of 5

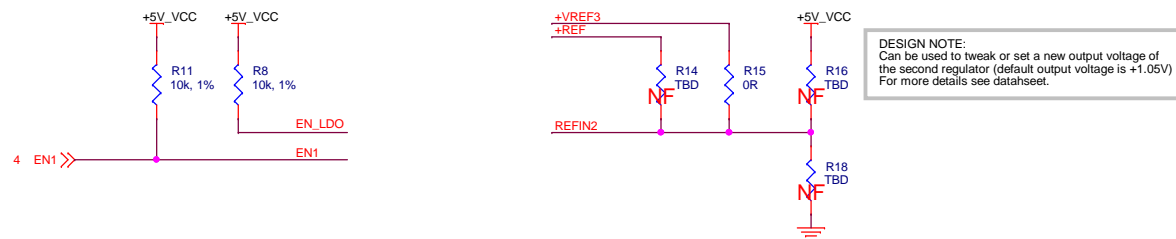
1.5V / 1.05V POWER SUPPLY

OUTPUT 1 (Default: +1.5V/5A)

OUTPUT 2 (Default: +1.05V/5A)



Enable/Disable regulators



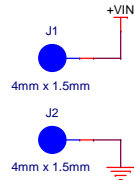
Learn Schematic and PCB Design - ONLINE

(c) 2017 FEDEVEL Academy		http://www.fedével.com/academy	
Title: ISL6236A Project		Variant: <Core Design>	
Page: 03) +1.5V / +1.05V POWER SUPPLY		Checked by: <Checked by>	
Size: Custom	Document: LEOA	Designed by: Robert Feranec	Rev: V111
Date: Friday, February 23, 2018		Sheet: 3	of 5

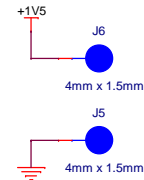
PADS, TESTPOINTS, MECHANICAL

Soldering pads for input and output wires

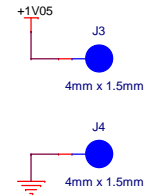
*+VIN: Main input power
(Default from +6V to +24V)*



*Power supply output 1
(Default +1.5V)*



*Power supply output 2
(Default +1.05V)*



Testpoints

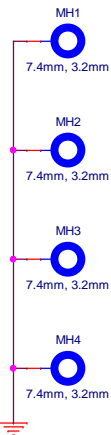
ENABLE Testpoint



POK Testpoints

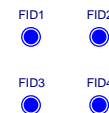


Mounting holes

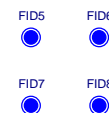


Fiducials

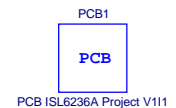
TOP



Bottom



PCB



Learn Schematic and PCB Design - ONLINE

(c) 2017 FEDEVEL Academy		http://www.fedevel.com/academy	
Title:	ISL6236A Project	Variant:	<Core Design>
Page:	04) PADS,TESTPOINTS, MECHANICAL	Checked by:	<Checked by>
Size:	Custom	Designed by:	Robert Feranec
Document:	LEOA	Rev:	V111
Date:	Wednesday, February 21, 2018	Sheet:	4 of 5

REVISION HISTORY

01-NOV-2017: Put here a brief info about the change
Detailed description of all the changes - Testing Lite



Learn Schematic and PCB Design - ONLINE

(c) 2017 FEDEVEL Academy		http://www.fedevel.com/academy	
Title: ISL6236A Project		Variant: <Core Design>	
Page: 05) REVISION HISTORY		Checked by: <Checked by>	
Size: Custom	Document: LEOA	Designed by: Robert Feranec	Rev: V111
Date: Wednesday, February 21, 2018		Sheet: 5	of 5