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HPC Week 1

1. Estimate the runtime for a matrix-matrix product for various number of unknowns systems that can sustain 1 MegaFLOP and 1 PetaFLOP. Provide a runtime graph.

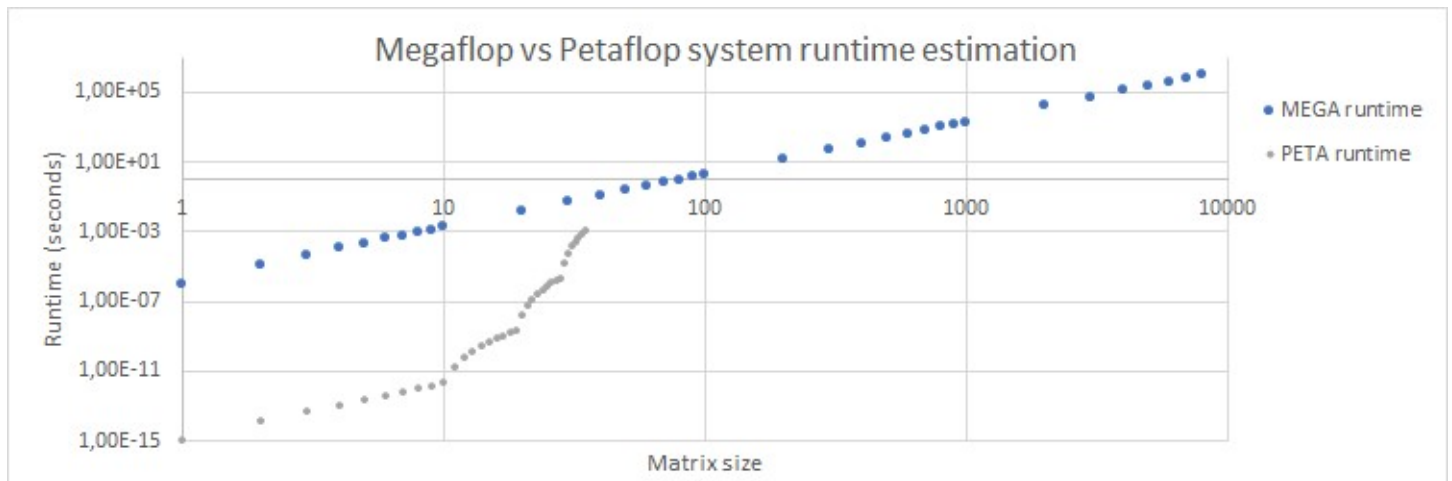
Assumptions and definitions:

1. Estimation scope applies only to matrix multiplication
2. Matrix A is of dimension $A \times C$
3. Matrix B is of dimension $C \times B$
4. Computation is serial

Floating point operations = $(AB)(2(C-1)) = (2ABC - AB)$

For a set of square matrices...

Floating point operations = $(2C^3 - C^2)$



(See Appendix for Megaflop and Petaflop system runtimes given multiple nodes)

2. Verify the theoretical performance (Rpeak) that can be reached by the top 3 computers in the Top500 list. Show all calculations and assumptions.

$$\begin{aligned}
 \text{Performance} &:= \text{Node}_{\text{count}} \cdot \text{Node}_{\text{TOTAL_Performance}} \\
 \text{Node}_{\text{TOTAL_Performance}} &:= \text{Node}_{\text{CPU_Performance}} + \text{Node}_{\text{GPU_Performance}} \\
 \text{Node}_{\text{CPU_Performance}} &:= \text{CPU}_{\text{Count}} \cdot \text{CPU}_{\text{Core}} \cdot \text{CPU}_{\text{Clock}} \cdot \text{CPU}_{\text{FP64_PerCycle}} \\
 \text{Node}_{\text{GPU_Performance}} &:= \text{GPU}_{\text{Count}} \cdot \text{GPU}_{\text{FP64_Performance}}
 \end{aligned}$$

2.1 Summit

$$\text{CPU}_{\text{Count}} := 2 \quad \text{CPU}_{\text{Core}} := 22 \quad \text{CPU}_{\text{Clock}} := 3,01 \text{ GHz} \quad \text{CPU}_{\text{FP64_PerCycle}} := 8$$

$$\text{Node}_{\text{CPU_Performance}} = 1,0595 \text{ THz}$$

$$\text{GPU}_{\text{Count}} := 6 \quad \text{GPU}_{\text{FP64_Performance}} := 7,8 \text{ THz}$$

$$\text{Node}_{\text{GPU_Performance}} = 46,8 \text{ THz}$$

$$\text{Node}_{\text{count}} := 4608$$

$$\text{Performance} = 220536,6682 \text{ THz}$$

$$\text{Top500 RPeak} = 200\,795 \text{ TFlops}$$

2.2 Sierra

$CPU_{Count} := 2$ $CPU_{Core} := 22$ $CPU_{Clock} := 3,4 \text{ GHz}$ $CPU_{FP64_PerCycle} := 8$

$Node_{CPU_Performance} = 1,1968 \text{ THz}$

$GPU_{Count} := 4$ $GPU_{FP64_Performance} := 7 \text{ THz}$

$Node_{GPU_Performance} = 28 \text{ THz}$

$Node_{count} := 4320$

$Performance = 126130,176 \text{ THz}$

Top500 RPeak = 125 712 TFlops

2.3 Sunway TaihuLight

$CPU_{Count} := 1$ $CPU_{Core} := 260$ $CPU_{Clock} := 1,45 \text{ GHz}$ $CPU_{FP64_PerCycle} := 8$

$Node_{CPU_Performance} = 3,016 \text{ THz}$

Assume Sunway TaihuLight does not implement GPU processing

$GPU_{Count} := 0$

$Node_{count} := 40960$

$Performance = 123535,36 \text{ THz}$

Top500 RPeak = 125 436 TFlops

Appendix

