Distributed and Parallel Computing Lecture 06

Alan P. Sexton

University of Birmingham

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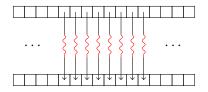
General Parallel Programming Patterns

There are a number of standard parallel programming patterns that form the basic building blocks of most GPU programs:

- Map
- Gather
- Scatter
- Stencil
- Transpose
- Reduce
- Scan/Sort
- Histogram

Мар

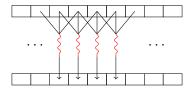
Map uses each thread to take an input value from the location corresponding to the block/thread id and write an output value to the location corresponding to the block/thread id with no two threads reading from or writing to the same location.



- One-to-one pattern
- No read/write race issues if source and destination are different
- Easy to ensure coalesced global memory accesses
- Easy to avoid shared memory bank collisions

Gather

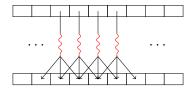
Gather uses each thread to take one or more input value from some locations (not just one from the direct location indicated by the thread and block id) and write an output value to one location with no two threads writing to the same location. Multiple different threads may share some read locations.



- Gather locations may have different patterns for each thread
- Many-to-one pattern
- No read/write race issues if source and destination are different
- Easy to ensure coalesced global memory accesses on writes
- Easy to avoid shared memory bank collisions on writes
- Care needed on read access patterns

Scatter

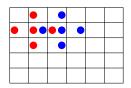
Scatter uses each thread to read the value from the location corresponding to the block/thread id and write output values to a number of locations Multiple different threads may share some write locations (e.g. using read/write operations such as increment)

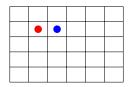


- Write locations may have different pattern for each thread
- One-to-many pattern
- Potential overwrite race issues
- Easy to ensure coalesced global memory accesses on reads
- Easy to avoid shared memory bank collisions on reads
- Care needed on write access patterns

Stencil

Stencil is a special case of Gather where the pattern of reads is constant across the threads.



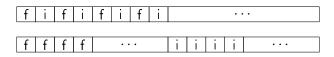


- Read locations have the same pattern for each thread
- Several-to-one pattern
- No read/write race issues if source and destination are different
- Easy to ensure coalesced global memory accesses on writes
- Easy to avoid shared memory bank collisions on writes
- Care needed on read access patterns

Transpose

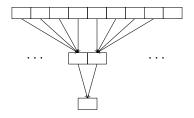
Transpose is most commonly the standard transpose operation on matrices, but is also used for restructuring datastructures for efficient memory accesses.

```
struct {
    float f;
    int i;
} X[LEN];
```



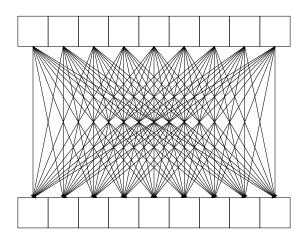
• Can be implemented with a Scatter or with a Gather

Reduce



• Many-to-one

Scan/Sort



• All-to-All (or at least Many-to-All)

Histogram — Serial Version

```
for (i=0; i < NUM_BINS; i++)
    bin[i] = 0;
for (i=0; i < NUM_VALS; i++)
    bin[computeBin(vals[i])] ++;</pre>
```

Histogram — Parallel Version (with error)

Histogram — Parallel Version (corrected)

Histogram — Parallel Version (corrected)

Scalability?

Histogram — Parallel Version (corrected)

- Scalability?
- 1M values ⇒ 1M atomic adds

 Have each thread calculate its own histogram on a subset of the data and then add to final result

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 - ullet 1M values, 1024 threads 16 bins \Rightarrow 1024 values per thread
 - ullet 1024 histograms of 16 bins \Rightarrow 16 * 1024 = 16284 atomic adds
- Better: Have each thread calculate its own histogram on a subset of the data and then REDUCE to final result

Further Improving Parallel Histogram

A more sophisticated, scalable approach:

- Calculate bin numbers (map)
- Sort bin numbers array (parallel-sort)
- Reduce-by-key of 1-array using bin numbers as key

Example: computeBin(val) = val / 3; //integer division:

Reduce

by ked,

Reduce-by-ked,

	4	0	8	6	2	4	1	7
	1	0	2	2	0	1	0	2
	0	0	0	1	1	2	2	2
	1	1	1	1	1	1	1	1
3 2 3								

raduce everytime key changes