Weekly Report

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March 4,2018

Week 8

Summary:

This week, I worked on the GUI part of my task. tktable has the trouble on installing on the Window system.

I get some trouble in installing Virtual Machine. I decided to use the tkinter to design table.

I finish basely my task but in fact, the result look not so beautiful.

Data:

Startpoint	wptr_full/wfull_reg				
Endpoint	fifomem/mem_reg	[3][0]			
Path Group	wclk				
Path Type	max				
Point		Сар	Trans	Incr	Path
clock wclk (rise edge)				0.000	0.000
clock network delay (ideal)				0.000	0.000
wptr_full/wfull_reg/CLK (DFFARX1)			0.000	0.000	0.000
wptr_full/wfull_reg/Q (DFFARX1)		0.570	0.018	0.059	0.059
wptr_full/wfull (wptr_full_ADDRSIZE2)				0.000	0.059
fifomem/wfull (fifomem_DATASIZE4_ADDRSIZE2)				0.000	0.059
fifomem/U35/A (INVX0)			0.018	0.026	0.085
ifomem/U35/Y (INVX0)		1.433	0.016	0.012	0.097
fifomem/U29/A2 (AND2X1)			0.016	0.049	0.146
ifomem/U29/Y (AND2X	1)	1.400	0.014	0.020	0.166
ifomem/U13/A2 (NAND)3X0)		0.014	0.048	0.214
fifomem/U13/Y (NAND3X0)		4.027	0.078	0.048	0.263
fifomem/U5/A (INVX1)			0.078	0.399	0.661
fifomem/U5/Y (INVX1)		2.866	0.048	0.020	0.681
fomem/U9/A2 (AO22X1)			0.048	0.218	0.900
ifomem/U9/Y (AO22X1)		0.734	0.024	0.035	0.935
fomem/mem_reg[3][0]/D (DFFX1)			0.024	0.011	0.946
data arrival time					0.946
:lock wclk (rise edge)				1.000	1.000
clock network delay (ideal)				0.000	1.000
fifomem/mem_reg[3][0]/CLK (DFFX1)				0.000	1.000

Next week:

- 1. I may make some adjustment using tktable.
- 2. Waiting for next task.