Weekly Report

Linyi Hong   Apr2, 2018   Week12

Summary:

I am working on task19 now, which is building a GUI for showing the report names and their details. It also need some build connection between different reports. It is almost done. Btw, I fix the bugs and things that we talk on Sunday, including,

- DoubleClick on listbox → SingleClick
- >,<,>=,<= or '' filter on timing slack, it also works on keyword section.
- The filter is based on regular expression
- When hit 'Clear' button, the search section will be reset
- The filter now only highlight what the user exactly looking for
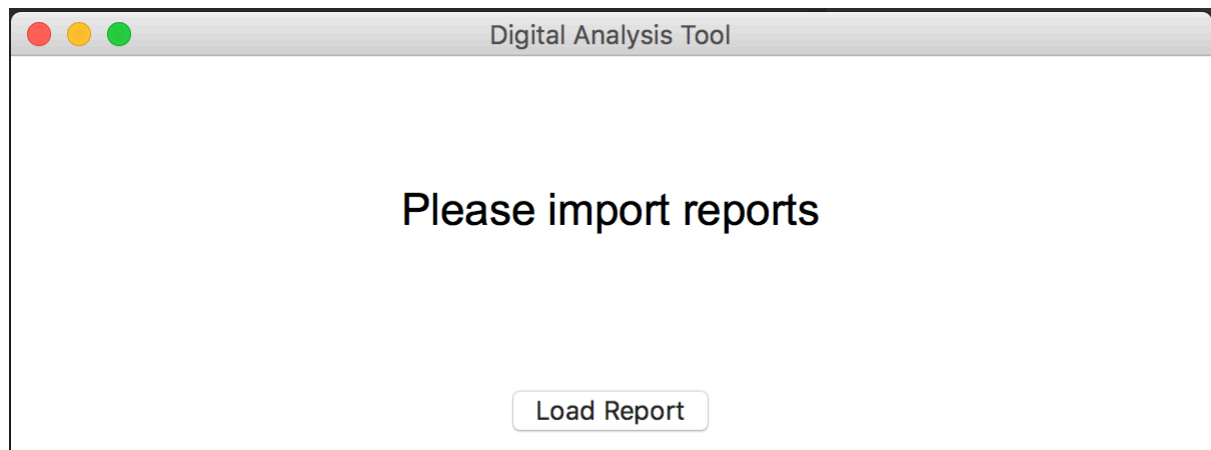- Bind the Enter with all the entry and the listbox

Issue:

- Did not try it on Linux, will check it later.
- I cannot come out with a prototype alone. We need to group up and talk about a blue print of prototype, otherwise is hard to build one.
- It will be great if someone can try to use this example code (wk12_example_Linyi.py) to make sure it is easy to use and no bugs.

Next:

Priority to work on my new task. Or will try to add a tab choice to the detail reports to show more detail reports in one GUI.

Data:

Begin

Import the first report

Search

- [ ] Startpoint [ ] Endpoint
- [ ] Path Group [ ] Timing Slack

Keyword [ ]

[Search]   [Clear]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rbin_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][3]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[1][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][3]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][3]	wclk	0.039

DoubleClick on certain path to get detail report

DC Analysis

Search

☐ Startpoint ☐ Endpoint
☐ Path Group ☐ Timing Slack

Keyword [_____]

[Search]   [Clear]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643

Startpoint: wdata[1] (input port clocked by wclk)
Endpoint: fifomem/mem_reg[1][1] (rising edge-triggered flip-flop clocked by wclk)
Path Group: INPUTS
Path Type: max

Des/Clust/Port	Wire Load Model	Library
fifo1	8000	saed32rvt_ff1p16v125c
fifomem_DATASIZE4_ADDRSIZE2		
	ForQA	saed32rvt_ff1p16v125c

Point	Cap	Trans	Incr	Path
clock wclk (rise edge)			0.000	0.000
clock network delay (ideal)			0.000	0.000
input external delay			0.100	0.100r
wdata[1] (in)	1.879	0.000	0.000	0.100r
fifomem/wdata[1] (fifomem_DATASIZE4_ADDRSIZE2)			0.000	0.100r
fifomem/U20/A2 (AO22X1)		0.000	0.196	0.296r
fifomem/U20/Y (AO22X1)	0.734	0.025	0.031	0.326r
fifomem/mem_reg[1][1]/D (DFFX1)		0.025	0.011	0.338r
data arrival time				0.338
clock wclk (rise edge)			1.000	1.000
clock network delay (ideal)			0.000	1.000

data required time   0.981
data arrival time   -0.338
----
slack (MET)   0.643

Both two table can be sort by the title column.

For every report, user can search by startpoint, endpoint, path group, timing slack or give any keywords. Except keywords, other four options have priority, which means if user give a keyword searching for path group, the string in 'Keyword' section will search based on path group. For example,

Find 'rptr' in Startpoint and 'rclk' in Path Group, and based on that, search '[1]' in other column, show all the result.

DC Analysis

Search
☑ Startpoint ☑ Endpoint
☑ Path Group ☑ Timing Slack
Keyword [1]
Startpoint rptr
Endpoint
Path Group rclk
Timing Slack (>value or else)
[Search] [Clear]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rbin_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][3]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[1][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][3]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][3]	wclk	wclk

Find 'rptr' in Startpoint ,'rptr_empty/rptr_reg[2]' in Endpoint and 'rclk' in Path Group.

DC Analysis

Search
☑ Startpoint ☑ Endpoint
☑ Path Group ☑ Timing Slack
Keyword
Startpoint rptr
Endpoint rptr_empty/rptr_re
Path Group rclk
Timing Slack (>value or else)
[Search] [Clear]

fifomem/mem_reg[0][2]
fifomem/mem_reg[1][2]
fifomem/mem_reg[2][0]
fifomem/mem_reg[2][1]
fifomem/mem_reg[2][2]
fifomem/mem_reg[2][3]
fifomem/mem_reg[3][2]
rptr_empty/rbin_reg[2]
rptr_empty/rptr_reg[2]
wptr_full/wbin_reg[2]
wptr_full/wptr_reg[2]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rbin_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][3]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[1][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][3]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][3]	wclk	wclk

Us < to filter the Timing Slack

DC Analysis

Search
- ☑ Startpoint ☑ Endpoint
- ☑ Path Group ☑ Timing Slack
- Keyword:
- Startpoint:
- Endpoint: [1]
- Path Group: inputs
- Timing Slack: <0.645

[Search] [Clear]

wdata[0]
wdata[1]
wdata[2]
wdata[3]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][3]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[1][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[1][3]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][0]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][1]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[2][2]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[3][3]		

Multiply GUI interaction

Digital Analysis Tool

Please import reports

[Load Report]

DC Analysis

Search
- ☐ Startpoint ☐ Endpoint
- ☐ Path Group ☐ Timing Slack
- Keyword:

[Search] [Clear]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
wptr_full/wfull_reg	fifomem/mem_reg[2][3]	wclk	0.039
wptr_full/wfull_reg	fifomem/mem_reg[0][0]	wclk	0.040
wptr_full/wfull_reg	fifomem/mem_reg[0][1]	wclk	0.040
wptr_full/wfull_reg	fifomem/mem_reg[0][2]	wclk	0.040
wptr_full/wfull_reg	fifomem/mem_reg[0][3]	wclk	0.040
/wbin_reg[0]	wptr_full/wfull_reg	wclk	0.156
/wbin_reg[0]	wptr_full/wptr_reg[1]	wclk	0.247
/wbin_reg[0]	wptr_full/wptr_reg[2]	wclk	0.362
/wbin_reg[0]	wptr_full/wptr_reg[2]	wclk	0.362
/wbin_reg[0]	wptr_full/wptr_reg[0]	wclk	0.434
/wbin_reg[0]	wptr_full/wbin_reg[1]	wclk	0.547
/wbin_reg[0]	wptr_full/wbin_reg[0]	wclk	0.724

DC Analysis

Search
- ☑ Startpoint ☑ Endpoint
- ☑ Path Group ☑ Timing Slack
- Keyword: <=0.645
- Startpoint:
- Endpoint: fifo
- Path Group: puts
- Timing Slack:

[Search] [Clear]

wdata[0]
wdata[1]
wdata[2]
wdata[3]

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035

Clear the search section

Search

- [ ] Startpoint
- [ ] Endpoint
- [ ] Path Group
- [ ] Timing Slack

Keyword [   ]

Search   Clear

DC Analysis

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
data[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
data[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
data[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
data[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
data[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
data[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
ty/rempty_reg	rempty	OUTPUTS	0.292
full/wfull_reg	wfull	OUTPUTS	0.815
ty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
ty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
ty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
ll/wfull_reg	fifomem/mem_reg[2][3]	wclk	0.039
ll/wfull_reg	fifomem/mem_reg[0][0]	wclk	0.040
ll/wfull_reg	fifomem/mem_reg[0][1]	wclk	0.040
ll/wfull_reg	fifomem/mem_reg[0][2]	wclk	0.040
ll/wfull_reg	fifomem/mem_reg[0][3]	wclk	0.040
/wbin_reg[0]	wptr_full/wfull_reg	wclk	0.156
/wbin_reg[0]	wptr_full/wptr_reg[1]	wclk	0.247
/wbin_reg[0]	wptr_full/wbin_reg[2]	wclk	0.362
/wbin_reg[0]	wptr_full/wptr_reg[2]	wclk	0.362
/wbin_reg[0]	wptr_full/wptr_reg[0]	wclk	0.434
/wbin_reg[0]	wptr_full/wbin_reg[1]	wclk	0.547
/wbin_reg[0]	wptr_full/wbin_reg[0]	wclk	0.724

Search

- [x] Startpoint
- [x] Endpoint
- [x] Path Group
- [x] Timing Slack

Keyword [   ]

Startpoint [   ]

Endpoint [   ]

Path Group [|]

Timing Slack (>value or else) [   ]

Search   Clear

DC Analysis

Startpoint	Endpoint	Path Group	Timing Slack
wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
wptr_full/wfull_reg	wfull	OUTPUTS	0.815
rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035