# **Weekly Report**

Linyi Hong Apr 8, 2018

Week13

## **Summary:**

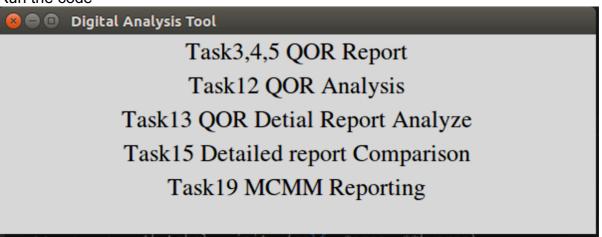
This week I was focus on combine partners' code. The folder will be uploaded only on git. My folder path is /u/linyi/Capstone. Prototype.py is the right one to run.

#### Issue:

There are some bugs in code but they will not affect to execute the codes.

#### Data:

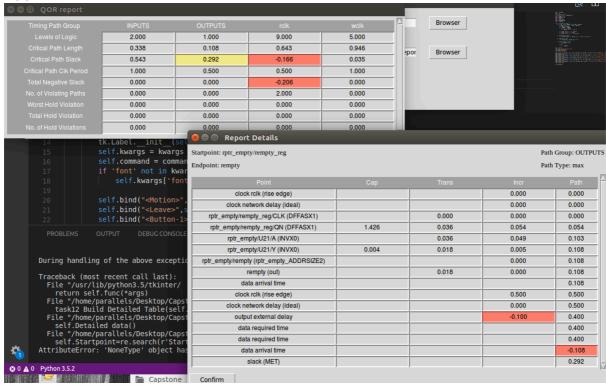
Run the code

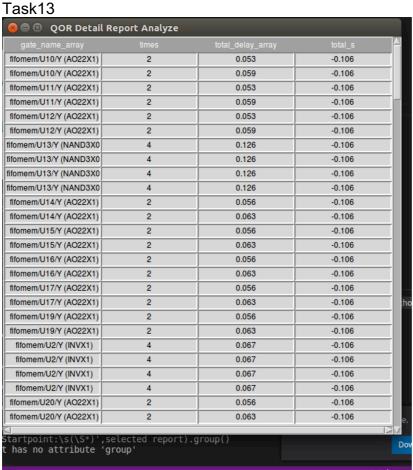


Task 3,4,5

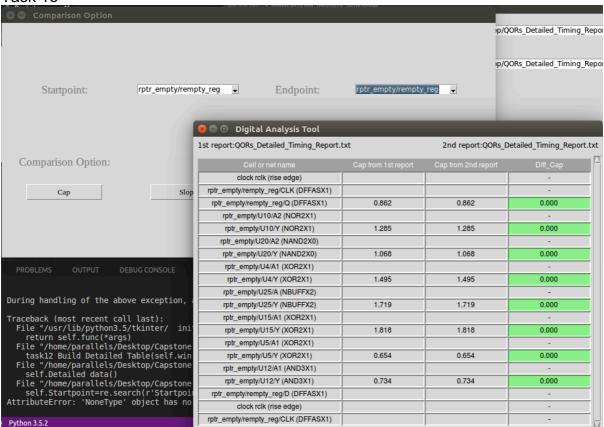


Task 12





Task 15



### Task 19

earch	Startpoint	Endpoint	Path Group	Timing Slack
Startpoint  Endpoint	wdata[0]	fifomem/mem_reg[0][0]	INPUTS	0.643
Path Group Timing Slack	wdata[1]	fifomem/mem_reg[0][1]	INPUTS	0.643
Keyword	wdata[2]	fifomem/mem_reg[0][2]	INPUTS	0.643
	wdata[3]	fifomem/mem_reg[0][3]	INPUTS	0.643
Search Clear	wdata[0]	fifomem/mem_reg[1][0]	INPUTS	0.643
	wdata[1]	fifomem/mem_reg[1][1]	INPUTS	0.643
	wdata[2]	fifomem/mem_reg[1][2]	INPUTS	0.643
	wdata[3]	fifomem/mem_reg[1][3]	INPUTS	0.643
	wdata[0]	fifomem/mem_reg[2][0]	INPUTS	0.643
	wdata[1]	fifomem/mem_reg[2][1]	INPUTS	0.643
	wdata[2]	fifomem/mem_reg[2][2]	INPUTS	0.643
	wdata[3]	fifomem/mem_reg[2][3]	INPUTS	0.643
	wdata[0]	fifomem/mem_reg[3][0]	INPUTS	0.645
	wdata[1]	fifomem/mem_reg[3][1]	INPUTS	0.645
	wdata[2]	fifomem/mem_reg[3][2]	INPUTS	0.645
	wdata[3]	fifomem/mem_reg[3][3]	INPUTS	0.645
	rptr_empty/rempty_reg	rempty	OUTPUTS	0.292
	wptr_full/wfull_reg	wfull	OUTPUTS	0.815
	rptr_empty/rempty_reg	rptr_empty/rempty_reg	rclk	-0.066
	rptr_empty/rempty_reg	rptr_empty/rptr_reg[1]	rclk	-0.040
	rptr_empty/rempty_reg	rptr_empty/rbin_reg[1]	rclk	0.002
	rptr_empty/rempty_reg	rptr_empty/rbin_reg[2]	rclk	0.013
	rptr_empty/rempty_reg	rptr_empty/rptr_reg[2]	rclk	0.013
	rptr_empty/rempty_reg	rptr_empty/rptr_reg[0]	rclk	0.055
	rptr_empty/rbin_reg[0]	rptr_empty/rbin_reg[0]	rclk	0.208
	wptr_full/wfull_reg	fifomem/mem_reg[3][0]	wclk	0.035
	wptr_full/wfull_reg	fifomem/mem_reg[3][1]	wclk	0.035
	wptr_full/wfull_reg	fifomem/mem_reg[3][2]	wclk	0.035
	wptr_full/wfull_reg	fifomem/mem_reg[3][3]	wclk	0.035
	wptr_full/wfull_reg	fifomem/mem_reg[1][0]	wclk	0.039
	wptr_full/wfull_reg	fifomem/mem_reg[1][1]	wclk	0.039
	wptr_full/wfull_reg	fifomem/mem_reg[1][2]	wclk	0.039
	wptr_full/wfull_reg	fifomem/mem_reg[1][3]	wclk	0.039