

1. (A)  $(111.111)_2 = (7.875)_{10}$

(B)  $(12.A)_{16} = (18.625)_{10}$

(C)  $(1110111)_2 = (77)_{16}$

(D)  $(AF0)_{16} = (1010\ 1111\ 0001\ 0000)_2$

2. (1) C (2)

3. (1) a. Make logic OR operation of the bit patterns in register 2 and register 3, and place the result in register 1.  
b. Move the bit patterns found in register E to register 1.

(2) 55

4.  $\square 0 \square$

$\square 0 \square$

reject.