Compiler Driver in VeGen

1 Header Files

The compiler driver of VeGen is called GSLP. GSLP directly includes the following header files:

1.1 Control Dependence

1.2 Block Builder

1.3 IR Vec

1.4 Inst Sema

1.5 Packer

1.6 Solver

1.7 Unroll Factor

1.8 Vector Pack Set

1.9 Scalarizer

2 Command Line Options

- Wrappers Dir: specify the directory of the wrapper functions

- Vectorize Only: only perform vectorization without packing

- Filter: select the loops to be vectorized

- Disable Unrolling: disable loop unrolling

- Disable Cleanup: disable cleanup passes after GSLP

- Disable Reduction Balancing: disable balancing the reduction tree

3 GSLP

GSLP is a function pass. It contains a reference to the bitcode of intrinsic wrapper functions and a LLVM target triple storing the architecture type.

GSLP depends on many LLVM analyses, such as Scalar Evolution, Lazy Value Info, Alias Analysis, Dependence Analysis, Dominator Tree, Loop Info, Post Dominator Tree, Target Transform Info, and Block Frequency Info.

3.1 Registration

GSLP runs after the following passes: Scalarization, GVN Hoist, Unify Function Exit Nodes, Loop Simplify, Loop Rotate, and LCSSA.

If cleanup passes are not disabled, the following passes will run after GSLP: CFG Simplification, Jump Threading, Instruction Combining, GVN, and Aggressive DCE.

3.2 Initialization

When initializing, GSLP gets the architecture type from an input module, then parses the IR file of wrapper functions accordingly.

3.3 Run on Function

3.3.1 Skip Unsupported Functions

For now, let me ignore the balancing of the reduction tree.

The first thing GSLP does when it runs on a function is to check whether it contains irreducible control flow. If it does, GSLP skips that function. Moreover, every basic block must terminate with a return instruction or a branch instruction, which excludes things like switches and invokes. GSLP also skips infinite loops.

3.3.2 Fill up Supported Intrinsics

3.3.3 Loop Unrolling

3.3.4 BottomUp Packing

3.3.5 Code Generation