MPU-6050 Accelerometer + Gyro

Introduction

The InvenSense MPU-6050 sensor contains a MEMS accelerometer and a MEMS gyro in a single chip. It is very accurate, as it contains 16-bits analog to digital conversion hardware for each channel. Therefor it captures the x, y, and z channel at the same time.

The MPU-6050 is not expensive, especially given the fact that it combines both an accelerometer and a gyro.



Photo: GY-521 breakout board

Also note that Invensense has combined the MPU-6050 with a magnetometer (compass) in a single chip called MPU-9150.

Reading raw values is easy, the rest is not

Reading the raw values for the accelerometer and gyro is easy. The sleep mode has to be disabled, and then the registers for the accelerometer and gyro can be read.

But the sensor also contains a 1024 byte FIFO buffer. The sensor values can be programmed to be placed in the FIFO buffer. And the buffer can be read by the Arduino.

The FIFO buffer is used together with the interrupt signal. If the MPU-6050 places data in the FIFO buffer, it signals the Arduino with the interrupt signal so the Arduino knows that there is data in the FIFO buffer waiting to be read.

A little more complicated is the ability to control a second I2C-device.

The MPU-6050 always acts as a slave to the Arduino with the SDA and SCL pins connected to the I2C-bus. But beside the normal I2C-bus, it has it's own I2C controller to be a master on a second (sub)-I2C-bus. It uses the pins AUX_DA and AUX_CL for that second (sub)-I2C-bus.

It can control, for example, a magnetometer. The values of the magnetometer can be passed on to the Arduino.

Things get really complex with the "DMP".

The sensor has a "Digital Motion Processor" (DMP), also called a "Digital Motion Processing Unit". This DMP can be programmed with firmware and is able to do complex calculations with the sensor values.

For this DMP, InvenSense has a discouragement policy, by not supplying enough information how to program the DMP. However, some have used reverse engineering to capture firmware.

The DMP ("Digital Motion Processor") can do fast calculations directy on the chip. This reduces the load for the microcontroller (like the Arduino). The DMP is even able to do calculations with the sensor values of another chip, for example a magnetometer connected to the second (sub)-I2C-bus.

Code

The accelerometer and gyro values are called the "raw" values. This is just as with other accelerometer and gyro sensors. A more sophisticated application is using the DMP to retrieve specific computed values from the sensor.

The <u>example code on this page</u> is just a simple and basic sketch to read the raw values.

For more serious use of the MPU-6050, Jeff Rowberg has done an excellent job.

See his I2C lib: http://www.i2cdevlib.com/devices/mpu6050

Or the latest code on github: https://github.com/jrowberg/i2cdevlib/tree/master/Arduino/MPU6050

The FreeIMU library includes the MPU-6050 code from Jeff Rowberg.

The FreeIMU library: http://www.varesano.net/projects/hardware/FreeIMU

To start with the MPU-6050, see the page of InvenSense: http://www.invensense.com/mems/gyro/mpu6050.html

For other programs and sensors, see the <u>Degrees Of Freedom</u>, <u>6DOF</u>, <u>9DOF</u>, <u>10DOF</u>, <u>11DOF</u>-<u>section</u> in the Playground index.

GY-521

This sensor board has a voltage regulator. When using 3.3V to the VCC the resulting voltage (after the onboard voltage regulator) might be too low for a good working I2C bus. It is preferred to apply 5V to the VCC pin of the sensor board. The board has pull-up resistors on the I2C-bus. The value of those pull-up resistors are sometimes 10k and sometimes 2k2. The 2k2 is rather low. If it is combined with other sensor board which have also pull-up resistors, the total pull-up impedance might be too low.

This schematic is hard to find, so here is a copy: http://playground.arduino.cc/uploads/Main/MPU6050-V1-SCH.jpg

Flyduino MPU6050 Break Out onboard 3.3V reg

http://flyduino.net/MPU6050-Break-Out-onboard-33V-reg_1

This sensor board contains a voltage regulator, so it can also be used with 5V. The pull-up resistors of the I2C-bus are 4k7. It is actually a GY-52 breakout board.

Flyduino 10DOF IMU GY-86 MPU6050+HMC58831+MS5611

http://flyduino.net/10DOF-IMU-GY-86-MPU6050-HMC58831-MS5611 1

Measurements

The raw values raises questions in the forums, since the raw values might seem unstable. Below are the raw values of the sensor that I measured, so you can compare them with your own raw values.

The raw values changes a lot due to a number of reasons. The default sensitivity is high, and the sensor returnes 16 bits, but the actual valid number of bits is less than 16 bits. Since they are 16 bits, a variation of 50 is just a very small variation.

The next measurement were done in these conditions:

- The sensor was placed as horizontal as possible.
- It was placed on concreet, not a wooden table.
- During the measurements, there was no traffic in the street.
- An battery of 12V was used, not the less stable voltage from the USB bus. I used a battery instead of an adapter to avoid any mains noise.
- The circuit was on for 15 minutes, to stabalize any temperature influence.
- The room temperature was 25 degrees Celcius.

```
MPU-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 184, -484, 14992
temperature: 29.635 degrees Celsius
gyro x,y,z: 367, 220, -812,
MPU-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 116, -364, 15056
temperature: 29.635 degrees Celsius
gyro x,y,z: 373, 226, -766,
MPII-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 232, -432, 15100
temperature: 29.682 degrees Celsius
gyro x,y,z: 382, 232, -790,
Read accel, temp and qyro, error = 0
accel x,y,z: 280, -468, 15136
temperature: 29.635 degrees Celsius
gyro x,y,z : 368, 211, -820,
```

```
MPU-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 140, -432, 15108
temperature: 29.588 degrees Celsius
gyro x,y,z: 388, 203, -806,

MPU-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 220, -464, 14920
temperature: 29.541 degrees Celsius
gyro x,y,z: 374, 196, -774,

MPU-6050
Read accel, temp and gyro, error = 0
accel x,y,z: 172, -440, 15100
temperature: 29.588 degrees Celsius
gyro x,y,z: 363, 200, -769,
```

Example sketch

The sketch below is the code made with Arduino 1.0.1 and it uses I2C-bus communication.

The code uses the Arduino functions as much as possible. It is just a simple and basic sketch to get the MPU-6050 working.

The I2C-address depends on the AD0 pin of the sensor. If it is connected to ground, the address is 0x68. If it is connected to VLOGIC (+3.3V) it is 0x69. There are a few sensor boards with the MPU-6050 sensor already soldered on it. Some of those boards have a pull-down resistor at AD0 (address = 0x68), others have a pull-up resistor (address = 0x69).

Search in the sketch for "MPU6050_I2C_ADDRESS" and set that to your own I2C address. A i2c_scanner can be used to check if the device is connected to the i2c bus.

The acceleration and gyro values of the sketch are raw values, which are not yet compensated for offset. The very first acceleration and gyro values after power up are sometimes not valid.

The sketch is about 7kbyte, and will fit in a ATmega8.

```
1. // MPU-6050 Accelerometer + Gyro
2. //-----
3. //
4. // By arduino.cc user "Krodal".
5. //
6. // June 2012
7. //
        first version
8. // July 2013
9. //
        The 'int' in the union for the x,y,z
10. //
        changed into int16_t to be compatible
        with Arduino Due.
11. //
12. //
13. // Open Source / Public Domain
15. // Using Arduino 1.0.1
16. // It will not work with an older version,
```

```
17. // since Wire.endTransmission() uses a parameter
18. // to hold or release the I2C bus.
19. //
20. // Documentation:
21. // - The InvenSense documents:
22. // - "MPU-6000 and MPU-6050 Product Specification",
23. // PS-MPU-6000A.pdf
24. // - "MPU-6000 and MPU-6050 Register Map and Descriptions",
       RM-MPU-6000A.pdf or RS-MPU-6000A.pdf
26. // - "MPU-6000/MPU-6050 9-Axis Evaluation Board User Guide"
       AN-MPU-6000EVB.pdf
27. //
28. //
29. // The accuracy is 16-bits.
30. //
31. // Temperature sensor from -40 to +85 degrees Celsius
32. // 340 per degrees, -512 at 35 degrees.
33. //
34. // At power-up, all registers are zero, except these two:
        Register 0x6B (PWR MGMT 2) = 0x40 (I read zero).
35. //
36. //
        Register 0x75 (WHO_AM_I) = 0x68.
37. //
38.
39. #include <Wire.h>
40.
41.
42. // The name of the sensor is "MPU-6050".
43. // For program code, I omit the '-',
44. // therefor I use the name "MPU6050....".
45.
46.
47. // Register names according to the datasheet.
48. // According to the InvenSense document
49. // "MPU-6000 and MPU-6050 Register Map
50. // and Descriptions Revision 3.2", there are no registers
51. // at 0x02 ... 0x18, but according other information
52. // the registers in that unknown area are for gain
53. // and offsets.
54. //
55. #define MPU6050_AUX_VDDIO
                                        0x01 // R/W
56. #define MPU6050 SMPLRT DIV
                                        0x19 // R/W
57. #define MPU6050 CONFIG
                                     0x1A // R/W
                                         0x1B // R/W
58. #define MPU6050 GYRO CONFIG
59. #define MPU6050_ACCEL_CONFIG
                                          0x1C // R/W
60. #define MPU6050_FF_THR
                                    0x1D // R/W
61. #define MPU6050 FF DUR
                                     0x1E // R/W
62. #define MPU6050_MOT_THR
                                      0x1F // R/W
63. #define MPU6050_MOT_DUR
                                      0x20 // R/W
64. #define MPU6050_ZRMOT_THR
                                        0x21 // R/W
65. #define MPU6050 ZRMOT DUR
                                        0x22 // R/W
66. #define MPU6050 FIFO EN
                                     0x23 // R/W
67. #define MPU6050_I2C_MST_CTRL
                                         0x24 // R/W
68. #define MPU6050_I2C_SLV0_ADDR
                                          0x25 // R/W
69. #define MPU6050_I2C_SLV0_REG
                                        0x26 // R/W
70. #define MPU6050_I2C_SLV0_CTRL
                                         0x27 // R/W
```

```
71. #define MPU6050 I2C SLV1 ADDR
                                     0x28 // R/W
72. #define MPU6050 I2C SLV1 REG
                                   0x29 // R/W
73. #define MPU6050_I2C_SLV1_CTRL
                                    0x2A // R/W
74. #define MPU6050_I2C_SLV2_ADDR
                                    0x2B // R/W
75. #define MPU6050_I2C_SLV2_REG
                                   0x2C // R/W
76. #define MPU6050_I2C_SLV2_CTRL
                                    0x2D // R/W
77. #define MPU6050_I2C_SLV3_ADDR
                                    0x2E // R/W
78. #define MPU6050_I2C_SLV3_REG
                                   0x2F // R/W
79. #define MPU6050 I2C SLV3 CTRL
                                    0x30 // R/W
80. #define MPU6050 I2C SLV4 ADDR
                                    0x31 // R/W
81. #define MPU6050_I2C_SLV4_REG
                                   0x32 // R/W
82. #define MPU6050 I2C SLV4 DO
                                   0x33 // R/W
83. #define MPU6050_I2C_SLV4_CTRL
                                    0x34 // R/W
84. #define MPU6050 I2C SLV4 DI
                                  0x35 // R
85. #define MPU6050_I2C_MST_STATUS
                                     0x36 // R
86. #define MPU6050_INT_PIN_CFG
                                   0x37 // R/W
87. #define MPU6050_INT_ENABLE
                                   0x38 // R/W
88. #define MPU6050 INT STATUS
                                   0x3A // R
89. #define MPU6050 ACCEL XOUT H
                                     0x3B // R
90. #define MPU6050 ACCEL XOUT L
                                     0x3C // R
91. #define MPU6050_ACCEL_YOUT_H
                                     0x3D // R
92. #define MPU6050 ACCEL YOUT L
                                     0x3E // R
93. #define MPU6050_ACCEL_ZOUT_H
                                     0x3F // R
94. #define MPU6050_ACCEL_ZOUT_L
                                     0x40 // R
95. #define MPU6050_TEMP_OUT_H
                                    0x41 // R
96. #define MPU6050_TEMP_OUT_L
                                   0x42 // R
97. #define MPU6050 GYRO XOUT H
                                     0x43 // R
98. #define MPU6050 GYRO XOUT L
                                     0x44 // R
99. #define MPU6050 GYRO YOUT H
                                     0x45 // R
100.
         #define MPU6050_GYRO_YOUT_L
                                          0x46 // R
101.
        #define MPU6050 GYRO ZOUT H
                                          0x47 // R
102.
        #define MPU6050 GYRO ZOUT L
                                          0x48 // R
        #define MPU6050_EXT_SENS_DATA_00 0x49 // R
103.
         #define MPU6050_EXT_SENS_DATA_01
104.
                                             0x4A // R
        #define MPU6050_EXT_SENS_DATA_02
105.
                                             0x4B // R
        #define MPU6050_EXT_SENS_DATA_03
                                             0x4C // R
106.
107.
        #define MPU6050 EXT SENS DATA 04
                                             0x4D // R
        #define MPU6050_EXT_SENS_DATA_05
108.
                                             0x4E // R
109.
        #define MPU6050_EXT_SENS_DATA_06
                                            0x4F // R
        #define MPU6050 EXT SENS DATA 07
                                             0x50 // R
110.
        #define MPU6050 EXT SENS DATA 08
111.
                                             0x51 // R
112.
        #define MPU6050 EXT SENS DATA 09
                                             0x52 // R
        #define MPU6050_EXT_SENS_DATA_10
113.
                                            0x53 // R
        #define MPU6050_EXT_SENS_DATA_11
114.
                                             0x54
                                                 // R
115.
        #define MPU6050 EXT SENS DATA 12
                                             0x55 // R
        #define MPU6050_EXT_SENS_DATA_13
116.
                                             0x56 // R
        #define MPU6050_EXT_SENS_DATA_14
                                             0x57
117.
                                                 // R
118.
        #define MPU6050_EXT_SENS_DATA_15
                                             0x58 // R
119.
        #define MPU6050 EXT SENS DATA 16
                                             0x59
                                                 // R
120.
         #define MPU6050 EXT SENS DATA 17
                                             0x5A // R
        #define MPU6050 EXT SENS DATA 18
121.
                                             0x5B // R
122.
        #define MPU6050_EXT_SENS_DATA_19
                                             0x5C // R
123.
        #define MPU6050_EXT_SENS_DATA_20 0x5D // R
        #define MPU6050_EXT_SENS_DATA_21 0x5E // R
124.
```

```
125.
         #define MPU6050 EXT SENS DATA 22 0x5F // R
126.
         #define MPU6050 EXT SENS DATA 23 0x60 // R
127.
         #define MPU6050_MOT_DETECT_STATUS 0x61 // R
128.
         #define MPU6050_I2C_SLV0_DO
                                           0x63 // R/W
129.
         #define MPU6050_I2C_SLV1_DO
                                           0x64 // R/W
130.
         #define MPU6050_I2C_SLV2_DO
                                           0x65 // R/W
131.
         #define MPU6050_I2C_SLV3_DO
                                           0x66 // R/W
132.
         #define MPU6050_I2C_MST_DELAY_CTRL 0x67 // R/W
133.
         #define MPU6050 SIGNAL PATH RESET 0x68 // R/W
134.
         #define MPU6050 MOT DETECT CTRL 0x69 // R/W
         #define MPU6050 USER CTRL
                                          0x6A // R/W
135.
         #define MPU6050 PWR MGMT 1
136.
                                            0x6B // R/W
         #define MPU6050_PWR_MGMT_2
137.
                                            0x6C // R/W
138.
         #define MPU6050 FIFO COUNTH
                                            0x72 // R/W
139.
         #define MPU6050_FIFO_COUNTL
                                            0x73 // R/W
140.
         #define MPU6050_FIFO_R_W
                                         0x74 // R/W
141.
         #define MPU6050_WHO_AM_I
                                          0x75 // R
142.
143.
144.
         // Defines for the bits, to be able to change
         // between bit number and binary definition.
145.
146.
         // By using the bit number, programming the sensor
         // is like programming the AVR microcontroller.
147.
         // But instead of using "(1<<X)", or "_BV(X)",
148.
         // the Arduino "bit(X)" is used.
149.
150.
         #define MPU6050_D0 0
151.
         #define MPU6050 D1 1
152.
         #define MPU6050 D2 2
         #define MPU6050 D3 3
153.
154.
         #define MPU6050 D4 4
155.
         #define MPU6050 D5 5
156.
         #define MPU6050 D6 6
157.
         #define MPU6050_D7 7
158.
         // AUX_VDDIO Register
159.
         #define MPU6050_AUX_VDDIO MPU6050_D7 // I2C high: 1=VDD, 0=VLOGIC
160.
161.
162.
         // CONFIG Register
163.
         // DLPF is Digital Low Pass Filter for both gyro and accelerometers.
         // These are the names for the bits.
164.
         // Use these only with the bit() macro.
165.
         #define MPU6050 DLPF CFG0
                                       MPU6050 D0
166.
         #define MPU6050_DLPF_CFG1
167.
                                       MPU6050_D1
         #define MPU6050_DLPF_CFG2
168.
                                       MPU6050 D2
         #define MPU6050 EXT SYNC SET0 MPU6050 D3
169.
         #define MPU6050_EXT_SYNC_SET1 MPU6050_D4
170.
         #define MPU6050_EXT_SYNC_SET2 MPU6050_D5
171.
172.
173.
         // Combined definitions for the EXT SYNC SET values
         #define MPU6050 EXT SYNC SET 0 (0)
174.
         #define MPU6050_EXT_SYNC_SET_1 (bit(MPU6050_EXT_SYNC_SET0))
175.
         #define MPU6050_EXT_SYNC_SET_2 (bit(MPU6050_EXT_SYNC_SET1))
176.
177.
         #define MPU6050_EXT_SYNC_SET_3
   (bit(MPU6050_EXT_SYNC_SET1)|bit(MPU6050_EXT_SYNC_SET0))
```

```
178.
        #define MPU6050 EXT SYNC SET 4 (bit(MPU6050 EXT SYNC SET2))
179.
        #define MPU6050 EXT SYNC SET 5
   (bit(MPU6050_EXT_SYNC_SET2)|bit(MPU6050_EXT_SYNC_SET0))
         #define MPU6050_EXT_SYNC_SET_6
180.
   (bit(MPU6050_EXT_SYNC_SET2)|bit(MPU6050_EXT_SYNC_SET1))
         #define MPU6050_EXT_SYNC_SET_7
181.
   (bit(MPU6050_EXT_SYNC_SET2)|bit(MPU6050_EXT_SYNC_SET1)|bit(MPU6050_EXT_SYNC_SET0))
182.
183.
        // Alternative names for the combined definitions.
184.
        #define MPU6050 EXT SYNC DISABLED
                                                MPU6050 EXT SYNC SET 0
        #define MPU6050_EXT_SYNC_TEMP_OUT_L MPU6050_EXT_SYNC_SET_1
185.
        #define MPU6050 EXT SYNC GYRO XOUT L MPU6050 EXT SYNC SET 2
186.
        #define MPU6050_EXT_SYNC_GYRO_YOUT_L MPU6050_EXT_SYNC_SET_3
187.
        #define MPU6050 EXT SYNC GYRO ZOUT L MPU6050 EXT SYNC SET 4
188.
189.
        #define MPU6050_EXT_SYNC_ACCEL_XOUT_L MPU6050_EXT_SYNC_SET_5
190.
        #define MPU6050_EXT_SYNC_ACCEL_YOUT_L MPU6050_EXT_SYNC_SET_6
191.
        #define MPU6050_EXT_SYNC_ACCEL_ZOUT_L MPU6050_EXT_SYNC_SET_7
192.
193.
        // Combined definitions for the DLPF CFG values
194.
        #define MPU6050 DLPF CFG 0 (0)
        #define MPU6050_DLPF_CFG_1 (bit(MPU6050_DLPF_CFG0))
195.
196.
        #define MPU6050 DLPF CFG 2 (bit(MPU6050 DLPF CFG1))
        #define MPU6050_DLPF_CFG_3 (bit(MPU6050_DLPF_CFG1)|bit(MPU6050_DLPF_CFG0))
197.
        #define MPU6050_DLPF_CFG_4 (bit(MPU6050_DLPF_CFG2))
198.
199.
        #define MPU6050_DLPF_CFG_5 (bit(MPU6050_DLPF_CFG2)|bit(MPU6050_DLPF_CFG0))
200.
        #define MPU6050_DLPF_CFG_6 (bit(MPU6050_DLPF_CFG2)|bit(MPU6050_DLPF_CFG1))
201.
        #define MPU6050 DLPF CFG 7
   (bit(MPU6050 DLPF CFG2)|bit(MPU6050 DLPF CFG1)|bit(MPU6050 DLPF CFG0))
202.
203.
        // Alternative names for the combined definitions
204.
        // This name uses the bandwidth (Hz) for the accelometer.
205.
        // for the gyro the bandwidth is almost the same.
206.
        #define MPU6050_DLPF_260HZ MPU6050_DLPF_CFG_0
207.
        #define MPU6050_DLPF_184HZ
                                     MPU6050_DLPF_CFG_1
        #define MPU6050_DLPF_94HZ
                                     MPU6050_DLPF_CFG_2
208.
                                     MPU6050_DLPF_CFG 3
209.
        #define MPU6050 DLPF 44HZ
        #define MPU6050 DLPF 21HZ
210.
                                     MPU6050 DLPF CFG 4
        #define MPU6050 DLPF 10HZ
                                     MPU6050 DLPF CFG 5
211.
212.
        #define MPU6050 DLPF 5HZ
                                     MPU6050 DLPF CFG 6
213.
        #define MPU6050 DLPF RESERVED MPU6050 DLPF CFG 7
214.
        // GYRO CONFIG Register
215.
        // The XG_ST, YG_ST, ZG_ST are bits for selftest.
216.
        // The FS SEL sets the range for the gyro.
217.
218.
        // These are the names for the bits.
219.
        // Use these only with the bit() macro.
        #define MPU6050_FS_SEL0 MPU6050_D3
220.
221.
        #define MPU6050_FS_SEL1 MPU6050_D4
222.
        #define MPU6050_ZG_ST MPU6050_D5
        #define MPU6050 YG ST MPU6050 D6
223.
        #define MPU6050 XG ST MPU6050 D7
224.
225.
226.
        // Combined definitions for the FS SEL values
227.
        #define MPU6050_FS_SEL_0 (0)
```

```
228.
         #define MPU6050 FS SEL 1 (bit(MPU6050 FS SEL0))
229.
         #define MPU6050_FS_SEL_2 (bit(MPU6050_FS_SEL1))
         #define MPU6050_FS_SEL_3 (bit(MPU6050_FS_SEL1)|bit(MPU6050_FS_SEL0))
230.
231.
232.
         // Alternative names for the combined definitions
         // The name uses the range in degrees per second.
233.
         #define MPU6050_FS_SEL_250 MPU6050_FS_SEL_0
234.
235.
         #define MPU6050_FS_SEL_500 MPU6050_FS_SEL_1
236.
         #define MPU6050 FS SEL 1000 MPU6050 FS SEL 2
237.
         #define MPU6050 FS SEL 2000 MPU6050 FS SEL 3
238.
         // ACCEL_CONFIG Register
239.
         // The XA_ST, YA_ST, ZA_ST are bits for selftest.
240.
         // The AFS SEL sets the range for the accelerometer.
241.
242.
         // These are the names for the bits.
243.
         // Use these only with the bit() macro.
         #define MPU6050_ACCEL_HPF0 MPU6050_D0
244.
         #define MPU6050_ACCEL_HPF1 MPU6050_D1
245.
         #define MPU6050 ACCEL HPF2 MPU6050 D2
246.
247.
         #define MPU6050 AFS SEL0 MPU6050 D3
248.
         #define MPU6050_AFS_SEL1 MPU6050_D4
249.
         #define MPU6050 ZA ST
                                  MPU6050 D5
250.
         #define MPU6050 YA ST
                                   MPU6050 D6
         #define MPU6050_XA_ST
251.
                                   MPU6050 D7
252.
253.
         // Combined definitions for the ACCEL_HPF values
254.
         #define MPU6050_ACCEL_HPF 0 (0)
         #define MPU6050 ACCEL HPF 1 (bit(MPU6050 ACCEL HPF0))
255.
         #define MPU6050_ACCEL_HPF_2 (bit(MPU6050_ACCEL_HPF1))
256.
257.
         #define MPU6050_ACCEL_HPF_3 (bit(MPU6050_ACCEL_HPF1)|bit(MPU6050_ACCEL_HPF0))
         #define MPU6050 ACCEL HPF 4 (bit(MPU6050 ACCEL HPF2))
258.
259.
         #define MPU6050 ACCEL HPF 7
   (bit(MPU6050_ACCEL_HPF2)|bit(MPU6050_ACCEL_HPF1)|bit(MPU6050_ACCEL_HPF0))
260.
261.
         // Alternative names for the combined definitions
262.
         // The name uses the Cut-off frequency.
         #define MPU6050 ACCEL HPF RESET MPU6050 ACCEL HPF 0
263.
         #define MPU6050_ACCEL_HPF_5HZ MPU6050_ACCEL_HPF_1
264.
265.
         #define MPU6050_ACCEL_HPF_2_5HZ MPU6050_ACCEL_HPF_2
         #define MPU6050 ACCEL HPF 1 25HZ MPU6050 ACCEL HPF 3
266.
         #define MPU6050 ACCEL HPF 0 63HZ MPU6050 ACCEL HPF 4
267.
         #define MPU6050 ACCEL HPF HOLD MPU6050 ACCEL HPF 7
268.
269.
270.
         // Combined definitions for the AFS SEL values
271.
         #define MPU6050 AFS SEL 0 (0)
272.
         #define MPU6050_AFS_SEL_1 (bit(MPU6050_AFS_SEL0))
         #define MPU6050_AFS_SEL_2 (bit(MPU6050_AFS_SEL1))
273.
274.
         #define MPU6050_AFS_SEL_3 (bit(MPU6050_AFS_SEL1)|bit(MPU6050_AFS_SEL0))
275.
276.
         // Alternative names for the combined definitions
277.
         // The name uses the full scale range for the accelerometer.
278.
         #define MPU6050_AFS_SEL_2G MPU6050_AFS_SEL_0
279.
         #define MPU6050 AFS SEL 4G MPU6050 AFS SEL 1
```

#define MPU6050_AFS_SEL_8G MPU6050_AFS_SEL_2

280.

```
281.
        #define MPU6050 AFS SEL 16G MPU6050 AFS SEL 3
282.
283.
        // FIFO_EN Register
        // These are the names for the bits.
284.
        // Use these only with the bit() macro.
285.
        #define MPU6050 SLV0 FIFO EN MPU6050 D0
286.
        #define MPU6050_SLV1_FIFO_EN MPU6050_D1
287.
288.
        #define MPU6050_SLV2_FIFO_EN MPU6050_D2
289.
        #define MPU6050 ACCEL FIFO EN MPU6050 D3
290.
        #define MPU6050 ZG FIFO EN
                                      MPU6050 D4
291.
        #define MPU6050 YG FIFO EN MPU6050 D5
292.
        #define MPU6050 XG FIFO EN MPU6050 D6
        #define MPU6050_TEMP_FIFO_EN MPU6050_D7
293.
294.
295.
        // I2C_MST_CTRL Register
296.
        // These are the names for the bits.
297.
        // Use these only with the bit() macro.
        #define MPU6050_I2C_MST_CLK0 MPU6050_D0
298.
299.
        #define MPU6050 I2C MST CLK1 MPU6050 D1
300.
        #define MPU6050 I2C MST CLK2 MPU6050 D2
        #define MPU6050_I2C_MST_CLK3 MPU6050_D3
301.
302.
        #define MPU6050 I2C MST P NSR MPU6050 D4
        #define MPU6050_SLV_3_FIFO_EN MPU6050_D5
303.
        #define MPU6050_WAIT_FOR_ES MPU6050_D6
304.
        #define MPU6050_MULT_MST_EN MPU6050_D7
305.
306.
307.
        // Combined definitions for the I2C MST CLK
        #define MPU6050_I2C_MST_CLK_0 (0)
308.
        #define MPU6050_I2C_MST_CLK_1 (bit(MPU6050_I2C_MST_CLK0))
309.
310.
        #define MPU6050_I2C_MST_CLK_2 (bit(MPU6050_I2C_MST_CLK1))
        #define MPU6050 I2C MST CLK 3
311.
   (bit(MPU6050 I2C MST CLK1)|bit(MPU6050 I2C MST CLK0))
         #define MPU6050_I2C_MST_CLK_4 (bit(MPU6050_I2C_MST_CLK2))
312.
313.
         #define MPU6050_I2C_MST_CLK_5
   (bit(MPU6050_I2C_MST_CLK2)|bit(MPU6050_I2C_MST_CLK0))
         #define MPU6050_I2C_MST_CLK_6
314.
   (bit(MPU6050 I2C MST CLK2)|bit(MPU6050 I2C MST CLK1))
         #define MPU6050 I2C MST CLK 7
315.
   (bit(MPU6050_I2C_MST_CLK2)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK0))
316.
        #define MPU6050 I2C MST CLK 8 (bit(MPU6050 I2C MST CLK3))
         #define MPU6050 I2C MST CLK 9
317.
   (bit(MPU6050 I2C MST CLK3)|bit(MPU6050 I2C MST CLK0))
318.
         #define MPU6050_I2C_MST_CLK_10
   (bit(MPU6050 I2C MST CLK3)|bit(MPU6050 I2C MST CLK1))
319.
        #define MPU6050 I2C MST CLK 11
   (bit(MPU6050_I2C_MST_CLK3)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK0))
320.
         #define MPU6050_I2C_MST_CLK_12
   (bit(MPU6050_I2C_MST_CLK3)|bit(MPU6050_I2C_MST_CLK2))
         #define MPU6050 I2C MST CLK 13
   (bit(MPU6050 I2C MST CLK3)|bit(MPU6050 I2C MST CLK2)|bit(MPU6050 I2C MST CLK0))
         #define MPU6050 I2C MST CLK 14
322.
   (bit(MPU6050_I2C_MST_CLK3)|bit(MPU6050_I2C_MST_CLK2)|bit(MPU6050_I2C_MST_CLK1))
```

```
323.
                #define MPU6050 I2C MST CLK 15
     (bit(MPU6050_I2C_MST_CLK3)|bit(MPU6050_I2C_MST_CLK2)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU6050_I2C_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(MPU60S_MST_CLK1)|bit(M
     U6050_I2C_MST_CLK0))
324.
325.
                // Alternative names for the combined definitions
326.
                // The names uses I2C Master Clock Speed in kHz.
327.
                #define MPU6050_I2C_MST_CLK_348KHZ MPU6050_I2C_MST_CLK_0
328.
                #define MPU6050_I2C_MST_CLK_333KHZ MPU6050_I2C_MST_CLK_1
                #define MPU6050_I2C_MST_CLK_320KHZ_MPU6050_I2C_MST_CLK_2
329.
330.
                #define MPU6050 I2C MST CLK 308KHZ MPU6050 I2C MST CLK 3
331.
                #define MPU6050_I2C_MST_CLK_296KHZ MPU6050_I2C_MST_CLK_4
                #define MPU6050 I2C MST CLK 286KHZ MPU6050 I2C MST CLK 5
332.
                #define MPU6050_I2C_MST_CLK_276KHZ MPU6050_I2C_MST_CLK_6
333.
                #define MPU6050 I2C MST CLK 267KHZ MPU6050 I2C MST CLK 7
334.
335.
                #define MPU6050_I2C_MST_CLK_258KHZ MPU6050_I2C_MST_CLK_8
336.
                #define MPU6050_I2C_MST_CLK_500KHZ MPU6050_I2C_MST_CLK_9
                #define MPU6050_I2C_MST_CLK_471KHZ MPU6050_I2C_MST_CLK_10
337.
                #define MPU6050_I2C_MST_CLK_444KHZ MPU6050_I2C_MST_CLK_11
338.
                #define MPU6050 I2C MST CLK 421KHZ MPU6050 I2C MST CLK 12
339.
340.
                #define MPU6050 I2C MST CLK 400KHZ MPU6050 I2C MST CLK 13
                #define MPU6050_I2C_MST_CLK_381KHZ MPU6050_I2C_MST_CLK_14
341.
342.
                #define MPU6050 I2C MST CLK 364KHZ MPU6050 I2C MST CLK 15
343.
                // I2C_SLV0_ADDR Register
344.
345.
                // These are the names for the bits.
                // Use these only with the bit() macro.
346.
347.
                #define MPU6050_I2C_SLV0_RW MPU6050_D7
348.
                // I2C SLV0 CTRL Register
349.
350.
                // These are the names for the bits.
351.
                // Use these only with the bit() macro.
352.
                #define MPU6050 I2C SLV0 LEN0
                                                                            MPU6050 D0
                #define MPU6050_I2C_SLV0_LEN1
353.
                                                                            MPU6050_D1
354.
                #define MPU6050_I2C_SLV0_LEN2
                                                                            MPU6050_D2
                #define MPU6050_I2C_SLV0_LEN3
355.
                                                                            MPU6050_D3
                #define MPU6050_I2C_SLV0_GRP
356.
                                                                            MPU6050 D4
357.
                #define MPU6050 I2C SLV0 REG DIS MPU6050 D5
                #define MPU6050 I2C SLV0 BYTE SW MPU6050 D6
358.
359.
                #define MPU6050 I2C SLV0 EN
                                                                          MPU6050 D7
360.
361.
                // A mask for the length
                #define MPU6050 I2C SLV0 LEN MASK 0x0F
362.
363.
                // I2C_SLV1_ADDR Register
364.
                // These are the names for the bits.
365.
                // Use these only with the bit() macro.
366.
                #define MPU6050_I2C_SLV1_RW MPU6050_D7
367.
368.
369.
                // I2C SLV1 CTRL Register
                // These are the names for the bits.
370.
371.
                // Use these only with the bit() macro.
372.
                #define MPU6050_I2C_SLV1_LEN0
                                                                            MPU6050 D0
373.
                #define MPU6050 I2C SLV1 LEN1
                                                                            MPU6050 D1
```

MPU6050 D2

#define MPU6050_I2C_SLV1_LEN2

374.

```
375.
         #define MPU6050 I2C SLV1 LEN3
                                           MPU6050 D3
376.
         #define MPU6050 I2C SLV1 GRP
                                           MPU6050 D4
         #define MPU6050_I2C_SLV1_REG_DIS MPU6050_D5
377.
         #define MPU6050_I2C_SLV1_BYTE_SW MPU6050_D6
378.
379.
         #define MPU6050 I2C SLV1 EN
                                          MPU6050 D7
380.
381.
         // A mask for the length
382.
         #define MPU6050_I2C_SLV1_LEN_MASK 0x0F
383.
384.
         // I2C SLV2 ADDR Register
         // These are the names for the bits.
385.
         // Use these only with the bit() macro.
386.
         #define MPU6050_I2C_SLV2_RW MPU6050_D7
387.
388.
389.
         // I2C_SLV2_CTRL Register
390.
         // These are the names for the bits.
391.
         // Use these only with the bit() macro.
         #define MPU6050_I2C_SLV2_LEN0
392.
                                           MPU6050 D0
393.
         #define MPU6050 I2C SLV2 LEN1
                                           MPU6050 D1
394.
         #define MPU6050 I2C SLV2 LEN2
                                           MPU6050 D2
         #define MPU6050_I2C_SLV2_LEN3
395.
                                           MPU6050 D3
396.
         #define MPU6050 I2C SLV2 GRP
                                           MPU6050 D4
         #define MPU6050_I2C_SLV2_REG_DIS MPU6050_D5
397.
         #define MPU6050_I2C_SLV2_BYTE_SW MPU6050_D6
398.
399.
         #define MPU6050_I2C_SLV2_EN
                                          MPU6050_D7
400.
401.
         // A mask for the length
         #define MPU6050 I2C SLV2 LEN MASK 0x0F
402.
403.
404.
         // I2C_SLV3_ADDR Register
405.
         // These are the names for the bits.
406.
         // Use these only with the bit() macro.
         #define MPU6050_I2C_SLV3_RW MPU6050_D7
407.
408.
409.
         // I2C_SLV3_CTRL Register
410.
         // These are the names for the bits.
411.
         // Use these only with the bit() macro.
         #define MPU6050 I2C SLV3 LEN0
412.
                                           MPU6050 D0
413.
         #define MPU6050_I2C_SLV3_LEN1
                                           MPU6050 D1
414.
         #define MPU6050 I2C SLV3 LEN2
                                           MPU6050 D2
         #define MPU6050_I2C_SLV3_LEN3
415.
                                           MPU6050 D3
         #define MPU6050 I2C SLV3 GRP
416.
                                           MPU6050 D4
417.
         #define MPU6050_I2C_SLV3_REG_DIS MPU6050_D5
         #define MPU6050_I2C_SLV3_BYTE_SW MPU6050_D6
418.
419.
         #define MPU6050_I2C_SLV3_EN
                                          MPU6050 D7
420.
         // A mask for the length
421.
422.
         #define MPU6050_I2C_SLV3_LEN_MASK 0x0F
423.
         // I2C SLV4 ADDR Register
424.
425.
         // These are the names for the bits.
426.
         // Use these only with the bit() macro.
427.
         #define MPU6050_I2C_SLV4_RW MPU6050_D7
428.
```

```
429.
         // I2C SLV4 CTRL Register
430.
         // These are the names for the bits.
         // Use these only with the bit() macro.
431.
432.
         #define MPU6050_I2C_MST_DLY0
                                          MPU6050_D0
         #define MPU6050_I2C_MST_DLY1
433.
                                          MPU6050 D1
434.
         #define MPU6050_I2C_MST_DLY2
                                          MPU6050_D2
435.
         #define MPU6050_I2C_MST_DLY3
                                          MPU6050_D3
436.
         #define MPU6050_I2C_MST_DLY4
                                          MPU6050_D4
         #define MPU6050 I2C SLV4 REG DIS MPU6050 D5
437.
438.
         #define MPU6050 I2C SLV4 INT EN MPU6050 D6
         #define MPU6050 I2C SLV4 EN
439.
                                         MPU6050 D7
440.
441.
         // A mask for the delay
         #define MPU6050 I2C MST DLY MASK 0x1F
442.
443.
444.
         // I2C_MST_STATUS Register
445.
         // These are the names for the bits.
446.
         // Use these only with the bit() macro.
         #define MPU6050 I2C SLV0 NACK MPU6050 D0
447.
448.
         #define MPU6050_I2C_SLV1_NACK MPU6050_D1
449.
         #define MPU6050_I2C_SLV2_NACK MPU6050_D2
450.
         #define MPU6050 I2C SLV3 NACK MPU6050 D3
         #define MPU6050_I2C_SLV4_NACK MPU6050_D4
451.
         #define MPU6050_I2C_LOST_ARB MPU6050_D5
452.
453.
         #define MPU6050_I2C_SLV4_DONE MPU6050_D6
454.
         #define MPU6050_PASS_THROUGH MPU6050_D7
455.
         // I2C PIN CFG Register
456.
         // These are the names for the bits.
457.
458.
         // Use these only with the bit() macro.
459.
         #define MPU6050 CLKOUT EN
                                         MPU6050 D0
460.
         #define MPU6050 I2C BYPASS EN MPU6050 D1
461.
         #define MPU6050_FSYNC_INT_EN
                                          MPU6050_D2
462.
         #define MPU6050_FSYNC_INT_LEVEL MPU6050_D3
         #define MPU6050_INT_RD_CLEAR
463.
                                          MPU6050_D4
464.
         #define MPU6050 LATCH INT EN
                                          MPU6050 D5
465.
         #define MPU6050 INT OPEN
                                       MPU6050 D6
         #define MPU6050_INT_LEVEL
466.
                                        MPU6050 D7
467.
468.
         // INT ENABLE Register
469.
         // These are the names for the bits.
         // Use these only with the bit() macro.
470.
471.
         #define MPU6050_DATA_RDY_EN
                                          MPU6050_D0
         #define MPU6050_I2C_MST_INT_EN MPU6050_D3
472.
473.
         #define MPU6050 FIFO OFLOW EN MPU6050 D4
474.
         #define MPU6050 ZMOT EN
                                       MPU6050 D5
475.
         #define MPU6050 MOT EN
                                      MPU6050 D6
476.
         #define MPU6050_FF_EN
                                    MPU6050_D7
477.
         // INT STATUS Register
478.
479.
         // These are the names for the bits.
480.
         // Use these only with the bit() macro.
481.
         #define MPU6050_DATA_RDY_INT MPU6050_D0
         #define MPU6050_I2C_MST_INT MPU6050_D3
482.
```

```
483.
         #define MPU6050 FIFO OFLOW INT MPU6050 D4
484.
         #define MPU6050 ZMOT INT
                                      MPU6050 D5
         #define MPU6050_MOT_INT
485.
                                      MPU6050_D6
486.
         #define MPU6050_FF_INT
                                    MPU6050_D7
487.
488.
         // MOT_DETECT_STATUS Register
489.
         // These are the names for the bits.
490.
         // Use these only with the bit() macro.
         #define MPU6050 MOT ZRMOT MPU6050 D0
491.
492.
         #define MPU6050 MOT ZPOS MPU6050 D2
         #define MPU6050 MOT ZNEG MPU6050 D3
493.
         #define MPU6050_MOT_YPOS MPU6050_D4
494.
         #define MPU6050_MOT_YNEG MPU6050_D5
495.
496.
         #define MPU6050 MOT XPOS MPU6050 D6
497.
         #define MPU6050_MOT_XNEG MPU6050_D7
498.
499.
        // IC2_MST_DELAY_CTRL Register
500.
         // These are the names for the bits.
501.
         // Use these only with the bit() macro.
502.
         #define MPU6050 I2C SLV0 DLY EN MPU6050 D0
         #define MPU6050_I2C_SLV1_DLY_EN MPU6050_D1
503.
504.
         #define MPU6050 I2C SLV2 DLY EN MPU6050 D2
         #define MPU6050_I2C_SLV3_DLY_EN MPU6050_D3
505.
         #define MPU6050_I2C_SLV4_DLY_EN MPU6050_D4
506.
507.
         #define MPU6050_DELAY_ES_SHADOW MPU6050_D7
508.
509.
        // SIGNAL PATH RESET Register
         // These are the names for the bits.
510.
511.
         // Use these only with the bit() macro.
512.
         #define MPU6050_TEMP_RESET MPU6050_D0
513.
         #define MPU6050 ACCEL RESET MPU6050 D1
514.
         #define MPU6050 GYRO RESET MPU6050 D2
515.
         // MOT_DETECT_CTRL Register
516.
         // These are the names for the bits.
517.
518.
         // Use these only with the bit() macro.
519.
         #define MPU6050 MOT COUNTO
                                         MPU6050 D0
         #define MPU6050 MOT COUNT1
520.
                                         MPU6050 D1
521.
         #define MPU6050_FF_COUNT0
                                        MPU6050 D2
522.
         #define MPU6050 FF COUNT1
                                        MPU6050 D3
         #define MPU6050 ACCEL ON DELAY0 MPU6050 D4
523.
         #define MPU6050 ACCEL ON DELAY1 MPU6050 D5
524.
525.
         // Combined definitions for the MOT COUNT
526.
527.
         #define MPU6050 MOT COUNT 0 (0)
528.
         #define MPU6050_MOT_COUNT_1 (bit(MPU6050_MOT_COUNT0))
         #define MPU6050_MOT_COUNT_2 (bit(MPU6050_MOT_COUNT1))
529.
530.
         #define MPU6050_MOT_COUNT_3
   (bit(MPU6050 MOT COUNT1)|bit(MPU6050 MOT COUNT0))
531.
532.
         // Alternative names for the combined definitions
533.
         #define MPU6050_MOT_COUNT_RESET MPU6050_MOT_COUNT_0
534.
535.
         // Combined definitions for the FF COUNT
```

```
536.
         #define MPU6050 FF COUNT 0(0)
537.
         #define MPU6050 FF COUNT 1 (bit(MPU6050 FF COUNT0))
538.
         #define MPU6050_FF_COUNT_2 (bit(MPU6050_FF_COUNT1))
         #define MPU6050_FF_COUNT_3 (bit(MPU6050_FF_COUNT1)|bit(MPU6050_FF_COUNT0))
539.
540.
         // Alternative names for the combined definitions
541.
         #define MPU6050_FF_COUNT_RESET MPU6050_FF_COUNT_0
542.
543.
         // Combined definitions for the ACCEL ON DELAY
544.
545.
         #define MPU6050 ACCEL ON DELAY 0 (0)
         #define MPU6050_ACCEL_ON_DELAY_1 (bit(MPU6050_ACCEL_ON_DELAY0))
546.
         #define MPU6050_ACCEL_ON_DELAY_2 (bit(MPU6050_ACCEL_ON_DELAY1))
547.
         #define MPU6050_ACCEL_ON_DELAY_3
548.
   (bit(MPU6050 ACCEL ON DELAY1)|bit(MPU6050 ACCEL ON DELAY0))
549.
550.
         // Alternative names for the ACCEL_ON_DELAY
551.
         #define MPU6050_ACCEL_ON_DELAY_0MS MPU6050_ACCEL_ON_DELAY_0
         #define MPU6050_ACCEL_ON_DELAY_1MS MPU6050_ACCEL_ON_DELAY_1
552.
         #define MPU6050 ACCEL ON DELAY 2MS MPU6050 ACCEL ON DELAY 2
553.
554.
         #define MPU6050 ACCEL ON DELAY 3MS MPU6050 ACCEL ON DELAY 3
555.
556.
        // USER CTRL Register
        // These are the names for the bits.
557.
558.
         // Use these only with the bit() macro.
559.
         #define MPU6050_SIG_COND_RESET MPU6050_D0
         #define MPU6050_I2C_MST_RESET MPU6050_D1
560.
         #define MPU6050 FIFO_RESET
561.
                                       MPU6050 D2
         #define MPU6050_I2C_IF_DIS
                                     MPU6050 D4 // must be 0 for MPU-6050
562.
         #define MPU6050 I2C MST EN
                                       MPU6050 D5
563.
564.
         #define MPU6050 FIFO EN
                                     MPU6050 D6
565.
566.
        // PWR MGMT 1 Register
        // These are the names for the bits.
567.
         // Use these only with the bit() macro.
568.
         #define MPU6050_CLKSEL0
569.
                                    MPU6050_D0
570.
         #define MPU6050 CLKSEL1
                                    MPU6050 D1
571.
         #define MPU6050 CLKSEL2
                                    MPU6050 D2
         #define MPU6050_TEMP_DIS
572.
                                     MPU6050 D3 // 1: disable temperature sensor
573.
         #define MPU6050 CYCLE
                                   MPU6050_D5 // 1: sample and sleep
574.
         #define MPU6050 SLEEP
                                   MPU6050 D6 // 1: sleep mode
575.
         #define MPU6050 DEVICE RESET MPU6050 D7 // 1: reset to default values
576.
577.
         // Combined definitions for the CLKSEL
         #define MPU6050 CLKSEL 0 (0)
578.
         #define MPU6050 CLKSEL 1 (bit(MPU6050 CLKSEL0))
579.
         #define MPU6050_CLKSEL_2 (bit(MPU6050_CLKSEL1))
580.
         #define MPU6050_CLKSEL_3 (bit(MPU6050_CLKSEL1)|bit(MPU6050_CLKSEL0))
581.
582.
         #define MPU6050_CLKSEL_4 (bit(MPU6050_CLKSEL2))
         #define MPU6050_CLKSEL_5 (bit(MPU6050_CLKSEL2)|bit(MPU6050_CLKSEL0))
583.
         #define MPU6050 CLKSEL 6 (bit(MPU6050 CLKSEL2)|bit(MPU6050 CLKSEL1))
584.
585.
         #define MPU6050_CLKSEL_7
   (bit(MPU6050_CLKSEL2)|bit(MPU6050_CLKSEL1)|bit(MPU6050_CLKSEL0))
586.
         // Alternative names for the combined definitions
587.
```

```
588.
         #define MPU6050 CLKSEL INTERNAL MPU6050 CLKSEL 0
589.
         #define MPU6050 CLKSEL X
                                          MPU6050 CLKSEL 1
590.
         #define MPU6050_CLKSEL_Y
                                          MPU6050_CLKSEL_2
591.
         #define MPU6050_CLKSEL_Z
                                         MPU6050_CLKSEL_3
         #define MPU6050_CLKSEL_EXT_32KHZ MPU6050_CLKSEL 4
592.
         #define MPU6050_CLKSEL_EXT_19_2MHZ MPU6050_CLKSEL_5
593.
594.
         #define MPU6050_CLKSEL_RESERVED
                                               MPU6050_CLKSEL_6
595.
         #define MPU6050_CLKSEL_STOP
                                            MPU6050_CLKSEL_7
596.
597.
         // PWR MGMT 2 Register
         // These are the names for the bits.
598.
         // Use these only with the bit() macro.
599.
600.
         #define MPU6050_STBY_ZG
                                      MPU6050_D0
601.
         #define MPU6050 STBY YG
                                       MPU6050 D1
         #define MPU6050_STBY_XG
602.
                                       MPU6050_D2
         #define MPU6050_STBY_ZA
603.
                                      MPU6050_D3
         #define MPU6050_STBY_YA
604.
                                       MPU6050_D4
         #define MPU6050 STBY XA
605.
                                       MPU6050 D5
         #define MPU6050 LP WAKE CTRL0 MPU6050 D6
606.
607.
         #define MPU6050_LP_WAKE_CTRL1 MPU6050_D7
608.
609.
         // Combined definitions for the LP WAKE CTRL
         #define MPU6050_LP_WAKE_CTRL_0 (0)
610.
         #define MPU6050_LP_WAKE_CTRL_1 (bit(MPU6050_LP_WAKE_CTRL0))
611.
612.
         #define MPU6050_LP_WAKE_CTRL_2 (bit(MPU6050_LP_WAKE_CTRL1))
         #define MPU6050_LP_WAKE_CTRL_3
613.
   (bit(MPU6050_LP_WAKE_CTRL1)|bit(MPU6050_LP_WAKE_CTRL0))
614.
         // Alternative names for the combined definitions
615.
         // The names uses the Wake-up Frequency.
616.
         #define MPU6050 LP WAKE 1 25HZ MPU6050 LP WAKE CTRL 0
617.
         #define MPU6050_LP_WAKE_2_5HZ MPU6050 LP WAKE CTRL 1
618.
         #define MPU6050_LP_WAKE_5HZ MPU6050_LP_WAKE_CTRL_2
619.
         #define MPU6050_LP_WAKE_10HZ MPU6050_LP_WAKE_CTRL_3
620.
621.
622.
         // Default I2C address for the MPU-6050 is 0x68.
623.
         // But only if the AD0 pin is low.
624.
625.
         // Some sensor boards have AD0 high, and the
         // I2C address thus becomes 0x69.
626.
         #define MPU6050_I2C_ADDRESS 0x68
627.
628.
629.
         // Declaring an union for the registers and the axis values.
630.
631.
         // The byte order does not match the byte order of
         // the compiler and AVR chip.
632.
         // The AVR chip (on the Arduino board) has the Low Byte
633.
634.
         // at the lower address.
         // But the MPU-6050 has a different order: High Byte at
635.
         // lower address, so that has to be corrected.
636.
         // The register part "reg" is only used internally,
637.
         // and are swapped in code.
638.
639.
         typedef union accel_t_gyro_union
640.
```

```
641.
            struct
642.
643.
             uint8_t x_accel_h;
644.
             uint8_t x_accel_1;
645.
             uint8_t y_accel_h;
646.
             uint8_t y_accel_1;
647.
             uint8_t z_accel_h;
648.
             uint8_t z_accel_l;
649.
             uint8_t t_h;
             uint8_t t_1;
650.
             uint8_t x_gyro_h;
651.
652.
             uint8_t x_gyro_1;
653.
             uint8_t y_gyro_h;
             uint8_t y_gyro_l;
654.
             uint8_t z_gyro_h;
655.
             uint8_t z_gyro_1;
656.
657.
            } reg;
658.
            struct
659.
             int16_t x_accel;
660.
661.
             int16_t y_accel;
             int16 tz accel;
662.
             int16_t temperature;
663.
             int16_t x_gyro;
664.
665.
             int16_t y_gyro;
             int16_t z_gyro;
666.
667.
            } value;
           };
668.
669.
670.
           void setup()
671.
672.
673.
            int error;
674.
            uint8_t c;
675.
676.
            Serial.begin(9600);
677.
            Serial.println(F("InvenSense MPU-6050"));
678.
679.
            Serial.println(F("June 2012"));
680.
            // Initialize the 'Wire' class for the I2C-bus.
681.
682.
            Wire.begin();
683.
684.
685.
            // default at power-up:
                Gyro at 250 degrees second
686.
            //
                Acceleration at 2g
687.
688.
               Clock source at internal 8MHz
689.
            //
                The device is in sleep mode.
            //
690.
691.
            error = MPU6050_read (MPU6050_WHO_AM_I, &c, 1);
692.
693.
            Serial.print(F("WHO_AM_I:"));
694.
            Serial.print(c,HEX);
```

```
695.
            Serial.print(F(", error = "));
696.
            Serial.println(error,DEC);
697.
            // According to the datasheet, the 'sleep' bit
698.
            // should read a '1'.
699.
            // That bit has to be cleared, since the sensor
700.
701.
            // is in sleep mode at power-up.
702.
            error = MPU6050_read (MPU6050_PWR_MGMT_1, &c, 1);
703.
            Serial.print(F("PWR MGMT 1:"));
704.
            Serial.print(c,HEX);
705.
            Serial.print(F(", error = "));
            Serial.println(error,DEC);
706.
707.
708.
709.
            // Clear the 'sleep' bit to start the sensor.
710.
            MPU6050_write_reg (MPU6050_PWR_MGMT_1, 0);
711.
           }
712.
713.
714.
           void loop()
715.
           {
716.
            int error:
717.
            double dT;
718.
            accel_t_gyro_union accel_t_gyro;
719.
720.
721.
            Serial.println(F(""));
            Serial.println(F("MPU-6050"));
722.
723.
724.
            // Read the raw values.
725.
            // Read 14 bytes at once.
726.
            // containing acceleration, temperature and gyro.
727.
            // With the default settings of the MPU-6050,
728.
            // there is no filter enabled, and the values
729.
            // are not very stable.
            error = MPU6050_read (MPU6050_ACCEL_XOUT_H, (uint8_t *) &accel_t_gyro,
730.
   sizeof(accel t gyro));
731.
            Serial.print(F("Read accel, temp and gyro, error = "));
732.
            Serial.println(error,DEC);
733.
734.
735.
            // Swap all high and low bytes.
736.
            // After this, the registers values are swapped,
737.
            // so the structure name like x_accel_1 does no
738.
            // longer contain the lower byte.
739.
            uint8 t swap;
740.
            #define SWAP(x,y) swap = x; x = y; y = swap
741.
742.
            SWAP (accel_t_gyro.reg.x_accel_h, accel_t_gyro.reg.x_accel_l);
743.
            SWAP (accel_t_gyro.reg.y_accel_h, accel_t_gyro.reg.y_accel_l);
744.
            SWAP (accel_t_gyro.reg.z_accel_h, accel_t_gyro.reg.z_accel_l);
745.
            SWAP (accel_t_gyro.reg.t_h, accel_t_gyro.reg.t_l);
746.
            SWAP (accel_t_gyro.reg.x_gyro_h, accel_t_gyro.reg.x_gyro_l);
747.
            SWAP (accel_t_gyro.reg.y_gyro_h, accel_t_gyro.reg.y_gyro_l);
```

```
748.
            SWAP (accel_t_gyro.reg.z_gyro_h, accel_t_gyro.reg.z_gyro_l);
749.
750.
            // Print the raw acceleration values
751.
752.
            Serial.print(F("accel x,y,z: "));
753.
754.
            Serial.print(accel_t_gyro.value.x_accel, DEC);
755.
            Serial.print(F(", "));
            Serial.print(accel t gyro.value.y accel, DEC);
756.
757.
            Serial.print(F(", "));
            Serial.print(accel_t_gyro.value.z_accel, DEC);
758.
            Serial.println(F(""));
759.
760.
761.
762.
            // The temperature sensor is -40 to +85 degrees Celsius.
763.
            // It is a signed integer.
764.
            // According to the datasheet:
            // 340 per degrees Celsius, -512 at 35 degrees.
765.
            // At 0 degrees: -512 - (340 * 35) = -12412
766.
767.
            Serial.print(F("temperature: "));
768.
769.
            dT = ((double) accel t gyro.value.temperature + 12412.0) / 340.0;
            Serial.print(dT, 3);
770.
            Serial.print(F(" degrees Celsius"));
771.
772.
            Serial.println(F(""));
773.
774.
775.
            // Print the raw gyro values.
776.
777.
            Serial.print(F("gyro x,y,z : "));
778.
            Serial.print(accel_t_gyro.value.x_gyro, DEC);
779.
            Serial.print(F(", "));
780.
            Serial.print(accel_t_gyro.value.y_gyro, DEC);
781.
            Serial.print(F(", "));
            Serial.print(accel_t_gyro.value.z_gyro, DEC);
782.
            Serial.print(F(", "));
783.
            Serial.println(F(""));
784.
785.
786.
            delay(1000);
787.
           }
788.
789.
790.
791.
           // MPU6050 read
792.
           //
793.
           // This is a common function to read multiple bytes
794.
           // from an I2C device.
795.
796.
           // It uses the boolean parameter for Wire.endTransMission()
797.
           // to be able to hold or release the I2C-bus.
798.
           // This is implemented in Arduino 1.0.1.
799.
           //
800.
           // Only this function is used to read.
801.
           // There is no function for a single byte.
```

```
802.
           //
803.
           int MPU6050_read(int start, uint8_t *buffer, int size)
804.
           {
805.
            int i, n, error;
806.
807.
            Wire.beginTransmission(MPU6050_I2C_ADDRESS);
808.
            n = Wire.write(start);
809.
            if (n != 1)
810.
             return (-10);
811.
            n = Wire.endTransmission(false); // hold the I2C-bus
812.
813.
            if (n != 0)
814.
             return (n);
815.
            // Third parameter is true: relase I2C-bus after data is read.
816.
            Wire.requestFrom(MPU6050_I2C_ADDRESS, size, true);
817.
818.
819.
            while(Wire.available() && i<size)
820.
821.
             buffer[i++]=Wire.read();
822.
823.
            if (i!=size)
             return (-11);
824.
825.
826.
            return (0); // return : no error
827.
           }
828.
829.
830.
831.
           // MPU6050_write
832.
833.
           // This is a common function to write multiple bytes to an I2C device.
834.
835.
           // If only a single register is written,
           // use the function MPU_6050_write_reg().
836.
837.
           //
838.
           // Parameters:
           // start : Start address, use a define for the register
839.
840.
           // pData : A pointer to the data to write.
841.
           // size : The number of bytes to write.
842.
           // If only a single register is written, a pointer
843.
           // to the data has to be used, and the size is
844.
           // a single byte:
845.
846.
           // int data = 0;
                               // the data to write
           // MPU6050_write (MPU6050_PWR_MGMT_1, &c, 1);
847.
           //
848.
849.
           int MPU6050_write(int start, const uint8_t *pData, int size)
850.
851.
            int n, error;
852.
            Wire.beginTransmission(MPU6050_I2C_ADDRESS);
853.
854.
            n = Wire.write(start);
                                   // write the start address
855.
            if (n != 1)
```

```
856.
             return (-20);
857.
858.
            n = Wire.write(pData, size); // write data bytes
859.
            if (n != size)
             return (-21);
860.
861.
862.
            error = Wire.endTransmission(true); // release the I2C-bus
863.
            if (error != 0)
             return (error);
864.
865.
866.
            return (0);
                            // return : no error
867.
           }
868.
869.
870.
           // MPU6050_write_reg
871.
872.
           // An extra function to write a single register.
           // It is just a wrapper around the MPU_6050_write()
873.
           // function, and it is only a convenient function
874.
           // to make it easier to write a single register.
875.
876.
877.
           int MPU6050_write_reg(int reg, uint8_t data)
878.
           {
879.
            int error;
880.
881.
            error = MPU6050_write(reg, &data, 1);
882.
883.
            return (error);
884.
```