

Yaggadi Abhiram

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[Linkedin](#) || [CodeChef](#)

PROFILE

Electronics and Communication Engineering student with a solid foundation in circuit design, embedded systems, and communication networks. Proficient in tools like MATLAB, Xilinx, and Multisim for circuit simulation and analysis. Focusing on VLSI design, and currently enhancing programming skills in Python and Verilog through the Code Basics Circuit Design Program. Seeking an internship to apply technical knowledge in real-world applications and develop expertise in cutting-edge technologies like IoT and 5G communication systems.

EDUCATION			
Name of course	Passing Year	Name of institute	CGPA
B-Tech	2026	CVR College of Engineering	8.53 (Current)
Class XII	2021	Narayana Junior College	95.2%
Class X	2019	Sri Chaitanya High School	9.0

CERTIFICATIONS

- Embedded System Developer Virtual Internship 10 Weeks By EduSkills
- PCB Basic design Certificate By Altium
- Mastering Digital Circuit Design Certificate By E-BOX
- ISWDP Level 1 Certificate By Synopsys

PROJECTS

Academic Project: DESIGN OF 4:2 APPROXIMATE COMPRESSOR FOR MULTIPLIER APPLICATIONS

- **Efficient Multiplier Design:** Developed a 4:2 approximate compressor to optimize the performance of multipliers by reducing power consumption and area.
- **Approximate Computing:** Leveraged approximation techniques to trade off minor accuracy loss for significant energy efficiency and speed improvements.
- **Applications:** Ideal for error-tolerant applications like image processing, machine learning, and digital signal processing.
- **Performance Metrics:** Achieved reduced power consumption, lower hardware complexity, and increased speed compared to exact designs.
- **Future Scope:** Potential integration into real-time hardware systems for AI and multimedia applications
- **Technologies Used :** Cadence Virtuoso Tool.

SKILLS & TOOLS

- **Programming Skills:** Verilog HDL
- **Soft Skills:** Effective Listener and Speaker , Adaptable to New Technologies, Situational Awareness.
- **Tools Known :** Xilinx Vivado, Cadence Virtuoso, Multisim, MATLAB.

DECLARATION: I hereby declare that the above-mentioned information is correct and true to my knowledge.

Place: HYDERABAD

(YAGGADI ABHIRAM)